

DI1	1x00001 2x00001 I:0	1,0x01 B:01			BIT R/O	
Current state of DI1:1=ON						
Current state of the digital input DIx =0:DI is OFF, =1:DI is ON						
DI2	1x00002 2x00002 I:1	0,0x00 B:00			BIT R/O	
Current state of DI2:0=OFF						
DI3	1x00003 2x00003 I:2	0,0x00 B:00			BIT R/O	
Current state of DI3:0=OFF						
DI4	1x00004 2x00004 I:3	0,0x00 B:00			BIT R/O	
Current state of DI4:0=OFF						
DI5	1x00005 2x00005 I:4	0,0x00 B:00			BIT R/O	
Current state of DI5:0=OFF						
DI6	1x00006 2x00006 I:5	0,0x00 B:00			BIT R/O	
Current state of DI6:0=OFF						
DI7	1x00007 2x00007 I:6	0,0x00 B:00			BIT R/O	
Current state of DI7:0=OFF						
DI8	1x00008 2x00008 I:7	0,0x00 B:00			BIT R/O	
Current state of DI8:0=OFF						
DI9	1x00009 2x00009 I:8	0,0x00 B:00			BIT R/O	
Current state of DI9:0=OFF						
DI10	1x00010 2x00010 I:9	0,0x00 B:00			BIT R/O	
Current state of DI10:0=OFF						
DI11	1x00011 2x00011 I:10	0,0x00 B:00			BIT R/O	
Current state of DI11:0=OFF						

DI12	1x00012 2x00012 I:11	0,0x00 B:00			BIT R/O	
Current state of DI12:0=OFF						
STATUS DIGITAL OUTPUTS						
DO1	1x00013 2x00013 I:12	1,0x01 B:01		1	BIT R/W	NO
Current state of DO1:1=ON				ENTER NEW STATE (0 or 1)		
Current state of the digital output DOx =0:DO is OFF, =1:DO is ON						
Writing on this register changes the state of the digital output						
DO2	1x00014 2x00014 I:13	1,0x01 B:01		0	BIT R/W	NO
Current state of DO2:1=ON				ENTER NEW STATE (0 or 1)		
DO3	1x00015 2x00015 I:14	1,0x01 B:01		0	BIT R/W	NO
Current state of DO3:1=ON				ENTER NEW STATE (0 or 1)		
DO4	1x00016 2x00016 I:15	1,0x01 B:01		0	BIT R/W	NO
Current state of DO4:1=ON				ENTER NEW STATE (0 or 1)		
DO5	1x00017 2x00017 I:16	1,0x01 B:01		0	BIT R/W	NO
Current state of DO5:1=ON				ENTER NEW STATE (0 or 1)		
DO6	1x00018 2x00018 I:17	1,0x01 B:01		0	BIT R/W	NO
Current state of DO6:1=ON				ENTER NEW STATE (0 or 1)		
DO7	1x00019 2x00019 I:18	1,0x01 B:01		0	BIT R/W	NO
Current state of DO7:1=ON				ENTER NEW STATE (0 or 1)		
DO8	1x00020 2x00020 I:19	1,0x01 B:01		0	BIT R/W	NO
Current state of DO8:1=ON				ENTER NEW STATE (0 or 1)		
DO9	1x00021 2x00021 I:20	1,0x01 B:01		0	BIT R/W	NO
Current state of DO9:1=ON				ENTER NEW STATE (0 or 1)		

DO10	1x00022 2x00022 I:21	1,0x01 B:01	0	BIT R/W	NO
		Current state of DO10:1=ON	ENTER NEW STATE (0 or 1)		
DO11	1x00023 2x00023 I:22	1,0x01 B:01	0	BIT R/W	NO
		Current state of DO11:1=ON	ENTER NEW STATE (0 or 1)		
DO12	1x00024 2x00024 I:23	1,0x01 B:01	0	BIT R/W	NO
		Current state of DO12:1=ON	ENTER NEW STATE (0 or 1)		
DIGITAL OUTPUTS: ENABLE OPEN WIRE DETECTION ON					
ENABLE OPEN WIRE DETECTION ON DO1	1x00025 2x00025 I:24	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state ON of DO1:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
Enables/disabled detection of an open wire in DO state ON for the digital output DOx =0:Open wire detection is OFF, =1:Open wire detection is ON					
Writing on this register changes the state of the open wire detection for this output					
ENABLE OPEN WIRE DETECTION ON DO2	1x00026 2x00026 I:25	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state ON of DO2:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO3	1x00027 2x00027 I:26	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state ON of DO3:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO4	1x00028 2x00028 I:27	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state ON of DO4:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO5	1x00029 2x00029 I:28	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state ON of DO5:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO6	1x00030 2x00030 I:29	0,0x00 B:00	1	BIT R/W	NO

		Current setup of open wire detection for state ON of DO6:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO7	1x00031 2x00031 I:30	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state ON of DO7:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO8	1x00032 2x00032 I:31	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state ON of DO8:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO9	1x00033 2x00033 I:32	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state ON of DO9:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO10	1x00034 2x00034 I:33	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state ON of DO10:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO11	1x00035 2x00035 I:34	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state ON of DO11:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO12	1x00036 2x00036 I:35	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state ON of DO12:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
DIGITAL OUTPUTS: ENABLE OPEN WIRE DETECTION OFF					
ENABLE OPEN WIRE DETECTION OFF DO1	1x00037 2x00037 I:36	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state OFF of DO1:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
Enables/disabled detection of an open wire in DO state OFF for the digital output DOx =0:Open wire detection is OFF, =1:Open wire detection is ON					
Writing on this register changes the state of the open wire detection for this output					
ENABLE OPEN WIRE DETECTION OFF DO2	1x00038 2x00038 I:37	0,0x00 B:00	1	BIT R/W	NO

		Current setup of open wire detection for state OFF of DO2:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO3	1x00039 2x00039 I:38	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state OFF of DO3:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO4	1x00040 2x00040 I:39	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state OFF of DO4:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO5	1x00041 2x00041 I:40	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state ON of DO5:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO6	1x00042 2x00042 I:41	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state OFF of DO6:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO7	1x00043 2x00043 I:42	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state OFF of DO7:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO8	1x00044 2x00044 I:43	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state OFF of DO8:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO9	1x00045 2x00045 I:44	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state OFF of DO9:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO10	1x00046 2x00046 I:45	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state OFF of DO10:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO11	1x00047 2x00047 I:46	0,0x00 B:00	1	BIT R/W	NO

		Current setup of open wire detection for state OFF of DO11:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO12	1x00048 2x00048 l:47	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state OFF of DO12:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
DIGITAL OUTPUTS: ENABLE SHORT TO VDD DETECTION					
ENABLE SHORT TO VDD DETECTION DO1	1x00049 2x00049 l:48	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state OFF of DO1:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
Enables/disabled detection of a shortcut to VDD in DO state OFF for the digital output DOx =0:Shortcut to VDD detection is OFF, =1:Shortcut to VDD detection is ON					
Writing on this register changes the state of the shortcut detection for this output					
ENABLE SHORT TO VDD DETECTION DO2	1x00050 2x00050 l:49	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state OFF of DO2:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO3	1x00051 2x00051 l:50	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state OFF of DO3:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO4	1x00052 2x00052 l:51	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state OFF of DO4:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO5	1x00053 2x00053 l:52	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state ON of DO5:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO6	1x00054 2x00054 l:53	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state OFF of DO6:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO7	1x00055 2x00055 l:54	0,0x00 B:00	1	BIT R/W	NO

		Current setup of open wire detection for state OFF of DO7:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO8	1x00056 2x00056 I:55	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state OFF of DO8:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO9	1x00057 2x00057 I:56	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state OFF of DO9:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO10	1x00058 2x00058 I:57	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state OFF of DO10:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO11	1x00059 2x00059 I:58	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state OFF of DO11:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO12	1x00060 2x00060 I:59	0,0x00 B:00	1	BIT R/W	NO
		Current setup of open wire detection for state OFF of DO12:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
DIGITAL OUTPUTS: OPEN WIRE DETECTION STATUS WHILE ON					
OPEN WIRE WHILE ON DO1	1x00061 2x00061 I:60	0,0x00 B:00		BIT R/O	
		Current detection state of an open wire in state ON for DO1:0=OK			
The current detection state of an open wire in the output state ON for the digital output DOx =0:No fault, =1:Fault-open wire detected					
OPEN WIRE WHILE ON DO2	1x00062 2x00062 I:61	0,0x00 B:00		BIT R/O	
		Current detection state of an open wire in state ON for DO2:0=OK			
OPEN WIRE WHILE ON DO3	1x00063 2x00063 I:62	0,0x00 B:00		BIT R/O	
		Current detection state of an open wire in state ON for DO3:0=OK			

OPEN WIRE WHILE ON DO4	1x00064 2x00064 I:63	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state ON for DO4:0=OK				
OPEN WIRE WHILE ON DO5	1x00065 2x00065 I:64	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state ON for DO5:0=OK				
OPEN WIRE WHILE ON DO6	1x00066 2x00066 I:65	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state ON for DO6:0=OK				
OPEN WIRE WHILE ON DO7	1x00067 2x00067 I:66	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state ON for DO7:0=OK				
OPEN WIRE WHILE ON DO8	1x00068 2x00068 I:67	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state ON for DO8:0=OK				
OPEN WIRE WHILE ON DO9	1x00069 2x00069 I:68	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state ON for DO9:0=OK				
OPEN WIRE WHILE ON DO10	1x00070 2x00070 I:69	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state ON for DO10:0=OK				
OPEN WIRE WHILE ON DO11	1x00071 2x00071 I:70	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state ON for DO11:0=OK				
OPEN WIRE WHILE ON DO12	1x00072 2x00072 I:71	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state ON for DO12:0=OK				

DIGITAL OUTPUTS: OPEN WIRE DETECTION STATUS WHILE OFF

OPEN WIRE WHILE OFF DO1	1x00073 2x00073 I:72	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state OFF for DO1:0=OK				
The current detection state of an open wire in the output state OFF for the digital output DOx =0:No fault, =1:Fault-open wire detected						
OPEN WIRE WHILE OFF DO2	1x00074 2x00074 I:73	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state OFF for DO2:0=OK				
OPEN WIRE WHILE OFF DO3	1x00075 2x00075 I:74	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state OFF for DO3:0=OK				
OPEN WIRE WHILE OFF DO4	1x00076 2x00076 I:75	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state OFF for DO4:0=OK				
OPEN WIRE WHILE OFF DO5	1x00077 2x00077 I:76	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state OFF for DO5:0=OK				
OPEN WIRE WHILE OFF DO6	1x00078 2x00078 I:77	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state OFF for DO6:0=OK				
OPEN WIRE WHILE OFF DO7	1x00079 2x00079 I:78	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state OFF for DO7:0=OK				
OPEN WIRE WHILE OFF DO8	1x00080 2x00080 I:79	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state OFF for DO8:0=OK				
OPEN WIRE WHILE OFF DO9	1x00081 2x00081 I:80	0,0x00 B:00			BIT R/O	

		Current detection state of an open wire in state OFF for DO9:0=OK			
OPEN WIRE WHILE OFF DO10	1x00082 2x00082 I:81	0,0x00 B:00			BIT R/O
		Current detection state of an open wire in state OFF for DO10:0=OK			
OPEN WIRE WHILE OFF DO11	1x00083 2x00083 I:82	0,0x00 B:00			BIT R/O
		Current detection state of an open wire in state OFF for DO11:0=OK			
OPEN WIRE WHILE OFF DO12	1x00084 2x00084 I:83	0,0x00 B:00			BIT R/O
		Current detection state of an open wire in state OFF for DO12:0=OK			
DIGITAL OUTPUTS: SHORTCUT DETECTION STATUS TO VDD WHILE OFF					
OPEN WIRE SHORTCUT TO VDD DO1	1x00085 2x00085 I:84	0,0x00 B:00			BIT R/O
		Current detection state of a shortcut to VDD in state OFF for DO1:0=OK			
The current detection state of a shortcut to VDD in the output state OFF for the digital output DOx =0:No fault, =1:Fault-shortcut to VDD detected					
OPEN WIRE SHORTCUT TO VDD DO2	1x00086 2x00086 I:85	0,0x00 B:00			BIT R/O
		Current detection state of a shortcut to VDD in state OFF for DO2:0=OK			
OPEN WIRE SHORTCUT TO VDD DO3	1x00087 2x00087 I:86	0,0x00 B:00			BIT R/O
		Current detection state of a shortcut to VDD in state OFF for DO3:0=OK			
OPEN WIRE SHORTCUT TO VDD DO4	1x00088 2x00088 I:87	0,0x00 B:00			BIT R/O
		Current detection state of a shortcut to VDD in state OFF for DO4:0=OK			
OPEN WIRE SHORTCUT TO VDD DO5	1x00089 2x00089 I:88	0,0x00 B:00			BIT R/O
		Current detection state of a shortcut to VDD in state OFF for DO5:0=OK			

OPEN WIRE SHORTCUT TO VDD DO6	1x00090 2x00090 I:89	0,0x00 B:00			BIT R/O	
		Current detection state of a shortcut to VDD in state OFF for DO6:0=OK				
OPEN WIRE SHORTCUT TO VDD DO7	1x00091 2x00091 I:90	0,0x00 B:00			BIT R/O	
		Current detection state of a shortcut to VDD in state OFF for DO7:0=OK				
OPEN WIRE SHORTCUT TO VDD DO8	1x00092 2x00092 I:91	0,0x00 B:00			BIT R/O	
		Current detection state of a shortcut to VDD in state OFF for DO8:0=OK				
OPEN WIRE SHORTCUT TO VDD DO9	1x00093 2x00093 I:92	0,0x00 B:00			BIT R/O	
		Current detection state of a shortcut to VDD in state OFF for DO9:0=OK				
OPEN WIRE SHORTCUT TO VDD DO10	1x00094 2x00094 I:93	0,0x00 B:00			BIT R/O	
		Current detection state of a shortcut to VDD in state OFF for DO10:0=OK				
OPEN WIRE SHORTCUT TO VDD DO11	1x00095 2x00095 I:94	0,0x00 B:00			BIT R/O	
		Current detection state of a shortcut to VDD in state OFF for DO11:0=OK				
OPEN WIRE SHORTCUT TO VDD DO12	1x00096 2x00096 I:95	0,0x00 B:00			BIT R/O	
		Current detection state of a shortcut to VDD in state OFF for DO12:0=OK				
DIGITAL OUTPUTS: THERMAL OVERLOAD DETECTION STATUS						
THERMAL OVERLOAD DETECTION STATUS DO1	1x00097 2x00097 I:96	0,0x00 B:00			BIT R/O	
		Current detection state of a thermal overload for DO1:0=OK				
The current detection state of a thermal overload for the digital output DOx =0:No fault, =1:Fault-thermal overload detected						
THERMAL OVERLOAD DETECTION STATUS DO2	1x00098 2x00098 I:97	0,0x00 B:00			BIT R/O	

		Current detection state of a thermal overload for DO2:0=OK			
THERMAL OVERLOAD DETECTION STATUS DO3	1x00099 2x00099 I:98	0,0x00 B:00			BIT R/O
		Current detection state of a thermal overload for DO3:0=OK			
THERMAL OVERLOAD DETECTION STATUS DO4	1x00100 2x00100 I:99	0,0x00 B:00			BIT R/O
		Current detection state of a thermal overload for DO4:0=OK			
THERMAL OVERLOAD DETECTION STATUS DO5	1x00101 2x00101 I:100	0,0x00 B:00			BIT R/O
		Current detection state of a thermal overload for DO5:0=OK			
THERMAL OVERLOAD DETECTION STATUS DO6	1x00102 2x00102 I:101	0,0x00 B:00			BIT R/O
		Current detection state of a thermal overload for DO6:0=OK			
THERMAL OVERLOAD DETECTION STATUS DO7	1x00103 2x00103 I:102	0,0x00 B:00			BIT R/O
		Current detection state of a thermal overload for DO7:0=OK			
THERMAL OVERLOAD DETECTION STATUS DO8	1x00104 2x00104 I:103	0,0x00 B:00			BIT R/O
		Current detection state of a thermal overload for DO8:0=OK			
THERMAL OVERLOAD DETECTION STATUS DO9	1x00105 2x00105 I:104	0,0x00 B:00			BIT R/O
		Current detection state of a thermal overload for DO9:0=OK			
THERMAL OVERLOAD DETECTION STATUS DO10	1x00106 2x00106 I:105	0,0x00 B:00			BIT R/O
		Current detection state of a thermal overload for DO10:0=OK			
THERMAL OVERLOAD DETECTION STATUS DO11	1x00107 2x00107 I:106	0,0x00 B:00			BIT R/O

		Current detection state of a thermal overload for DO11:0=OK			
THERMAL OVERLOAD DETECTION STATUS DO12	1x00108 2x00108 I:107	0,0x00 B:00			BIT R/O
		Current detection state of a thermal overload for DO12:0=OK			
DIGITAL OUTPUTS: CURRENT LIMIT DETECTION STATUS WHILE ON					
CURRENT LIMIT DETECTION STATUS WHILE ON DO1	1x00109 2x00109 I:108	0,0x00 B:00			BIT R/O
		Current detection state of a current limit while ON for DO1:0=OK			
The current detection state of a current limit while output is ON for the digital output DOx =0:No fault, =1:Fault-current limit error					
CURRENT LIMIT DETECTION STATUS WHILE ON DO2	1x00110 2x00110 I:109	0,0x00 B:00			BIT R/O
		Current detection state of a current limit while ON for DO2:0=OK			
CURRENT LIMIT DETECTION STATUS WHILE ON DO3	1x00111 2x00111 I:110	0,0x00 B:00			BIT R/O
		Current detection state of a current limit while ON for DO3:0=OK			
CURRENT LIMIT DETECTION STATUS WHILE ON DO4	1x00112 2x00112 I:111	0,0x00 B:00			BIT R/O
		Current detection state of a current limit while ON for DO4:0=OK			
CURRENT LIMIT DETECTION STATUS WHILE ON DO5	1x00113 2x00113 I:112	0,0x00 B:00			BIT R/O
		Current detection state of a current limit while ON for DO5:0=OK			
CURRENT LIMIT DETECTION STATUS WHILE ON DO6	1x00114 2x00114 I:113	0,0x00 B:00			BIT R/O
		Current detection state of a current limit while ON for DO6:0=OK			
CURRENT LIMIT DETECTION STATUS WHILE ON DO7	1x00115 2x00115 I:114	0,0x00 B:00			BIT R/O
		Current detection state of a current limit while ON for DO7:0=OK			

CURRENT LIMIT DETECTION STATUS WHILE ON DO8	1x00116 2x00116 I:115	0,0x00 B:00			BIT R/O	
		Current detection state of a current limit while ON for DO8:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO9	1x00117 2x00117 I:116	0,0x00 B:00			BIT R/O	
		Current detection state of a current limit while ON for DO9:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO10	1x00118 2x00118 I:117	0,0x00 B:00			BIT R/O	
		Current detection state of a current limit while ON for DO10:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO11	1x00119 2x00119 I:118	0,0x00 B:00			BIT R/O	
		Current detection state of a current limit while ON for DO11:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO12	1x00120 2x00120 I:119	0,0x00 B:00			BIT R/O	
		Current detection state of a current limit while ON for DO12:0=OK				
DIGITAL OUTPUTS: GLOBAL ERRORS DO1-DO6						
GLOBAL ERRORS FOR DO1-DO6 BIT 0	1x00121 2x00121 I:120	0,0x00 B:00			BIT R/O	
		BIT 0:Internal under voltage detected:0=OK				
GLOBAL ERRORS FOR DO1-DO6 BIT 1	1x00122 2x00122 I:121	0,0x00 B:00			BIT R/O	
		BIT 1:VA under voltage detected (<2.3V):0=OK				
GLOBAL ERRORS FOR DO1-DO6 BIT 2	1x00123 2x00123 I:122	0,0x00 B:00			BIT R/O	
		BIT 2:VDD not good detected (<17V):0=OK				
GLOBAL ERRORS FOR DO1-DO6 BIT 3	1x00124 2x00124 I:123	0,0x00 B:00			BIT R/O	
		BIT 3:VDD warning detected (<12V):0=OK				
GLOBAL ERRORS FOR DO1-DO6 BIT 4	1x00125 2x00125 I:124	0,0x00 B:00			BIT R/O	
		BIT 4:VDD under voltage detected (<8V):0=OK				

GLOBAL ERRORS FOR DO1-DO6 BIT 5	1x00126 2x00126 I:125	0,0x00 B:00			BIT R/O	
BIT 5:Thermal shutdown:0=OK						
GLOBAL ERRORS FOR DO1-DO6 BIT 6	1x00127 2x00127 I:126	0,0x00 B:00			BIT R/O	
BIT 6:Synchronisation error detected:0=OK						
GLOBAL ERRORS FOR DO1-DO6 BIT 7	1x00128 2x00128 I:127	0,0x00 B:00			BIT R/O	
BIT 7:Watchdog error detected:0=OK						
The global error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
DIGITAL OUTPUTS: GLOBAL ERRORS DO7-DO12						
GLOBAL ERRORS FOR DO7-DO12 BIT 0	1x00129 2x00129 I:128	0,0x00 B:00			BIT R/O	
BIT 0:Internal under voltage detected:0=OK						
GLOBAL ERRORS FOR DO7-DO12 BIT 1	1x00130 2x00130 I:129	0,0x00 B:00			BIT R/O	
BIT 1:VA under voltage detected (<2.3V):0=OK						
GLOBAL ERRORS FOR DO7-DO12 BIT 2	1x00131 2x00131 I:130	0,0x00 B:00			BIT R/O	
BIT 2:VDD not good detected (<17V):0=OK						
GLOBAL ERRORS FOR DO7-DO12 BIT 3	1x00132 2x00132 I:131	0,0x00 B:00			BIT R/O	
BIT 3:VDD warning detected (<12V):0=OK						
GLOBAL ERRORS FOR DO7-DO12 BIT 4	1x00133 2x00133 I:132	0,0x00 B:00			BIT R/O	
BIT 4:VDD under voltage detected (<8V):0=OK						
GLOBAL ERRORS FOR DO7-DO12 BIT 5	1x00134 2x00134 I:133	0,0x00 B:00			BIT R/O	
BIT 5:Thermal shutdown:0=OK						
GLOBAL ERRORS FOR DO7-DO12 BIT 6	1x00135 2x00135 I:134	0,0x00 B:00			BIT R/O	
BIT 6:Synchronisation error detected:0=OK						
GLOBAL ERRORS FOR DO7-DO12 BIT 7	1x00136 2x00136 I:135	0,0x00 B:00			BIT R/O	

		BIT 7:Watchdog error detected:0=OK				
The global error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
DIGITAL OUTPUTS: INTERRUPT STATUS DO1-DO6						
INTERRUPT STATUS FOR DO1-DO6 BIT 0	1x00137 2x00137 I:136	0,0x00 B:00			BIT R/O	
		BIT 0:Overload detected:0=OK				
INTERRUPT STATUS FOR DO1-DO6 BIT 1	1x00138 2x00138 I:137	0,0x00 B:00			BIT R/O	
		BIT 1:Current limit detected:0=OK				
INTERRUPT STATUS FOR DO1-DO6 BIT 2	1x00139 2x00139 I:138	0,0x00 B:00			BIT R/O	
		BIT 2:Open wire while OFF detected:0=OK				
INTERRUPT STATUS FOR DO1-DO6 BIT 3	1x00140 2x00140 I:139	0,0x00 B:00			BIT R/O	
		BIT 3:Open wire while ON detected:0=OK				
INTERRUPT STATUS FOR DO1-DO6 BIT 4	1x00141 2x00141 I:140	0,0x00 B:00			BIT R/O	
		BIT 4:Short to VDD while ON detected:0=OK				
INTERRUPT STATUS FOR DO1-DO6 BIT 5	1x00142 2x00142 I:141	0,0x00 B:00			BIT R/O	
		BIT 5:Thermal error detected-shutdown:0=OK				
INTERRUPT STATUS FOR DO1-DO6 BIT 6	1x00143 2x00143 I:142	0,0x00 B:00			BIT R/O	
		BIT 6:Supply error detected:0=OK				
INTERRUPT STATUS FOR DO1-DO6 BIT 7	1x00144 2x00144 I:143	0,0x00 B:00			BIT R/O	
		BIT 7:Communication error detected:0=OK				
The global interrupt error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
DIGITAL OUTPUTS: INTERRUPT STATUS DO7-DO12						
INTERRUPT STATUS FOR DO7-DO12 BIT 0	1x00145 2x00145 I:144	0,0x00 B:00			BIT R/O	
		BIT 0:Overload detected:0=OK				
INTERRUPT STATUS FOR DO7-DO12 BIT 1	1x00146 2x00146 I:145	0,0x00 B:00			BIT R/O	

		BIT 1:Current limit detected:0=OK				
INTERRUPT STATUS FOR DO7-DO12 BIT 2	1x00147 2x00147 I:146	0,0x00 B:00			BIT R/O	
		BIT 2:Open wire while OFF detected:0=OK				
INTERRUPT STATUS FOR DO7-DO12 BIT 3	1x00148 2x00148 I:147	0,0x00 B:00			BIT R/O	
		BIT 3:Open wire while ON detected:0=OK				
INTERRUPT STATUS FOR DO7-DO12 BIT 4	1x00149 2x00149 I:148	0,0x00 B:00			BIT R/O	
		BIT 4:Short to VDD while ON detected:0=OK				
INTERRUPT STATUS FOR DO7-DO12 BIT 5	1x00150 2x00150 I:149	0,0x00 B:00			BIT R/O	
		BIT 5:Thermal error detected-shutdown:0=OK				
INTERRUPT STATUS FOR DO7-DO12 BIT 6	1x00151 2x00151 I:150	0,0x00 B:00			BIT R/O	
		BIT 6:Supply error detected:0=OK				
INTERRUPT STATUS FOR DO7-DO12 BIT 7	1x00152 2x00152 I:151	0,0x00 B:00			BIT R/O	
		BIT 7:Communication error detected:0=OK				
The global interrupt error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
SPI COMMUNICATION DIGITAL OUTPUTS						
SPI COMMUNICATION DO1-DO6 BIT 1	1x00153 2x00153 I:152	0,0x00 B:00			BIT R/O	
		Bit 1:Current SPI communication for DO1-DO6:0=NO FAULT				
The current monitoring state of the SPI communication for the digital output group =0:No fault, =1:Fault						
SPI COMMUNICATION DO1-DO6 BIT 2	1x00154 2x00154 I:153	0,0x00 B:00			BIT R/O	
		Bit 2:Current SPI communication for DO1-DO6:0=NO FAULT				
SPI COMMUNICATION DO1-DO6 BIT 3	1x00155 2x00155 I:154	0,0x00 B:00			BIT R/O	
		Bit 3:Current SPI communication for DO1-DO6:0=NO FAULT				
SPI COMMUNICATION DO1-DO6 BIT 4	1x00156 2x00156 I:155	0,0x00 B:00			BIT R/O	
		Bit 4:Current SPI communication for DO1-DO6:0=NO FAULT				

SPI COMMUNICATION DO1-DO6 BIT 5	1x00157 2x00157 I:156	0,0x00 B:00			BIT R/O	
Bit 5:Current SPI communication for DO1-DO6:0=NO FAULT						
SPI COMMUNICATION DO1-DO6 BIT 6	1x00158 2x00158 I:157	0,0x00 B:00			BIT R/O	
Bit 6:Current SPI communication for DO1-DO6:0=NO FAULT						
SPI COMMUNICATION DO1-DO6 BIT 7	1x00159 2x00159 I:158	0,0x00 B:00			BIT R/O	
Bit 7:Current SPI communication for DO1-DO6:0=NO FAULT						
SPI COMMUNICATION DO7-DO12 BIT 1	1x00160 2x00160 I:159	0,0x00 B:00			BIT R/O	
Bit 1:Current SPI communication for DO7-DO12:0=NO FAULT						
The current monitoring state of the SPI communication for the digital output group =0:No fault, =1:Fault						
SPI COMMUNICATION DO7-DO12 BIT 2	1x00161 2x00161 I:160	0,0x00 B:00			BIT R/O	
Bit 2:Current SPI communication for DO7-DO12:0=NO FAULT						
SPI COMMUNICATION DO7-DO12 BIT 3	1x00162 2x00162 I:161	0,0x00 B:00			BIT R/O	
Bit 3:Current SPI communication for DO7-DO12:0=NO FAULT						
SPI COMMUNICATION DO7-DO12 BIT 4	1x00163 2x00163 I:162	0,0x00 B:00			BIT R/O	
Bit 4:Current SPI communication for DO7-DO12:0=NO FAULT						
SPI COMMUNICATION DO7-DO12 BIT 5	1x00164 2x00164 I:163	0,0x00 B:00			BIT R/O	
Bit 5:Current SPI communication for DO7-DO12:0=NO FAULT						
SPI COMMUNICATION DO7-DO12 BIT 6	1x00165 2x00165 I:164	0,0x00 B:00			BIT R/O	
Bit 6:Current SPI communication for DO7-DO12:0=NO FAULT						
SPI COMMUNICATION DO7-DO12 BIT 7	1x00166 2x00166 I:165	0,0x00 B:00			BIT R/O	
Bit 7:Current SPI communication for DO7-DO12:0=NO FAULT						
STATUS DIGITAL INPUTS						
DI1	1x15001 2x15001 I:15000	1,0x01 B:01			BIT R/O	

		Current state of DI1:1=ON				
Current state of the digital input Dlx =0:DI is OFF, =1:DI is ON						
DI2	1x15002 2x15002 I:15001	0,0x00 B:00			BIT R/O	
Current state of DI2:0=OFF						
DI3	1x15003 2x15003 I:15002	0,0x00 B:00			BIT R/O	
Current state of DI3:0=OFF						
DI4	1x15004 2x15004 I:15003	0,0x00 B:00			BIT R/O	
Current state of DI4:0=OFF						
DI5	1x15005 2x15005 I:15004	0,0x00 B:00			BIT R/O	
Current state of DI5:0=OFF						
DI6	1x15006 2x15006 I:15005	0,0x00 B:00			BIT R/O	
Current state of DI6:0=OFF						
DI7	1x15007 2x15007 I:15006	0,0x00 B:00			BIT R/O	
Current state of DI7:0=OFF						
DI8	1x15008 2x15008 I:15007	0,0x00 B:00			BIT R/O	
Current state of DI8:0=OFF						
DI9	1x15009 2x15009 I:15008	0,0x00 B:00			BIT R/O	
Current state of DI9:0=OFF						
DI10	1x15010 2x15010 I:15009	0,0x00 B:00			BIT R/O	
Current state of DI10:0=OFF						
DI11	1x15011 2x15011 I:15010	0,0x00 B:00			BIT R/O	
Current state of DI11:0=OFF						
DI12	1x15012 2x15012 I:15011	0,0x00 B:00			BIT R/O	

		Current state of DI12:0=OFF				
STATUS UNFILTERED DIGITAL INPUTS						
DI1 UNFILTERED	1x15001 2x15001 I:15000	1,0x01 B:01			BIT R/O	
		Current state of DI1 UNFILTERED:1=ON				
Current state of the unfiltered digital input DIx =0:DI is OFF, =1:DI is ON						
DI2 UNFILTERED	1x15002 2x15002 I:15001	0,0x00 B:00			BIT R/O	
		Current state of DI2 UNFILTERED:0=OFF				
DI3 UNFILTERED	1x15003 2x15003 I:15002	0,0x00 B:00			BIT R/O	
		Current state of DI3 UNFILTERED:0=OFF				
DI4 UNFILTERED	1x15004 2x15004 I:15003	0,0x00 B:00			BIT R/O	
		Current state of DI4 UNFILTERED:0=OFF				
DI5 UNFILTERED	1x15005 2x15005 I:15004	0,0x00 B:00			BIT R/O	
		Current state of DI5 UNFILTERED:0=OFF				
DI6 UNFILTERED	1x15006 2x15006 I:15005	0,0x00 B:00			BIT R/O	
		Current state of DI6 UNFILTERED:0=OFF				
DI7 UNFILTERED	1x15007 2x15007 I:15006	0,0x00 B:00			BIT R/O	
		Current state of DI7 UNFILTERED:0=OFF				
DI8 UNFILTERED	1x15008 2x15008 I:15007	0,0x00 B:00			BIT R/O	
		Current state of DI8 UNFILTERED:0=OFF				
DI9 UNFILTERED	1x15009 2x15009 I:15008	0,0x00 B:00			BIT R/O	
		Current state of DI9 UNFILTERED:0=OFF				
DI10 UNFILTERED	1x15010 2x15010 I:15009	0,0x00 B:00			BIT R/O	
		Current state of DI10 UNFILTERED:0=OFF				

DI1 UNFILTERED	1x15011 2x15011 I:15010	0,0x00 B:00			BIT R/O	
Current state of DI11 UNFILTERED:0=OFF						
DI12 UNFILTERED	1x15012 2x15012 I:15011	0,0x00 B:00			BIT R/O	
Current state of DI12 UNFILTERED:0=OFF						
DIGITAL INPUTS: RESET						
RESET COUNTERS	1x10000 2x10000 I:9999	0,0x00 B:00		1:PERFORM RESET	BIT R/W	NO
If this register is written to 1, all internal edge counters and event counters are set to 0. 0 is always returned when reading.						
STATUS DIGITAL INPUTS						
DI1	3x00001 4x00001 I:0	1,0x0001 B:00 01			UINT16 R/O	
Current state of DI1:1=ON						
Current state of the digital input DIx =0:DI is OFF, =1:DI is ON						
DI2	3x00002 4x00002 I:1	0,0x0000 B:00 00			UINT16 R/O	
Current state of DI2:0=OFF						
DI3	3x00003 4x00003 I:2	0,0x0000 B:00 00			UINT16 R/O	
Current state of DI3:0=OFF						
DI4	3x00004 4x00004 I:3	0,0x0000 B:00 00			UINT16 R/O	
Current state of DI4:0=OFF						
DI5	3x00005 4x00005 I:4	0,0x0000 B:00 00			UINT16 R/O	
Current state of DI5:0=OFF						
DI6	3x00006 4x00006 I:5	0,0x0000 B:00 00			UINT16 R/O	
Current state of DI6:0=OFF						
DI7	3x00007 4x00007 I:6	0,0x0000 B:00 00			UINT16 R/O	
Current state of DI7:0=OFF						

DI8	3x00008 4x00008 I:7	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI8:0=OFF				
DI9	3x00009 4x00009 I:8	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI9:0=OFF				
DI10	3x00010 4x00010 I:9	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI10:0=OFF				
DI11	3x00011 4x00011 I:10	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI11:0=OFF				
DI12	3x00012 4x00012 I:11	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI12:0=OFF				
STATUS DIGITAL OUTPUTS						
DO1	3x00013 4x00013 I:12	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Current state of DO1:1=ON		ENTER NEW STATE (0 or 1)		
Current state of the digital output DOx =0:DO is OFF, =1:DO is ON						
Writing on this register changes the state of the digital output						
DO2	3x00014 4x00014 I:13	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Current state of DO2:1=ON		ENTER NEW STATE (0 or 1)		
DO3	3x00015 4x00015 I:14	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Current state of DO3:1=ON		ENTER NEW STATE (0 or 1)		
DO4	3x00016 4x00016 I:15	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Current state of DO4:1=ON		ENTER NEW STATE (0 or 1)		
DO5	3x00017 4x00017 I:16	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Current state of DO5:1=ON		ENTER NEW STATE (0 or 1)		

DO6	3x00018 4x00018 I:17	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Current state of DO6:1=ON		ENTER NEW STATE (0 or 1)		
DO7	3x00019 4x00019 I:18	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Current state of DO7:1=ON		ENTER NEW STATE (0 or 1)		
DO8	3x00020 4x00020 I:19	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Current state of DO8:1=ON		ENTER NEW STATE (0 or 1)		
DO9	3x00021 4x00021 I:20	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Current state of DO9:1=ON		ENTER NEW STATE (0 or 1)		
DO10	3x00022 4x00022 I:21	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Current state of DO10:1=ON		ENTER NEW STATE (0 or 1)		
DO11	3x00023 4x00023 I:22	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Current state of DO11:1=ON		ENTER NEW STATE (0 or 1)		
DO12	3x00024 4x00024 I:23	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Current state of DO12:1=ON		ENTER NEW STATE (0 or 1)		
DIGITAL OUTPUTS: ENABLE OPEN WIRE DETECTION ON						
ENABLE OPEN WIRE DETECTION ON DO1	3x00025 4x00025 I:24	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Current setup of open wire detection for state ON of DO1:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
Enables/disabled detection of an open wire in DO state ON for the digital output DOx =0:Open wire detection is OFF, =1:Open wire detection is ON						
Writing on this register changes the state of the open wire detection for this output						
ENABLE OPEN WIRE DETECTION ON DO2	3x00026 4x00026 I:25	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Current setup of open wire detection for state ON of DO2:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO3	3x00027 4x00027 I:26	0,0x0000 B:00 00		1	UINT16 R/W	NO

		Current setup of open wire detection for state ON of DO3:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO4	3x00028 4x00028 I:27	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state ON of DO4:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO5	3x00029 4x00029 I:28	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state ON of DO5:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO6	3x00030 4x00030 I:29	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state ON of DO6:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO7	3x00031 4x00031 I:30	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state ON of DO7:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO8	3x00032 4x00032 I:31	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state ON of DO8:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO9	3x00033 4x00033 I:32	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state ON of DO9:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO10	3x00034 4x00034 I:33	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state ON of DO10:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO11	3x00035 4x00035 I:34	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state ON of DO11:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO12	3x00036 4x00036 I:35	0,0x0000 B:00 00	1	UINT16 R/W	NO

		Current setup of open wire detection for state ON of DO12:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
DIGITAL OUTPUTS: ENABLE OPEN WIRE DETECTION OFF					
ENABLE OPEN WIRE DETECTION OFF DO1	3x00037 4x00037 I:36	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO1:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
Enables/disabled detection of an open wire in DO state OFF for the digital output DOx =0:Open wire detection is OFF, =1:Open wire detection is ON					
Writing on this register changes the state of the open wire detection for this output					
ENABLE OPEN WIRE DETECTION OFF DO2	3x00038 4x00038 I:37	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO2:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO3	3x00039 4x00039 I:38	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO3:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO4	3x00040 4x00040 I:39	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO4:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO5	3x00041 4x00041 I:40	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state ON of DO5:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO6	3x00042 4x00042 I:41	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO6:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO7	3x00043 4x00043 I:42	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO7:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO8	3x00044 4x00044 I:43	0,0x0000 B:00 00	1	UINT16 R/W	NO

		Current setup of open wire detection for state OFF of DO8:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO9	3x00045 4x00045 I:44	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO9:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO10	3x00046 4x00046 I:45	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO10:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO11	3x00047 4x00047 I:46	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO11:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO12	3x00048 4x00048 I:47	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO12:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
DIGITAL OUTPUTS: ENABLE SHORT TO VDD DETECTION					
ENABLE SHORT TO VDD DETECTION DO1	3x00049 4x00049 I:48	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO1:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
Enables/disabled detection of a shortcut to VDD in DO state OFF for the digital output DOx =0:Shortcut to VDD detection is OFF, =1:Shortcut to VDD detection is ON					
Writing on this register changes the state of the shortcut detection for this output					
ENABLE SHORT TO VDD DETECTION DO2	3x00050 4x00050 I:49	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO2:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO3	3x00051 4x00051 I:50	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO3:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO4	3x00052 4x00052 I:51	0,0x0000 B:00 00	1	UINT16 R/W	NO

		Current setup of open wire detection for state OFF of DO4:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO5	3x00053 4x00053 I:52	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state ON of DO5:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO6	3x00054 4x00054 I:53	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO6:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO7	3x00055 4x00055 I:54	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO7:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO8	3x00056 4x00056 I:55	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO8:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO9	3x00057 4x00057 I:56	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO9:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO10	3x00058 4x00058 I:57	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO10:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO11	3x00059 4x00059 I:58	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO11:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO12	3x00060 4x00060 I:59	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO12:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
DIGITAL OUTPUTS: OPEN WIRE DETECTION STATUS WHILE ON					
OPEN WIRE WHILE ON DO1	3x00061 4x00061 I:60	0,0x0000 B:00 00		UINT16 R/O	

		Current detection state of an open wire in state ON for DO1:0=OK			
The current detection state of an open wire in the output state ON for the digital output DOx =0:No fault, =1:Fault-open wire detected					
OPEN WIRE WHILE ON DO2	3x00062 4x00062 I:61	0,0x0000 B:00 00			UINT16 R/O
		Current detection state of an open wire in state ON for DO2:0=OK			
OPEN WIRE WHILE ON DO3	3x00063 4x00063 I:62	0,0x0000 B:00 00			UINT16 R/O
		Current detection state of an open wire in state ON for DO3:0=OK			
OPEN WIRE WHILE ON DO4	3x00064 4x00064 I:63	0,0x0000 B:00 00			UINT16 R/O
		Current detection state of an open wire in state ON for DO4:0=OK			
OPEN WIRE WHILE ON DO5	3x00065 4x00065 I:64	0,0x0000 B:00 00			UINT16 R/O
		Current detection state of an open wire in state ON for DO5:0=OK			
OPEN WIRE WHILE ON DO6	3x00066 4x00066 I:65	0,0x0000 B:00 00			UINT16 R/O
		Current detection state of an open wire in state ON for DO6:0=OK			
OPEN WIRE WHILE ON DO7	3x00067 4x00067 I:66	0,0x0000 B:00 00			UINT16 R/O
		Current detection state of an open wire in state ON for DO7:0=OK			
OPEN WIRE WHILE ON DO8	3x00068 4x00068 I:67	0,0x0000 B:00 00			UINT16 R/O
		Current detection state of an open wire in state ON for DO8:0=OK			
OPEN WIRE WHILE ON DO9	3x00069 4x00069 I:68	0,0x0000 B:00 00			UINT16 R/O
		Current detection state of an open wire in state ON for DO9:0=OK			

OPEN WIRE WHILE ON DO10	3x00070 4x00070 I:69	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of an open wire in state ON for DO10:0=OK				
OPEN WIRE WHILE ON DO11	3x00071 4x00071 I:70	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of an open wire in state ON for DO11:0=OK				
OPEN WIRE WHILE ON DO12	3x00072 4x00072 I:71	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of an open wire in state ON for DO12:0=OK				
DIGITAL OUTPUTS: OPEN WIRE DETECTION STATUS WHILE OFF						
OPEN WIRE WHILE OFF DO1	3x00073 4x00073 I:72	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of an open wire in state OFF for DO1:0=OK				
The current detection state of an open wire in the output state OFF for the digital output DOx =0:No fault, =1:Fault-open wire detected						
OPEN WIRE WHILE OFF DO2	3x00074 4x00074 I:73	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of an open wire in state OFF for DO2:0=OK				
OPEN WIRE WHILE OFF DO3	3x00075 4x00075 I:74	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of an open wire in state OFF for DO3:0=OK				
OPEN WIRE WHILE OFF DO4	3x00076 4x00076 I:75	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of an open wire in state OFF for DO4:0=OK				
OPEN WIRE WHILE OFF DO5	3x00077 4x00077 I:76	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of an open wire in state OFF for DO5:0=OK				
OPEN WIRE WHILE OFF DO6	3x00078 4x00078 I:77	0,0x0000 B:00 00			UINT16 R/O	

		Current detection state of an open wire in state OFF for DO6:0=OK			
OPEN WIRE WHILE OFF DO7	3x00079 4x00079 I:78	0,0x0000 B:00 00			UINT16 R/O
		Current detection state of an open wire in state OFF for DO7:0=OK			
OPEN WIRE WHILE OFF DO8	3x00080 4x00080 I:79	0,0x0000 B:00 00			UINT16 R/O
		Current detection state of an open wire in state OFF for DO8:0=OK			
OPEN WIRE WHILE OFF DO9	3x00081 4x00081 I:80	0,0x0000 B:00 00			UINT16 R/O
		Current detection state of an open wire in state OFF for DO9:0=OK			
OPEN WIRE WHILE OFF DO10	3x00082 4x00082 I:81	0,0x0000 B:00 00			UINT16 R/O
		Current detection state of an open wire in state OFF for DO10:0=OK			
OPEN WIRE WHILE OFF DO11	3x00083 4x00083 I:82	0,0x0000 B:00 00			UINT16 R/O
		Current detection state of an open wire in state OFF for DO11:0=OK			
OPEN WIRE WHILE OFF DO12	3x00084 4x00084 I:83	0,0x0000 B:00 00			UINT16 R/O
		Current detection state of an open wire in state OFF for DO12:0=OK			
DIGITAL OUTPUTS: SHORTCUT DETECTION STATUS TO VDD WHILE OFF					
OPEN WIRE SHORTCUT TO VDD DO1	3x00085 4x00085 I:84	0,0x0000 B:00 00			UINT16 R/O
		Current detection state of a shortcut to VDD in state OFF for DO1:0=OK			
The current detection state of a shortcut to VDD in the output state OFF for the digital output DOx =0:No fault, =1:Fault-shortcut to VDD detected					
OPEN WIRE SHORTCUT TO VDD DO2	3x00086 4x00086 I:85	0,0x0000 B:00 00			UINT16 R/O
		Current detection state of a shortcut to VDD in state OFF for DO2:0=OK			

OPEN WIRE SHORTCUT TO VDD DO3	3x00087 4x00087 I:86	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a shortcut to VDD in state OFF for DO3:0=OK				
OPEN WIRE SHORTCUT TO VDD DO4	3x00088 4x00088 I:87	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a shortcut to VDD in state OFF for DO4:0=OK				
OPEN WIRE SHORTCUT TO VDD DO5	3x00089 4x00089 I:88	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a shortcut to VDD in state OFF for DO5:0=OK				
OPEN WIRE SHORTCUT TO VDD DO6	3x00090 4x00090 I:89	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a shortcut to VDD in state OFF for DO6:0=OK				
OPEN WIRE SHORTCUT TO VDD DO7	3x00091 4x00091 I:90	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a shortcut to VDD in state OFF for DO7:0=OK				
OPEN WIRE SHORTCUT TO VDD DO8	3x00092 4x00092 I:91	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a shortcut to VDD in state OFF for DO8:0=OK				
OPEN WIRE SHORTCUT TO VDD DO9	3x00093 4x00093 I:92	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a shortcut to VDD in state OFF for DO9:0=OK				
OPEN WIRE SHORTCUT TO VDD DO10	3x00094 4x00094 I:93	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a shortcut to VDD in state OFF for DO10:0=OK				
OPEN WIRE SHORTCUT TO VDD DO11	3x00095 4x00095 I:94	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a shortcut to VDD in state OFF for DO11:0=OK				

OPEN WIRE SHORTCUT TO VDD DO12	3x00096 4x00096 I:95	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a shortcut to VDD in state OFF for DO12:0=OK				
DIGITAL OUTPUTS: THERMAL OVERLOAD DETECTION STATUS						
THERMAL OVERLOAD DETECTION STATUS DO1	3x00097 4x00097 I:96	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a thermal overload for DO1:0=OK				
The current detection state of a thermal overload for the digital output DOx =0:No fault, =1:Fault-thermal overload detected						
THERMAL OVERLOAD DETECTION STATUS DO2	3x00098 4x00098 I:97	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a thermal overload for DO2:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO3	3x00099 4x00099 I:98	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a thermal overload for DO3:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO4	3x00100 4x00100 I:99	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a thermal overload for DO4:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO5	3x00101 4x00101 I:100	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a thermal overload for DO5:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO6	3x00102 4x00102 I:101	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a thermal overload for DO6:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO7	3x00103 4x00103 I:102	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a thermal overload for DO7:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO8	3x00104 4x00104 I:103	0,0x0000 B:00 00			UINT16 R/O	

		Current detection state of a thermal overload for DO8:0=OK			
THERMAL OVERLOAD DETECTION STATUS DO9	3x00105 4x00105 I:104	0,0x0000 B:00 00			UINT16 R/O
		Current detection state of a thermal overload for DO9:0=OK			
THERMAL OVERLOAD DETECTION STATUS DO10	3x00106 4x00106 I:105	0,0x0000 B:00 00			UINT16 R/O
		Current detection state of a thermal overload for DO10:0=OK			
THERMAL OVERLOAD DETECTION STATUS DO11	3x00107 4x00107 I:106	0,0x0000 B:00 00			UINT16 R/O
		Current detection state of a thermal overload for DO11:0=OK			
THERMAL OVERLOAD DETECTION STATUS DO12	3x00108 4x00108 I:107	0,0x0000 B:00 00			UINT16 R/O
		Current detection state of a thermal overload for DO12:0=OK			
DIGITAL OUTPUTS: CURRENT LIMIT DETECTION STATUS WHILE ON					
CURRENT LIMIT DETECTION STATUS WHILE ON DO1	3x00109 4x00109 I:108	0,0x0000 B:00 00			UINT16 R/O
		Current detection state of a current limit while ON for DO1:0=OK			
The current detection state of a current limit while output is ON for the digital output DOx =0:No fault, =1:Fault-current limit error					
CURRENT LIMIT DETECTION STATUS WHILE ON DO2	3x00110 4x00110 I:109	0,0x0000 B:00 00			UINT16 R/O
		Current detection state of a current limit while ON for DO2:0=OK			
CURRENT LIMIT DETECTION STATUS WHILE ON DO3	3x00111 4x00111 I:110	0,0x0000 B:00 00			UINT16 R/O
		Current detection state of a current limit while ON for DO3:0=OK			
CURRENT LIMIT DETECTION STATUS WHILE ON DO4	3x00112 4x00112 I:111	0,0x0000 B:00 00			UINT16 R/O
		Current detection state of a current limit while ON for DO4:0=OK			

CURRENT LIMIT DETECTION STATUS WHILE ON DO5	3x00113 4x00113 I:112	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a current limit while ON for DO5:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO6	3x00114 4x00114 I:113	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a current limit while ON for DO6:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO7	3x00115 4x00115 I:114	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a current limit while ON for DO7:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO8	3x00116 4x00116 I:115	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a current limit while ON for DO8:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO9	3x00117 4x00117 I:116	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a current limit while ON for DO9:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO10	3x00118 4x00118 I:117	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a current limit while ON for DO10:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO11	3x00119 4x00119 I:118	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a current limit while ON for DO11:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO12	3x00120 4x00120 I:119	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a current limit while ON for DO12:0=OK				
DIGITAL OUTPUTS: GLOBAL ERRORS DO1-DO6						
GLOBAL ERRORS FOR DO1-DO6 BIT 0	3x00121 4x00121 I:120	0,0x0000 B:00 00			UINT16 R/O	
		BIT 0:Internal under voltage detected:0=OK				

GLOBAL ERRORS FOR DO1-DO6 BIT 1	3x00122 4x00122 I:121	0,0x0000 B:00 00			UINT16 R/O	
BIT 1:VA under voltage detected (<2.3V):0=OK						
GLOBAL ERRORS FOR DO1-DO6 BIT 2	3x00123 4x00123 I:122	0,0x0000 B:00 00			UINT16 R/O	
BIT 2:VDD not good detected (<17V):0=OK						
GLOBAL ERRORS FOR DO1-DO6 BIT 3	3x00124 4x00124 I:123	0,0x0000 B:00 00			UINT16 R/O	
BIT 3:VDD warning detected (<12V):0=OK						
GLOBAL ERRORS FOR DO1-DO6 BIT 4	3x00125 4x00125 I:124	0,0x0000 B:00 00			UINT16 R/O	
BIT 4:VDD under voltage detected (<8V):0=OK						
GLOBAL ERRORS FOR DO1-DO6 BIT 5	3x00126 4x00126 I:125	0,0x0000 B:00 00			UINT16 R/O	
BIT 5:Thermal shutdown:0=OK						
GLOBAL ERRORS FOR DO1-DO6 BIT 6	3x00127 4x00127 I:126	0,0x0000 B:00 00			UINT16 R/O	
BIT 6:Synchronisation error detected:0=OK						
GLOBAL ERRORS FOR DO1-DO6 BIT 7	3x00128 4x00128 I:127	0,0x0000 B:00 00			UINT16 R/O	
BIT 7:Watchdog error detected:0=OK						
The global error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
DIGITAL OUTPUTS: GLOBAL ERRORS DO7-DO12						
GLOBAL ERRORS FOR DO7-DO12 BIT 0	3x00129 4x00129 I:128	0,0x0000 B:00 00			UINT16 R/O	
BIT 0:Internal under voltage detected:0=OK						
GLOBAL ERRORS FOR DO7-DO12 BIT 1	3x00130 4x00130 I:129	0,0x0000 B:00 00			UINT16 R/O	
BIT 1:VA under voltage detected (<2.3V):0=OK						
GLOBAL ERRORS FOR DO7-DO12 BIT 2	3x00131 4x00131 I:130	0,0x0000 B:00 00			UINT16 R/O	
BIT 2:VDD not good detected (<17V):0=OK						
GLOBAL ERRORS FOR DO7-DO12 BIT 3	3x00132 4x00132 I:131	0,0x0000 B:00 00			UINT16 R/O	

		BIT 3:VDD warning detected (<12V):0=OK				
GLOBAL ERRORS FOR DO7-DO12 BIT 4	3x00133 4x00133 I:132	0,0x0000 B:00 00			UINT16 R/O	
		BIT 4:VDD under voltage detected (<8V):0=OK				
GLOBAL ERRORS FOR DO7-DO12 BIT 5	3x00134 4x00134 I:133	0,0x0000 B:00 00			UINT16 R/O	
		BIT 5:Thermal shutdown:0=OK				
GLOBAL ERRORS FOR DO7-DO12 BIT 6	3x00135 4x00135 I:134	0,0x0000 B:00 00			UINT16 R/O	
		BIT 6:Synchronisation error detected:0=OK				
GLOBAL ERRORS FOR DO7-DO12 BIT 7	3x00136 4x00136 I:135	0,0x0000 B:00 00			UINT16 R/O	
		BIT 7:Watchdog error detected:0=OK				
The global error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
DIGITAL OUTPUTS: INTERRUPT STATUS DO1-DO6						
INTERRUPT STATUS FOR DO1-DO6 BIT 0	3x00137 4x00137 I:136	0,0x0000 B:00 00			UINT16 R/O	
		BIT 0:Overload detected:0=OK				
INTERRUPT STATUS FOR DO1-DO6 BIT 1	3x00138 4x00138 I:137	0,0x0000 B:00 00			UINT16 R/O	
		BIT 1:Current limit detected:0=OK				
INTERRUPT STATUS FOR DO1-DO6 BIT 2	3x00139 4x00139 I:138	0,0x0000 B:00 00			UINT16 R/O	
		BIT 2:Open wire while OFF detected:0=OK				
INTERRUPT STATUS FOR DO1-DO6 BIT 3	3x00140 4x00140 I:139	0,0x0000 B:00 00			UINT16 R/O	
		BIT 3:Open wire while ON detected:0=OK				
INTERRUPT STATUS FOR DO1-DO6 BIT 4	3x00141 4x00141 I:140	0,0x0000 B:00 00			UINT16 R/O	
		BIT 4:Short to VDD while ON detected:0=OK				
INTERRUPT STATUS FOR DO1-DO6 BIT 5	3x00142 4x00142 I:141	0,0x0000 B:00 00			UINT16 R/O	
		BIT 5:Thermal error detected-shutdown:0=OK				

INTERRUPT STATUS FOR DO1-DO6 BIT 6	3x00143 4x00143 I:142	0,0x0000 B:00 00			UINT16 R/O	
BIT 6:Supply error detected:0=OK						
INTERRUPT STATUS FOR DO1-DO6 BIT 7	3x00144 4x00144 I:143	0,0x0000 B:00 00			UINT16 R/O	
BIT 7:Communication error detected:0=OK						
The global interrupt error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
DIGITAL OUTPUTS: INTERRUPT STATUS DO7-DO12						
INTERRUPT STATUS FOR DO7-DO12 BIT 0	3x00145 4x00145 I:144	0,0x0000 B:00 00			UINT16 R/O	
BIT 0:Overload detected:0=OK						
INTERRUPT STATUS FOR DO7-DO12 BIT 1	3x00146 4x00146 I:145	0,0x0000 B:00 00			UINT16 R/O	
BIT 1:Current limit detected:0=OK						
INTERRUPT STATUS FOR DO7-DO12 BIT 2	3x00147 4x00147 I:146	0,0x0000 B:00 00			UINT16 R/O	
BIT 2:Open wire while OFF detected:0=OK						
INTERRUPT STATUS FOR DO7-DO12 BIT 3	3x00148 4x00148 I:147	0,0x0000 B:00 00			UINT16 R/O	
BIT 3:Open wire while ON detected:0=OK						
INTERRUPT STATUS FOR DO7-DO12 BIT 4	3x00149 4x00149 I:148	0,0x0000 B:00 00			UINT16 R/O	
BIT 4:Short to VDD while ON detected:0=OK						
INTERRUPT STATUS FOR DO7-DO12 BIT 5	3x00150 4x00150 I:149	0,0x0000 B:00 00			UINT16 R/O	
BIT 5:Thermal error detected-shutdown:0=OK						
INTERRUPT STATUS FOR DO7-DO12 BIT 6	3x00151 4x00151 I:150	0,0x0000 B:00 00			UINT16 R/O	
BIT 6:Supply error detected:0=OK						
INTERRUPT STATUS FOR DO7-DO12 BIT 7	3x00152 4x00152 I:151	0,0x0000 B:00 00			UINT16 R/O	
BIT 7:Communication error detected:0=OK						
The global interrupt error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
SPI COMMUNICATION DIGITAL OUTPUTS						

SPI COMMUNICATION DO1-DO6 BIT 1	3x00153 4x00153 I:152	0,0x0000 B:00 00			UINT16 R/O	
Bit 1:Current SPI communication for DO1-DO6:0=NO FAULT						
The current monitoring state of the SPI communication for the digital output group =0:No fault, =1:Fault						
SPI COMMUNICATION DO1-DO6 BIT 2	3x00154 4x00154 I:153	0,0x0000 B:00 00			UINT16 R/O	
Bit 2:Current SPI communication for DO1-DO6:0=NO FAULT						
SPI COMMUNICATION DO1-DO6 BIT 3	3x00155 4x00155 I:154	0,0x0000 B:00 00			UINT16 R/O	
Bit 3:Current SPI communication for DO1-DO6:0=NO FAULT						
SPI COMMUNICATION DO1-DO6 BIT 4	3x00156 4x00156 I:155	0,0x0000 B:00 00			UINT16 R/O	
Bit 4:Current SPI communication for DO1-DO6:0=NO FAULT						
SPI COMMUNICATION DO1-DO6 BIT 5	3x00157 4x00157 I:156	0,0x0000 B:00 00			UINT16 R/O	
Bit 5:Current SPI communication for DO1-DO6:0=NO FAULT						
SPI COMMUNICATION DO1-DO6 BIT 6	3x00158 4x00158 I:157	0,0x0000 B:00 00			UINT16 R/O	
Bit 6:Current SPI communication for DO1-DO6:0=NO FAULT						
SPI COMMUNICATION DO1-DO6 BIT 7	3x00159 4x00159 I:158	0,0x0000 B:00 00			UINT16 R/O	
Bit 7:Current SPI communication for DO1-DO6:0=NO FAULT						
SPI COMMUNICATION DO7-DO12 BIT 1	3x00160 4x00160 I:159	0,0x0000 B:00 00			UINT16 R/O	
Bit 1:Current SPI communication for DO7-DO12:0=NO FAULT						
The current monitoring state of the SPI communication for the digital output group =0:No fault, =1:Fault						
SPI COMMUNICATION DO7-DO12 BIT 2	3x00161 4x00161 I:160	0,0x0000 B:00 00			UINT16 R/O	
Bit 2:Current SPI communication for DO7-DO12:0=NO FAULT						
SPI COMMUNICATION DO7-DO12 BIT 3	3x00162 4x00162 I:161	0,0x0000 B:00 00			UINT16 R/O	
Bit 3:Current SPI communication for DO7-DO12:0=NO FAULT						

SPI COMMUNICATION DO7-DO12 BIT 4	3x00163 4x00163 I:162	0,0x0000 B:00 00			UINT16 R/O	
Bit 4:Current SPI communication for DO7-DO12:0=NO FAULT						
SPI COMMUNICATION DO7-DO12 BIT 5	3x00164 4x00164 I:163	0,0x0000 B:00 00			UINT16 R/O	
Bit 5:Current SPI communication for DO7-DO12:0=NO FAULT						
SPI COMMUNICATION DO7-DO12 BIT 6	3x00165 4x00165 I:164	0,0x0000 B:00 00			UINT16 R/O	
Bit 6:Current SPI communication for DO7-DO12:0=NO FAULT						
SPI COMMUNICATION DO7-DO12 BIT 7	3x00166 4x00166 I:165	0,0x0000 B:00 00			UINT16 R/O	
Bit 7:Current SPI communication for DO7-DO12:0=NO FAULT						
DIGITAL INPUTS						
STATUS DI1 A	3x00501 4x00501 I:500	35974,0x8C86 B:8C 86			UINT16 R/O	
DI:0,CC:6,REC:4,FEC:3						
Status for the digital input Dlx Bit 0-4: Lower 5 bits of CHANGE COUNTER Bit 5-9: Lower 5 bits of RISING EDGE COUNTER Bit 10-14: Lower 5 bits of FALLING EDGE COUNTER Bit 15: Current Status of Dlx =0: Dlx si OFF, =1: Dlx is ON						
STATUS DI1 B	3x00502 4x00502 I:501	34913,0x8861 B:88 61			UINT16 R/O	
DI:1,SKE:1,LKSE:3,LKEE:2						
Status for the digital input Dlx Bit 0-4: Lower 5 bits of SHORT KEYPRESS EVENTS Bit 5-9: Lower 5 bits of LONG KEYPRESS START EVENTS Bit 10-14: Lower 5 bits of LONG KEYPRESS END EVENTS Bit 15: Current Status of Dlx =0: Dlx si OFF, =1: Dlx is ON						
STATUS DI2 A	3x00503 4x00503 I:502	0,0x0000 B:00 00			UINT16 R/O	
DI:0,CC:0,REC:0,FEC:0						
STATUS DI2 B	3x00504 4x00504 I:503	0,0x0000 B:00 00			UINT16 R/O	
DI:0,SKE:0,LKSE:0,LKEE:0						
STATUS DI3 A	3x00505 4x00505 I:504	0,0x0000 B:00 00			UINT16 R/O	
DI:0,CC:0,REC:0,FEC:0						

STATUS DI3 B	3x00506 4x00506 I:505	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI4 A	3x00507 4x00507 I:506	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI4 B	3x00508 4x00508 I:507	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI5 A	3x00509 4x00509 I:508	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI5 B	3x00510 4x00510 I:509	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI6 A	3x00511 4x00511 I:510	2116,0x0844 B:08 44			UINT16 R/O	
		DI:0,CC:4,REC:2,FEC:2				
STATUS DI6 B	3x00512 4x00512 I:511	2112,0x0840 B:08 40			UINT16 R/O	
		DI:0,SKE:0,LKSE:2,LKEE:2				
STATUS DI7 A	3x00513 4x00513 I:512	1058,0x0422 B:04 22			UINT16 R/O	
		DI:1,CC:2,REC:1,FEC:1				
STATUS DI7 B	3x00514 4x00514 I:513	1056,0x0420 B:04 20			UINT16 R/O	
		DI:1,SKE:0,LKSE:1,LKEE:1				
STATUS DI8 A	3x00515 4x00515 I:514	1057,0x0421 B:04 21			UINT16 R/O	
		DI:1,CC:1,REC:1,FEC:1				
STATUS DI8 B	3x00516 4x00516 I:515	1,0x0001 B:00 01			UINT16 R/O	
		DI:0,SKE:1,LKSE:0,LKEE:0				

STATUS DI9 A	3x00517 4x00517 I:516	1057,0x0421 B:04 21			UINT16 R/O	
		DI:1,CC:1,REC:1,FEC:1				
STATUS DI9 B	3x00518 4x00518 I:517	1,0x0001 B:00 01			UINT16 R/O	
		DI:0,SKE:1,LKSE:0,LKEE:0				
STATUS DI10 A	3x00519 4x00519 I:518	1057,0x0421 B:04 21			UINT16 R/O	
		DI:1,CC:1,REC:1,FEC:1				
STATUS DI10 B	3x00520 4x00520 I:519	1,0x0001 B:00 01			UINT16 R/O	
		DI:0,SKE:1,LKSE:0,LKEE:0				
STATUS DI11 A	3x00521 4x00521 I:520	1057,0x0421 B:04 21			UINT16 R/O	
		DI:1,CC:1,REC:1,FEC:1				
STATUS DI11 B	3x00522 4x00522 I:521	1,0x0001 B:00 01			UINT16 R/O	
		DI:0,SKE:1,LKSE:0,LKEE:0				
STATUS DI12 A	3x00523 4x00523 I:522	2115,0x0843 B:08 43			UINT16 R/O	
		DI:0,CC:3,REC:2,FEC:2				
STATUS DI12 B	3x00524 4x00524 I:523	1057,0x0421 B:04 21			UINT16 R/O	
		DI:1,SKE:1,LKSE:1,LKEE:1				
STATUS						
FILTER PATTERN DI1	3x00525 4x00525 I:524	4294967295,0xFFFFFFFF B:FF FF FF FF			UINT32 R/O	
The internal pattern for corresponding digital input for AC/DC filtering. The internal used state is created out of this internal pattern via oversampling.						
FILTER PATTERN DI2	3x00527 4x00527 I:526	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI3	3x00529 4x00529 I:528	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI4	3x00531 4x00531 I:530	0,0x00000000 B:00 00 00 00			UINT32 R/O	

FILTER PATTERN DI5	3x00533 4x00533 I:532	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI6	3x00535 4x00535 I:534	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI7	3x00537 4x00537 I:536	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI8	3x00539 4x00539 I:538	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI9	3x00541 4x00541 I:540	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI10	3x00543 4x00543 I:542	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI11	3x00545 4x00545 I:544	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI12	3x00547 4x00547 I:546	0,0x00000000 B:00 00 00 00			UINT32 R/O	
GENERAL STATUS OF DIS						
RESET COUNTERS	3x10000 4x10000 I:9999	0,0x0000 B:00 00		1:PERFORM RESET	UINT16 R/W	NO
If this register is written to 1, all internal edge counters and event counters are set to 0. 0 is always returned when reading.						
HAS DIS CHANGED	3x10001 4x10001 I:10000	19,0x0013 B:00 13			UINT16 R/O	
		19 event(s)				
As soon as the module registrates an event on one of the available digital inputs, this global event counter is incremented by 1. Possible events are: Detection of a short keypress Detection of the start of a long keypress Detection of the end of a long keypress						
STATUS OF ALL DIS DI1..DI12	3x10002 4x10002 I:10001	1,0x0001 B:00 01			UINT16 R/O	
		Current state of DI1:1=ON				
		Current state of DI2:0=OFF				
		Current state of DI3:0=OFF				
		Current state of DI4:0=OFF				
		Current state of DI5:0=OFF				
		Current state of DI6:0=OFF				

		Current state of DI7:0=OFF				
		Current state of DI8:0=OFF				
		Current state of DI9:0=OFF				
		Current state of DI10:0=OFF				
		Current state of DI11:0=OFF				
		Current state of DI12:0=OFF				
Current state of all digital inputs DI1..DI12 Bit 0: =0:DI1 is OFF, =1:DI1 is ON Bit 1: =0:DI2 is OFF, =1:DI2 is ON ... Bit 10: =0:DI11 is OFF, =1:DI11 is ON Bit 11: =0:DI12 is OFF, =1:DI12 is ON						
STATUS OF DIGITAL OUTPUTS						
STATUS OF DO1-DO12	3x10003 4x10003 l:10002	4095,0x0FFF B:0F FF		0x0FFF	UINT16 R/W	NO
		Current state of DO1:1=ON	1			
		Current state of DO2:1=ON	1			
		Current state of DO3:1=ON	1			
		Current state of DO4:1=ON	1			
		Current state of DO5:1=ON	1			
		Current state of DO6:1=ON	1			
		Current state of DO7:1=ON	1			
		Current state of DO8:1=ON	1			
		Current state of DO9:1=ON	1			
		Current state of DO10:1=ON	1			
		Current state of DO11:1=ON	1			
		Current state of DO12:1=ON	1			
Current state of all digital outputs Bit 0: =0:DO1 is OFF, =1:DO1 is ON Bit 1: =0:DO2 is OFF, =1:DO2 is ON ... Bit 10: =0:DO11 is OFF, =1:DO11 is ON Bit 11: =0:DO12 is OFF, =1:DO12 is ON						
Write on this register sets all digital outputs to a new state						
DIGITAL OUTPUTS:ENABLE OPEN WIRE DETECTION WHILE ON						
ENABLE OPEN WIRE DETECTION WHILE ON DO1-DO12	3x10004 4x10004 l:10003	0,0x0000 B:00 00		0x0FFF	UINT16 R/W	NO
		Current setup of open wire detection while ON for DO1:0=OFF	1			
		Current setup of open wire detection while ON for DO2:0=OFF	1			
		Current setup of open wire detection while ON for DO3:0=OFF	1			
		Current setup of open wire detection while ON for DO4:0=OFF	1			

		Current setup of open wire detection while ON for DO5:0=OFF	1		
		Current setup of open wire detection while ON for DO6:0=OFF	1		
		Current setup of open wire detection while ON for DO7:0=OFF	1		
		Current setup of open wire detection while ON for DO8:0=OFF	1		
		Current setup of open wire detection while ON for DO9:0=OFF	1		
		Current setup of open wire detection while ON for DO10:0=OFF	1		
		Current setup of open wire detection while ON for DO11:0=OFF	1		
		Current setup of open wire detection while ON for DO12:0=OFF	1		
<p>Current setup state for open wire detection while ON for digital output DOx Bit 0: =0:Open wire detection for DO1 is OFF, =1:Open wire detection for DO1 is ON Bit 1: =0:Open wire detection for DO2 is OFF, =1:Open wire detection for DO2 is ON ... Bit 10: =0:Open wire detection for DO11 is OFF, =1:Open wire detection for DO11 is ON Bit 11: =0:Open wire detection for DO12 is OFF, =1:Open wire detection for DO12 is ON</p> <p>Write on this register sets for all digital outputs a new setup state</p>					
DIGITAL OUTPUTS:ENABLE OPEN WIRE DETECTION WHILE OFF					
ENABLE OPEN WIRE DETECTION WHILE OFF DO1-DO12	3x10005 4x10005 1:10004	0,0x0000 B:00 00		0x0FFF	UINT16 R/W
		Current setup of open wire detection while OFF for DO1:0=OFF	1		
		Current setup of open wire detection while OFF for DO2:0=OFF	1		
		Current setup of open wire detection while OFF for DO3:0=OFF	1		
		Current setup of open wire detection while OFF for DO4:0=OFF	1		
		Current setup of open wire detection while OFF for DO5:0=OFF	1		
		Current setup of open wire detection while OFF for DO6:0=OFF	1		
		Current setup of open wire detection while OFF for DO7:0=OFF	1		
		Current setup of open wire detection while OFF for DO8:0=OFF	1		
		Current setup of open wire detection while OFF for DO9:0=OFF	1		

		Current setup of open wire detection while OFF for DO10:0=OFF	1		
		Current setup of open wire detection while OFF for DO11:0=OFF	1		
		Current setup of open wire detection while OFF for DO12:0=OFF	1		
Current setup state for open wire detection while OFF for digital output DOx Bit 0: =0:Open wire detection for DO1 is OFF, =1:Open wire detection for DO1 is ON Bit 1: =0:Open wire detection for DO2 is OFF, =1:Open wire detection for DO2 is ON ... Bit 10: =0:Open wire detection for DO11 is OFF, =1:Open wire detection for DO11 is ON Bit 11: =0:Open wire detection for DO12 is OFF, =1:Open wire detection for DO12 is ON					
Write on this register sets for all digital outputs a new setup state					
DIGITAL OUTPUTS:ENABLE SHORTCUT TO VDD DETECTION WHILE OFF					
ENABLE SHORTCUT TO VDD DETECTION WHILE OFF DO1-DO12	3x10006 4x10006 1:10005	0,0x0000 B:00 00		0x0FFF	UINT16 R/W
		Current setup of shortcut detection to VDD while OFF for DO1:0=OFF	1		
		Current setup of shortcut detection to VDD while OFF for DO2:0=OFF	1		
		Current setup of shortcut detection to VDD while OFF for DO3:0=OFF	1		
		Current setup of shortcut detection to VDD while OFF for DO4:0=OFF	1		
		Current setup of shortcut detection to VDD while OFF for DO5:0=OFF	1		
		Current setup of shortcut detection to VDD while OFF for DO6:0=OFF	1		
		Current setup of shortcut detection to VDD while OFF for DO7:0=OFF	1		
		Current setup of shortcut detection to VDD while OFF for DO8:0=OFF	1		
		Current setup of shortcut detection to VDD while OFF for DO9:0=OFF	1		
		Current setup of shortcut detection to VDD while OFF for DO10:0=OFF	1		
		Current setup of shortcut detection to VDD while OFF for DO11:0=OFF	1		
		Current setup of shortcut detection to VDD while OFF for DO12:0=OFF	1		

Current setup state for shortcut to VDD detection while OFF for digital output DOx
 Bit 0: =0:Shortcut to VDD detection for DO1 is OFF, =1:Shortcut to VDD detection for DO1 is ON
 Bit 1: =0:Shortcut to VDD detection for DO2 is OFF, =1:Shortcut to VDD detection for DO2 is ON
 ...
 Bit 10: =0:Shortcut to VDD detection for DO11 is OFF, =1:Shortcut to VDD detection for DO11 is ON
 Bit 11: =0:Shortcut to VDD detection for DO12 is OFF, =1:Shortcut to VDD detection for DO12 is ON

Write on this register sets for all digital outputs a new setup state

DIGITAL OUTPUTS:OPEN WIRE WHILE ON DETECTION STATE

OPEN WIRE DETECTION STATE WHILE ON DO1-DO12	3x10007 4x10007 l:10006	0,0x0000 B:00 00			UINT16 R/O	
		Current state of open wire detection while ON for DO1:0=OK				
		Current state of open wire detection while ON for DO2:0=OK				
		Current state of open wire detection while ON for DO3:0=OK				
		Current state of open wire detection while ON for DO4:0=OK				
		Current state of open wire detection while ON for DO5:0=OK				
		Current state of open wire detection while ON for DO6:0=OK				
		Current state of open wire detection while ON for DO7:0=OK				
		Current state of open wire detection while ON for DO8:0=OK				
		Current state of open wire detection while ON for DO9:0=OK				
		Current state of open wire detection while ON for DO10:0=OK				
		Current state of open wire detection while ON for DO11:0=OK				
		Current state of open wire detection while ON for DO12:0=OK				

Current diagnostic state for open wire detection while ON for digital output DOx
 Bit 0: =0:Output DO1 is OK, =1:Fault-Open wire detected on DO1
 Bit 1: =0:Output DO2 is OK, =1:Fault-Open wire detected on DO2
 ...
 Bit 10: =0:Output DO11 is OK, =1:Fault-Open wire detected on DO11
 Bit 11: =0:Output DO12 is OK, =1:Fault-Open wire detected on DO12

DIGITAL OUTPUTS:OPEN WIRE WHILE OFF DETECTION STATE

OPEN WIRE DETECTION STATE WHILE OFF DO1-DO12	3x10008 4x10008 l:10007	0,0x0000 B:00 00			UINT16 R/O	
		Current state of open wire detection while OFF for DO1:0=OK				

		Current state of open wire detection while OFF for DO2:0=OK			
		Current state of open wire detection while OFF for DO3:0=OK			
		Current state of open wire detection while OFF for DO4:0=OK			
		Current state of open wire detection while OFF for DO5:0=OK			
		Current state of open wire detection while OFF for DO6:0=OK			
		Current state of open wire detection while OFF for DO7:0=OK			
		Current state of open wire detection while OFF for DO8:0=OK			
		Current state of open wire detection while OFF for DO9:0=OK			
		Current state of open wire detection while OFF for DO10:0=OK			
		Current state of open wire detection while OFF for DO11:0=OK			
		Current state of open wire detection while OFF for DO12:0=OK			

Current diagnostic state for open wire detection while OFF for digital output DOx

Bit 0: =0:Output DO1 is OK, =1:Fault-Open wire detected on DO1

Bit 1: =0:Output DO2 is OK, =1:Fault-Open wire detected on DO2

...

Bit 10: =0:Output DO11 is OK, =1:Fault-Open wire detected on DO11

Bit 11: =0:Output DO12 is OK, =1:Fault-Open wire detected on DO12

DIGITAL OUTPUTS:SHORTCUT TO VDD WHILE OFF DETECTION STATE

SHORTCUT TO VDD	3x10009	0,0x0000			UINT16
WHILE OFF DETECTION STATE	4x10009	B:00 00			R/O
DO1-DO12	l:10008				
		Current state of shortcut to VDD detection while OFF for DO1:0=OK			
		Current state of shortcut to VDD detection while OFF for DO2:0=OK			
		Current state of shortcut to VDD detection while OFF for DO3:0=OK			
		Current state of shortcut to VDD detection while OFF for DO4:0=OK			
		Current state of shortcut to VDD detection while OFF for DO5:0=OK			
		Current state of shortcut to VDD detection while OFF for DO6:0=OK			
		Current state of shortcut to VDD detection while OFF for DO7:0=OK			

		Current state of shortcut to VDD detection while OFF for DO8:0=OK			
		Current state of shortcut to VDD detection while OFF for DO9:0=OK			
		Current state of shortcut to VDD detection while OFF for DO10:0=OK			
		Current state of shortcut to VDD detection while OFF for DO11:0=OK			
		Current state of shortcut to VDD detection while OFF for DO12:0=OK			
Current diagnostic state for shortcut to VDD detection while OFF for digital output DOx Bit 0: =0:Output DO1 is OK, =1:Fault-shortcut detected on DO1 Bit 1: =0:Output DO2 is OK, =1:Fault-shortcut detected on DO2 ... Bit 10: =0:Output DO11 is OK, =1:Fault-shortcut detected on DO11 Bit 11: =0:Output DO12 is OK, =1:Fault-shortcut detected on DO12					
DIGITAL OUTPUTS:THERMAL OVERLOAD DETECTION STATE					
THERMAL OVERLOAD DETECTION STATE DO1-DO12	3x10010 4x10010 1:10009	0,0x0000 B:00 00		UINT16 R/O	
		Current thermal overload detection state for DO1:0=OK			
		Current thermal overload detection state for DO2:0=OK			
		Current thermal overload detection state for DO3:0=OK			
		Current thermal overload detection state for DO4:0=OK			
		Current thermal overload detection state for DO5:0=OK			
		Current thermal overload detection state for DO6:0=OK			
		Current thermal overload detection state for DO7:0=OK			
		Current thermal overload detection state for DO8:0=OK			
		Current thermal overload detection state for DO9:0=OK			
		Current thermal overload detection state for DO10:0=OK			
		Current thermal overload detection state for DO11:0=OK			
		Current thermal overload detection state for DO12:0=OK			

Current thermal overload detection state for digital output DOx
 Bit 0: =0:Output DO1 is OK, =1:Fault-Thermal overload on DO1
 Bit 1: =0:Output DO2 is OK, =1:Fault-Thermal overload on DO2
 ...
 Bit 10: =0:Output DO11 is OK, =1:Fault-Thermal overload on DO11
 Bit 11: =0:Output DO12 is OK, =1:Fault-Thermal overload on DO12

DIGITAL OUTPUTS:CURRENT LIMIT DETECTION STATE

CURRENT LIMIT DETECTION STATE DO1-DO12	3x10011 4x10011 1:10010	0,0x0000 B:00 00		UINT16 R/O	
		Current current limit detection state for DO1:0=OK			
		Current current limit detection state for DO2:0=OK			
		Current current limit detection state for DO3:0=OK			
		Current current limit detection state for DO4:0=OK			
		Current current limit detection state for DO5:0=OK			
		Current current limit detection state for DO6:0=OK			
		Current current limit detection state for DO7:0=OK			
		Current current limit detection state for DO8:0=OK			
		Current current limit detection state for DO9:0=OK			
		Current current limit detection state for DO10:0=OK			
		Current current limit detection state for DO11:0=OK			
		Current current limit detection state for DO12:0=OK			

Current current limit detection state for digital output DOx
 Bit 0: =0:Output DO1 is OK, =1:Fault-Current limit on DO1
 Bit 1: =0:Output DO2 is OK, =1:Fault-Current limit on DO2
 ...
 Bit 10: =0:Output DO11 is OK, =1:Fault-Current limit on DO11
 Bit 11: =0:Output DO12 is OK, =1:Fault-Current limit on DO12

DIGITAL OUTPUTS: GLOBAL ERRORS

GLOBAL ERRORS FOR DO1-DO6	3x10012 4x10012 1:10011	0,0x00 B:00 00		BIT R/O	
		BIT 0:Internal under voltage detected:0=FAULT			
		BIT 1:VA under voltage detected (<2.3V):0=FAULT			
		BIT 2:VDD not good detected (<17V):0=FAULT			

		BIT 3:VDD warning detected (<12V):0=FAULT			
		BIT 4:VDD under voltage detected (<8V):0=FAULT			
		BIT 5:Thermal shutdown:0=FAULT			
		BIT 6:Synchronisation error detected:0=FAULT			
		BIT 7:Watchdog error detected:0=FAULT			
The global error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault					
GLOBAL ERRORS FOR DO7-DO12	3x10013 4x10013 I:10012	0,0x00 B:00 00			BIT R/O
		BIT 0:Internal under voltage detected:0=FAULT			
		BIT 1:VA under voltage detected (<2.3V):0=FAULT			
		BIT 2:VDD not good detected (<17V):0=FAULT			
		BIT 3:VDD warning detected (<12V):0=FAULT			
		BIT 4:VDD under voltage detected (<8V):0=FAULT			
		BIT 5:Thermal shutdown:0=FAULT			
		BIT 6:Synchronisation error detected:0=FAULT			
		BIT 7:Watchdog error detected:0=FAULT			
The global error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault					
DIGITAL OUTPUTS: INTERRUPT STATUS					
INTERRUPT STATUS FOR DO1-DO6	3x10014 4x10014 I:10013	0,0x00 B:00 00			BIT R/O
		BIT 0:Overload detected:0=FAULT			
		BIT 1:Current limit detected:0=FAULT			
		BIT 2:Open wire while OFF detected:0=FAULT			
		BIT 3:Open wire while ON detected:0=FAULT			
		BIT 4:Shortcut to VDD detected:0=FAULT			
		BIT 5:Thermal shutdown:0=FAULT			
		BIT 6:Supply error detected:0=FAULT			
		BIT 7:Communication error detected:0=FAULT			
The interrupt state for the output group. Each bit stands for a different error =0:No fault, =1:Fault					
INTERRUPT STATUS FOR DO7-DO12	3x10015 4x10015 I:10014	0,0x00 B:00 00			BIT R/O
		BIT 0:Overload detected:0=FAULT			
		BIT 1:Current limit detected:0=FAULT			
		BIT 2:Open wire while OFF detected:0=FAULT			
		BIT 3:Open wire while ON detected:0=FAULT			
		BIT 4:Shortcut to VDD detected:0=FAULT			
		BIT 5:Thermal shutdown:0=FAULT			
		BIT 6:Supply error detected:0=FAULT			
		BIT 7:Communication error detected:0=FAULT			

The interrupt state for the output group. Each bit stands for a different error

=0:No fault, =1:Fault

SPI COMMUNICATION DIGITAL OUTPUTS

SPI COMMUNICATION	3x10016	0,0x0000			UINT16	
DIGITAL OUTPUTS	4x10016	B:00 00			R/O	
	I:10015					
Current SPI communcation state of DO1-DO6:0=OK						
Current SPI communcation state of DO7-DO12:0=OK						

The current monitoring state of the SPI communication for the digital output group

=0:No fault, =1:Fault

Current SPI communication state of all digital output groups

Bit 0: =0:DO1-DO6 No fault, =1:DO1-DO6 Fault

Bit 1: =0:DO7-DO12 No fault, =1:DO7-DO12 Fault

DIGITAL OUTPUTS: CHIPSET TYPE

DIGITAL OUTPUTS	3x10100	1,0x0001			UINT16	
CHIPSET TYPE	4x10100	B:00 01			R/O	
	I:10099					
Current chipset for DOs:1=MAX14915						

The current chipset for the digital outputs:

=0: NCV7608

=1: MAX14915

PULSE TIME FOR DIGITAL OUTPUTS

PULSE TIME DO1	3x20001	0,0x0000	200	20,0	UINT16	YES
	4x20001	B:00 00			R/W	
	I:20000					
Generate a pulse on digital output x in 100ms units (0,1 to 6553,5 Seconds selectable) If you write onto this register, the digital output will be switched on for the desired time in 100ms units.						
PULSE TIME DO2	3x20002	0,0x0000	300	30,0	UINT16	NO
	4x20002	B:00 00			R/W	
	I:20001					
PULSE TIME DO3	3x20003	0,0x0000	400	40,0	UINT16	NO
	4x20003	B:00 00			R/W	
	I:20002					
PULSE TIME DO4	3x20004	0,0x0000	500	50,0	UINT16	NO
	4x20004	B:00 00			R/W	
	I:20003					
PULSE TIME DO5	3x20005	0,0x0000	20	2,0	UINT16	NO
	4x20005	B:00 00			R/W	
	I:20004					
PULSE TIME DO6	3x20006	0,0x0000	20	2,0	UINT16	NO
	4x20006	B:00 00			R/W	
	I:20005					
PULSE TIME DO7	3x20007	0,0x0000	20	2,0	UINT16	NO
	4x20007	B:00 00			R/W	
	I:20006					

PULSE TIME DO8	3x20008 4x20008 I:20007	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO9	3x20009 4x20009 I:20008	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO10	3x20010 4x20010 I:20009	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO11	3x20011 4x20011 I:20010	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO12	3x20012 4x20012 I:20011	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE STATUS FOR DIGITAL OUTPUTS						
PULSE TIMER DO1	3x21001 4x21001 I:21000	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
Remaining time of the pulse on digital output x in Milliseconds.						
PULSE TIMER DO2	3x21003 4x21003 I:21002	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO3	3x21005 4x21005 I:21004	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO4	3x21007 4x21007 I:21006	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO5	3x21009 4x21009 I:21008	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO6	3x21011 4x21011 I:21010	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO7	3x21013 4x21013 I:21012	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				

PULSE TIMER DO8	3x21015 4x21015 I:21014	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO9	3x21017 4x21017 I:21016	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO10	3x21019 4x21019 I:21018	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO11	3x21021 4x21021 I:21020	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO12	3x21023 4x21023 I:21022	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE STATUS FOR DIGITAL OUTPUTS						
PULSE TIMER DO1	3x21025 4x21025 I:21024	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
Remaining time of the pulse on digital output x in Milliseconds.						
PULSE TIMER DO2	3x21027 4x21027 I:21026	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO3	3x21029 4x21029 I:21028	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO4	3x21031 4x21031 I:21030	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO5	3x21033 4x21033 I:21032	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO6	3x21035 4x21035 I:21034	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				

PULSE TIMER DO7	3x21037 4x21037 I:21036	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO8	3x21039 4x21039 I:21038	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO9	3x21041 4x21041 I:21040	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO10	3x21043 4x21043 I:21042	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO11	3x21045 4x21045 I:21044	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO12	3x21047 4x21047 I:21046	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI1						
RISE DI1	3x30001 4x30001 I:30000	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
Counter for rising edges on the digital input Di _x . If the module detects a rising edge on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.						
FALL DI1	3x30002 4x30002 I:30001	3,0x0003 B:00 03			UINT16 R/O	
		3 event(s)				
Counter for falling edges on the digital input Di _x . If the module detects a falling edge on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.						
CHANGE DI1	3x30003 4x30003 I:30002	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				

Counter for events on the digital input DIx. If the module detects an event on the digital input, this counter is incremented by 1.
After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.
The following events are available:
Detection of a short keypress
Detection of the start of a long keypress
Detection of the end of a long keypress

SHORT KEYPRESS DI1	3x30004 4x30004 I:30003	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				

Counter for short keypress events on the digital input DIx. If the module detects a short keypress on the digital input, this counter is incremented by 1.
After power on or a soft reset this counter is set always to 0.
With the function RESET COUNTER this counter is also set to 0.

LONG KEYPRESS START DI1	3x30005 4x30005 I:30004	3,0x0003 B:00 03			UINT16 R/O	
		3 event(s)				

Counter for start events of long keypress actions on the digital input DIx. If the module detects the start of a long keypress action on the digital input, this counter is incremented by 1.
After power on or a soft reset this counter is set always to 0.
With the function RESET COUNTER this counter is also set to 0.

LONG KEYPRESS END DI1	3x30006 4x30006 I:30005	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				

Counter for end events of long keypress actions on the digital input DIx. If the module detects the end of a long keypress action on the digital input, this counter is incremented by 1.
After power on or a soft reset this counter is set always to 0.
With the function RESET COUNTER this counter is also set to 0.

DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI2

RISE DI2	3x30011 4x30011 I:30010	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI2	3x30012 4x30012 I:30011	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI2	3x30013 4x30013 I:30012	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI2	3x30014 4x30014 I:30013	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI2	3x30015 4x30015 I:30014	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

LONG KEYPRESS END DI2	3x30016 4x30016 I:30015	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI3						
RISE DI3	3x30021 4x30021 I:30020	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI3	3x30022 4x30022 I:30021	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI3	3x30023 4x30023 I:30022	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI3	3x30024 4x30024 I:30023	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI3	3x30025 4x30025 I:30024	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI3	3x30026 4x30026 I:30025	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI4						
RISE DI4	3x30031 4x30031 I:30030	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI4	3x30032 4x30032 I:30031	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI4	3x30033 4x30033 I:30032	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI4	3x30034 4x30034 I:30033	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

LONG KEYPRESS START DI4	3x30035 4x30035 I:30034	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI4	3x30036 4x30036 I:30035	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI5						
RISE DI5	3x30041 4x30041 I:30040	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI5	3x30042 4x30042 I:30041	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI5	3x30043 4x30043 I:30042	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI5	3x30044 4x30044 I:30043	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI5	3x30045 4x30045 I:30044	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI5	3x30046 4x30046 I:30045	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI6						
RISE DI6	3x30051 4x30051 I:30050	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
FALL DI6	3x30052 4x30052 I:30051	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
CHANGE DI6	3x30053 4x30053 I:30052	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				

SHORT KEYPRESS DI6	3x30054 4x30054 I:30053	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI6	3x30055 4x30055 I:30054	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI6	3x30056 4x30056 I:30055	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI7						
RISE DI7	3x30061 4x30061 I:30060	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI7	3x30062 4x30062 I:30061	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI7	3x30063 4x30063 I:30062	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
SHORT KEYPRESS DI7	3x30064 4x30064 I:30063	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI7	3x30065 4x30065 I:30064	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS END DI7	3x30066 4x30066 I:30065	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI8						
RISE DI8	3x30071 4x30071 I:30070	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI8	3x30072 4x30072 I:30071	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				

CHANGE DI8	3x30073 4x30073 I:30072	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI8	3x30074 4x30074 I:30073	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI8	3x30075 4x30075 I:30074	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI8	3x30076 4x30076 I:30075	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI9						
RISE DI9	3x30081 4x30081 I:30080	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI9	3x30082 4x30082 I:30081	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI9	3x30083 4x30083 I:30082	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI9	3x30084 4x30084 I:30083	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI9	3x30085 4x30085 I:30084	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI9	3x30086 4x30086 I:30085	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI10						
RISE DI10	3x30091 4x30091 I:30090	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				

FALL DI10	3x30092 4x30092 I:30091	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI10	3x30093 4x30093 I:30092	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI10	3x30094 4x30094 I:30093	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI10	3x30095 4x30095 I:30094	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI10	3x30096 4x30096 I:30095	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI11						
RISE DI11	3x30101 4x30101 I:30100	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI11	3x30102 4x30102 I:30101	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI11	3x30103 4x30103 I:30102	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI11	3x30104 4x30104 I:30103	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI11	3x30105 4x30105 I:30104	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI11	3x30106 4x30106 I:30105	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI12						

RISE DI12	3x30111 4x30111 I:30110	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
FALL DI12	3x30112 4x30112 I:30111	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
CHANGE DI12	3x30113 4x30113 I:30112	3,0x0003 B:00 03			UINT16 R/O	
		3 event(s)				
SHORT KEYPRESS DI12	3x30114 4x30114 I:30113	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI12	3x30115 4x30115 I:30114	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS END DI12	3x30116 4x30116 I:30115	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
INITIAL & WATCHDOG STATUS FOR ALL DIGITAL OUTPUTS						
INITIAL & WATCHDOG STATUS OF DO1-DO12	3x59001 4x59001 I:59000	4095,0x0FFF B:0F FF		0x0FFF	UINT16 R/W	NO
		Current init & watchdog state of DO1:1=ON	1			
		Current init & watchdog state of DO2:1=ON	1			
		Current init & watchdog state of DO3:1=ON	1			
		Current init & watchdog state of DO4:1=ON	1			
		Current init & watchdog state of DO5:1=ON	1			
		Current init & watchdog state of DO6:1=ON	1			
		Current init & watchdog state of DO7:1=ON	1			
		Current init & watchdog state of DO8:1=ON	1			
		Current init & watchdog state of DO9:1=ON	1			
		Current init & watchdog state of DO10:1=ON	1			
		Current init & watchdog state of DO11:1=ON	1			
		Current init & watchdog state of DO12:1=ON	1			

Current initial and watchdog state of all digital outputs. This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured

Bit 0: =0:DO1 is OFF, =1:DO1 is ON

Bit 1: =0:DO2 is OFF, =1:DO2 is ON

...

Bit 10: =0:DO11 is OFF, =1:DO11 is ON

Bit 11: =0:DO12 is OFF, =1:DO12 is ON

Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FLASH

DIGITAL OUTPUTS:ENABLE OPEN WIRE DETECTION WHILE ON

INITIAL & WATCHDOG SETUP	3x59002	0,0x0000		0x0FFF	UINT16 R/W	NO
ENABLE OPEN WIRE DETECTION WHILE ON DO1-DO12	4x59002 1:59001	B:00 00				
		Initial setup of open wire detection while ON for DO1:0=OFF	1			
		Initial setup of open wire detection while ON for DO2:0=OFF	1			
		Initial setup of open wire detection while ON for DO3:0=OFF	1			
		Initial setup of open wire detection while ON for DO4:0=OFF	1			
		Initial setup of open wire detection while ON for DO5:0=OFF	1			
		Initial setup of open wire detection while ON for DO6:0=OFF	1			
		Initial setup of open wire detection while ON for DO7:0=OFF	1			
		Initial setup of open wire detection while ON for DO8:0=OFF	1			
		Initial setup of open wire detection while ON for DO9:0=OFF	1			
		Initial setup of open wire detection while ON for DO10:0=OFF	1			
		Initial setup of open wire detection while ON for DO11:0=OFF	1			
		Initial setup of open wire detection while ON for DO12:0=OFF	1			

Initial setup state for open wire detection while ON for digital output DOx. Used after power on and watchdog event

Bit 0: =0:Open wire detection for DO1 is OFF, =1:Open wire detection for DO1 is ON

Bit 1: =0:Open wire detection for DO2 is OFF, =1:Open wire detection for DO2 is ON

...

Bit 10: =0:Open wire detection for DO11 is OFF, =1:Open wire detection for DO11 is ON

Bit 11: =0:Open wire detection for DO12 is OFF, =1:Open wire detection for DO12 is ON

Write on this register sets for all digital outputs a new setup state

DIGITAL OUTPUTS:ENABLE OPEN WIRE DETECTION WHILE OFF

INITIAL & WATCHDOG SETUP	3x59003	0,0x0000		0x0FFF	UINT16 R/W	NO
ENABLE OPEN WIRE DETECTION WHILE OFF DO1-DO12	4x59003 1:59002	B:00 00				

		Initial setup of open wire detection while OFF for DO1:0=OFF	1		
		Initial setup of open wire detection while OFF for DO2:0=OFF	1		
		Initial setup of open wire detection while OFF for DO3:0=OFF	1		
		Initial setup of open wire detection while OFF for DO4:0=OFF	1		
		Initial setup of open wire detection while OFF for DO5:0=OFF	1		
		Initial setup of open wire detection while OFF for DO6:0=OFF	1		
		Initial setup of open wire detection while OFF for DO7:0=OFF	1		
		Initial setup of open wire detection while OFF for DO8:0=OFF	1		
		Initial setup of open wire detection while OFF for DO9:0=OFF	1		
		Initial setup of open wire detection while OFF for DO10:0=OFF	1		
		Initial setup of open wire detection while OFF for DO11:0=OFF	1		
		Initial setup of open wire detection while OFF for DO12:0=OFF	1		

Initial setup state for open wire detection while OFF for digital output DOx. Used after power on and watchdog event

Bit 0: =0:Open wire detection for DO1 is OFF, =1:Open wire detection for DO1 is ON

Bit 1: =0:Open wire detection for DO2 is OFF, =1:Open wire detection for DO2 is ON

...

Bit 10: =0:Open wire detection for DO11 is OFF, =1:Open wire detection for DO11 is ON

Bit 11: =0:Open wire detection for DO12 is OFF, =1:Open wire detection for DO12 is ON

Write on this register sets for all digital outputs a new setup state

DIGITAL OUTPUTS:ENABLE OPEN WIRE DETECTION WHILE OFF

INITIAL & WATCHDOG SETUP ENABLE SHOTCUT TO VDD DETECTION WHILE OFF DO1-DO12	3x59004 4x59004 1:59003	0,0x0000 B:00 00		0x0FFF	UINT16 R/W	NO
		Initial setup of shortcut to VDD detection while OFF for DO1:0=OFF	1			
		Initial setup of shortcut to VDD detection while OFF for DO2:0=OFF	1			
		Initial setup of shortcut to VDD detection while OFF for DO3:0=OFF	1			
		Initial setup of shortcut to VDD detection while OFF for DO4:0=OFF	1			
		Initial setup of shortcut to VDD detection while OFF for DO5:0=OFF	1			

		Initial setup of shortcut to VDD detection while OFF for DO6:0=OFF	1		
		Initial setup of shortcut to VDD detection while OFF for DO7:0=OFF	1		
		Initial setup of shortcut to VDD detection while OFF for DO8:0=OFF	1		
		Initial setup of shortcut to VDD detection while OFF for DO9:0=OFF	1		
		Initial setup of shortcut to VDD detection while OFF for DO10:0=OFF	1		
		Initial setup of shortcut to VDD detection while OFF for DO11:0=OFF	1		
		Initial setup of shortcut to VDD detection while OFF for DO12:0=OFF	1		
<p>Initial setup state for shortcut to VDD detection while OFF for digital output DOx. Used after power on and watchdog event Bit 0: =0:Shortcut detection for DO1 is OFF, =1:Shortcut detection for DO1 is ON Bit 1: =0:Shortcut detection for DO2 is OFF, =1:Shortcut detection for DO2 is ON ... Bit 10: =0:Shortcut detection for DO11 is OFF, =1:Shortcut detection for DO11 is ON Bit 11: =0:Shortcut detection for DO12 is OFF, =1:Shortcut detection for DO12 is ON</p> <p>Write on this register sets for all digital outputs a new setup state</p>					

UPDATE DIGITAL INPUTS-OUTPUTS	ASCII WRITE COMMAND	#UDIOS:<OutAlIDOS> <CR> Result: #UDIOS:<InAlIDIS> <CR>	ASCII	NO
	DO1	1:ON		
	DO2	0:OFF		
	DO3	1:ON		
	DO4	0:OFF		
	DO5	0:OFF		
	DO6	0:OFF		
	DO7	0:OFF		
	DO8	0:OFF		
	DO9	0:OFF		
	DO10	0:OFF		
	DO11	0:OFF		
	DO12	0:OFF		
	TX	#UDIOS:5 <CR>		
	RX	N/A		
		Current status of digital inputs:???		
<p>Sets all digital outputs to the new state OutAlIDOS and returns the actual status of all digital inputs InAlIDIS The new state for all digital outputs Bit 0: State of DO1 (=0:OFF, =1:ON) Bit 1: State of DO2 (=0:OFF, =1:ON) ... Bit 10: State of DO11 (=0:OFF, =1:ON) Bit 11: State of DO12 (=0:OFF, =1:ON)</p>				
<p>Returns the current state of all digital inputs as decimal number and as hexadecimal number. DISDec, DISHex The current state of all digital inputs: Bit 0: State of DI1 (=0:OFF, =1:ON) Bit 1: State of DI2 (=0:OFF, =1:ON) ... Bit 10: State of DI11 (=0:OFF, =1:ON) Bit 11: State of DI12 (=0:OFF, =1:ON)</p>				
DIGITAL INPUTS				
EVENTS ON	ASCII WRITE COMMAND	#EVTON <CR> Result: #OK <CR>	ASCII	NO
	TX	#EVTON <CR>		
	RX	#1,OK <CR>		
<p>Activates event sending of changes on digital inputs Whenever a change is detected on the digital inputs, the IO module sends immediately #<BusAdr>,EVT:DIS:<AlIDISasDec>,<AlIDISasHex> <CR></p>				
EVENTS OFF	ASCII WRITE COMMAND	#EVTOFF <CR> Result: #OK <CR>	ASCII	NO

		Current counter for changes on DI1:1		
		Current counter for changes on DI2:0		
		Current counter for changes on DI3:0		
		Current counter for changes on DI4:0		
		Current counter for changes on DI5:0		
		Current counter for changes on DI6:0		
		Current counter for changes on DI7:0		
		Current counter for changes on DI8:0		
		Current counter for changes on DI9:0		
		Current counter for changes on DI10:0		
		Current counter for changes on DI11:0		
		Current counter for changes on DI12:0		
Returns for each digital input the counter for changes. As soon as the module detects a signal change on a digital input, the change counter for the affected digital input is incremented by 1. A signal change can be: Detection of a short keypress Detection of the start of a long keypress Detection of a release of a long keypress				
CHANGE DIx	ASCII READ COMMAND	#CDI<DINR> <CR> Result: #CDI<DINR>:<ChangesDec>, <ChangesHex> <CR>	ASCII	
	DINR	12		
	TX	#CDI12 <CR>		
	RX	#1,CDI12:0,0x0 <CR>		
		Current counter for changes on digital input DI12:0		
Returns for digital input <DINR> the counter for signal changes. As soon as the module detects a signal change on a digital input, the change counter for the affected digital input is incremented by 1. A signal change can be: Detection of a short keypress Detection of the start of a long keypress Detection of a release of a long keypress				
SHORT KEY ALL DIS	ASCII READ COMMAND	#SKADIS <CR> Result: #SKADIS:<ShortKeyDI1Dec>, ..., <ShortKeyDIxDec>, <ShortKeyDI1Hex>, ..., <ShortKeyDIxHex> <CR>	ASCII	
	TX	#SKADIS <CR>		
	RX	#1,SKADIS:0,0,0,0,0,0,0,0,0,0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0 <CR>		
		Current counter for short keypress events on DI1:0		
		Current counter for short keypress events on DI2:0		
		Current counter for short keypress events on DI3:0		
		Current counter for short keypress events on DI4:0		
		Current counter for short keypress events on DI5:0		
		Current counter for short keypress events on DI6:0		
		Current counter for short keypress events on DI7:0		
		Current counter for short keypress events on DI8:0		
		Current counter for short keypress events on DI9:0		
		Current counter for short keypress events on DI10:0		

		Current counter for short keypress events on DI11:0		
		Current counter for short keypress events on DI12:0		
Returns for each digital input the counter for short keypress events. As soon as the module detects a short keypress on a digital input, the counter for the affected digital input is incremented by 1.				
SHORT KEY Dlx	ASCII READ COMMAND	#SKDI<DINR> <CR> Result: #SKDI<DINR>:<ShortKeyDec>,<ShortKeyHex> <CR>	ASCII	
	DINR	12		
	TX	#SKDI12 <CR>		
	RX	#1,SKDI12:0,0x0 <CR>		
		Current counter for short keypress events on digital input DI12:0		
Returns for digital input <DINR> the counter for short keypress events. As soon as the module detects a short keypress on a digital input, the counter for the affected digital input is incremented by 1.				
LONG KEY START ALL DIS	ASCII READ COMMAND	#LKSADIS <CR> Result: #LKSADIS:<LongKeyStartDI1Dec>,...,<LongKeyStartDlxDec>, <LongKeyStartDI1Hex>,...,<LongKeyStartDlxHex> <CR>	ASCII	
	TX	#LKSADIS <CR>		
	RX	#1,LKSADIS:1,0,0,0,0,0,0,0,0,0,0,0,0,0x1,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0 <CR>		
		Current counter for long keypress start events on DI1:1		
		Current counter for long keypress start events on DI2:0		
		Current counter for long keypress start events on DI3:0		
		Current counter for long keypress start events on DI4:0		
		Current counter for long keypress start events on DI5:0		
		Current counter for long keypress start events on DI6:0		
		Current counter for long keypress start events on DI7:0		
		Current counter for long keypress start events on DI8:0		
		Current counter for long keypress start events on DI9:0		
		Current counter for long keypress start events on DI10:0		
		Current counter for long keypress start events on DI11:0		
		Current counter for long keypress start events on DI12:0		
Returns for each digital input the counter for long keypress start events. As soon as the module detects the start of a long keypress on a digital input, the counter for the affected digital input is incremented by 1.				
LONG KEY START Dlx	ASCII READ COMMAND	#LKSDI<DINR> <CR> Result: #LKSDI<DINR>:<LongKeyStartDec>,<LongKeyStartHex> <CR>	ASCII	
	DINR	12		
	TX	#LKSDI12 <CR>		
	RX	#1,LKSDI12:0,0x0 <CR>		
		Current counter for long keypress start events on digital input DI12:0		
Returns for digital input <DINR> the counter for long keypress start events. As soon as the module detects the start of a long keypress on a digital input, the counter for the affected digital input is incremented by 1.				
LONG KEY END ALL DIS	ASCII READ COMMAND	#LKEADIS <CR> Result: #LKEADIS:<LongKeyEndDI1Dec>,...,<LongKeyEndDlxDec>, <LongKeyEndDI1Hex>,...,<LongKeyEndDlxHex> <CR>	ASCII	
	TX	#LKEADIS <CR>		
	RX	#1,LKEADIS:0,0,0,0,0,0,0,0,0,0,0,0,0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0 <CR>		

		Current counter for long keypress end events on DI1:0		
		Current counter for long keypress end events on DI2:0		
		Current counter for long keypress end events on DI3:0		
		Current counter for long keypress end events on DI4:0		
		Current counter for long keypress end events on DI5:0		
		Current counter for long keypress end events on DI6:0		
		Current counter for long keypress end events on DI7:0		
		Current counter for long keypress end events on DI8:0		
		Current counter for long keypress end events on DI9:0		
		Current counter for long keypress end events on DI10:0		
		Current counter for long keypress end events on DI11:0		
		Current counter for long keypress end events on DI12:0		
Returns for each digital input the counter for long keypress end events. As soon as the module detects the end of a long keypress on a digital input, the counter for the affected digital input is incremented by 1.				
LONG KEY END DIx	ASCII READ COMMAND	#LKEDI<DINR> <CR> Result: #LKEDI<DINR>:<LongKeyEndDec>,<LongKeyEndHex> <CR>	ASCII	
	DINR	12		
	TX	#LKEDI12 <CR>		
	RX	#1,LKEDI12:0,0x0 <CR>		
		Current counter for long keypress end events on digital input DI12:0		
Returns for digital input <DINR> the counter for long keypress end events. As soon as the module detects the end of a long keypress on a digital input, the counter for the affected digital input is incremented by 1.				
RISE ALL DIS	ASCII READ COMMAND	#RADIS <CR> Result: #RADIS:<RiseDI1Dec>,<RiseDIxDec>,<RiseDI1Hex>,<RiseDIxHex> <CR>	ASCII	
	TX	#RADIS <CR>		
	RX	#1,RADIS:1,0,0,0,0,0,0,0,0,0,0,0,0x1,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0 <CR>		
		Current counter for rising edges on DI1:1		
		Current counter for rising edges on DI2:0		
		Current counter for rising edges on DI3:0		
		Current counter for rising edges on DI4:0		
		Current counter for rising edges on DI5:0		
		Current counter for rising edges on DI6:0		
		Current counter for rising edges on DI7:0		
		Current counter for rising edges on DI8:0		
		Current counter for rising edges on DI9:0		
		Current counter for rising edges on DI10:0		
		Current counter for rising edges on DI11:0		
		Current counter for rising edges on DI12:0		
Returns for each digital input the counter for rising edges. As soon as the module detects a rising edge on a digital input, the rising edge counter for the affected digital input is incremented by 1.				
RISE DIx	ASCII READ COMMAND	#RDI<DINR> <CR> Result: #RDI<DINR>:<RiseDec>,<RiseHex> <CR>	ASCII	
	DINR	12		

	TX	#RDI12<CR>		
	RX	#1,RDI12:0,0x0<CR>		
		Current counter for rising edges on digital input DI12:0		
Returns for digital input <DINR> the counter for rising edges. As soon as the module detects a rising edge on a digital input, the rising edge counter for the affected digital input is incremented by 1.				
FALL ALL DIS	ASCII READ COMMAND	#FADIS<CR> Result: #FADIS:<FallDI1Dec>,...,<FallDIxDec>,<FallDI1Hex>,...,<FallDIxHex><CR>	ASCII	
	TX	#FADIS<CR>		
	RX	#1,FADIS:0,0,0,0,0,0,0,0,0,0,0,0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0<CR>		
		Current counter for falling edges on DI1:0		
		Current counter for falling edges on DI2:0		
		Current counter for falling edges on DI3:0		
		Current counter for falling edges on DI4:0		
		Current counter for falling edges on DI5:0		
		Current counter for falling edges on DI6:0		
		Current counter for falling edges on DI7:0		
		Current counter for falling edges on DI8:0		
		Current counter for falling edges on DI9:0		
		Current counter for falling edges on DI10:0		
		Current counter for falling edges on DI11:0		
		Current counter for falling edges on DI12:0		
Returns for each digital input the counter for falling edges. As soon as the module detects a falling edge on a digital input, the falling edge counter for the affected digital input is incremented by 1.				
FALL DIx	ASCII READ COMMAND	#FDI<DINR><CR> Result: #FDI<DINR>:<FallDec>,<FallHex><CR>	ASCII	
	DINR	12		
	TX	#FDI12<CR>		
	RX	#1,FDI12:0,0x0<CR>		
		Current counter for falling edges on digital input DI12:0		
Returns for digital input <DINR> the counter for falling edges. As soon as the module detects a falling edge on a digital input, the falling edge counter for the affected digital input is incremented by 1.				
RESET COUNTERS	ASCII WRITE COMMAND	#RC<CR> Result: #OK<CR>	ASCII	NO
	TX	#RC<CR>		
	RX	N/A		
Resets all internal counters for digital inputs and events on this digital inputs to 0.				
DIGITAL OUTPUTS				
SET DIGITAL OUTPUTS	ASCII WRITE COMMAND	#SDOS:<OutAllIDOS><CR> Result: #OK<CR>	ASCII	NO
	DO1	1:ON		
	DO2	0:OFF		
	DO3	1:ON		
	DO4	0:OFF		

	DO5	0:OFF		
	DO6	0:OFF		
	DO7	0:OFF		
	DO8	0:OFF		
	DO9	0:OFF		
	DO10	0:OFF		
	DO11	0:OFF		
	DO12	0:OFF		
	TX	#SDOS:5 <CR>		
	RX	N/A		
Sets all digital outputs to the new state OutAllDOS The new state for all digital outputs Bit 0: State of DO1 (=0:OFF, =1:ON) Bit 1: State of DO2 (=0:OFF, =1:ON) ... Bit 10: State of DO11 (=0:OFF, =1:ON) Bit 11: State of DO12 (=0:OFF, =1:ON)				
SET DIGITAL OUTPUT DOx	ASCII WRITE COMMAND	#SDO<DONR>:<Out> <CR> Result: #OK<CR>	ASCII	NO
	DONR	1		
	DO1	1:ON		
	TX	#SDO1:1<CR>		
	RX	N/A		
Sets the new state for digital output DOx. The state is defined with <Out>. Out The new state of the digital output DOx: =0: digital output is OFF =1: digital output is ON				
GET DIGITAL OUTPUTS	ASCII READ COMMAND	#GDOS<CR> Result: #GDOS:<DOSDec>,<DOSHex> <CR>	ASCII	
	TX	#GDOS<CR>		
	RX	#1,GDOS:4095,0xFF <CR>		
		Current status of digital outputs:1111.1111.1111		
Returns the current state of the digital outputs as decimal number and as hexadecimal number. DOSDec, DOSHex The current state of the digital outputs: Bit 0: State of DO1 (=0:OFF, =1:ON) Bit 1: State of DO2 (=0:OFF, =1:ON) ... Bit 10: State of DO11 (=0:OFF, =1:ON) Bit 11: State of DO12 (=0:OFF, =1:ON)				
GET DIGITAL OUTPUT DOx	ASCII READ COMMAND	#GDO<DONR> <CR> Result: #GDO<DONR>:<DOxDec>,<DOxHex> <CR>	ASCII	

	DONR	1		
	TX	#GDO1<CR>		
	RX	#1,GDO1:1,0x1<CR>		
		Current status of digital output DO1:1=ON		

Returns the current state of the digital output DOx as decimal number and as hexadecimal number.

DOxDec, DOxHex

The current state of the digital output DOx:

=0: digital output is OFF

=1: digital output is ON

DIGITAL OUTPUTS: ENABLE OPEN WIRE DETECTION WHILE ON

SET DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE ON	ASCII WRITE COMMAND	#SDOEOWDONS:<OpenWireDOS><CR> Result: #OK<CR>	ASCII	NO
	DO1	1:ENABLE		
	DO2	1:ENABLE		
	DO3	1:ENABLE		
	DO4	1:ENABLE		
	DO5	1:ENABLE		
	DO6	1:ENABLE		
	DO7	1:ENABLE		
	DO8	1:ENABLE		
	DO9	1:ENABLE		
	DO10	1:ENABLE		
	DO11	1:ENABLE		
	DO12	1:ENABLE		
	TX	#SDOEOWDONS:4095<CR>		
	RX	N/A		

Sets the open wire mode for all digital outputs to the new mode OpenWireDOS. This enables the diagnostic of open wire while the digital output is ON.

The new state for all digital outputs

Bit 0: New mode for DO1 (=0:DISABLED, =1:ENABLED)

Bit 1: New mode for DO2 (=0:DISABLED, =1:ENABLED)

...

Bit 10: New mode for DO11 (=0:DISABLED, =1:ENABLED)

Bit 11: New mode for DO12 (=0:DISABLED, =1:ENABLED)

SET DIGITAL OUTPUT ENABLE OPEN WIRE DETECTION WHILE ON	ASCII WRITE COMMAND	#SDOEOWDON<DONR>:<OpenWireDOx><CR> Result: #OK<CR>	ASCII	NO
	DONR	1		
	DO1	0:DISABLE		
	TX	#SDOEOWDON1:0<CR>		
	RX	N/A		

Sets the open wire mode for digital output DOx to the new mode OpenWireDOx. This enables the diagnostic of open wire while the digital output is ON.

The new mode of the digital output DOx:

=0: diagnostic mode for digital output is DISABLED

=1: diagnostic mode for digital output is ENABLED

GET DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE ON	ASCII READ COMMAND	#GDOEOWDONS<CR> Result: #GDOEOWDONS:<OpenWireDOSDec>,<OpenWireDOSHex><CR>	ASCII	
	TX	#GDOEOWDONS<CR>		
	RX	#1,GDOEOWDONS:0,0x0<CR>		
		Current mode for open wire diagnostic while ON of digital outputs:0000.0000.0000		
Returns the current mode for open wire diagnostic while digital output is ON as decimal number and as hexadecimal number. OpenWireDOSDec, OpenWireDOSHex The current mode for open wire diagnostic while ON of the digital outputs: Bit 0: Open wire diagnostic mode of DO1 (=0:DISABLED, =1:ENABLED) Bit 1: Open wire diagnostic mode of DO2 (=0:DISABLED, =1:ENABLED) ... Bit 10: Open wire diagnostic mode of DO11 (=0:DISABLED, =1:ENABLED) Bit 11: Open wire diagnostic mode of DO12 (=0:DISABLED, =1:ENABLED)				
GET DIGITAL OUTPUT ENABLE OPEN WIRE DETECTION WHILE ON	ASCII READ COMMAND	#GDOEOWDON<DONR><CR> Result: #GDOEOWDON<DONR>:<OpenWireDOxDec>,<OpenWireDOxHex><CR>	ASCII	
	DONR	1		
	TX	#GDOEOWDON1<CR>		
	RX	#1,GDOEOWDON1:0,0x0<CR>		
		Current open wire diagnostic mode while ON of digital output DO1:0=DISABLED		
Returns the current open wire diagnostic mode while ON of the digital output DOx as decimal number and as hexadecimal number. X stands for the number of the digital output. OpenWireDOxDec, OpenWireDOxHex The current diagnostic mode of the digital output DOx: =0: open wire diagnostic mode for digital output is DISABLED =1: open wire diagnostic mode for digital output is ENABLED				
DIGITAL OUTPUTS: ENABLE OPEN WIRE DETECTION WHILE OFF				
SET DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE OFF	ASCII WRITE COMMAND	#SDOEOWDOFFS:<OpenWireDOS><CR> Result: #OK<CR>	ASCII	NO
	DO1	1:ENABLE		
	DO2	1:ENABLE		
	DO3	1:ENABLE		
	DO4	1:ENABLE		
	DO5	1:ENABLE		
	DO6	1:ENABLE		
	DO7	1:ENABLE		
	DO8	1:ENABLE		
	DO9	1:ENABLE		
	DO10	1:ENABLE		
	DO11	1:ENABLE		
	DO12	1:ENABLE		
	TX	#SDOEOWDOFFS:4095<CR>		
	RX	N/A		

Sets the open wire mode for all digital outputs to the new mode OpenWireDOS. This enables the diagnostic of open wire while the digital output is OFF.

The new state for all digital outputs

Bit 0: New mode for DO1 (=0:DISABLED, =1:ENABLED)

Bit 1: New mode for DO2 (=0:DISABLED, =1:ENABLED)

...

Bit 10: New mode for DO11 (=0:DISABLED, =1:ENABLED)

Bit 11: New mode for DO12 (=0:DISABLED, =1:ENABLED)

SET DIGITAL OUTPUT ENABLE OPEN WIRE DETECTION WHILE OFF	ASCII WRITE COMMAND	#SDOEOWDOFF<DONR>:<OpenWireDOx> <CR> Result: #OK<CR>	ASCII	NO
	DONR	1		
	DO1	0:DISABLE		
	TX	#SDOEOWDOFF1:0<CR>		
	RX	N/A		

Sets the open wire mode for digital output DOx to the new mode OpenWireDOx. This enables the diagnostic of open wire while the digital output is OFF.

The new mode of the digital output DOx:

=0: diagnostic mode for digital output is DISABLED

=1: diagnostic mode for digital output is ENABLED

GET DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE OFF	ASCII READ COMMAND	#GDOEOWDOFFS<CR> Result: #GDOEOWDOFFS:<OpenWireDOSDec>,<OpenWireDOSHex> <CR>	ASCII	
	TX	#GDOEOWDOFFS<CR>		
	RX	#1,GDOEOWDOFFS:0,0x0<CR>		
		Current mode for open wire diagnostic while OFF of digital outputs:0000.0000.0000		

Returns the current mode for open wire diagnostic while digital output is OFF as decimal number and as hexadecimal number.

OpenWireDOSDec, OpenWireDOSHex

The current mode for open wire diagnostic while OFF of the digital outputs:

Bit 0: Open wire diagnostic mode of DO1 (=0:DISABLED, =1:ENABLED)

Bit 1: Open wire diagnostic mode of DO2 (=0:DISABLED, =1:ENABLED)

...

Bit 10: Open wire diagnostic mode of DO11 (=0:DISABLED, =1:ENABLED)

Bit 11: Open wire diagnostic mode of DO12 (=0:DISABLED, =1:ENABLED)

GET DIGITAL OUTPUT ENABLE OPEN WIRE DETECTION WHILE OFF	ASCII READ COMMAND	#GDOEOWDOFF<DONR> <CR> Result: #GDOEOWDOFF<DONR>:<OpenWireDOxDec>,<OpenWireDOxHex> <CR>	ASCII	
	DONR	1		
	TX	#GDOEOWDOFF1<CR>		
	RX	#1,GDOEOWDOFF1:0,0x0<CR>		
		Current open wire diagnostic mode while OFF of digital output DO1:0=DISABLED		

Returns the current open wire diagnostic mode while OFF of the digital output DOx as decimal number and as hexadecimal number. X stands for the number of the digital output.

OpenWireDOxDec, OpenWireDOxHex

The current diagnostic mode of the digital output DOx:

=0: open wire diagnostic mode for digital output is DISABLED

=1: open wire diagnostic mode for digital output is ENABLED

DIGITAL OUTPUTS: ENABLE SHORTCUT TO VDD DETECTION WHILE OFF

SET DIGITAL OUTPUTS ENABLE SHORTCUT TO VDD DETECTION WHILE OFF	ASCII WRITE COMMAND	#SDOESVDDS:<ShortCutDOS> <CR> Result: #OK<CR>	ASCII	NO
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	DO1	1:ENABLE		
	DO2	1:ENABLE		
	DO3	1:ENABLE		
	DO4	1:ENABLE		
	DO5	1:ENABLE		
	DO6	1:ENABLE		
	DO7	1:ENABLE		
	DO8	1:ENABLE		
	DO9	1:ENABLE		
	DO10	1:ENABLE		
	DO11	1:ENABLE		
	DO12	1:ENABLE		
	TX	#SDOESVDDS:4095 <CR>		
	RX	N/A		

Sets the shortcut to VDD detection mode for all digital outputs to the new mode ShortCutDOS. This enables the diagnostic of shortcut to VDD while the digital output is OFF.

The new state for all digital outputs

Bit 0: New mode for DO1 (=0:DISABLED, =1:ENABLED)

Bit 1: New mode for DO2 (=0:DISABLED, =1:ENABLED)

...

Bit 10: New mode for DO11 (=0:DISABLED, =1:ENABLED)

Bit 11: New mode for DO12 (=0:DISABLED, =1:ENABLED)

SET DIGITAL OUTPUT ENABLE SHORTCUT TO VDD DETECTION WHILE OFF	ASCII WRITE COMMAND	#SDOESVDD<DONR>:<ShortCutDOx><CR> Result: #OK<CR>	ASCII	NO
	DONR	1		
	DO1	0:DISABLE		
	TX	#SDOESVDD1:0<CR>		
	RX	N/A		

Sets the shortcut to VDD mode for digital output DOx to the new mode ShortCutDOx. This enables the diagnostic of shortcut to VDD while the digital output is OFF.

The new mode of the digital output DOx:

=0: diagnostic mode for digital output is DISABLED

=1: diagnostic mode for digital output is ENABLED

GET DIGITAL OUTPUTS ENABLE SHORT CUT TO VDD DETECTION WHILE OFF	ASCII READ COMMAND	#GDOESVDDS<CR> Result: #GDOESDDS:<ShortCutDOSDec>,<ShortCutDOSHex><CR>	ASCII	
	TX	#GDOESVDDS<CR>		
	RX	#1,GDOESVDDS:0,0x0<CR>		
		Current mode for shortcut to VDD diagnostic while OFF of digital outputs:0000.0000.0000		

Returns the current mode for shortcut to VDD diagnostic while digital output is OFF as decimal number and as hexadecimal number.

ShortCutDOSDec, ShortCutDOSHex

The current mode for shortcut diagnostic while OFF of the digital outputs:

Bit 0: Open wire diagnostic mode of DO1 (=0:DISABLED, =1:ENABLED)

Bit 1: Open wire diagnostic mode of DO2 (=0:DISABLED, =1:ENABLED)

...

Bit 10: Open wire diagnostic mode of DO11 (=0:DISABLED, =1:ENABLED)

Bit 11: Open wire diagnostic mode of DO12 (=0:DISABLED, =1:ENABLED)

GET DIGITAL OUTPUT ENABLE SHORT CUT TO VDD DETECTION WHILE OFF	ASCII READ COMMAND	#GDOESVDD<DONR> <CR> Result: #GDOESVDD<DONR>:<ShortCutDOxDec>,<ShortCutDOxHex> <CR>	ASCII	
	DONR	1		
	TX	#GDOESVDD1<CR>		
	RX	#1,GDOESVDDD1:0,0x0 <CR>		
		Current shortcut to VDD diagnostic mode while OFF of digital output DO1:0=DISABLED		

Returns the current short cut to VDD diagnostic mode while OFF of the digital output DOx as decimal number and as hexadecimal number. X stands for the number of the digital output.

ShortCutDOxDec, ShortCutDOxHex

The current diagnostic mode of the digital output DOx:

=0: open wire diagnostic mode for digital output is DISABLED

=1: open wire diagnostic mode for digital output is ENABLED

DIGITAL OUTPUTS: SPI STATUS

GET SPI STATUS DIGITAL OUTPUT GROUPS	ASCII READ COMMAND	#GSSDOGS<CR> Result: #GSSDOGS:<SPIDOGSDec>,<SPIDOGSHex> <CR>	ASCII	
	TX	#GSSDOGS <CR>		
	RX	#1,GSSDOGS:0,0x0 <CR>		
		Current SPI status of digital output groups:00		

Returns the current SPI communication state of the corresponding output group as decimal number and as hexadecimal number.

SPIDOGSDec, SPIDOGSHex

The current SPI communication state of the digital output group:

Bit 0: SPI communication state for digital output group #1 (DO1-DO6) (=0:NO FAULT, =1:FAULT)

Bit 1: SPI communication state for digital output group #2 (DO7-DO12) (=0:NO FAULT, =1:FAULT)

GET SPI STATUS DIGITAL OUTPUT GROUPx	ASCII READ COMMAND	#GSSDOG<DOGRP> <CR> Result: #GSSDOG<DOGRP>:<SPIDOGxDec>,<SPIDOGxHex> <CR>	ASCII	
	DOGRP	2		
	TX	#GSSDOG2 <CR>		
	RX	#1,GSSDOG2:0,0x0 <CR>		
		Current SPI status of digital output group DOG2:0=NO FAULT		

Returns the current SPI communication state of the digital output group DOGRP as decimal number and as hexadecimal number. X stands for the number of the digital output group.

SPIDOGxDec, SPIDOGxHex

The current SPI communication state of the digital output group DOGRP:

=0: SPI communication state for output group is OK (NO FAULT)

=1: SPI communication state for output group is FAULT

Digital output group #1 is DO1-DO6, #2 is DO7-DO12

DIGITAL OUTPUTS: INTERRUPT STATUS

GET DIGITAL OUTPUTS INTERRUPT STATUS	ASCII READ COMMAND	#GDOINTS<CR> Result: #GDOINTS:<InterruptStatusDec>,<InterruptStatusHex> <CR>	ASCII	
	TX	#GDOINTS <CR>		
	RX	#1,GDOINTS:0,0x0 <CR>		
		Current interrupt status of all digital output groups:0000.0000.0000.0000		

Returns the current interrupt state state of all output groups as decimal number and as hexadecimal number.

InterruptStatusDec,InterruptStatusHex

The current interrupt state of all digital output groups

Bit 0:DO1-DO6:Overload detected (0=OK,1=FAULT)

Bit 1:DO1-DO6:Current limit detected(0=OK,1=FAULT)

Bit 2:DO1-DO6:Open wire while OFF detected (0=OK,1=FAULT)

Bit 3:DO1-DO6:Open wire while ON detected (0=OK,1=FAULT)

Bit 4:DO1-DO6:Short to VDD while ON detected (0=OK,1=FAULT)

Bit 5:DO1-DO6:Thermal error detected-shutdown (0=OK,1=FAULT)

Bit 6:DO1-DO6:Supply error detected (0=OK,1=FAULT)

Bit 7:DO1-DO6:Communication error detected (0=OK,1=FAULT)

Bit 8:DO7-DO12:Overload detected (0=OK,1=FAULT)

Bit 9:DO7-DO12:Current limit detected(0=OK,1=FAULT)

Bit 10:DO7-DO12:Open wire while OFF detected (0=OK,1=FAULT)

Bit 11:DO7-DO12:Open wire while ON detected (0=OK,1=FAULT)

Bit 12:DO7-DO12:Short to VDD while ON detected (0=OK,1=FAULT)

Bit 13:DO7-DO12:Thermal error detected-shutdown (0=OK,1=FAULT)

Bit 14:DO7-DO12:Supply error detected (0=OK,1=FAULT)

Bit 15:DO7-DO12:Communication error detected (0=OK,1=FAULT)

GET DIGITAL OUTPUT GROUPx INTERRUPT STATUS	ASCII READ COMMAND	#GDOINT<DOGRP> <CR> Result: #GDOINT<DOGRP>:<InterruptStatusDec>,<InterruptStatusHex> <CR>	ASCII	
	DOGRP	2		
	TX	#GDOINT2<CR>		
	RX	#1,GDOINT2:0,0x0<CR>		
		Current interrupt status of digital output group 2:0000.0000		

Returns the current interrupt state of the digital output group DOGRP as decimal number and as hexadecimal number. X stands for the number of the digital output group.

InterruptStatusDec, InterruptStatusHex

Group 1: DO1-DO6

Group 2: DO7-DO12

Bit 0:Overload detected (0=OK,1=FAULT)

Bit 1:Current limit detected(0=OK,1=FAULT)

Bit 2:Open wire while OFF detected (0=OK,1=FAULT)

Bit 3:Open wire while ON detected (0=OK,1=FAULT)

Bit 4:Short to VDD while ON detected (0=OK,1=FAULT)

Bit 5:Thermal error detected-shutdown (0=OK,1=FAULT)

Bit 6:Supply error detected (0=OK,1=FAULT)

Bit 7:Communication error detected (0=OK,1=FAULT)

DIGITAL OUTPUTS: GLOBAL ERRORS

GET DIGITAL OUTPUTS GLOBAL ERRORS	ASCII READ COMMAND	#GDOERRS<CR> Result: #GDOERRS:<GlobalErrorsDec>,<GlobalErrorsHex> <CR>	ASCII	
	TX	#GDOERRS<CR>		
	RX	#1,GDOERRS:0,0x0<CR>		
		Current global errors of all digital output groups:0000.0000.0000.0000		

Returns the current interrupt state state of all output groups as decimal number and as hexadecimal number.

InterruptStatusDec,InterruptStatusHex

The current interrupt state of all digital output groups

Bit 0:DO1-DO6:Internal under voltage detected (0=OK,1=FAULT)

Bit 1:DO1-DO6:VA under voltage detected (<2.3V) (0=OK,1=FAULT)

Bit 2:DO1-DO6:VDD not good detected (<17V) (0=OK,1=FAULT)

Bit 3:DO1-DO6:VDD warning detected (<12V) (0=OK,1=FAULT)

Bit 4:DO1-DO6:VDD under voltage detected (<8V) (0=OK,1=FAULT)

Bit 5:DO1-DO6:Thermal shutdown (0=OK,1=FAULT)

Bit 6:DO1-DO6:Synchronisation error detected (0=OK,1=FAULT)

Bit 7:DO1-DO6:Watchdog error detected (0=OK,1=FAULT)

Bit 8:DO7-DO12:Internal under voltage detected (0=OK,1=FAULT)

Bit 9:DO7-DO12:VA under voltage detected (<2.3V) (0=OK,1=FAULT)

Bit 10:DO7-DO12:VDD not good detected (<17V) (0=OK,1=FAULT)

Bit 11:DO7-DO12:VDD warning detected (<12V) (0=OK,1=FAULT)

Bit 12:DO7-DO12:VDD under voltage detected (<8V) (0=OK,1=FAULT)

Bit 13:DO7-DO12:Thermal shutdown (0=OK,1=FAULT)

Bit 14:DO7-DO12:Synchronisation error detected (0=OK,1=FAULT)

Bit 15:DO7-DO12:Watchdog error detected (0=OK,1=FAULT)

GET DIGITAL OUTPUT GROUPx GLOBAL ERRORS	ASCII READ COMMAND	#GDOERR<DOGRP><CR> Result: #GDOERR<DOGRP>:<GlobalErrorsDec>,<GlobalErrorsHex><CR>	ASCII	
	DOGRP	2		
	TX	#GDOERR2<CR>		
	RX	#1,GDOERR2:0,0x0<CR>		
		Current global errors of digital output group 2:0000.0000		

Returns the current interrupt state of the digital output group DOGRP as decimal number and as hexadecimal number. X stands for the number of the digital output group.

InterruptStatusDec, InterruptStatusHex

Group 1: DO1-DO6

Group 2: DO7-DO12

Bit 0:Internal under voltage detected (0=OK,1=FAULT)

Bit 1:VA under voltage detected (<2.3V) (0=OK,1=FAULT)

Bit 2:VDD not good detected (<17V) (0=OK,1=FAULT)

Bit 3:VDD warning detected (<12V) (0=OK,1=FAULT)

Bit 4:VDD under voltage detected (<8V) (0=OK,1=FAULT)

Bit 5:Thermal shutdown (0=OK,1=FAULT)

Bit 6:Synchronisation error detected (0=OK,1=FAULT)

Bit 7:Watchdog error detected (0=OK,1=FAULT)

DIGITAL OUTPUTS: THERMAL OVERLOAD DETECTION

GET DIGITAL OUTPUTS THERMAL OVERLOAD DETECTION	ASCII READ COMMAND	#GDOTOS<CR> Result: #GDOTOS:<StatusDOSDec>,<StatusDOSHex><CR>	ASCII	
	TX	#GDOTOS<CR>		
	RX	#1,GDOTOS:0,0x0<CR>		
		Current thermal overload detection status of digital outputs:0000.0000.0000		

Returns the current state of the thermal overload detection for all digital outputs as decimal number and as hexadecimal number.

StatusDOSDec, StatusDOSHex

The current detection state of the digital outputs:

Bit 0: State of DO1 (=0:OFF, =1:ON)

Bit 1: State of DO2 (=0:OFF, =1:ON)

...

Bit 10: State of DO11 (=0:OFF, =1:ON)

Bit 11: State of DO12 (=0:OFF, =1:ON)

GET DIGITAL OUTPUT DOx THERMAL OVERLOAD DETECTION	ASCII READ COMMAND	#GDOTO<DONR> <CR> Result: #GDOTO<DONR>:<StatusDOxDec>,<StatusDOxHex> <CR>	ASCII	
	DONR	1		
	TX	#GDOTO1<CR>		
	RX	#1,GDOTO1:0,0x0<CR>		
		Current thermal overload detection status of DO1:0=OFF		

Returns the current state of the thermal overload detection for digital output DOx as decimal number and as hexadecimal number.

StatusDOxDec, StatusDOxHex

The current detection state for digital output DOx:

=0: digital output is OK

=1: FAULT detected on digital output

DIGITAL OUTPUTS: CURRENT LIMIT DETECTION

GET DIGITAL OUTPUTS CURRENT LIMIT DETECTION	ASCII READ COMMAND	#GDOCLS<CR> Result: #GDOCLS:<StatusDOSDec>,<StatusDOSHex> <CR>	ASCII	
	TX	#GDOCLS<CR>		
	RX	#1,GDOCLS:0,0x0<CR>		
		Current current limit detection status of digital outputs:0000.0000.0000		

Returns the current state of the current limit detection for all digital outputs as decimal number and as hexadecimal number.

StatusDOSDec, StatusDOSHex

The current detection state of the digital outputs:

Bit 0: State of DO1 (=0:OFF, =1:ON)

Bit 1: State of DO2 (=0:OFF, =1:ON)

...

Bit 10: State of DO11 (=0:OFF, =1:ON)

Bit 11: State of DO12 (=0:OFF, =1:ON)

GET DIGITAL OUTPUT DOx CURRENT LIMIT DETECTION	ASCII READ COMMAND	#GDOCL<DONR> <CR> Result: #GDOCL<DONR>:<StatusDOxDec>,<StatusDOxHex> <CR>	ASCII	
	DONR	1		
	TX	#GDOCL1<CR>		
	RX	#1,GDOCL1:0,0x0<CR>		
		Current current limit detection status of DO1:0=OFF		

Returns the current state of the current limit detection for digital output DOx as decimal number and as hexadecimal number.

StatusDOxDec, StatusDOxHex

The current detection state for digital output DOx:

=0: digital output is OK

=1: FAULT detected on digital output

DIGITAL OUTPUTS: OPEN WIRE DETECTION WHILE ON

GET DIGITAL OUTPUTS OPEN WIRE FAULT DETECTION WHILE ON	ASCII READ COMMAND	#GDOOWFONS<CR> Result: #GDOOWFONS:<StatusDOSDec>,<StatusDOSHex><CR>	ASCII	
	TX	#GDOOWFONS<CR>		
	RX	#1,GDOOWFONS:0,0x0<CR>		
		Current open wire fault detection status while ON of digital outputs:0000.0000.0000		
Returns the current state of the open wire fault detection while ON for all digital outputs as decimal number and as hexadecimal number. StatusDOSDec, StatusDOSHex The current detection state of the digital outputs: Bit 0: State of DO1 (=0:OFF, =1:ON) Bit 1: State of DO2 (=0:OFF, =1:ON) ... Bit 10: State of DO11 (=0:OFF, =1:ON) Bit 11: State of DO12 (=0:OFF, =1:ON)				
GET DIGITAL OUTPUT DOx OPEN WIRE FAULT DETECTION WHILE ON	ASCII READ COMMAND	#GDOOWFON<DONR><CR> Result: #GDOOWFON<DONR>:<StatusDOxDec>,<StatusDOxHex><CR>	ASCII	
	DONR	1		
	TX	#GDOOWFON1<CR>		
	RX	#1,GDOOWFON1:0,0x0<CR>		
		Current open wire fault detection status while ON of DO1:0=OFF		
Returns the current state of the open wire fault detection while ON for digital output DOx as decimal number and as hexadecimal number. StatusDOxDec, StatusDOxHex The current detection state for digital output DOx: =0: digital output is OK =1: FAULT detected on digital output				
DIGITAL OUTPUTS: OPEN WIRE DETECTION WHILE OFF				
GET DIGITAL OUTPUTS OPEN WIRE FAULT DETECTION WHILE OFF	ASCII READ COMMAND	#GDOOWFOFFS<CR> Result: #GDOOWFOFFS:<StatusDOSDec>,<StatusDOSHex><CR>	ASCII	
	TX	#GDOOWFOFFS<CR>		
	RX	#1,GDOOWFOFFS:0,0x0<CR>		
		Current open wire fault detection status while OFF of digital outputs:0000.0000.0000		
Returns the current state of the open wire fault detection while OFF for all digital outputs as decimal number and as hexadecimal number. StatusDOSDec, StatusDOSHex The current detection state of the digital outputs: Bit 0: State of DO1 (=0:OFF, =1:ON) Bit 1: State of DO2 (=0:OFF, =1:ON) ... Bit 10: State of DO11 (=0:OFF, =1:ON) Bit 11: State of DO12 (=0:OFF, =1:ON)				
GET DIGITAL OUTPUT DOx OPEN WIRE FAULT DETECTION WHILE OFF	ASCII READ COMMAND	#GDOOWFOFF<DONR><CR> Result: #GDOOWFOFF<DONR>:<StatusDOxDec>,<StatusDOxHex><CR>	ASCII	
	DONR	1		
	TX	#GDOOWFOFF1<CR>		
	RX	#1,GDOOWFOFF1:0,0x0<CR>		

Current open wire fault detection status while OFF of DO1:0=OFF				
Returns the current state of the open wire fault detection while OFF for digital output DOx as decimal number and as hexadecimal number. StatusDOxDec, StatusDOxHex The current detection state for digital output DOx: =0: digital output is OK =1: FAULT detected on digital output				
DIGITAL OUTPUTS: SHORTCUT TO VDD DETECTION WHILE OFF				
GET DIGITAL OUTPUTS SHORTCUT TO VDD FAULT DETECTION WHILE OFF	ASCII READ COMMAND	#GDOSVDDS<CR> Result: #GDOSVDDS:<StatusDOSDec>,<StatusDOSHex><CR>	ASCII	
	TX	#GDOSVDDS<CR>		
	RX	#1,GDOSVDDS:0,0x0<CR>		
Current shortcut to VDD fault detection status while OFF of digital outputs:0000.0000.0000				
Returns the current state of the shortcut to VDD fault detection while OFF for all digital outputs as decimal number and as hexadecimal number. StatusDOSDec, StatusDOSHex The current detection state of the digital outputs: Bit 0: State of DO1 (=0:OFF, =1:ON) Bit 1: State of DO2 (=0:OFF, =1:ON) ... Bit 10: State of DO11 (=0:OFF, =1:ON) Bit 11: State of DO12 (=0:OFF, =1:ON)				
GET DIGITAL OUTPUT DOx SHORTCUT TO VDD FAULT DETECTION WHILE OFF	ASCII READ COMMAND	#GDOSVDD<DONR><CR> Result: #GDOSVDD<DONR>:<StatusDOxDec>,<StatusDOxHex><CR>	ASCII	
	DONR	1		
	TX	#GDOSVDD1<CR>		
	RX	#1,GDOSVDD1:0,0x0<CR>		
Current shortcut to VDD fault detection status while OFF of DO1:0=OFF				
Returns the current state of the shortcut to VDD fault detection while OFF for digital output DOx as decimal number and as hexadecimal number. StatusDOxDec, StatusDOxHex The current detection state for digital output DOx: =0: digital output is OK =1: FAULT detected on digital output				
DIGITAL OUTPUTS: PULSE OUTPUT				
PULSE DOx	ASCII WRITE COMMAND	#PDO<DONR>:<Time><CR> Result: #OK<CR>	ASCII	NO
	DONR	1		
	TIME	200		
	TX	#PDO1:200<CR>		
	RX	N/A		
This command switches the digital output DOx on for the pulse duration <PulseTimeln100ms>*100ms. PulseTimeln100ms: A duration in 100ms units. The corresponding digital output is switched on for this time period.				
GET PULSE TIMER DOx	ASCII READ COMMAND	#GPT<DONR><CR> Result: #GPT:<TimeDec>,<TimeHex><CR>	ASCII	

	DONR	12		
	TX	#GPT12<CR>		
	RX	#1,GPT12:0,0x0<CR>		
		Current pulse time for DO12:0,0s		

Returns the remaining timer value of the pulse for digital output DOx in ms.

PulseTimeInMSDec, PulseTimeInMSHex

The remaining time of the pulse in Milliseconds

INITIAL & WATCHDOG STATE FOR DIGITAL OUTPUTS

SET INITIAL & WATCHDOG STATE FOR DIGITAL OUTPUTS	ASCII WRITE COMMAND	#SCDOS:<OutAllDOS><CR> Result: #OK<CR>	ASCII	NO
	DO1	0:OFF		
	DO2	0:OFF		
	DO3	0:OFF		
	DO4	0:OFF		
	DO5	0:OFF		
	DO6	0:OFF		
	DO7	0:OFF		
	DO8	0:OFF		
	DO9	0:OFF		
	DO10	0:OFF		
	DO11	0:OFF		
	DO12	0:OFF		
	TX	#SCDOS:0<CR>		
	RX	N/A		

Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FLASH.

This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured.

OutAllDOS

The new state for the digital outputs:

Bit 0: New state of DO1 (=0:OFF, =1:ON)

Bit 1: New state of DO2 (=0:OFF, =1:ON)

...

Bit 10: New state of DO11 (=0:OFF, =1:ON)

Bit 11: New state of DO12 (=0:OFF, =1:ON)

GET INITIAL & WATCHDOG STATE FOR DIGITAL OUTPUTS	ASCII READ COMMAND	#GDOS<CR> Result: #GDOS:<DOSDec>,<DOSHex><CR>	ASCII	
	TX	#GDOS<CR>		
	RX	#1,GDOS:4095,0xFFF<CR>		
		Current status of digital outputs:1111.1111.1111		

Returns the current initial and watchdog state of the digital outputs as decimal number and as hexadecimal number. This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured

DOSDec, DOSHex

The current state of the digital outputs:

Bit 0: State of DO1 (=0:OFF, =1:ON)

Bit 1: State of DO2 (=0:OFF, =1:ON)

...

Bit 10: State of DO11 (=0:OFF, =1:ON)

Bit 11: State of DO12 (=0:OFF, =1:ON)

DIGITAL OUTPUTS: INIT & WATCHDOG ENABLE OPEN WIRE DETECTION WHILE ON

SET CONFIG DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE ON	ASCII WRITE COMMAND	#SCDOEOWDONS:<OpenWireDOS> <CR> Result: #OK<CR>	ASCII	NO
	DO1	1:ENABLE		
	DO2	1:ENABLE		
	DO3	1:ENABLE		
	DO4	1:ENABLE		
	DO5	1:ENABLE		
	DO6	1:ENABLE		
	DO7	1:ENABLE		
	DO8	1:ENABLE		
	DO9	1:ENABLE		
	DO10	1:ENABLE		
	DO11	1:ENABLE		
	DO12	1:ENABLE		
	TX	#SCDOEOWDONS:4095<CR>		
	RX	N/A		

Sets the open wire mode for all digital outputs to the new mode OpenWireDOS for init & watchdog. This enables the diagnostic of open wire while the digital output is ON.

This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured

The new state for all digital outputs

Bit 0: New mode for DO1 (=0:DISABLED, =1:ENABLED)

Bit 1: New mode for DO2 (=0:DISABLED, =1:ENABLED)

...

Bit 10: New mode for DO11 (=0:DISABLED, =1:ENABLED)

Bit 11: New mode for DO12 (=0:DISABLED, =1:ENABLED)

GET CONFIG DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE ON	ASCII READ COMMAND	#GCDOEOWDONS<CR> Result: #GCDOEOWDONS:<OpenWireDOSDec>,<OpenWireDOSHex> <CR>	ASCII	
	TX	#GCDOEOWDONS<CR>		
	RX	#1,GCDOEOWDONS:0,0x0<CR>		
		Init & watchdog mode for open wire diagnostic while ON of digital outputs:0000.0000.0000		

Returns the current mode for open wire diagnostic while digital output is ON as decimal number and as hexadecimal number.

This values are used after power on of the module an after a watchdog event.

OpenWireDOSDec, OpenWireDOSHex

The current mode for open wire diagnostic while ON of the digital outputs:

Bit 0: Open wire diagnostic mode of DO1 (=0:DISABLED, =1:ENABLED)

Bit 1: Open wire diagnostic mode of DO2 (=0:DISABLED, =1:ENABLED)

...

Bit 10: Open wire diagnostic mode of DO11 (=0:DISABLED, =1:ENABLED)

Bit 11: Open wire diagnostic mode of DO12 (=0:DISABLED, =1:ENABLED)

DIGITAL OUTPUTS: INIT & WATCHDOG ENABLE OPEN WIRE DETECTION WHILE OFF

SET CONFIG DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE OFF	ASCII WRITE COMMAND	#SCDOEOWDOFFS:<OpenWireDOS> <CR> Result: #OK <CR>	ASCII	NO
	DO1	1:ENABLE		
	DO2	1:ENABLE		
	DO3	1:ENABLE		
	DO4	1:ENABLE		
	DO5	1:ENABLE		
	DO6	1:ENABLE		
	DO7	1:ENABLE		
	DO8	1:ENABLE		
	DO9	1:ENABLE		
	DO10	1:ENABLE		
	DO11	1:ENABLE		
	DO12	1:ENABLE		
	TX	#SCDOEOWDOFFS:4095 <CR>		
	RX	N/A		

Sets the open wire mode for all digital outputs to the new mode OpenWireDOS for init & watchdog. This enables the diagnostic of open wire while the digital output is OFF.

This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured

The new state for all digital outputs

Bit 0: New mode for DO1 (=0:DISABLED, =1:ENABLED)

Bit 1: New mode for DO2 (=0:DISABLED, =1:ENABLED)

...

Bit 10: New mode for DO11 (=0:DISABLED, =1:ENABLED)

Bit 11: New mode for DO12 (=0:DISABLED, =1:ENABLED)

GET CONFIG DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE OFF	ASCII READ COMMAND	#GCDOEOWDOFFS<CR> Result: #GCDOEOWDOFFS:<OpenWireDOSDec>, <OpenWireDOSHex> <CR>	ASCII	
	TX	#GCDOEOWDOFFS<CR>		
	RX	#1,GCDOEOWDOFFS:0,0x0<CR>		
		Current mode for open wire diagnostic while OFF of digital outputs:0000.0000.0000		

Returns the current mode for open wire diagnostic while digital output is OFF as decimal number and as hexadecimal number.

This values are used after power on of the module an after a watchdog event.

OpenWireDOSDec, OpenWireDOSHex

The current mode for open wire diagnostic while OFF of the digital outputs:

Bit 0: Open wire diagnostic mode of DO1 (=0:DISABLED, =1:ENABLED)

Bit 1: Open wire diagnostic mode of DO2 (=0:DISABLED, =1:ENABLED)

...

Bit 10: Open wire diagnostic mode of DO11 (=0:DISABLED, =1:ENABLED)

Bit 11: Open wire diagnostic mode of DO12 (=0:DISABLED, =1:ENABLED)

DIGITAL OUTPUTS: INIT & WATCHDOG ENABLE SHORTCUT TO VDD DETECTION WHILE OFF

SET CONFIG DIGITAL OUTPUTS ENABLE SHORTCUT TO VDD DETECTION WHILE OFF	ASCII WRITE COMMAND	#SCDOESVDDS:<ShortCutDOS> <CR> Result: #OK <CR>	ASCII	NO
	DO1	1:ENABLE		
	DO2	1:ENABLE		
	DO3	1:ENABLE		
	DO4	1:ENABLE		
	DO5	1:ENABLE		
	DO6	1:ENABLE		
	DO7	1:ENABLE		
	DO8	1:ENABLE		
	DO9	1:ENABLE		
	DO10	1:ENABLE		
	DO11	1:ENABLE		
	DO12	1:ENABLE		
	TX	#SCDOESVDDS:4095 <CR>		
	RX	N/A		

Sets the shortcut to VDD detection mode for all digital outputs to the new mode ShortCutDOS for init & watchdog. This enables the diagnostic of shortcut to VDD while the digital output is OFF.

This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured

The new state for all digital outputs

Bit 0: New mode for DO1 (=0:DISABLED, =1:ENABLED)

Bit 1: New mode for DO2 (=0:DISABLED, =1:ENABLED)

...

Bit 10: New mode for DO11 (=0:DISABLED, =1:ENABLED)

Bit 11: New mode for DO12 (=0:DISABLED, =1:ENABLED)

GET CONFIG DIGITAL OUTPUTS ENABLE SHORT CUT TO VDD DETECTION WHILE OFF	ASCII READ COMMAND	#GCDOESVDDS <CR> Result: #GCDOESDDS:<ShortCutDOSDec>, <ShortCutDOSHex> <CR>	ASCII	
	TX	#GCDOESVDDS <CR>		
	RX	#1,GCDOESVDDS:0,0x0 <CR>		
		Current mode for shortcut to VDD diagnostic while OFF of digital outputs:0000.0000.0000		

Returns the current mode for shortcut to VDD diagnostic while digital output is OFF as decimal number and as hexadecimal number.

This values are used after power on of the module an after a watchdog event.

ShortCutDOSDec, ShortCutDOSHex

The current mode for shortcut diagnostic while OFF of the digital outputs:

Bit 0: Open wire diagnostic mode of DO1 (=0:DISABLED, =1:ENABLED)

Bit 1: Open wire diagnostic mode of DO2 (=0:DISABLED, =1:ENABLED)

...

Bit 10: Open wire diagnostic mode of DO11 (=0:DISABLED, =1:ENABLED)

Bit 11: Open wire diagnostic mode of DO12 (=0:DISABLED, =1:ENABLED)

GET DIGITAL OUTPUTS CHIPSET NAME	ASCII READ COMMAND	#GDOCHIPSET <CR> Result: #GDOCHIPSET: <ChipSetName>	ASCII	
	TX	#GDOCHIPSET <CR>		
	RX	#1,GDOCHIPSET:MAX14915 <CR>		
		Current name of chipset for digital outputs:MAX14915		

Returns the current name of the chipset of the digital outputs