DI1	1x00001 2x00001 I:0	0,0x00 B:00			BIT R/O	
		Current state of DI1:0=OFF				
Current state of the digital input DIx =0:DI is OFF, =1:DI is ON						
DI2	1x00002 2x00002 I:1	0,0x00 B:00			BIT R/O	
		Current state of DI2:0=OFF				
DI3	1x00003 2x00003 I:2	0,0x00 B:00			BIT R/O	
		Current state of DI3:0=OFF				
DI4	1x00004 2x00004 I:3	0,0x00 B:00			BIT R/O	
		Current state of DI4:0=OFF				
DI5	1x00005 2x00005 I:4	0,0x00 B:00			BIT R/O	
		Current state of DI5:0=OFF				
DI6	1x00006 2x00006 I:5	0,0x00 B:00			BIT R/O	
		Current state of DI6:0=OFF				
DI7	1x00007 2x00007 I:6	0,0x00 B:00			BIT R/O	
		Current state of DI7:0=OFF				
DI8	1x00008 2x00008 I:7	0,0x00 B:00			BIT R/O	
		Current state of DI8:0=OFF				
DI9	1x00009 2x00009 I:8	0,0x00 B:00			BIT R/O	
		Current state of DI9:0=OFF				
DI10	1x00010 2x00010 I:9	0,0x00 B:00			BIT R/O	
		Current state of DI10:0=OFF				
DI11	1x00011 2x00011 I:10	0,0x00 B:00			BIT R/O	

		Current state of DI11:0=OFF				
DI12	1x00012 2x00012 I:11	0,0x00 B:00			BIT R/O	
		Current state of DI12:0=OFF				
DI13	1x00013 2x00013 I:12	0,0x00 B:00			BIT R/O	
		Current state of DI13:0=OFF				
DI14	1x00014 2x00014 I:13	0,0x00 B:00			BIT R/O	
		Current state of DI14:0=OFF				
DI15	1x00015 2x00015 I:14	0,0x00 B:00			BIT R/O	
		Current state of DI15:0=OFF				
DI16	1x00016 2x00016 I:15	0,0x00 B:00			BIT R/O	
		Current state of DI16:0=OFF				
DI17	1x00017 2x00017 I:16	0,0x00 B:00			BIT R/O	
		Current state of DI17:0=OFF				
DI18	1x00018 2x00018 I:17	0,0x00 B:00			BIT R/O	
		Current state of DI18:0=OFF				
DI19	1x00019 2x00019 I:18	0,0x00 B:00			BIT R/O	
		Current state of DI19:0=OFF				
DI20	1x00020 2x00020 I:19	0,0x00 B:00			BIT R/O	
		Current state of DI20:0=OFF				
DI21	1x00021 2x00021 I:20	0,0x00 B:00			BIT R/O	
		Current state of DI21:0=OFF				
DI22	1x00022 2x00022 I:21	0,0x00 B:00			BIT R/O	
		Current state of DI22:0=OFF				

DI23	1x00023 2x00023 I:22	0,0x00 B:00			BIT R/O	
		Current state of DI23:0=OFF				
DI24	1x00024 2x00024 I:23	0,0x00 B:00			BIT R/O	
		Current state of DI24:0=OFF				
DI25	1x00025 2x00025 I:24	0,0x00 B:00			BIT R/O	
		Current state of DI25:0=OFF				
DI26	1x00026 2x00026 I:25	0,0x00 B:00			BIT R/O	
		Current state of DI26:0=OFF				
DI27	1x00027 2x00027 I:26	0,0x00 B:00			BIT R/O	
		Current state of DI27:0=OFF				
DI28	1x00028 2x00028 I:27	0,0x00 B:00			BIT R/O	
		Current state of DI28:0=OFF				
DI29	1x00029 2x00029 I:28	0,0x00 B:00			BIT R/O	
		Current state of DI29:0=OFF				
DI30	1x00030 2x00030 I:29	0,0x00 B:00			BIT R/O	
		Current state of DI30:0=OFF				
DI31	1x00031 2x00031 I:30	0,0x00 B:00			BIT R/O	
		Current state of DI31:0=OFF				
DI32	1x00032 2x00032 I:31	0,0x00 B:00			BIT R/O	
		Current state of DI32:0=OFF				
DI33	1x00033 2x00033 I:32	0,0x00 B:00			BIT R/O	
		Current state of DI33:0=OFF				

DI34	1x00034 2x00034 I:33	0,0x00 B:00			BIT R/O	
		Current state of DI34:0=OFF				
DI35	1x00035 2x00035 I:34	0,0x00 B:00			BIT R/O	
		Current state of DI35:0=OFF				
DI36	1x00036 2x00036 I:35	0,0x00 B:00			BIT R/O	
		Current state of DI36:0=OFF				
DI37	1x00037 2x00037 I:36	0,0x00 B:00			BIT R/O	
		Current state of DI37:0=OFF				
DI38	1x00038 2x00038 I:37	0,0x00 B:00			BIT R/O	
		Current state of DI38:0=OFF				
DI39	1x00039 2x00039 I:38	0,0x00 B:00			BIT R/O	
		Current state of DI39:0=OFF				
DI40	1x00040 2x00040 I:39	0,0x00 B:00			BIT R/O	
		Current state of DI40:0=OFF				
DI41	1x00041 2x00041 I:40	0,0x00 B:00			BIT R/O	
		Current state of DI41:0=OFF				
DI42	1x00042 2x00042 I:41	0,0x00 B:00			BIT R/O	
		Current state of DI42:0=OFF				
DI43	1x00043 2x00043 I:42	0,0x00 B:00			BIT R/O	
		Current state of DI43:0=OFF				
DI44	1x00044 2x00044 I:43	0,0x00 B:00			BIT R/O	
		Current state of DI44:0=OFF				

DI45	1x00045 2x00045 I:44	0,0x00 B:00			BIT R/O	
		Current state of DI45:0=OFF				
DI46	1x00046 2x00046 I:45	0,0x00 B:00			BIT R/O	
		Current state of DI46:0=OFF				
DI47	1x00047 2x00047 I:46	0,0x00 B:00			BIT R/O	
		Current state of DI47:0=OFF				
DI48	1x00048 2x00048 I:47	0,0x00 B:00			BIT R/O	
		Current state of DI48:0=OFF				
STATUS						
DIP SWITCH 1	1x00091 2x00091 I:90	1,0x01 B:01			BIT R/O	
		Current state of DIP SWITCH1:1=ON				
Current state of DIP switch x =0:Dip switch is OFF, =1: Dip switch is ON						
DIP SWITCH 2	1x00092 2x00092 I:91	0,0x00 B:00			BIT R/O	
		Current state of DIP SWITCH2:0=OFF				
DIP SWITCH 3	1x00093 2x00093 I:92	0,0x00 B:00			BIT R/O	
		Current state of DIP SWITCH3:0=OFF				
DIP SWITCH 4	1x00094 2x00094 I:93	0,0x00 B:00			BIT R/O	
		Current state of DIP SWITCH4:0=OFF				
DIP SWITCH 5	1x00095 2x00095 I:94	0,0x00 B:00			BIT R/O	
		Current state of DIP SWITCH5:0=OFF				
DIP SWITCH 6	1x00096 2x00096 I:95	0,0x00 B:00			BIT R/O	
		Current state of DIP SWITCH6:0=OFF				

DIP SWITCH 7	1x00097 2x00097 I:96	0,0x00 B:00			BIT R/O	
		Current state of DIP SWITCH7:0=OFF				
DIP SWITCH 8	1x00098 2x00098 I:97	0,0x00 B:00			BIT R/O	
		Current state of DIP SWITCH8:0=OFF				
STATUS						
DI1	1x15001 2x15001 I:15000	0,0x00 B:00			BIT R/O	
		Current state of DI1:0=OFF				
Current state of the digital input DIx =0:DI is OFF, =1:DI is ON						
DI2	1x15002 2x15002 I:15001	0,0x00 B:00			BIT R/O	
		Current state of DI2:0=OFF				
DI3	1x15003 2x15003 I:15002	0,0x00 B:00			BIT R/O	
		Current state of DI3:0=OFF				
DI4	1x15004 2x15004 I:15003	0,0x00 B:00			BIT R/O	
		Current state of DI4:0=OFF				
DI5	1x15005 2x15005 I:15004	0,0x00 B:00			BIT R/O	
		Current state of DI5:0=OFF				
DI6	1x15006 2x15006 I:15005	0,0x00 B:00			BIT R/O	
		Current state of DI6:0=OFF				
DI7	1x15007 2x15007 I:15006	0,0x00 B:00			BIT R/O	
		Current state of DI7:0=OFF				
DI8	1x15008 2x15008 I:15007	0,0x00 B:00			BIT R/O	
		Current state of DI8:0=OFF				

DI9	1x15009 2x15009 I:15008	0,0x00 B:00			BIT R/O	
		Current state of DI9:0=OFF				
DI10	1x15010 2x15010 I:15009	0,0x00 B:00			BIT R/O	
		Current state of DI10:0=OFF				
DI11	1x15011 2x15011 I:15010	0,0x00 B:00			BIT R/O	
		Current state of DI11:0=OFF				
DI12	1x15012 2x15012 I:15011	0,0x00 B:00			BIT R/O	
		Current state of DI12:0=OFF				
DI13	1x15013 2x15013 I:15012	0,0x00 B:00			BIT R/O	
		Current state of DI13:0=OFF				
DI14	1x15014 2x15014 I:15013	0,0x00 B:00			BIT R/O	
		Current state of DI14:0=OFF				
DI15	1x15015 2x15015 I:15014	0,0x00 B:00			BIT R/O	
		Current state of DI15:0=OFF				
DI16	1x15016 2x15016 I:15015	0,0x00 B:00			BIT R/O	
		Current state of DI16:0=OFF				
DI17	1x15017 2x15017 I:15016	0,0x00 B:00			BIT R/O	
		Current state of DI17:0=OFF				
DI18	1x15018 2x15018 I:15017	0,0x00 B:00			BIT R/O	
		Current state of DI18:0=OFF				
DI19	1x15019 2x15019 I:15018	0,0x00 B:00			BIT R/O	
		Current state of DI19:0=OFF				

DI20	1x15020 2x15020 I:15019	0,0x00 B:00			BIT R/O	
		Current state of DI20:0=OFF				
DI21	1x15021 2x15021 I:15020	0,0x00 B:00			BIT R/O	
		Current state of DI21:0=OFF				
DI22	1x15022 2x15022 I:15021	0,0x00 B:00			BIT R/O	
		Current state of DI22:0=OFF				
DI23	1x15023 2x15023 I:15022	0,0x00 B:00			BIT R/O	
		Current state of DI23:0=OFF				
DI24	1x15024 2x15024 I:15023	0,0x00 B:00			BIT R/O	
		Current state of DI24:0=OFF				
DI25	1x15025 2x15025 I:15024	0,0x00 B:00			BIT R/O	
		Current state of DI25:0=OFF				
DI26	1x15026 2x15026 I:15025	0,0x00 B:00			BIT R/O	
		Current state of DI26:0=OFF				
DI27	1x15027 2x15027 I:15026	0,0x00 B:00			BIT R/O	
		Current state of DI27:0=OFF				
DI28	1x15028 2x15028 I:15027	0,0x00 B:00			BIT R/O	
		Current state of DI28:0=OFF				
DI29	1x15029 2x15029 I:15028	0,0x00 B:00			BIT R/O	
		Current state of DI29:0=OFF				
DI30	1x15030 2x15030 I:15029	0,0x00 B:00			BIT R/O	
		Current state of DI30:0=OFF				

DI31	1x15031 2x15031 I:15030	0,0x00 B:00			BIT R/O	
		Current state of DI31:0=OFF				
DI32	1x15032 2x15032 I:15031	0,0x00 B:00			BIT R/O	
		Current state of DI32:0=OFF				
DI33	1x15033 2x15033 I:15032	0,0x00 B:00			BIT R/O	
		Current state of DI33:0=OFF				
DI34	1x15034 2x15034 I:15033	0,0x00 B:00			BIT R/O	
		Current state of DI34:0=OFF				
DI35	1x15035 2x15035 I:15034	0,0x00 B:00			BIT R/O	
		Current state of DI35:0=OFF				
DI36	1x15036 2x15036 I:15035	0,0x00 B:00			BIT R/O	
		Current state of DI36:0=OFF				
DI37	1x15037 2x15037 I:15036	0,0x00 B:00			BIT R/O	
		Current state of DI37:0=OFF				
DI38	1x15038 2x15038 I:15037	0,0x00 B:00			BIT R/O	
		Current state of DI38:0=OFF				
DI39	1x15039 2x15039 I:15038	0,0x00 B:00			BIT R/O	
		Current state of DI39:0=OFF				
DI40	1x15040 2x15040 I:15039	0,0x00 B:00			BIT R/O	
		Current state of DI40:0=OFF				
DI41	1x15041 2x15041 I:15040	0,0x00 B:00			BIT R/O	
		Current state of DI41:0=OFF				

DI42	1x15042 2x15042 I:15041	0,0x00 B:00			BIT R/O	
		Current state of DI42:0=OFF				
DI43	1x15043 2x15043 I:15042	0,0x00 B:00			BIT R/O	
		Current state of DI43:0=OFF				
DI44	1x15044 2x15044 I:15043	0,0x00 B:00			BIT R/O	
		Current state of DI44:0=OFF				
DI45	1x15045 2x15045 I:15044	0,0x00 B:00			BIT R/O	
		Current state of DI45:0=OFF				
DI46	1x15046 2x15046 I:15045	0,0x00 B:00			BIT R/O	
		Current state of DI46:0=OFF				
DI47	1x15047 2x15047 I:15046	0,0x00 B:00			BIT R/O	
		Current state of DI47:0=OFF				
DI48	1x15048 2x15048 I:15047	0,0x00 B:00			BIT R/O	
		Current state of DI48:0=OFF				
STATUS						
REAL DI1	1x15049 2x15049 I:15048	0,0x00 B:00			BIT R/O	
		Current state of REAL DI1:0=OFF				
Current state of the REAL Digital input REAL DIx before AC filtering =0:REAL DI is OFF, =1:REAL DI is ON						
REAL DI2	1x15050 2x15050 I:15049	0,0x00 B:00			BIT R/O	
		Current state of REAL DI2:0=OFF				
REAL DI3	1x15051 2x15051 I:15050	0,0x00 B:00			BIT R/O	
		Current state of REAL DI3:0=OFF				

REAL DI4	1x15052 2x15052 I:15051	0,0x00 B:00			BIT R/O	
		Current state of REAL DI4:0=OFF				
REAL DI5	1x15053 2x15053 I:15052	0,0x00 B:00			BIT R/O	
		Current state of REAL DI5:0=OFF				
REAL DI6	1x15054 2x15054 I:15053	0,0x00 B:00			BIT R/O	
		Current state of REAL DI6:0=OFF				
REAL DI7	1x15055 2x15055 I:15054	0,0x00 B:00			BIT R/O	
		Current state of REAL DI7:0=OFF				
REAL DI8	1x15056 2x15056 I:15055	0,0x00 B:00			BIT R/O	
		Current state of REAL DI8:0=OFF				
REAL DI9	1x15057 2x15057 I:15056	0,0x00 B:00			BIT R/O	
		Current state of REAL DI9:0=OFF				
REAL DI10	1x15058 2x15058 I:15057	0,0x00 B:00			BIT R/O	
		Current state of REAL DI10:0=OFF				
REAL DI11	1x15059 2x15059 I:15058	0,0x00 B:00			BIT R/O	
		Current state of REAL DI11:0=OFF				
REAL DI12	1x15060 2x15060 I:15059	0,0x00 B:00			BIT R/O	
		Current state of REAL DI12:0=OFF				
REAL DI13	1x15061 2x15061 I:15060	0,0x00 B:00			BIT R/O	
		Current state of REAL DI13:0=OFF				
REAL DI14	1x15062 2x15062 I:15061	0,0x00 B:00			BIT R/O	
		Current state of REAL DI14:0=OFF				

REAL DI15	1x15063 2x15063 I:15062	0,0x00 B:00			BIT R/O	
		Current state of REAL DI15:0=OFF				
REAL DI16	1x15064 2x15064 I:15063	0,0x00 B:00			BIT R/O	
		Current state of REAL DI16:0=OFF				
REAL DI17	1x15065 2x15065 I:15064	0,0x00 B:00			BIT R/O	
		Current state of REAL DI17:0=OFF				
REAL DI18	1x15066 2x15066 I:15065	0,0x00 B:00			BIT R/O	
		Current state of REAL DI18:0=OFF				
REAL DI19	1x15067 2x15067 I:15066	0,0x00 B:00			BIT R/O	
		Current state of REAL DI19:0=OFF				
REAL DI20	1x15068 2x15068 I:15067	0,0x00 B:00			BIT R/O	
		Current state of REAL DI20:0=OFF				
REAL DI21	1x15069 2x15069 I:15068	0,0x00 B:00			BIT R/O	
		Current state of REAL DI21:0=OFF				
REAL DI22	1x15070 2x15070 I:15069	0,0x00 B:00			BIT R/O	
		Current state of REAL DI22:0=OFF				
REAL DI23	1x15071 2x15071 I:15070	0,0x00 B:00			BIT R/O	
		Current state of REAL DI23:0=OFF				
REAL DI24	1x15072 2x15072 I:15071	0,0x00 B:00			BIT R/O	
		Current state of REAL DI24:0=OFF				
REAL DI25	1x15073 2x15073 I:15072	0,0x00 B:00			BIT R/O	
		Current state of REAL DI25:0=OFF				

REAL DI26	1x15074 2x15074 I:15073	0,0x00 B:00			BIT R/O	
		Current state of REAL DI26:0=OFF				
REAL DI27	1x15075 2x15075 I:15074	0,0x00 B:00			BIT R/O	
		Current state of REAL DI27:0=OFF				
REAL DI28	1x15076 2x15076 I:15075	0,0x00 B:00			BIT R/O	
		Current state of REAL DI28:0=OFF				
REAL DI29	1x15077 2x15077 I:15076	0,0x00 B:00			BIT R/O	
		Current state of REAL DI29:0=OFF				
REAL DI30	1x15078 2x15078 I:15077	0,0x00 B:00			BIT R/O	
		Current state of REAL DI30:0=OFF				
REAL DI31	1x15079 2x15079 I:15078	0,0x00 B:00			BIT R/O	
		Current state of REAL DI31:0=OFF				
REAL DI32	1x15080 2x15080 I:15079	0,0x00 B:00			BIT R/O	
		Current state of REAL DI32:0=OFF				
REAL DI33	1x15081 2x15081 I:15080	0,0x00 B:00			BIT R/O	
		Current state of REAL DI33:0=OFF				
REAL DI34	1x15082 2x15082 I:15081	0,0x00 B:00			BIT R/O	
		Current state of REAL DI34:0=OFF				
REAL DI35	1x15083 2x15083 I:15082	0,0x00 B:00			BIT R/O	
		Current state of REAL DI35:0=OFF				
REAL DI36	1x15084 2x15084 I:15083	0,0x00 B:00			BIT R/O	
		Current state of REAL DI36:0=OFF				

REAL DI37	1x15085 2x15085 I:15084	0,0x00 B:00			BIT R/O	
		Current state of REAL DI37:0=OFF				
REAL DI38	1x15086 2x15086 I:15085	0,0x00 B:00			BIT R/O	
		Current state of REAL DI38:0=OFF				
REAL DI39	1x15087 2x15087 I:15086	0,0x00 B:00			BIT R/O	
		Current state of REAL DI39:0=OFF				
REAL DI40	1x15088 2x15088 I:15087	0,0x00 B:00			BIT R/O	
		Current state of REAL DI40:0=OFF				
REAL DI41	1x15089 2x15089 I:15088	0,0x00 B:00			BIT R/O	
		Current state of REAL DI41:0=OFF				
REAL DI42	1x15090 2x15090 I:15089	0,0x00 B:00			BIT R/O	
		Current state of REAL DI42:0=OFF				
REAL DI43	1x15091 2x15091 I:15090	0,0x00 B:00			BIT R/O	
		Current state of REAL DI43:0=OFF				
REAL DI44	1x15092 2x15092 I:15091	0,0x00 B:00			BIT R/O	
		Current state of REAL DI44:0=OFF				
REAL DI45	1x15093 2x15093 I:15092	0,0x00 B:00			BIT R/O	
		Current state of REAL DI45:0=OFF				
REAL DI46	1x15094 2x15094 I:15093	0,0x00 B:00			BIT R/O	
		Current state of REAL DI46:0=OFF				
REAL DI47	1x15095 2x15095 I:15094	0,0x00 B:00			BIT R/O	
		Current state of REAL DI47:0=OFF				

REAL DI48	1x15096 2x15096 I:15095	0,0x00 B:00			BIT R/O	
		Current state of REAL DI48:0=OFF				
STATUS						
DI1	3x00001 4x00001 I:0	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI1:0=OFF				
Current state of the digital input DIx =0:DI is OFF, =1:DI is ON						
DI2	3x00002 4x00002 I:1	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI2:0=OFF				
DI3	3x00003 4x00003 I:2	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI3:0=OFF				
DI4	3x00004 4x00004 I:3	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI4:0=OFF				
DI5	3x00005 4x00005 I:4	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI5:0=OFF				
DI6	3x00006 4x00006 I:5	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI6:0=OFF				
DI7	3x00007 4x00007 I:6	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI7:0=OFF				
DI8	3x00008 4x00008 I:7	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI8:0=OFF				
DI9	3x00009 4x00009 I:8	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI9:0=OFF				

DI10	3x00010 4x00010 I:9	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI10:0=OFF				
DI11	3x00011 4x00011 I:10	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI11:0=OFF				
DI12	3x00012 4x00012 I:11	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI12:0=OFF				
DI13	3x00013 4x00013 I:12	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI13:0=OFF				
DI14	3x00014 4x00014 I:13	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI14:0=OFF				
DI15	3x00015 4x00015 I:14	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI15:0=OFF				
DI16	3x00016 4x00016 I:15	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI16:0=OFF				
DI17	3x00017 4x00017 I:16	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI17:0=OFF				
DI18	3x00018 4x00018 I:17	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI18:0=OFF				
DI19	3x00019 4x00019 I:18	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI19:0=OFF				
DI20	3x00020 4x00020 I:19	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI20:0=OFF				

DI21	3x00021 4x00021 I:20	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI21:0=OFF				
DI22	3x00022 4x00022 I:21	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI22:0=OFF				
DI23	3x00023 4x00023 I:22	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI23:0=OFF				
DI24	3x00024 4x00024 I:23	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI24:0=OFF				
DI25	3x00025 4x00025 I:24	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI25:0=OFF				
DI26	3x00026 4x00026 I:25	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI26:0=OFF				
DI27	3x00027 4x00027 I:26	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI27:0=OFF				
DI28	3x00028 4x00028 I:27	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI28:0=OFF				
DI29	3x00029 4x00029 I:28	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI29:0=OFF				
DI30	3x00030 4x00030 I:29	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI30:0=OFF				
DI31	3x00031 4x00031 I:30	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI31:0=OFF				

DI32	3x00032 4x00032 I:31	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI32:0=OFF				
DI33	3x00033 4x00033 I:32	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI33:0=OFF				
DI34	3x00034 4x00034 I:33	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI34:0=OFF				
DI35	3x00035 4x00035 I:34	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI35:0=OFF				
DI36	3x00036 4x00036 I:35	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI36:0=OFF				
DI37	3x00037 4x00037 I:36	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI37:0=OFF				
DI38	3x00038 4x00038 I:37	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI38:0=OFF				
DI39	3x00039 4x00039 I:38	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI39:0=OFF				
DI40	3x00040 4x00040 I:39	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI40:0=OFF				
DI41	3x00041 4x00041 I:40	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI41:0=OFF				
DI42	3x00042 4x00042 I:41	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI42:0=OFF				

DI43	3x00043 4x00043 I:42	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI43:0=OFF				
DI44	3x00044 4x00044 I:43	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI44:0=OFF				
DI45	3x00045 4x00045 I:44	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI45:0=OFF				
DI46	3x00046 4x00046 I:45	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI46:0=OFF				
DI47	3x00047 4x00047 I:46	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI47:0=OFF				
DI48	3x00048 4x00048 I:47	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI48:0=OFF				
STATUS						
REAL DI1	3x00049 4x00049 I:48	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI1:0=OFF				
Current state of the REAL Digital input REAL DIx before AC filtering =0:REAL DI is OFF, =1:REAL DI is ON						
REAL DI2	3x00050 4x00050 I:49	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI2:0=OFF				
REAL DI3	3x00051 4x00051 I:50	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI3:0=OFF				
REAL DI4	3x00052 4x00052 I:51	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI4:0=OFF				

REAL DI5	3x00053 4x00053 I:52	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI5:0=OFF				
REAL DI6	3x00054 4x00054 I:53	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI6:0=OFF				
REAL DI7	3x00055 4x00055 I:54	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI7:0=OFF				
REAL DI8	3x00056 4x00056 I:55	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI8:0=OFF				
REAL DI9	3x00057 4x00057 I:56	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI9:0=OFF				
REAL DI10	3x00058 4x00058 I:57	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI10:0=OFF				
REAL DI11	3x00059 4x00059 I:58	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI11:0=OFF				
REAL DI12	3x00060 4x00060 I:59	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI12:0=OFF				
REAL DI13	3x00061 4x00061 I:60	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI13:0=OFF				
REAL DI14	3x00062 4x00062 I:61	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI14:0=OFF				
REAL DI15	3x00063 4x00063 I:62	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI15:0=OFF				

REAL DI16	3x00064 4x00064 I:63	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI16:0=OFF				
REAL DI17	3x00065 4x00065 I:64	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI17:0=OFF				
REAL DI18	3x00066 4x00066 I:65	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI18:0=OFF				
REAL DI19	3x00067 4x00067 I:66	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI19:0=OFF				
REAL DI20	3x00068 4x00068 I:67	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI20:0=OFF				
REAL DI21	3x00069 4x00069 I:68	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI21:0=OFF				
REAL DI22	3x00070 4x00070 I:69	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI22:0=OFF				
REAL DI23	3x00071 4x00071 I:70	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI23:0=OFF				
REAL DI24	3x00072 4x00072 I:71	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI24:0=OFF				
REAL DI25	3x00073 4x00073 I:72	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI25:0=OFF				
REAL DI26	3x00074 4x00074 I:73	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI26:0=OFF				

REAL DI27	3x00075 4x00075 I:74	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI27:0=OFF				
REAL DI28	3x00076 4x00076 I:75	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI28:0=OFF				
REAL DI29	3x00077 4x00077 I:76	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI29:0=OFF				
REAL DI30	3x00078 4x00078 I:77	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI30:0=OFF				
REAL DI31	3x00079 4x00079 I:78	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI31:0=OFF				
REAL DI32	3x00080 4x00080 I:79	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI32:0=OFF				
REAL DI33	3x00081 4x00081 I:80	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI33:0=OFF				
REAL DI34	3x00082 4x00082 I:81	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI34:0=OFF				
REAL DI35	3x00083 4x00083 I:82	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI35:0=OFF				
REAL DI36	3x00084 4x00084 I:83	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI36:0=OFF				
REAL DI37	3x00085 4x00085 I:84	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI37:0=OFF				

REAL DI38	3x00086 4x00086 I:85	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI38:0=OFF				
REAL DI39	3x00087 4x00087 I:86	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI39:0=OFF				
REAL DI40	3x00088 4x00088 I:87	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI40:0=OFF				
REAL DI41	3x00089 4x00089 I:88	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI41:0=OFF				
REAL DI42	3x00090 4x00090 I:89	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI42:0=OFF				
REAL DI43	3x00091 4x00091 I:90	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI43:0=OFF				
REAL DI44	3x00092 4x00092 I:91	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI44:0=OFF				
REAL DI45	3x00093 4x00093 I:92	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI45:0=OFF				
REAL DI46	3x00094 4x00094 I:93	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI46:0=OFF				
REAL DI47	3x00095 4x00095 I:94	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI47:0=OFF				
REAL DI48	3x00096 4x00096 I:95	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI48:0=OFF				
STATUS						

STATUS DI1 A	3x00097 4x00097 I:96	9517,0x252D B:25 2D			UINT16 R/O	
		DI:1,CC:13,REC:9,FEC:9				
Status for the digital input DIx Bit 0-4: Lower 5 bits of CHANGE COUNTER Bit 5-9: Lower 5 bits of RISING EDGE COUNTER Bit 10-14: Lower 5 bits of FALLING EDGE COUNTER Bit 15: Current Status of DIx =0: DIx si OFF, =1: DIx is ON						
STATUS DI1 B	3x00098 4x00098 I:97	4229,0x1085 B:10 85			UINT16 R/O	
		DI:0,SKE:5,LKSE:4,LKEE:4				
Status for the digital input DIx Bit 0-4: Lower 5 bits of SHORT KEYPRESS EVENTS Bit 5-9: Lower 5 bits of LONG KEYPRESS START EVENTS Bit 10-14: Lower 5 bits of LONG KEYPRESS END EVENTS Bit 15: Current Status of DIx =0: DIx si OFF, =1: DIx is ON						
STATUS DI2 A	3x00099 4x00099 I:98	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI2 B	3x00100 4x00100 I:99	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI3 A	3x00101 4x00101 I:100	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI3 B	3x00102 4x00102 I:101	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI4 A	3x00103 4x00103 I:102	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI4 B	3x00104 4x00104 I:103	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI5 A	3x00105 4x00105 I:104	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				

STATUS DI5 B	3x00106 4x00106 I:105	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI6 A	3x00107 4x00107 I:106	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI6 B	3x00108 4x00108 I:107	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI7 A	3x00109 4x00109 I:108	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI7 B	3x00110 4x00110 I:109	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI8 A	3x00111 4x00111 I:110	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI8 B	3x00112 4x00112 I:111	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI9 A	3x00113 4x00113 I:112	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI9 B	3x00114 4x00114 I:113	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI10 A	3x00115 4x00115 I:114	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI10 B	3x00116 4x00116 I:115	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				

STATUS DI11 A	3x00117 4x00117 I:116	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI11 B	3x00118 4x00118 I:117	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI12 A	3x00119 4x00119 I:118	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI12 B	3x00120 4x00120 I:119	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI13 A	3x00121 4x00121 I:120	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI13 B	3x00122 4x00122 I:121	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI14 A	3x00123 4x00123 I:122	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI14 B	3x00124 4x00124 I:123	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI15 A	3x00125 4x00125 I:124	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI15 B	3x00126 4x00126 I:125	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI16 A	3x00127 4x00127 I:126	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				

STATUS DI16 B	3x00128 4x00128 I:127	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI17 A	3x00129 4x00129 I:128	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI17 B	3x00130 4x00130 I:129	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI18 A	3x00131 4x00131 I:130	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI18 B	3x00132 4x00132 I:131	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI19 A	3x00133 4x00133 I:132	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI19 B	3x00134 4x00134 I:133	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI20 A	3x00135 4x00135 I:134	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI20 B	3x00136 4x00136 I:135	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI21 A	3x00137 4x00137 I:136	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI21 B	3x00138 4x00138 I:137	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				

STATUS DI22 A	3x00139 4x00139 I:138	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI22 B	3x00140 4x00140 I:139	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI23 A	3x00141 4x00141 I:140	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI23 B	3x00142 4x00142 I:141	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI24 A	3x00143 4x00143 I:142	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI24 B	3x00144 4x00144 I:143	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI25 A	3x00145 4x00145 I:144	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI25 B	3x00146 4x00146 I:145	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI26 A	3x00147 4x00147 I:146	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI26 B	3x00148 4x00148 I:147	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI27 A	3x00149 4x00149 I:148	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				

STATUS DI27 B	3x00150 4x00150 I:149	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI28 A	3x00151 4x00151 I:150	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI28 B	3x00152 4x00152 I:151	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI29 A	3x00153 4x00153 I:152	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI29 B	3x00154 4x00154 I:153	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI30 A	3x00155 4x00155 I:154	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI30 B	3x00156 4x00156 I:155	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI31 A	3x00157 4x00157 I:156	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI31 B	3x00158 4x00158 I:157	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI32 A	3x00159 4x00159 I:158	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI32 B	3x00160 4x00160 I:159	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				

STATUS DI33 A	3x00161 4x00161 I:160	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI33 B	3x00162 4x00162 I:161	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI34 A	3x00163 4x00163 I:162	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI34 B	3x00164 4x00164 I:163	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI35 A	3x00165 4x00165 I:164	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI35 B	3x00166 4x00166 I:165	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI36 A	3x00167 4x00167 I:166	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI36 B	3x00168 4x00168 I:167	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI37 A	3x00169 4x00169 I:168	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI37 B	3x00170 4x00170 I:169	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI38 A	3x00171 4x00171 I:170	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				

STATUS DI38 B	3x00172 4x00172 I:171	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI39 A	3x00173 4x00173 I:172	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI39 B	3x00174 4x00174 I:173	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI40 A	3x00175 4x00175 I:174	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI40 B	3x00176 4x00176 I:175	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI41 A	3x00177 4x00177 I:176	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI41 B	3x00178 4x00178 I:177	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI42 A	3x00179 4x00179 I:178	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI42 B	3x00180 4x00180 I:179	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI43 A	3x00181 4x00181 I:180	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI43 B	3x00182 4x00182 I:181	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				

STATUS DI44 A	3x00183 4x00183 I:182	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI44 B	3x00184 4x00184 I:183	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI45 A	3x00185 4x00185 I:184	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI45 B	3x00186 4x00186 I:185	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI46 A	3x00187 4x00187 I:186	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI46 B	3x00188 4x00188 I:187	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI47 A	3x00189 4x00189 I:188	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI47 B	3x00190 4x00190 I:189	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI48 A	3x00191 4x00191 I:190	3174,0x0C66 B:0C 66			UINT16 R/O	
		DI:1,CC:6,REC:3,FEC:3				
STATUS DI48 B	3x00192 4x00192 I:191	3168,0x0C60 B:0C 60			UINT16 R/O	
		DI:1,SKE:0,LKSE:3,LKEE:3				
STATUS						
FILTER PATTERN DI1	3x00193 4x00193 I:192	0,0x00000000 B:00 00 00 00			UINT32 R/O	
The internal pattern for corresponding digital input for AC/DC filtering. The internal used state is created out of this internal pattern via oversampling.						

FILTER PATTERN DI2	3x00195 4x00195 I:194	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI3	3x00197 4x00197 I:196	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI4	3x00199 4x00199 I:198	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI5	3x00201 4x00201 I:200	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI6	3x00203 4x00203 I:202	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI7	3x00205 4x00205 I:204	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI8	3x00207 4x00207 I:206	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI9	3x00209 4x00209 I:208	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI10	3x00211 4x00211 I:210	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI11	3x00213 4x00213 I:212	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI12	3x00215 4x00215 I:214	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI13	3x00217 4x00217 I:216	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI14	3x00219 4x00219 I:218	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI15	3x00221 4x00221 I:220	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI16	3x00223 4x00223 I:222	0,0x00000000 B:00 00 00 00			UINT32 R/O	

FILTER PATTERN DI17	3x00225 4x00225 I:224	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI18	3x00227 4x00227 I:226	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI19	3x00229 4x00229 I:228	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI20	3x00231 4x00231 I:230	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI21	3x00233 4x00233 I:232	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI22	3x00235 4x00235 I:234	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI23	3x00237 4x00237 I:236	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI24	3x00239 4x00239 I:238	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI25	3x00241 4x00241 I:240	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI26	3x00243 4x00243 I:242	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI27	3x00245 4x00245 I:244	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI28	3x00247 4x00247 I:246	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI29	3x00249 4x00249 I:248	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI30	3x00251 4x00251 I:250	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI31	3x00253 4x00253 I:252	0,0x00000000 B:00 00 00 00			UINT32 R/O	

FILTER PATTERN DI32	3x00255 4x00255 I:254	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI33	3x00257 4x00257 I:256	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI34	3x00259 4x00259 I:258	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI35	3x00261 4x00261 I:260	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI36	3x00263 4x00263 I:262	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI37	3x00265 4x00265 I:264	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI38	3x00267 4x00267 I:266	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI39	3x00269 4x00269 I:268	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI40	3x00271 4x00271 I:270	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI41	3x00273 4x00273 I:272	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI42	3x00275 4x00275 I:274	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI43	3x00277 4x00277 I:276	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI44	3x00279 4x00279 I:278	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI45	3x00281 4x00281 I:280	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI46	3x00283 4x00283 I:282	0,0x00000000 B:00 00 00 00			UINT32 R/O	

FILTER PATTERN DI47	3x00285 4x00285 I:284	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI48	3x00287 4x00287 I:286	4294901767,0xFFFF0007 B:FF FF 00 07			UINT32 R/O	
GENERAL STATUS OF DIS-ROS						
RESET COUNTERS	3x10000 4x10000 I:9999	0,0x0000 B:00 00		1:PERFORM RESET	UINT16 R/W	YES
If this register is written to 1, all internal edge counters and event counters are set to 0. 0 is always returned when reading.						
HAS DIS CHANGED	3x10001 4x10001 I:10000	19,0x0013 B:00 13			UINT16 R/O	
		19 event(s)				
As soon as the module registrates an event on one of the available digital inputs, this global event counter is incremented by 1. Possible events are: Detection of a short keypress Detection of the start of a long keypress Detection of the end of a long keypress						
STATUS OF ALL DIS DI1..DI16	3x10002 4x10002 I:10001	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI1:0=OFF				
		Current state of DI2:0=OFF				
		Current state of DI3:0=OFF				
		Current state of DI4:0=OFF				
		Current state of DI5:0=OFF				
		Current state of DI6:0=OFF				
		Current state of DI7:0=OFF				
		Current state of DI8:0=OFF				
		Current state of DI9:0=OFF				
		Current state of DI10:0=OFF				
		Current state of DI11:0=OFF				
		Current state of DI12:0=OFF				
		Current state of DI13:0=OFF				
		Current state of DI14:0=OFF				
		Current state of DI15:0=OFF				
		Current state of DI16:0=OFF				

Current state of all digital inputs DI1..DI16
Bit 0: =0:DI1 is OFF, =1:DI1 is ON
Bit 1: =0:DI2 is OFF, =1:DI2 is ON
Bit 2: =0:DI3 is OFF, =1:DI3 is ON
Bit 3: =0:DI4 is OFF, =1:DI4 is ON
Bit 4: =0:DI5 is OFF, =1:DI5 is ON
Bit 5: =0:DI6 is OFF, =1:DI6 is ON
Bit 6: =0:DI7 is OFF, =1:DI7 is ON
Bit 7: =0:DI8 is OFF, =1:DI8 is ON
Bit 8: =0:DI9 is OFF, =1:DI9 is ON
Bit 9: =0:DI10 is OFF, =1:DI10 is ON
Bit 10: =0:DI11 is OFF, =1:DI11 is ON
Bit 11: =0:DI12 is OFF, =1:DI12 is ON
Bit 12: =0:DI13 is OFF, =1:DI13 is ON
Bit 13: =0:DI14 is OFF, =1:DI14 is ON
Bit 14: =0:DI15 is OFF, =1:DI15 is ON
Bit 15: =0:DI16 is OFF, =1:DI16 is ON

STATUS OF ALL DIS DI17..DI32	3x10003 4x10003 I:10002	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI17:0=OFF				
		Current state of DI18:0=OFF				
		Current state of DI19:0=OFF				
		Current state of DI20:0=OFF				
		Current state of DI21:0=OFF				
		Current state of DI22:0=OFF				
		Current state of DI23:0=OFF				
		Current state of DI24:0=OFF				
		Current state of DI25:0=OFF				
		Current state of DI26:0=OFF				
		Current state of DI27:0=OFF				
		Current state of DI28:0=OFF				
		Current state of DI29:0=OFF				
		Current state of DI30:0=OFF				
		Current state of DI31:0=OFF				
		Current state of DI32:0=OFF				

Current state of all digital inputs DI17..DI32
Bit 0: =0:DI17 is OFF, =1:DI17 is ON
Bit 1: =0:DI18 is OFF, =1:DI18 is ON
Bit 2: =0:DI19 is OFF, =1:DI19 is ON
Bit 3: =0:DI20 is OFF, =1:DI20 is ON
Bit 4: =0:DI21 is OFF, =1:DI21 is ON
Bit 5: =0:DI22 is OFF, =1:DI22 is ON
Bit 6: =0:DI23 is OFF, =1:DI23 is ON
Bit 7: =0:DI24 is OFF, =1:DI24 is ON
Bit 8: =0:DI25 is OFF, =1:DI25 is ON
Bit 9: =0:DI26 is OFF, =1:DI26 is ON
Bit 10: =0:DI27 is OFF, =1:DI27 is ON
Bit 11: =0:DI28 is OFF, =1:DI28 is ON
Bit 12: =0:DI29 is OFF, =1:DI29 is ON
Bit 13: =0:DI30 is OFF, =1:DI30 is ON
Bit 14: =0:DI31 is OFF, =1:DI31 is ON
Bit 15: =0:DI32 is OFF, =1:DI32 is ON

STATUS OF ALL DIS DI33..DI48	3x10004 4x10004 I:10003	32768,0x8000 B:80 00			UINT16 R/O	
		Current state of DI33:0=OFF				
		Current state of DI34:0=OFF				
		Current state of DI35:0=OFF				
		Current state of DI36:0=OFF				
		Current state of DI37:0=OFF				
		Current state of DI38:0=OFF				
		Current state of DI39:0=OFF				
		Current state of DI40:0=OFF				
		Current state of DI41:0=OFF				
		Current state of DI42:0=OFF				
		Current state of DI43:0=OFF				
		Current state of DI44:0=OFF				
		Current state of DI45:0=OFF				
		Current state of DI46:0=OFF				
		Current state of DI47:0=OFF				
		Current state of DI48:1=ON				

Current state of all digital inputs DI33..DI48
Bit 0: =0:DI33 is OFF, =1:DI33 is ON
Bit 1: =0:DI34 is OFF, =1:DI34 is ON
Bit 2: =0:DI35 is OFF, =1:DI35 is ON
Bit 3: =0:DI36 is OFF, =1:DI36 is ON
Bit 4: =0:DI37 is OFF, =1:DI37 is ON
Bit 5: =0:DI38 is OFF, =1:DI38 is ON
Bit 6: =0:DI39 is OFF, =1:DI39 is ON
Bit 7: =0:DI40 is OFF, =1:DI40 is ON
Bit 8: =0:DI41 is OFF, =1:DI41 is ON
Bit 9: =0:DI42 is OFF, =1:DI42 is ON
Bit 10: =0:DI43 is OFF, =1:DI43 is ON
Bit 11: =0:DI44 is OFF, =1:DI44 is ON
Bit 12: =0:DI45 is OFF, =1:DI45 is ON
Bit 13: =0:DI46 is OFF, =1:DI46 is ON
Bit 14: =0:DI47 is OFF, =1:DI47 is ON
Bit 15: =0:DI48 is OFF, =1:DI48 is ON

STATUS OF DIP SWITCH	3x10010 4x10010 I:10009	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DIP SWITCH1:0=OFF				
		Current state of DIP SWITCH2:0=OFF				
		Current state of DIP SWITCH3:0=OFF				
		Current state of DIP SWITCH4:0=OFF				
		Current state of DIP SWITCH5:0=OFF				
		Current state of DIP SWITCH6:0=OFF				
		Current state of DIP SWITCH7:0=OFF				
		Current state of DIP SWITCH8:0=OFF				

Current state of the DIP switch
Bit 0: DIP switch 1 (=0:OFF, =1:ON)
Bit 1: DIP switch 2 (=0:OFF, =1:ON)
Bit 2: DIP switch 3 (=0:OFF, =1:ON)
Bit 3: DIP switch 4 (=0:OFF, =1:ON)
Bit 4: DIP switch 5 (=0:OFF, =1:ON)
Bit 5: DIP switch 6 (=0:OFF, =1:ON)
Bit 6: DIP switch 7 (=0:OFF, =1:ON)
Bit 7: DIP switch 8 (=0:OFF, =1:ON)

STATUS OF ALL DIS DI1..DI16 BEFORE AC FILTERING	3x10101 4x10101 I:10100	0,0x0000 B:00 00			UINT16 R/O	
		Current state without AC filter of DI1:0=OFF				
		Current state without AC filter of DI2:0=OFF				
		Current state without AC filter of DI3:0=OFF				
		Current state without AC filter of DI4:0=OFF				
		Current state without AC filter of DI5:0=OFF				
		Current state without AC filter of DI6:0=OFF				
		Current state without AC filter of DI7:0=OFF				
		Current state without AC filter of DI8:0=OFF				

		Current state without AC filter of DI9:0=OFF			
		Current state without AC filter of DI10:0=OFF			
		Current state without AC filter of DI11:0=OFF			
		Current state without AC filter of DI12:0=OFF			
		Current state without AC filter of DI13:0=OFF			
		Current state without AC filter of DI14:0=OFF			
		Current state without AC filter of DI15:0=OFF			
		Current state without AC filter of DI16:0=OFF			
Current state of all digital inputs DI1..DI16 before AC filtering Bit 0: =0:DI1 is OFF, =1:DI1 is ON Bit 1: =0:DI2 is OFF, =1:DI2 is ON Bit 2: =0:DI3 is OFF, =1:DI3 is ON Bit 3: =0:DI4 is OFF, =1:DI4 is ON Bit 4: =0:DI5 is OFF, =1:DI5 is ON Bit 5: =0:DI6 is OFF, =1:DI6 is ON Bit 6: =0:DI7 is OFF, =1:DI7 is ON Bit 7: =0:DI8 is OFF, =1:DI8 is ON Bit 8: =0:DI9 is OFF, =1:DI9 is ON Bit 9: =0:DI10 is OFF, =1:DI10 is ON Bit 10: =0:DI11 is OFF, =1:DI11 is ON Bit 11: =0:DI12 is OFF, =1:DI12 is ON Bit 12: =0:DI13 is OFF, =1:DI13 is ON Bit 13: =0:DI14 is OFF, =1:DI14 is ON Bit 14: =0:DI15 is OFF, =1:DI15 is ON Bit 15: =0:DI16 is OFF, =1:DI16 is ON					
STATUS OF ALL DIS DI17..DI32 BEFORE AC FILTERING	3x10102 4x10102 I:10101	0,0x0000 B:00 00			UINT16 R/O
		Current state without AC filter of DI17:0=OFF			
		Current state without AC filter of DI18:0=OFF			
		Current state without AC filter of DI19:0=OFF			
		Current state without AC filter of DI20:0=OFF			
		Current state without AC filter of DI21:0=OFF			
		Current state without AC filter of DI22:0=OFF			
		Current state without AC filter of DI23:0=OFF			
		Current state without AC filter of DI24:0=OFF			
		Current state without AC filter of DI25:0=OFF			
		Current state without AC filter of DI26:0=OFF			
		Current state without AC filter of DI27:0=OFF			
		Current state without AC filter of DI28:0=OFF			
		Current state without AC filter of DI29:0=OFF			
		Current state without AC filter of DI30:0=OFF			
		Current state without AC filter of DI31:0=OFF			
		Current state without AC filter of DI32:0=OFF			

Current state of all digital inputs DI17..DI32 before AC filtering
Bit 0: =0:DI17 is OFF, =1:DI17 is ON
Bit 1: =0:DI18 is OFF, =1:DI18 is ON
Bit 2: =0:DI19 is OFF, =1:DI19 is ON
Bit 3: =0:DI20 is OFF, =1:DI20 is ON
Bit 4: =0:DI21 is OFF, =1:DI21 is ON
Bit 5: =0:DI22 is OFF, =1:DI22 is ON
Bit 6: =0:DI23 is OFF, =1:DI23 is ON
Bit 7: =0:DI24 is OFF, =1:DI24 is ON
Bit 8: =0:DI25 is OFF, =1:DI25 is ON
Bit 9: =0:DI26 is OFF, =1:DI26 is ON
Bit 10: =0:DI27 is OFF, =1:DI27 is ON
Bit 11: =0:DI28 is OFF, =1:DI28 is ON
Bit 12: =0:DI29 is OFF, =1:DI29 is ON
Bit 13: =0:DI30 is OFF, =1:DI30 is ON
Bit 14: =0:DI31 is OFF, =1:DI31 is ON
Bit 15: =0:DI32 is OFF, =1:DI32 is ON

STATUS OF ALL DIS DI33..DI48 BEFORE AC FILTERING	3x10103 4x10103 I:10102	0,0x0000 B:00 00			UINT16 R/O	
		Current state without AC filter of DI33:0=OFF				
		Current state without AC filter of DI34:0=OFF				
		Current state without AC filter of DI35:0=OFF				
		Current state without AC filter of DI36:0=OFF				
		Current state without AC filter of DI37:0=OFF				
		Current state without AC filter of DI38:0=OFF				
		Current state without AC filter of DI39:0=OFF				
		Current state without AC filter of DI40:0=OFF				
		Current state without AC filter of DI41:0=OFF				
		Current state without AC filter of DI42:0=OFF				
		Current state without AC filter of DI43:0=OFF				
		Current state without AC filter of DI44:0=OFF				
		Current state without AC filter of DI45:0=OFF				
		Current state without AC filter of DI46:0=OFF				
		Current state without AC filter of DI47:0=OFF				
		Current state without AC filter of DI48:0=OFF				

Current state of all digital inputs DI33..DI48 before AC filtering Bit 0: =0:DI33 is OFF, =1:DI33 is ON Bit 1: =0:DI34 is OFF, =1:DI34 is ON Bit 2: =0:DI35 is OFF, =1:DI35 is ON Bit 3: =0:DI36 is OFF, =1:DI36 is ON Bit 4: =0:DI37 is OFF, =1:DI37 is ON Bit 5: =0:DI38 is OFF, =1:DI38 is ON Bit 6: =0:DI39 is OFF, =1:DI39 is ON Bit 7: =0:DI40 is OFF, =1:DI40 is ON Bit 8: =0:DI41 is OFF, =1:DI41 is ON Bit 9: =0:DI42 is OFF, =1:DI42 is ON Bit 10: =0:DI43 is OFF, =1:DI43 is ON Bit 11: =0:DI44 is OFF, =1:DI44 is ON Bit 12: =0:DI45 is OFF, =1:DI45 is ON Bit 13: =0:DI46 is OFF, =1:DI46 is ON Bit 14: =0:DI47 is OFF, =1:DI47 is ON Bit 15: =0:DI48 is OFF, =1:DI48 is ON						
STATUS						
DI1	3x15001 4x15001 I:15000	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI1:0=OFF				
Current state of the digital input DIx =0:DI is OFF, =1:DI is ON						
DI2	3x15002 4x15002 I:15001	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI2:0=OFF				
DI3	3x15003 4x15003 I:15002	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI3:0=OFF				
DI4	3x15004 4x15004 I:15003	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI4:0=OFF				
DI5	3x15005 4x15005 I:15004	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI5:0=OFF				
DI6	3x15006 4x15006 I:15005	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI6:0=OFF				
DI7	3x15007 4x15007 I:15006	0,0x0000 B:00 00			UINT16 R/O	

		Current state of DI7:0=OFF				
DI8	3x15008 4x15008 I:15007	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI8:0=OFF				
DI9	3x15009 4x15009 I:15008	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI9:0=OFF				
DI10	3x15010 4x15010 I:15009	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI10:0=OFF				
DI11	3x15011 4x15011 I:15010	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI11:0=OFF				
DI12	3x15012 4x15012 I:15011	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI12:0=OFF				
DI13	3x15013 4x15013 I:15012	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI13:0=OFF				
DI14	3x15014 4x15014 I:15013	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI14:0=OFF				
DI15	3x15015 4x15015 I:15014	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI15:0=OFF				
DI16	3x15016 4x15016 I:15015	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI16:0=OFF				
DI17	3x15017 4x15017 I:15016	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI17:0=OFF				
DI18	3x15018 4x15018 I:15017	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI18:0=OFF				

DI19	3x15019 4x15019 I:15018	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI19:0=OFF				
DI20	3x15020 4x15020 I:15019	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI20:0=OFF				
DI21	3x15021 4x15021 I:15020	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI21:0=OFF				
DI22	3x15022 4x15022 I:15021	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI22:0=OFF				
DI23	3x15023 4x15023 I:15022	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI23:0=OFF				
DI24	3x15024 4x15024 I:15023	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI24:0=OFF				
DI25	3x15025 4x15025 I:15024	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI25:0=OFF				
DI26	3x15026 4x15026 I:15025	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI26:0=OFF				
DI27	3x15027 4x15027 I:15026	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI27:0=OFF				
DI28	3x15028 4x15028 I:15027	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI28:0=OFF				
DI29	3x15029 4x15029 I:15028	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI29:0=OFF				

DI30	3x15030 4x15030 I:15029	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI30:0=OFF				
DI31	3x15031 4x15031 I:15030	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI31:0=OFF				
DI32	3x15032 4x15032 I:15031	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI32:0=OFF				
DI33	3x15033 4x15033 I:15032	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI33:0=OFF				
DI34	3x15034 4x15034 I:15033	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI34:0=OFF				
DI35	3x15035 4x15035 I:15034	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI35:0=OFF				
DI36	3x15036 4x15036 I:15035	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI36:0=OFF				
DI37	3x15037 4x15037 I:15036	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI37:0=OFF				
DI38	3x15038 4x15038 I:15037	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI38:0=OFF				
DI39	3x15039 4x15039 I:15038	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI39:0=OFF				
DI40	3x15040 4x15040 I:15039	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI40:0=OFF				

DI41	3x15041 4x15041 I:15040	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI41:0=OFF				
DI42	3x15042 4x15042 I:15041	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI42:0=OFF				
DI43	3x15043 4x15043 I:15042	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI43:0=OFF				
DI44	3x15044 4x15044 I:15043	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI44:0=OFF				
DI45	3x15045 4x15045 I:15044	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI45:0=OFF				
DI46	3x15046 4x15046 I:15045	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI46:0=OFF				
DI47	3x15047 4x15047 I:15046	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI47:0=OFF				
DI48	3x15048 4x15048 I:15047	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI48:0=OFF				
STATUS						
REAL DI1	3x15049 4x15049 I:15048	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI1:0=OFF				
Current state of the REAL Digital input REAL DIx before AC filtering =0:REAL DI is OFF, =1:REAL DI is ON						
REAL DI2	3x15050 4x15050 I:15049	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI2:0=OFF				

REAL DI3	3x15051 4x15051 I:15050	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI3:0=OFF				
REAL DI4	3x15052 4x15052 I:15051	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI4:0=OFF				
REAL DI5	3x15053 4x15053 I:15052	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI5:0=OFF				
REAL DI6	3x15054 4x15054 I:15053	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI6:0=OFF				
REAL DI7	3x15055 4x15055 I:15054	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI7:0=OFF				
REAL DI8	3x15056 4x15056 I:15055	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI8:0=OFF				
REAL DI9	3x15057 4x15057 I:15056	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI9:0=OFF				
REAL DI10	3x15058 4x15058 I:15057	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI10:0=OFF				
REAL DI11	3x15059 4x15059 I:15058	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI11:0=OFF				
REAL DI12	3x15060 4x15060 I:15059	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI12:0=OFF				
REAL DI13	3x15061 4x15061 I:15060	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI13:0=OFF				

REAL DI14	3x15062 4x15062 I:15061	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI14:0=OFF				
REAL DI15	3x15063 4x15063 I:15062	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI15:0=OFF				
REAL DI16	3x15064 4x15064 I:15063	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI16:0=OFF				
REAL DI17	3x15065 4x15065 I:15064	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI17:0=OFF				
REAL DI18	3x15066 4x15066 I:15065	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI18:0=OFF				
REAL DI19	3x15067 4x15067 I:15066	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI19:0=OFF				
REAL DI20	3x15068 4x15068 I:15067	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI20:0=OFF				
REAL DI21	3x15069 4x15069 I:15068	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI21:0=OFF				
REAL DI22	3x15070 4x15070 I:15069	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI22:0=OFF				
REAL DI23	3x15071 4x15071 I:15070	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI23:0=OFF				
REAL DI24	3x15072 4x15072 I:15071	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI24:0=OFF				

REAL DI25	3x15073 4x15073 I:15072	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI25:0=OFF				
REAL DI26	3x15074 4x15074 I:15073	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI26:0=OFF				
REAL DI27	3x15075 4x15075 I:15074	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI27:0=OFF				
REAL DI28	3x15076 4x15076 I:15075	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI28:0=OFF				
REAL DI29	3x15077 4x15077 I:15076	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI29:0=OFF				
REAL DI30	3x15078 4x15078 I:15077	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI30:0=OFF				
REAL DI31	3x15079 4x15079 I:15078	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI31:0=OFF				
REAL DI32	3x15080 4x15080 I:15079	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI32:0=OFF				
REAL DI33	3x15081 4x15081 I:15080	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI33:0=OFF				
REAL DI34	3x15082 4x15082 I:15081	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI34:0=OFF				
REAL DI35	3x15083 4x15083 I:15082	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI35:0=OFF				

REAL DI36	3x15084 4x15084 I:15083	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI36:0=OFF				
REAL DI37	3x15085 4x15085 I:15084	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI37:0=OFF				
REAL DI38	3x15086 4x15086 I:15085	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI38:0=OFF				
REAL DI39	3x15087 4x15087 I:15086	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI39:0=OFF				
REAL DI40	3x15088 4x15088 I:15087	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI40:0=OFF				
REAL DI41	3x15089 4x15089 I:15088	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI41:0=OFF				
REAL DI42	3x15090 4x15090 I:15089	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI42:0=OFF				
REAL DI43	3x15091 4x15091 I:15090	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI43:0=OFF				
REAL DI44	3x15092 4x15092 I:15091	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI44:0=OFF				
REAL DI45	3x15093 4x15093 I:15092	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI45:0=OFF				
REAL DI46	3x15094 4x15094 I:15093	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI46:0=OFF				

REAL DI47	3x15095 4x15095 I:15094	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI47:0=OFF				
REAL DI48	3x15096 4x15096 I:15095	0,0x0000 B:00 00			UINT16 R/O	
		Current state of REAL DI48:0=OFF				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI1						
RISE DI1	3x30001 4x30001 I:30000	9,0x0009 B:00 09			UINT16 R/O	
		9 event(s)				
Counter for rising edges on the digital input DIx. If the module detects a rising edge on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.						
FALL DI1	3x30002 4x30002 I:30001	9,0x0009 B:00 09			UINT16 R/O	
		9 event(s)				
Counter for falling edges on the digital input DIx. If the module detects a falling edge on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.						
CHANGE DI1	3x30003 4x30003 I:30002	13,0x000D B:00 0D			UINT16 R/O	
		13 event(s)				
Counter for events on the digital input DIx. If the module detects an event on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0. The following events are available: Detecion of a short keypress Detection of the start of a long keypress Detection of the end of a long keypress						
SHORT KEYPRESS DI1	3x30004 4x30004 I:30003	5,0x0005 B:00 05			UINT16 R/O	
		5 event(s)				
Counter for short keypress events on the digital input DIx. If the module detects a short keypress on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.						
LONG KEYPRESS START DI1	3x30005 4x30005 I:30004	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
Counter for start events of long keypress actions on the digital input DIx. If the module detects the start of a long keypress action on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.						

LONG KEYPRESS END DI1	3x30006 4x30006 I:30005	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
Counter for end events of long keypress actions on the digital input DIx. If the module detects the end of a long keypress action on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.						
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI2						
RISE DI2	3x30011 4x30011 I:30010	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI2	3x30012 4x30012 I:30011	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI2	3x30013 4x30013 I:30012	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI2	3x30014 4x30014 I:30013	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI2	3x30015 4x30015 I:30014	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI2	3x30016 4x30016 I:30015	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI3						
RISE DI3	3x30021 4x30021 I:30020	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI3	3x30022 4x30022 I:30021	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI3	3x30023 4x30023 I:30022	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

SHORT KEYPRESS DI3	3x30024 4x30024 I:30023	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI3	3x30025 4x30025 I:30024	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI3	3x30026 4x30026 I:30025	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI4						
RISE DI4	3x30031 4x30031 I:30030	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI4	3x30032 4x30032 I:30031	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI4	3x30033 4x30033 I:30032	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI4	3x30034 4x30034 I:30033	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI4	3x30035 4x30035 I:30034	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI4	3x30036 4x30036 I:30035	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI5						
RISE DI5	3x30041 4x30041 I:30040	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI5	3x30042 4x30042 I:30041	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)				
CHANGE DI5	3x30043 4x30043 I:30042	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI5	3x30044 4x30044 I:30043	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI5	3x30045 4x30045 I:30044	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI5	3x30046 4x30046 I:30045	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI6						
RISE DI6	3x30051 4x30051 I:30050	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI6	3x30052 4x30052 I:30051	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI6	3x30053 4x30053 I:30052	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI6	3x30054 4x30054 I:30053	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI6	3x30055 4x30055 I:30054	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI6	3x30056 4x30056 I:30055	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI7						

RISE DI7	3x30061 4x30061 I:30060	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI7	3x30062 4x30062 I:30061	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI7	3x30063 4x30063 I:30062	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI7	3x30064 4x30064 I:30063	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI7	3x30065 4x30065 I:30064	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI7	3x30066 4x30066 I:30065	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI8						
RISE DI8	3x30071 4x30071 I:30070	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI8	3x30072 4x30072 I:30071	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI8	3x30073 4x30073 I:30072	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI8	3x30074 4x30074 I:30073	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI8	3x30075 4x30075 I:30074	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

LONG KEYPRESS END DI8	3x30076 4x30076 I:30075	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI9						
RISE DI9	3x30081 4x30081 I:30080	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI9	3x30082 4x30082 I:30081	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI9	3x30083 4x30083 I:30082	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI9	3x30084 4x30084 I:30083	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI9	3x30085 4x30085 I:30084	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI9	3x30086 4x30086 I:30085	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI10						
RISE DI10	3x30091 4x30091 I:30090	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI10	3x30092 4x30092 I:30091	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI10	3x30093 4x30093 I:30092	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI10	3x30094 4x30094 I:30093	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)			
LONG KEYPRESS START DI10	3x30095 4x30095 I:30094	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
LONG KEYPRESS END DI10	3x30096 4x30096 I:30095	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI11					
RISE DI11	3x30101 4x30101 I:30100	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
FALL DI11	3x30102 4x30102 I:30101	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
CHANGE DI11	3x30103 4x30103 I:30102	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
SHORT KEYPRESS DI11	3x30104 4x30104 I:30103	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
LONG KEYPRESS START DI11	3x30105 4x30105 I:30104	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
LONG KEYPRESS END DI11	3x30106 4x30106 I:30105	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI12					
RISE DI12	3x30111 4x30111 I:30110	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
FALL DI12	3x30112 4x30112 I:30111	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			

CHANGE DI12	3x30113 4x30113 I:30112	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI12	3x30114 4x30114 I:30113	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI12	3x30115 4x30115 I:30114	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI12	3x30116 4x30116 I:30115	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI13						
RISE DI13	3x30121 4x30121 I:30120	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI13	3x30122 4x30122 I:30121	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI13	3x30123 4x30123 I:30122	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI13	3x30124 4x30124 I:30123	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI13	3x30125 4x30125 I:30124	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI13	3x30126 4x30126 I:30125	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI14						
RISE DI14	3x30131 4x30131 I:30130	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)				
FALL DI14	3x30132 4x30132 I:30131	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI14	3x30133 4x30133 I:30132	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI14	3x30134 4x30134 I:30133	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI14	3x30135 4x30135 I:30134	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI14	3x30136 4x30136 I:30135	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI15						
RISE DI15	3x30141 4x30141 I:30140	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI15	3x30142 4x30142 I:30141	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI15	3x30143 4x30143 I:30142	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI15	3x30144 4x30144 I:30143	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI15	3x30145 4x30145 I:30144	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI15	3x30146 4x30146 I:30145	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI16						
RISE DI16	3x30151 4x30151 I:30150	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI16	3x30152 4x30152 I:30151	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI16	3x30153 4x30153 I:30152	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI16	3x30154 4x30154 I:30153	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI16	3x30155 4x30155 I:30154	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI16	3x30156 4x30156 I:30155	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI17						
RISE DI17	3x30161 4x30161 I:30160	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI17	3x30162 4x30162 I:30161	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI17	3x30163 4x30163 I:30162	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI17	3x30164 4x30164 I:30163	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

LONG KEYPRESS START DI17	3x30165 4x30165 I:30164	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI17	3x30166 4x30166 I:30165	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI18						
RISE DI18	3x30171 4x30171 I:30170	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI18	3x30172 4x30172 I:30171	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI18	3x30173 4x30173 I:30172	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI18	3x30174 4x30174 I:30173	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI18	3x30175 4x30175 I:30174	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI18	3x30176 4x30176 I:30175	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI19						
RISE DI19	3x30181 4x30181 I:30180	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI19	3x30182 4x30182 I:30181	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI19	3x30183 4x30183 I:30182	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)				
SHORT KEYPRESS DI19	3x30184 4x30184 I:30183	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI19	3x30185 4x30185 I:30184	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI19	3x30186 4x30186 I:30185	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI20						
RISE DI20	3x30191 4x30191 I:30190	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI20	3x30192 4x30192 I:30191	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI20	3x30193 4x30193 I:30192	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI20	3x30194 4x30194 I:30193	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI20	3x30195 4x30195 I:30194	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI20	3x30196 4x30196 I:30195	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI21						
RISE DI21	3x30201 4x30201 I:30200	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

FALL DI21	3x30202 4x30202 I:30201	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI21	3x30203 4x30203 I:30202	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI21	3x30204 4x30204 I:30203	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI21	3x30205 4x30205 I:30204	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI21	3x30206 4x30206 I:30205	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI22						
RISE DI22	3x30211 4x30211 I:30210	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI22	3x30212 4x30212 I:30211	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI22	3x30213 4x30213 I:30212	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI22	3x30214 4x30214 I:30213	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI22	3x30215 4x30215 I:30214	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI22	3x30216 4x30216 I:30215	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI23						
RISE DI23	3x30221 4x30221 I:30220	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI23	3x30222 4x30222 I:30221	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI23	3x30223 4x30223 I:30222	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI23	3x30224 4x30224 I:30223	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI23	3x30225 4x30225 I:30224	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI23	3x30226 4x30226 I:30225	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI24						
RISE DI24	3x30231 4x30231 I:30230	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI24	3x30232 4x30232 I:30231	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI24	3x30233 4x30233 I:30232	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI24	3x30234 4x30234 I:30233	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI24	3x30235 4x30235 I:30234	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)				
LONG KEYPRESS END DI24	3x30236 4x30236 I:30235	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI25						
RISE DI25	3x30241 4x30241 I:30240	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI25	3x30242 4x30242 I:30241	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI25	3x30243 4x30243 I:30242	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI25	3x30244 4x30244 I:30243	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI25	3x30245 4x30245 I:30244	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI25	3x30246 4x30246 I:30245	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI26						
RISE DI26	3x30251 4x30251 I:30250	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI26	3x30252 4x30252 I:30251	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI26	3x30253 4x30253 I:30252	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

SHORT KEYPRESS DI26	3x30254 4x30254 I:30253	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
Counter for short keypress events on the digital input DIx. If the module detects a short keypress on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.						
LONG KEYPRESS START DI26	3x30255 4x30255 I:30254	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI26	3x30256 4x30256 I:30255	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI27						
RISE DI27	3x30261 4x30261 I:30260	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI27	3x30262 4x30262 I:30261	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI27	3x30263 4x30263 I:30262	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI27	3x30264 4x30264 I:30263	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI27	3x30265 4x30265 I:30264	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI27	3x30266 4x30266 I:30265	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI28						
RISE DI28	3x30271 4x30271 I:30270	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

FALL DI28	3x30272 4x30272 I:30271	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI28	3x30273 4x30273 I:30272	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI28	3x30274 4x30274 I:30273	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI28	3x30275 4x30275 I:30274	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI28	3x30276 4x30276 I:30275	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI29						
RISE DI29	3x30281 4x30281 I:30280	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI29	3x30282 4x30282 I:30281	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI29	3x30283 4x30283 I:30282	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI29	3x30284 4x30284 I:30283	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI29	3x30285 4x30285 I:30284	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI29	3x30286 4x30286 I:30285	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI30						
RISE DI30	3x30291 4x30291 I:30290	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI30	3x30292 4x30292 I:30291	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI30	3x30293 4x30293 I:30292	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI30	3x30294 4x30294 I:30293	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI30	3x30295 4x30295 I:30294	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI30	3x30296 4x30296 I:30295	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI31						
RISE DI31	3x30301 4x30301 I:30300	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI31	3x30302 4x30302 I:30301	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI31	3x30303 4x30303 I:30302	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI31	3x30304 4x30304 I:30303	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI31	3x30305 4x30305 I:30304	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)				
LONG KEYPRESS END DI31	3x30306 4x30306 I:30305	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI32						
RISE DI32	3x30311 4x30311 I:30310	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI32	3x30312 4x30312 I:30311	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI32	3x30313 4x30313 I:30312	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI32	3x30314 4x30314 I:30313	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI32	3x30315 4x30315 I:30314	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI32	3x30316 4x30316 I:30315	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI33						
RISE DI33	3x30321 4x30321 I:30320	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI33	3x30322 4x30322 I:30321	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI33	3x30323 4x30323 I:30322	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

SHORT KEYPRESS DI33	3x30324 4x30324 I:30323	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI33	3x30325 4x30325 I:30324	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI33	3x30326 4x30326 I:30325	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI34						
RISE DI34	3x30331 4x30331 I:30330	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI34	3x30332 4x30332 I:30331	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI34	3x30333 4x30333 I:30332	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI34	3x30334 4x30334 I:30333	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI34	3x30335 4x30335 I:30334	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI34	3x30336 4x30336 I:30335	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI35						
RISE DI35	3x30341 4x30341 I:30340	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI35	3x30342 4x30342 I:30341	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)				
CHANGE DI35	3x30343 4x30343 I:30342	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI35	3x30344 4x30344 I:30343	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI35	3x30345 4x30345 I:30344	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI35	3x30346 4x30346 I:30345	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI36						
RISE DI36	3x30351 4x30351 I:30350	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI36	3x30352 4x30352 I:30351	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI36	3x30353 4x30353 I:30352	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI36	3x30354 4x30354 I:30353	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI36	3x30355 4x30355 I:30354	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI36	3x30356 4x30356 I:30355	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI37						

RISE DI37	3x30361 4x30361 I:30360	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI37	3x30362 4x30362 I:30361	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI37	3x30363 4x30363 I:30362	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI37	3x30364 4x30364 I:30363	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI37	3x30365 4x30365 I:30364	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI37	3x30366 4x30366 I:30365	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI38						
RISE DI38	3x30371 4x30371 I:30370	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI38	3x30372 4x30372 I:30371	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI38	3x30373 4x30373 I:30372	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI38	3x30374 4x30374 I:30373	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI38	3x30375 4x30375 I:30374	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

LONG KEYPRESS END DI38	3x30376 4x30376 I:30375	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI39						
RISE DI39	3x30381 4x30381 I:30380	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI39	3x30382 4x30382 I:30381	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI39	3x30383 4x30383 I:30382	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI39	3x30384 4x30384 I:30383	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI39	3x30385 4x30385 I:30384	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI39	3x30386 4x30386 I:30385	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI40						
RISE DI40	3x30391 4x30391 I:30390	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI40	3x30392 4x30392 I:30391	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI40	3x30393 4x30393 I:30392	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI40	3x30394 4x30394 I:30393	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)			
LONG KEYPRESS START DI40	3x30395 4x30395 I:30394	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
LONG KEYPRESS END DI40	3x30396 4x30396 I:30395	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI41					
RISE DI41	3x30401 4x30401 I:30400	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
FALL DI41	3x30402 4x30402 I:30401	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
CHANGE DI41	3x30403 4x30403 I:30402	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
SHORT KEYPRESS DI41	3x30404 4x30404 I:30403	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
LONG KEYPRESS START DI41	3x30405 4x30405 I:30404	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
LONG KEYPRESS END DI41	3x30406 4x30406 I:30405	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI42					
RISE DI42	3x30411 4x30411 I:30410	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
FALL DI42	3x30412 4x30412 I:30411	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			

CHANGE DI42	3x30413 4x30413 I:30412	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI42	3x30414 4x30414 I:30413	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI42	3x30415 4x30415 I:30414	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI42	3x30416 4x30416 I:30415	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI43						
RISE DI43	3x30421 4x30421 I:30420	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI43	3x30422 4x30422 I:30421	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI43	3x30423 4x30423 I:30422	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI43	3x30424 4x30424 I:30423	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI43	3x30425 4x30425 I:30424	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI43	3x30426 4x30426 I:30425	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI44						
RISE DI44	3x30431 4x30431 I:30430	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)				
FALL DI44	3x30432 4x30432 I:30431	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI44	3x30433 4x30433 I:30432	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI44	3x30434 4x30434 I:30433	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI44	3x30435 4x30435 I:30434	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI44	3x30436 4x30436 I:30435	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI45						
RISE DI45	3x30441 4x30441 I:30440	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI45	3x30442 4x30442 I:30441	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI45	3x30443 4x30443 I:30442	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI45	3x30444 4x30444 I:30443	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI45	3x30445 4x30445 I:30444	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI45	3x30446 4x30446 I:30445	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI46						
RISE DI46	3x30451 4x30451 I:30450	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI46	3x30452 4x30452 I:30451	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI46	3x30453 4x30453 I:30452	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI46	3x30454 4x30454 I:30453	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI46	3x30455 4x30455 I:30454	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI46	3x30456 4x30456 I:30455	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI47						
RISE DI47	3x30461 4x30461 I:30460	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI47	3x30462 4x30462 I:30461	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI47	3x30463 4x30463 I:30462	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI47	3x30464 4x30464 I:30463	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

LONG KEYPRESS START DI47	3x30465 4x30465 I:30464	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI47	3x30466 4x30466 I:30465	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI48						
RISE DI48	3x30471 4x30471 I:30470	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI48	3x30472 4x30472 I:30471	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI48	3x30473 4x30473 I:30472	7,0x0007 B:00 07			UINT16 R/O	
		7 event(s)				
SHORT KEYPRESS DI48	3x30474 4x30474 I:30473	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI48	3x30475 4x30475 I:30474	3,0x0003 B:00 03			UINT16 R/O	
		3 event(s)				
LONG KEYPRESS END DI48	3x30476 4x30476 I:30475	3,0x0003 B:00 03			UINT16 R/O	
		3 event(s)				

DIGITAL INPUTS				
GET DIGITAL INPUTS	ASCII READ COMMAND	#GDIS<CR> Result: #GDIS:<DISDec>,<DISHex><CR>	ASCII	
	TX	#GDIS<CR>		
	RX	#1,GDIS:140737488355328,0x800000000000<CR>		
		Current status of digital inputs:1000.0000.0000.0000.0000.0000.0000.0000.0000.0000.0000.0000		

Returns the current state of all 48 digital inputs as decimal number and as hexadecimal number.
DISDec, DISHex
The current state of all digital inputs:
Bit 0: State of DI1 (=0:OFF, =1:ON)
Bit 1: State of DI2 (=0:OFF, =1:ON)
...
Bit 47: State of DI48 (=0:OFF, =1:ON)

GET DIGITAL INPUT DIx	ASCII READ COMMAND	#GDI<DINR><CR> Result: #GDI<DINR>:<DIxDec>,<DIxHex><CR>	ASCII	
	DINR	48		
	TX	#GDI48<CR>		
	RX	#1,GDI48:1,0x1<CR>		
		Current status of digital input DI48:1=ON		

Returns the current state of the digital input Dix as decimal number and as hexadecimal number. X stands for the desired digital input between 1 and 16.
DIxDec, DIxHex:
The current state of the digital input x:
=0: Digital input is OFF
=1: Digital input is ON

GET ALL CHANGES	ASCII READ COMMAND	#GAC<CR> Result: #GAC:<ChangesDec>,<ChangesHex><CR>	ASCII	
	TX	#GAC<CR>		
	RX	#1,GAC:21,0x15<CR>		
		Current change counter:21		

Returns the counter for changes on all digital inputs.
As soon as the module detects a short keypress or long key press or long key release event, this counter is incremented by 1.
If this values has changed sience the last polling request, the host knows, that at least one digital input has changed its state.

CHANGE ALL DIS	ASCII READ COMMAND	#CADIS<CR> Result: #CADIS:<ChangeDI1Dec>,...,<ChangeDI48Dec>,<ChangeDI1Hex>,...,<ChangeDI48Hex><CR>	ASCII	
	TX	#CADIS<CR>		
	RX	#1,CADIS:13,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,8,0xD,0x 0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0, 0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x8<CR>		
		Current counter for changes on DI1:13		
		Current counter for changes on DI2:0		
		Current counter for changes on DI3:0		
		Current counter for changes on DI4:0		

		Current counter for changes on DI5:0		
		Current counter for changes on DI6:0		
		Current counter for changes on DI7:0		
		Current counter for changes on DI8:0		
		Current counter for changes on DI9:0		
		Current counter for changes on DI10:0		
		Current counter for changes on DI11:0		
		Current counter for changes on DI12:0		
		Current counter for changes on DI13:0		
		Current counter for changes on DI14:0		
		Current counter for changes on DI15:0		
		Current counter for changes on DI16:0		
		Current counter for changes on DI17:0		
		Current counter for changes on DI18:0		
		Current counter for changes on DI19:0		
		Current counter for changes on DI20:0		
		Current counter for changes on DI21:0		
		Current counter for changes on DI22:0		
		Current counter for changes on DI23:0		
		Current counter for changes on DI24:0		
		Current counter for changes on DI25:0		
		Current counter for changes on DI26:0		
		Current counter for changes on DI27:0		
		Current counter for changes on DI28:0		
		Current counter for changes on DI29:0		
		Current counter for changes on DI30:0		
		Current counter for changes on DI31:0		
		Current counter for changes on DI32:0		
		Current counter for changes on DI33:0		
		Current counter for changes on DI34:0		
		Current counter for changes on DI35:0		
		Current counter for changes on DI36:0		
		Current counter for changes on DI37:0		
		Current counter for changes on DI38:0		
		Current counter for changes on DI39:0		
		Current counter for changes on DI40:0		
		Current counter for changes on DI41:0		
		Current counter for changes on DI42:0		
		Current counter for changes on DI43:0		
		Current counter for changes on DI44:0		
		Current counter for changes on DI45:0		
		Current counter for changes on DI46:0		
		Current counter for changes on DI47:0		
		Current counter for changes on DI48:8		

Returns for each digital input the counter for changes. As soon as the module detects a signal change on a digital input, the change counter for the affected digital input is incremented by 1.
A signal change can be:
Detection of a short keypress
Detection of the start of a long keypress
Detection of a release of a long keypress

CHANGE DIx	ASCII READ COMMAND	#CDI<DINR><CR> Result: #CDI<DINR>:<ChangesDec>,<ChangesHex><CR>	ASCII	
	DINR	48		
	TX	#CDI48<CR>		
	RX	#1,CDI48:8,0x8<CR>		
		Current counter for changes on digital input DI48:8		

Returns for digital input <DINR> the counter for signal changes. As soon as the module detects a signal change on a digital input, the change counter for the affected digital input is incremented by 1.
A signal change can be:
Detection of a short keypress
Detection of the start of a long keypress
Detection of a release of a long keypress

SHORT KEY ALL DIS	ASCII READ COMMAND	#SKADIS<CR> Result: #SKADIS:<ShortKeyDI1Dec>,...,<ShortKeyDI48Dec>, <ShortKeyDI1Hex>,...,<ShortKeyDI48Hex><CR>	ASCII	
	TX	#SKADIS<CR>		
	RX	#1,SKADIS:5,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,1,0x5,0x0 ,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0 x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x1<CR>		
		Current counter for short keypress events on DI1:5		
		Current counter for short keypress events on DI2:0		
		Current counter for short keypress events on DI3:0		
		Current counter for short keypress events on DI4:0		
		Current counter for short keypress events on DI5:0		
		Current counter for short keypress events on DI6:0		
		Current counter for short keypress events on DI7:0		
		Current counter for short keypress events on DI8:0		
		Current counter for short keypress events on DI9:0		
		Current counter for short keypress events on DI10:0		
		Current counter for short keypress events on DI11:0		
		Current counter for short keypress events on DI12:0		
		Current counter for short keypress events on DI13:0		
		Current counter for short keypress events on DI14:0		
		Current counter for short keypress events on DI15:0		
		Current counter for short keypress events on DI16:0		
		Current counter for short keypress events on DI17:0		
		Current counter for short keypress events on DI18:0		
		Current counter for short keypress events on DI19:0		
		Current counter for short keypress events on DI20:0		

		Current counter for short keypress events on DI21:0		
		Current counter for short keypress events on DI22:0		
		Current counter for short keypress events on DI23:0		
		Current counter for short keypress events on DI24:0		
		Current counter for short keypress events on DI25:0		
		Current counter for short keypress events on DI26:0		
		Current counter for short keypress events on DI27:0		
		Current counter for short keypress events on DI28:0		
		Current counter for short keypress events on DI29:0		
		Current counter for short keypress events on DI30:0		
		Current counter for short keypress events on DI31:0		
		Current counter for short keypress events on DI32:0		
		Current counter for short keypress events on DI33:0		
		Current counter for short keypress events on DI34:0		
		Current counter for short keypress events on DI35:0		
		Current counter for short keypress events on DI36:0		
		Current counter for short keypress events on DI37:0		
		Current counter for short keypress events on DI38:0		
		Current counter for short keypress events on DI39:0		
		Current counter for short keypress events on DI40:0		
		Current counter for short keypress events on DI41:0		
		Current counter for short keypress events on DI42:0		
		Current counter for short keypress events on DI43:0		
		Current counter for short keypress events on DI44:0		
		Current counter for short keypress events on DI45:0		
		Current counter for short keypress events on DI46:0		
		Current counter for short keypress events on DI47:0		
		Current counter for short keypress events on DI48:1		
Returns for each digital input the counter for short keypress events. As soon as the module detects a short keypress on a digital input, the counter for the affected digital input is incremented by 1.				
SHORT KEY DIx	ASCII READ COMMAND	#SKDI<DINR><CR> Result: #SKDI<DINR>:<ShortKeyDec>,<ShortKeyHex><CR>	ASCII	
	DINR	48		
	TX	#SKDI48<CR>		
	RX	#1,SKDI48:1,0x1<CR>		
		Current counter for short keypress events on digital input DI48:1		
Returns for digital input <DINR> the counter for short keypress events. As soon as the module detects a short keypress on a digital input, the counter for the affected digital input is				
LONG KEY START ALL DIS	ASCII READ COMMAND	#LKSADIS<CR> Result: #LKSADIS:<LongKeyStartDI1Dec>,...,<LongKeyStartDI48Dec>, <LongKeyStartDI1Hex>,...,<LongKeyStartDI48Hex><CR>	ASCII	
	TX	#LKSADIS<CR>		

	RX	#1,LKSADIS:4,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,4,0x4,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x4<CR>		
		Current counter for long keypress start events on DI1:4		
		Current counter for long keypress start events on DI2:0		
		Current counter for long keypress start events on DI3:0		
		Current counter for long keypress start events on DI4:0		
		Current counter for long keypress start events on DI5:0		
		Current counter for long keypress start events on DI6:0		
		Current counter for long keypress start events on DI7:0		
		Current counter for long keypress start events on DI8:0		
		Current counter for long keypress start events on DI9:0		
		Current counter for long keypress start events on DI10:0		
		Current counter for long keypress start events on DI11:0		
		Current counter for long keypress start events on DI12:0		
		Current counter for long keypress start events on DI13:0		
		Current counter for long keypress start events on DI14:0		
		Current counter for long keypress start events on DI15:0		
		Current counter for long keypress start events on DI16:0		
		Current counter for long keypress start events on DI17:0		
		Current counter for long keypress start events on DI18:0		
		Current counter for long keypress start events on DI19:0		
		Current counter for long keypress start events on DI20:0		
		Current counter for long keypress start events on DI21:0		
		Current counter for long keypress start events on DI22:0		
		Current counter for long keypress start events on DI23:0		
		Current counter for long keypress start events on DI24:0		
		Current counter for long keypress start events on DI25:0		
		Current counter for long keypress start events on DI26:0		
		Current counter for long keypress start events on DI27:0		
		Current counter for long keypress start events on DI28:0		
		Current counter for long keypress start events on DI29:0		
		Current counter for long keypress start events on DI30:0		
		Current counter for long keypress start events on DI31:0		
		Current counter for long keypress start events on DI32:0		
		Current counter for long keypress start events on DI33:0		
		Current counter for long keypress start events on DI34:0		
		Current counter for long keypress start events on DI35:0		
		Current counter for long keypress start events on DI36:0		
		Current counter for long keypress start events on DI37:0		
		Current counter for long keypress start events on DI38:0		
		Current counter for long keypress start events on DI39:0		
		Current counter for long keypress start events on DI40:0		
		Current counter for long keypress start events on DI41:0		

		Current counter for long keypress start events on DI42:0		
		Current counter for long keypress start events on DI43:0		
		Current counter for long keypress start events on DI44:0		
		Current counter for long keypress start events on DI45:0		
		Current counter for long keypress start events on DI46:0		
		Current counter for long keypress start events on DI47:0		
		Current counter for long keypress start events on DI48:4		
Returns for each digital input the counter for long keypress start events. As soon as the module detects the start of a long keypress on a digital input, the counter for the affected digital input is incremented by 1.				
LONG KEY START DIx	ASCII READ COMMAND	#LKSDI<DINR><CR> Result: #LKSDI<DINR>:<LongKeyStartDec>,<LongKeyStartHex><CR>	ASCII	
	DINR	48		
	TX	#LKSDI48<CR>		
	RX	#1,LKSDI48:4,0x4<CR>		
		Current counter for long keypress start events on digital input DI48:4		
Returns for digital input <DINR> the counter for long keypress start events. As soon as the module detects the start of a long keypress on a digital input, the counter for the affected digital input is incremented by 1.				
LONG KEY END ALL DIS	ASCII READ COMMAND	#LKEADIS<CR> Result: #LKEADIS:<LongKeyEndDI1Dec>,...,<LongKeyEndDI48Dec>, <LongKeyEndDI1Hex>,...,<LongKeyEndDI48Hex><CR>	ASCII	
	TX	#LKEADIS<CR>		
	RX	#1,LKEADIS:4,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,3,0x4,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x3<CR>		
		Current counter for long keypress end events on DI1:4		
		Current counter for long keypress end events on DI2:0		
		Current counter for long keypress end events on DI3:0		
		Current counter for long keypress end events on DI4:0		
		Current counter for long keypress end events on DI5:0		
		Current counter for long keypress end events on DI6:0		
		Current counter for long keypress end events on DI7:0		
		Current counter for long keypress end events on DI8:0		
		Current counter for long keypress end events on DI9:0		
		Current counter for long keypress end events on DI10:0		
		Current counter for long keypress end events on DI11:0		
		Current counter for long keypress end events on DI12:0		
		Current counter for long keypress end events on DI13:0		
		Current counter for long keypress end events on DI14:0		
		Current counter for long keypress end events on DI15:0		
		Current counter for long keypress end events on DI16:0		
		Current counter for long keypress end events on DI17:0		
		Current counter for long keypress end events on DI18:0		
		Current counter for long keypress end events on DI19:0		
		Current counter for long keypress end events on DI20:0		

		Current counter for long keypress end events on DI21:0		
		Current counter for long keypress end events on DI22:0		
		Current counter for long keypress end events on DI23:0		
		Current counter for long keypress end events on DI24:0		
		Current counter for long keypress end events on DI25:0		
		Current counter for long keypress end events on DI26:0		
		Current counter for long keypress end events on DI27:0		
		Current counter for long keypress end events on DI28:0		
		Current counter for long keypress end events on DI29:0		
		Current counter for long keypress end events on DI30:0		
		Current counter for long keypress end events on DI31:0		
		Current counter for long keypress end events on DI32:0		
		Current counter for long keypress end events on DI33:0		
		Current counter for long keypress end events on DI34:0		
		Current counter for long keypress end events on DI35:0		
		Current counter for long keypress end events on DI36:0		
		Current counter for long keypress end events on DI37:0		
		Current counter for long keypress end events on DI38:0		
		Current counter for long keypress end events on DI39:0		
		Current counter for long keypress end events on DI40:0		
		Current counter for long keypress end events on DI41:0		
		Current counter for long keypress end events on DI42:0		
		Current counter for long keypress end events on DI43:0		
		Current counter for long keypress end events on DI44:0		
		Current counter for long keypress end events on DI45:0		
		Current counter for long keypress end events on DI46:0		
		Current counter for long keypress end events on DI47:0		
		Current counter for long keypress end events on DI48:3		
Returns for each digital input the counter for long keypress end events. As soon as the module detects the end of a long keypress on a digital input, the counter for the affected digital input is incremented by 1.				
LONG KEY END DIx	ASCII READ COMMAND	#LKEDI<DINR><CR> Result: #LKEDI<DINR>:<LongKeyEndDec>,<LongKeyEndHex><CR>	ASCII	
	DINR	48		
	TX	#LKEDI48<CR>		
	RX	#1,LKEDI48:3,0x3<CR>		
		Current counter for long keypress end events on digital input DI48:3		
Returns for digital input <DINR> the counter for long keypress end events. As soon as the module detects the end of a long keypress on a digital input, the counter for the affected digital input is incremented by 1.				
RISE ALL DIS	ASCII READ COMMAND	#RADIS<CR> Result: #RADIS:<RiseDI1Dec>,...,<RiseDI48Dec>,<RiseDI1Hex>,...,<RiseDI48Hex><CR>	ASCII	
	TX	#RADIS<CR>		
	RX	#1,RADIS:9,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,5,0x9,0x0, 0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x 0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x5<CR>		

		Current counter for rising edges on DI1:9		
		Current counter for rising edges on DI2:0		
		Current counter for rising edges on DI3:0		
		Current counter for rising edges on DI4:0		
		Current counter for rising edges on DI5:0		
		Current counter for rising edges on DI6:0		
		Current counter for rising edges on DI7:0		
		Current counter for rising edges on DI8:0		
		Current counter for rising edges on DI9:0		
		Current counter for rising edges on DI10:0		
		Current counter for rising edges on DI11:0		
		Current counter for rising edges on DI12:0		
		Current counter for rising edges on DI13:0		
		Current counter for rising edges on DI14:0		
		Current counter for rising edges on DI15:0		
		Current counter for rising edges on DI16:0		
		Current counter for rising edges on DI17:0		
		Current counter for rising edges on DI18:0		
		Current counter for rising edges on DI19:0		
		Current counter for rising edges on DI20:0		
		Current counter for rising edges on DI21:0		
		Current counter for rising edges on DI22:0		
		Current counter for rising edges on DI23:0		
		Current counter for rising edges on DI24:0		
		Current counter for rising edges on DI25:0		
		Current counter for rising edges on DI26:0		
		Current counter for rising edges on DI27:0		
		Current counter for rising edges on DI28:0		
		Current counter for rising edges on DI29:0		
		Current counter for rising edges on DI30:0		
		Current counter for rising edges on DI31:0		
		Current counter for rising edges on DI32:0		
		Current counter for rising edges on DI33:0		
		Current counter for rising edges on DI34:0		
		Current counter for rising edges on DI35:0		
		Current counter for rising edges on DI36:0		
		Current counter for rising edges on DI37:0		
		Current counter for rising edges on DI38:0		
		Current counter for rising edges on DI39:0		
		Current counter for rising edges on DI40:0		
		Current counter for rising edges on DI41:0		
		Current counter for rising edges on DI42:0		
		Current counter for rising edges on DI43:0		
		Current counter for rising edges on DI44:0		

		Current counter for rising edges on DI45:0		
		Current counter for rising edges on DI46:0		
		Current counter for rising edges on DI47:0		
		Current counter for rising edges on DI48:5		
Returns for each digital input the counter for rising edges. As soon as the module detects a rising edge on a digital input, the rising edge counter for the affected digital input is incremented by 1.				
RISE DIx	ASCII READ COMMAND	#RDI<DINR><CR> Result: #RDI<DINR>:<RiseDec>,<RiseHex><CR>	ASCII	
	DINR	48		
	TX	#RDI48<CR>		
	RX	#1,RDI48:5,0x5<CR>		
		Current counter for rising edges on digital input DI48:5		
Returns for digital input <DINR> the counter for rising edges. As soon as the module detects a rising edge on a digital input, the rising edge counter for the affected digital input is incremented by 1.				
FALL ALL DIS	ASCII READ COMMAND	#FADIS<CR> Result: #FADIS:<FallDI1Dec>,...,<FallDI48Dec>,<FallDI1Hex>,...,<FallDI48Hex><CR>	ASCII	
	TX	#FADIS<CR>		
	RX	#1,FADIS:9,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,4,0x9,0x0, 0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x 0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x4<CR>		
		Current counter for falling edges on DI1:9		
		Current counter for falling edges on DI2:0		
		Current counter for falling edges on DI3:0		
		Current counter for falling edges on DI4:0		
		Current counter for falling edges on DI5:0		
		Current counter for falling edges on DI6:0		
		Current counter for falling edges on DI7:0		
		Current counter for falling edges on DI8:0		
		Current counter for falling edges on DI9:0		
		Current counter for falling edges on DI10:0		
		Current counter for falling edges on DI11:0		
		Current counter for falling edges on DI12:0		
		Current counter for falling edges on DI13:0		
		Current counter for falling edges on DI14:0		
		Current counter for falling edges on DI15:0		
		Current counter for falling edges on DI16:0		
		Current counter for falling edges on DI17:0		
		Current counter for falling edges on DI18:0		
		Current counter for falling edges on DI19:0		
		Current counter for falling edges on DI20:0		
		Current counter for falling edges on DI21:0		
		Current counter for falling edges on DI22:0		
		Current counter for falling edges on DI23:0		
		Current counter for falling edges on DI24:0		

		Current counter for falling edges on DI25:0		
		Current counter for falling edges on DI26:0		
		Current counter for falling edges on DI27:0		
		Current counter for falling edges on DI28:0		
		Current counter for falling edges on DI29:0		
		Current counter for falling edges on DI30:0		
		Current counter for falling edges on DI31:0		
		Current counter for falling edges on DI32:0		
		Current counter for falling edges on DI33:0		
		Current counter for falling edges on DI34:0		
		Current counter for falling edges on DI35:0		
		Current counter for falling edges on DI36:0		
		Current counter for falling edges on DI37:0		
		Current counter for falling edges on DI38:0		
		Current counter for falling edges on DI39:0		
		Current counter for falling edges on DI40:0		
		Current counter for falling edges on DI41:0		
		Current counter for falling edges on DI42:0		
		Current counter for falling edges on DI43:0		
		Current counter for falling edges on DI44:0		
		Current counter for falling edges on DI45:0		
		Current counter for falling edges on DI46:0		
		Current counter for falling edges on DI47:0		
		Current counter for falling edges on DI48:4		
Returns for each digital input the counter for falling edges. As soon as the module detects a falling edge on a digital input, the falling edge counter for the affected digital input is incremented by 1.				
FALL DIx	**ASCII READ COMMAND**	#FDI<DINR><CR> Result: #FDI<DINR>:<FallDec>,<FallHex><CR>	ASCII	
	DINR	48		
	TX	#FDI48<CR>		
	RX	#1,FDI48:4,0x4<CR>		
		Current counter for falling edges on digital input DI48:4		
Returns for digital input <DINR> the counter for falling edges. As soon as the module detects a falling edge on a digital input, the falling edge counter for the affected digital input is incremented by 1.				
RESET COUNTERS	**ASCII WRITE COMMAND**	#RC<CR> Result: #OK<CR>	ASCII	NO
	TX	#RC<CR>		
	RX	N/A		
Resets all internal counters for digital inputs and events on this digital inputs to 0.				