Command NAME	ASCII command type	ASCII command structure	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
ASCII COMMANDS						
HEART BEAT	ASCII READ COMMAND	#HB<CR> Result: #HB<CR>			ASCII	
	TX	#1,HB<CR>				
	RX	#1,HB<CR>				
Sends an Heartbeat to test the communcation						
GET VERSION	ASCII READ COMMAND	#VERSION<CR> Result: #VERSION:<VersionHi>,<VersionMed>,<VersionLo><CR>			ASCII	
	TX	#1,VERSION<CR>				
	RX	#1,VERSION:1.2.00<CR>				
		Current SW version:1.2.00				
Returns the version number of the module VersionHi: Version number high (1..255) VersionMed: Version number medium (1..255) VersionLo: Version number low (1..255)						
GET TYPE	ASCII READ COMMAND	#TYPE<CR> Result: #TYPE:<Type><CR>			ASCII	
	TX	#1,TYPE<CR>				
	RX	#1,TYPE:RESI-64DI-SIO<CR>				
		Current module type:RESI-64DI-SIO				
Returns the current module type						
GET FEATURES	ASCII READ COMMAND	#FTRS<CR> Result: #FTRS:<Type><CR>			ASCII	
	TX	#1,FTRS<CR>				
	RX	#1,FTRS:RESI-64DI-SIO,RS485,DI:64<CR>				
		Current module type:#1,FTRS:RESI-64DI-SIO,RS485,DI:64<CR>				
Returns the current module features						
GET OWNER	ASCII READ COMMAND	#OWNER<CR> Result: #OWNER:<Owner><CR>			ASCII	
	TX	#1,OWNER<CR>				
	RX	#1,OWNER:RESI<CR>				
		Current owner:RESI				
Returns the current owner of the module						

GET CREATOR	ASCII READ COMMAND	#CREATOR<CR> Result: #CREATOR:<Creator><CR>	ASCII	
	TX	#1,CREATOR<CR>		
	RX	#1,CREATOR:DI HC SIGL,MSC<CR>		
		Current creator:DI HC SIGL,MSC		
Returns the current creator of the module				
GET COPYRIGHT	ASCII READ COMMAND	#COPYRIGHT<CR> Result: #COPYRIGHT:<Copyright><CR>	ASCII	
	TX	#1,COPYRIGHT<CR>		
	RX	#1,COPYRIGHT:2015-24 BY RESI AND DI HC SIGL,MSC WWW.RESI.CC<CR>		
		Current copyright:2015-24 BY RESI AND DI HC SIGL,MSC WWW.RESI.CC		
Returns the current copyright of the module				
GET SERIAL NUMBER	ASCII READ COMMAND	#SN<CR> Result: #SN:<Serial><CR>	ASCII	
	TX	#1,SN<CR>		
	RX	#1,SN:38002D001457435535343920<CR>		
		Current serial number:38002D001457435535343920		
Returns the current serial number of the module				
GET INTERNAL STATUS	ASCII READ COMMAND	#INTSTAT<CR> Result: #INTSTAT:<Status><CR>	ASCII	
	TX	#1,INTSTAT<CR>		
	RX	#1,INTSTAT:I2C1:0,I2C2:0,FRAM:20<CR>		
Returns the device specific internal status				
GET DIP SWITCH	ASCII READ COMMAND	#GDIP<CR> Result: #GDIP:<DIPSwitchDec>,<DIPSwitchHex><CR>	ASCII	
	TX	#1,GDIP<CR>		
	RX	#1,GDIP:65,0x41<CR>		
		Current DIP SWITCH settings:0100.0001		
Returns the current setting of the Dip switches as decimal number and as hexadecimal number. DIPSwitchDec DIPSwitchHex The current value of the DIP switches: Bit 0: DIP Switch 1 (=0:OFF, =1:ON) Bit 1: DIP Switch 2 (=0:OFF, =1:ON) Bit 2: DIP Switch 3 (=0:OFF, =1:ON) Bit 3: DIP Switch 4 (=0:OFF, =1:ON) Bit 4: DIP Switch 5, if available (=0:OFF, =1:ON) Bit 5: DIP Switch 6, if available (=0:OFF, =1:ON) Bit 6: DIP Switch 7, if available (=0:OFF, =1:ON) Bit 7: DIP Switch 8, if available (=0:OFF, =1:ON)				
ASCII COMMANDS				

SET MODBUS ADDRESS	ASCII WRITE COMMAND	#SMBADR:<UNITID><CR> Result: #OK<CR>	ASCII	NO
	UNITID	123		
	TX	#1,SMBADR:123<CR>		
	RX	N/A		

Redefines the unit ID of the module. This change will affect the MODBUS/RTU communication immediately. As a Unit IO you can use the values 0dec to 255dec.

HINT: The new settings are activated after a system reboot or power off on cycle!

SET MODBUS BAUDRATE	ASCII WRITE COMMAND	#SMBBAUD:<BAUD><CR> Result: #OK<CR>	ASCII	NO
	BAUD	128000:128000BD		
	TX	#1,SMBBAUD:128000<CR>		
	RX	N/A		

Sets a new baud rate in the FLASH
For ULTRA SLIM IOs RESI-xxx-SIO: This baudrate is only used, if DIP switch mode DIP1=ON+DIP2=ON (BR) (default is 57600bd)
For BIG IOs RESI-xxx-SIO: This baudrate is only used, if DIP switch mode DIP7=ON (PARAMETER) (default is 57600bd)
The following baudrates are allowed:
300bd, 600bd, 900bd, 1200bd, 2400bd, 4800bd,
9600bd, 19200bd, 38400bd, 57600bd, 115200bd, 128000bd
230400bd, 250000bd, 256000bd

HINT: The new setup parameters will be active after a restart of the module.

SET MODBUS PARITY	ASCII WRITE COMMAND	#SMBPAR:<PARITY><CR> Result: #OK<CR>	ASCII	NO
	PARITY	ODD:ODD PARITY		
	TX	#1,SMBPAR:ODD<CR>		
	RX	N/A		

Sets a new parity for the serial interface.
MBParity:
NONE: no parity
EVEN: even parity
ODD: odd parity

HINT: The new setup parameters will be active after a restart of the module.

SET MODBUS STOPS	ASCII WRITE COMMAND	#SMBSTOP:<STOPBIT><CR> Result: #OK<CR>	ASCII	NO
	STOPBIT	TWO:TWO STOPBITS		
	TX	#1,SMBSTOP:TWO<CR>		
	RX	N/A		

Sets a new amount of stop bits for the serial interface.
MBStops
ONE: one stop bit
TWO: two stop bits

HINT: The new setup parameters will be active after a restart of the module.

SET MODBUS PARAMS	ASCII WRITE COMMAND	#SMBPARAMS:<UNITID>,<BAUD>,<PARITY>,<STOPBIT><CR> Result: #OK<CR>	ASCII	NO
	UNITID	3		
	BAUD	115200:115200BD		
	PARITY	EVEN:EVEN PARITY		
	STOPBIT	TWO:TWO STOPBITS		
	TX	#1,SMBPARAMS:3,115200,EVEN,TWO<CR>		
	RX	N/A		

Sets all parameters for serrial interface

GET MODBUS ADDRESS	ASCII READ COMMAND	#GMBADR<CR> Result: #GMBADR:<MBUnitDec>,<MBFLASHDec>,<MBUnitHex>,<MBFLASHHex><CR>	ASCII	
	TX	#1,GMBADR<CR>		
	RX	#1,GMBADR:1,15,0x1,0xF<CR>		
		Current MODBUS unit ID:1,15,0x1,0xF		

Shows the current used MODBUS/RTU or ASCII unit address and shows also the stored unit address in the FLASH memory, which is only used if the DIP switch for the bus address is set to 0.

MBUnitDec,MBUnitHex
The current used MODBUS/RTU unit or ASCII address for communication

MBFLASHDec,MBFLASHHex
The internal stored MODBUS/RTU unit address or ASCII address from the FLASH memory, if the DIP switch DIP3 is OFF.

GET MODBUS BAUDRATE	ASCII READ COMMAND	#GMBBAUD<CR> Result: #GMBBAUD:<BaudRate><CR>	ASCII	
	TX	#1,GMBBAUD<CR>		
	RX	#1,GMBBAUD:115200,0x1C200<CR>		
		Current baudrate:115200,0x1C200		

This is the current configured baud rate in the FLASH
For ULTRA SLIM IOs RESI-xxx-SIO: This baudrate is only used, if DIP switch mode DIP1=ON+DIP2=ON (BR) (default is 57600bd)
For BIG IOs RESI-xxx-SIO: This baudrate is only used, if DIP switch mode DIP7=ON (PARAMETER) (default is 57600bd)
The following baudrates are allowed:
300bd, 600bd, 900bd, 1200bd, 2400bd, 4800bd,
9600bd, 19200bd, 38400bd, 57600bd, 115200bd, 128000bd
230400bd, 250000bd, 256000bd

GET MODBUS PARITY	ASCII READ COMMAND	#GMBPAR<CR> Result: #GMBPAR:<MBParity><CR>	ASCII	
	TX	#1,GMBPAR<CR>		
	RX	#1,GMBPAR:NONE<CR>		

		Current parity:NONE		
Shows the current configured parity of the serial interface. MBParity NONE: no parity EVEN: even parity ODD: odd parity				
GET MODBUS STOP	ASCII READ COMMAND	#GMBSTOP<CR> Result: #GMBSTOP:<MBStop><CR>	ASCII	
	TX	#1,GMBSTOP<CR>		
	RX	#1,GMBPAR:ONE<CR>		
		Current stopbit(s):ONE		
Shows the current configured parity of the serial interface. MBParity NONE: no parity EVEN: even parity ODD: odd parity				
GET MODBUS PARAMS	ASCII READ COMMAND	#GMBPARAMS<CR> Result: #GMBPARAMS:<MBUnitDec>,<MBFLASHDec>,<MBUnitHex>,<MBFLASHHex>, <MBBaudrateDec>,<MBBaudrateHex>,<MBParity>,<MBStops><CR>	ASCII	
	TX	#1,GMBPARAMS<CR>		
	RX	#1,GMBPARAMS:1,0x1,15,0xF,115200,0x1C200,NONE,ONE<CR>		
		Current MODBUS unit ID used:1		
		Current MODBUS unit ID in FLASH:15		
		Current baudrate in FLASH:115200		
		Current parity in FLASH:NONE		
		Current stopbit(s) in FLASH:ONE		
Returns the complete settings for serial interface				
ASCII COMMANDS				
RESET	ASCII WRITE COMMAND	#RST<CR> Result: #OK<CR>	ASCII	NO
	TX	#1,RST<CR>		
	RX	N/A		
Executes a software reset (Reboot) of the module.				
FACTORY RESET	ASCII WRITE COMMAND	#FRST<CR> Result: #OK<CR>	ASCII	NO
	TX	#1,FRST<CR>		
	RX	N/A		
Performs a factory reset of all internal saved parameters				
SET MODBUS WATCHDOG TIMER	ASCII WRITE COMMAND	#SMBWATCHDOG:<WDTIME><CR> Result: #OK<CR>	ASCII	YES

	WDTIME	100		
	TX	#1,SMBWATCHDOG:100<CR>		
	RX	#1,OK<CR>		
Enables or disables the WATCHDOG Timer for the IO module. WDTIME: 1..65535: Time for Watchdog in 1/100s =0: Watchdog is deactivated HINT: The Watchdog is internally handled every 100ms. If the IO module receives no valid frame within this time period, the outputs are set to predefined values!				
GET MODBUS WATCHDOG TIMER	ASCII READ COMMAND	#GMBWATCHDOG<CR> Result: #GMBWATCHDOG:<WDTIME><CR>	ASCII	
	TX	#1,GMBWATCHDOG<CR>		
	RX	#1,GMBWATCHDOG:0,0x0<CR>		
		Current watchdog time:0 -> 0,0s		
Shows the actual configured time for the telegram watchdog function of the IO module. WDTIME: 1..65535: Time for Watchdog in 1/100s =0: Watchdog is deactivated HINT: The Watchdog is internally handled every 100ms. If the IO module receives no valid frame within this time period, the outputs are set to predefined values!				
CPU PARAMETERS				
GET CPU VOLTAGE	ASCII READ COMMAND	#GCPUTEMP<CR> Result: #GCPUTEMP:<CPUTemp><CR>	ASCII	
	TX	#1,GCPUTEMP<CR>		
	RX	#1,GCPUTEMP:41.0951<CR>		
		Current internal temperature of CPU:41.0951°C		
Current internal temperature of CPU in ° Celsius.				
GET CPU VOLTAGE	ASCII READ COMMAND	#GCPUVOLT<CR> Result: #GCPUVOLT:<CPUVoltage><CR>	ASCII	
	TX	#1,GCPUVOLT<CR>		
	RX	#1,GCPUVOLT:3.3908<CR>		
		Current supply voltage of CPU:3.3908V		
Current internal supply voltage of CPU in Volt.				
GET CPU BACKUP	ASCII READ COMMAND	#GCPUBACK<CR> Result: #GCPUBACK:<CPUBackupVoltage><CR>	ASCII	
	TX	#1,GCPUBACK<CR>		
	RX	#1,GCPUBACK:0.0004<CR>		
		Current backup voltage of CPU:0.0004V		
Current internal backup capacitor voltage of CPU in Volt.				

Register NAME	MODBUS Register	Register VALUE	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
PRODUCT DATA						
HW_GROUP	3x65201 4x65201 I:65200	16384,0x4000 B:40 00			UINT16 R/O	
This is the group of hardware of the current product						
SW_GROUP	3x65202 4x65202 I:65201	32773,0x8005 B:80 05			UINT16 R/O	
This is the group of software of the current product						
SW_VERSION	3x65203 4x65203 I:65202	4608,0x1200 B:12 00			UINT16 R/O	
		SW VERSION:1.2.0				
This is the current software version of the firmware						
SW_AUTHOR	3x65204 4x65204 I:65203	18771,0x4953 B:49 53			UINT16 R/O	
This is the current software author of the firmware						
MODBUS SETTINGS						
UNIT_ID	3x65222 4x65222 I:65221	1,0x0001 B:00 01			UINT16 R/O	
		UNIT ID:1				
If the host reads this register, the current defined unit ID is returned.						
FLASH UNIT_ID	3x65223 4x65223 I:65222	15,0x000F B:00 0F		27	UINT16 R/W	NO
		UNIT ID:15				
If the host reads this register, the current defined unit ID from the FLASH is returned. This UnitID is used if DIP switch for UnitID is set to 15						
HINT:This settings will be active after you repower or reset your device !!						
BAUD_RATE	3x65224 4x65224 I:65223	115200,0x0001C200 B:00 01 C2 00	57600	57600	UINT32 R/W	NO
		115200Bd		ENTER BAUD RATE		
This is the current configured baud rate in the FLASH For ULTRA SLIM IOs RESI-xxx-SIO: This baudrate is only used, if DIP switch mode DIP1=ON+DIP2=ON (BR) (default is 57600bd) For BIG IOs RESI-xxx-SIO: This baudrate is only used, if DIP switch mode DIP7=ON (PARAMETER) (default is 57600bd)						

Valid baud rates are:
300bd, 600bd, 900bd, 1200bd, 2400bd, 4800bd,
9600bd, 19200bd, 38400bd, 57600bd, 115200bd, 128000bd
230400bd, 250000bd, 256000bd

HINT:This settings will be active after you repower or reset your device !!

PARITY	3x65226 4x65226 I:65225	0,0x0000 B:00 00		1:EVEN PARITY	UINT16 R/W	NO
		NO PARITY		SELECT PARITY		

If the register is read out, the currently set parity of the serial interface is returned.
Writing a value to this register will change the new parity in FLASH. This will only take effect after a restart of the module. This can be triggered by writing to the RESET SYSTEM register.

Parity values are
0: no parity
1: even parity
2: odd parity

STOP BITS	3x65227 4x65227 I:65226	1,0x0001 B:00 01		2:TWO STOPBITS	UINT16 R/W	NO
		ONE STOPBIT		SELECT STOPBITS		

If the register is read out, the currently set number of stop bits of the serial interface is returned.
Writing a value to this register will change the new number of stop bits in the FLASH. This will only take effect after a restart of the module. This can be triggered by writing to the RESET SYSTEM register.

Values for stop bits are
1: one stop bit
2: two stop bits

MODBUS TIMING	3x65228 4x65228 I:65227	0,0x0000 B:00 00		10	UINT16 R/W	NO
		Actual timing:0ms				

If the host reads this register, the current defined timing for MODBUS telegrams is returned. This timing is a time in ms which extends the standard 1.5 character timeout between two consecutive bytes on the serial line.
If you write a new value to this register, the new settings are stored into the internal FLASH. Reboot the device to activate the new settings.

MODBUS WATCHDOG TIME	3x65229 4x65229 I:65228	0,0x0000 B:00 00		50	UINT16 R/W	YES
		Actual watchdog time in 1/100s:0 -> 0,0s				

Writing a value onto this register defines a new time for the internal communcation watchdog timer. The value is a timespan in 1/100s.
=0: The communcation watchdog is disabled
=1..65535: Communcation watchdog will be trigegred after x 1/100s pause on communcation line

In case of an communcation watchdog, the module sets all outputs to the states defined in the configuration output registers

Reading this registe will return the current stored time from the internal FLASH

CPU DATA

SERIAL1	3x65521 4x65521 I:65520	56,0x0038 B:00 38			UINT16 R/O	

Serial number of module as 96 bit unsigned integer number						
SERIAL2	3x65522 4x65522 I:65521	45,0x002D B:00 2D			UINT16 R/O	
SERIAL3	3x65523 4x65523 I:65522	22292,0x5714 B:57 14			UINT16 R/O	
SERIAL4	3x65524 4x65524 I:65523	21827,0x5543 B:55 43			UINT16 R/O	
SERIAL5	3x65525 4x65525 I:65524	13365,0x3435 B:34 35			UINT16 R/O	
SERIAL6	3x65526 4x65526 I:65525	8249,0x2039 B:20 39			UINT16 R/O	
		SERIAL:38002D001457435535343920				
Serial number of module as 96 bit unsigned integer number						
CPU TEMPERATURE	3x65527 4x65527 I:65526	4115,0x1013 B:10 13			UINT16 R/O	
		Current internal temperature of CPU:41,2°C				
Current internal temperature of CPU in ° Celsius multiplied by 10.						
CPU VOLTAGE	3x65528 4x65528 I:65527	339,0x0153 B:01 53			UINT16 R/O	
		Current supply voltage of CPU:3,39V				
Current internal supply voltage of CPU in Volt multiplied by 1000.						
CPU BACKUP VOLTAGE	3x65529 4x65529 I:65528	0,0x0000 B:00 00			UINT16 R/O	
		Current backup voltage of CPU:0,00V				
Current internal backup capacitor voltage of CPU in Volt multiplied by 1000.						
DIP SWITCH STATUS						
DIP SWITCH	3x65300 4x65300 I:65299	65,0x0041 B:00 41			UINT16 R/O	
Returns the current setting of the Dip switches. Bit 0: DIP Switch 1 (=0:OFF, =1:ON) Bit 1: DIP Switch 2 (=0:OFF, =1:ON) Bit 2: DIP Switch 3 (=0:OFF, =1:ON) Bit 3: DIP Switch 4 (=0:OFF, =1:ON) Bit 4: DIP Switch 5 (=0:OFF, =1:ON) Bit 5: DIP Switch 6 (=0:OFF, =1:ON) Bit 6: DIP Switch 7 (=0:OFF, =1:ON) Bit 7: DIP Switch 8 (=0:OFF, =1:ON)						

CONVERTER STATUS						
CONVERTER STATUS	3x65534 4x65534 I:65533	0,0x0000 B:00 00			UINT16 R/O	
Current status of the converter						
FACTORY RESET	3x65535 4x65535 I:65534	0,0x0000 B:00 00		1:PERFORM FACTORY RESET	UINT16 R/W	NO
Performs a factory reset of all internal saved parameters						
SOFTWARE RESET						
RESET	1x65536 2x65536 I:65535	0,0x00 B:00		N/A:NO CHANGE	BIT R/W	NO
Performs a software reset, whenever 1 is written to this register. If the host writes to this register 1, the module executes a soft reset (reboot).						
RESET	3x65536 4x65535 I:65535	0,0x0000 B:00 00		N/A:NO CHANGE	UINT16 R/W	NO
Performs a software reset, whenever 1 is written to this register. If the host writes to this register 1, the module executes a soft reset (reboot).						

Command NAME	ASCII command type	ASCII command structure	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
ASCII COMMANDS						
DIGITAL INPUTS						
GET DIGITAL INPUTS	ASCII READ COMMAND	#GDIS<CR> Result: #GDIS:<DIS1Dec>,<DIS2Dec>,<DIS1Hex>,<DIS2Hex><CR>			ASCII	
	TX	#1,GDIS<CR>				
	RX	#1,GDIS:0,2147483648,0x0,0x80000000<CR>				
		Actual status of digital inputs DI1-DI32:0000.0000.0000.0000				
		Actual status of digital inputs DI33-DI64:0000.0000.0000.0000				
Returns the actual state of all digital inputs as decimal number and as hexadecimal number. DIS1Dec, DIS1Hex The current state of all digital inputs: Bit 0: State of DI1 (=0:OFF, =1:ON) Bit 1: State of DI2 (=0:OFF, =1:ON) Bit 2: State of DI3 (=0:OFF, =1:ON) ... Bit 29: State of DI30 (=0:OFF, =1:ON) Bit 30: State of DI31 (=0:OFF, =1:ON) Bit 31: State of DI32 (=0:OFF, =1:ON)						
DIS2Dec, DIS2Hex The current state of all digital inputs: Bit 0: State of DI33 (=0:OFF, =1:ON) Bit 1: State of DI34 (=0:OFF, =1:ON) Bit 2: State of DI35 (=0:OFF, =1:ON) ... Bit 29: State of DI62 (=0:OFF, =1:ON) Bit 30: State of DI63 (=0:OFF, =1:ON) Bit 31: State of DI64 (=0:OFF, =1:ON)						
GET DIGITAL INPUT DIx	ASCII READ COMMAND	#GDI<DINR><CR> Result: #GDI<DINR>:<DIxDec>,<DIxHex><CR>			ASCII	
	DINR	1				
	TX	#1,GDI1<CR>				
	RX	#1,GDI1:0,0x0<CR>				
		Actual status of digital input DI1:0=OFF				
<DINR>: 1=DI1..64=DI64						
Returns the actual state of the digital input Dix as decimal number and as hexadecimal number. DIxDec, DIxHex: The current state of the digital input x: =0: Digital input is OFF =1: Digital input is ON						

GET ALL CHANGES	ASCII READ COMMAND	#GAC<CR> Result: #GAC:<ChangesDec>,<ChangesHex><CR>	ASCII	
	TX	#1,GAC<CR>		
	RX	#1,GAC:1,0x1<CR>		
		Actual change counter:1		
Returns the counter for changes on all digital inputs. As soon as the module detects a short keypress or long key press or long key release event, this counter is incremented by 1. If this values has changed sience the last polling request, the host knows, that at least one digital input has changed its state.				
CHANGE ALL DIS PART x	ASCII READ COMMAND	#CADISP<PART><CR> Result: #CADISP<PART>:<ChangeDInDec>,...,<ChangeDIn+15Dec>, <ChangeDInHex>,...,<ChangeDIn+15Hex><CR>	ASCII	
	PART	4		
	TX	#1,CADISP4<CR>		
	RX	#1,CADISP4:0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,1,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x1<CR>		
		Actual counter for changes on DI49:0		
		Actual counter for changes on DI50:0		
		Actual counter for changes on DI51:0		
		Actual counter for changes on DI52:0		
		Actual counter for changes on DI53:0		
		Actual counter for changes on DI54:0		
		Actual counter for changes on DI55:0		
		Actual counter for changes on DI56:0		
		Actual counter for changes on DI57:0		
		Actual counter for changes on DI58:0		
		Actual counter for changes on DI59:0		
		Actual counter for changes on DI60:0		
		Actual counter for changes on DI61:0		
		Actual counter for changes on DI62:0		
		Actual counter for changes on DI63:0		
		Actual counter for changes on DI64:1		
<PART>: 1..4, 1=DI1-DI16, 2=DI17-DI32, 3=DI33-DI48, 4=DI49-DI64				
Returns for each digital input the counter for changes. As soon as the module detects a signal change on a digital input, the change counter for the affected digital input is incremented by 1. A signal change can be: Detection of a short keypress Detection of the start of a long keypress Detection of a release of a long keypress The parameter <PART> defines the part of the digital inputs. The command returns maximal 16 digital inputs.				
CHANGE DIx	ASCII READ COMMAND	#CDI<DINR><CR> Result: #CDI<DINR>:<ChangesDec>,<ChangesHex><CR>	ASCII	

	DINR	1		
	TX	#1,CDI1<CR>		
	RX	#1,CDI1:0,0x0<CR>		
		Actual counter for changes on digital input DI1:0		
<DINR>: 1=DI1..64=DI64				
Returns for digital input <DINR> the counter for signal changes. As soon as the module detects a signal change on a digital input, the change counter for the affected digital input is incremented by 1. A signal change can be: Detection of a short keypress Detection of the start of a long keypress Detection of a release of a long keypress				
SHORT KEY ALL DIS PART x	ASCII READ COMMAND	#SKADISP<PART><CR> Result: #SKADISP<PART>:<ShortKeyDInDec>,...,<ShortKeyDIn+15Dec>, <ShortKeyDInHex>,...,<ShortKeyDIn+15Hex><CR>	ASCII	
	PART	4		
	TX	#1,SKADISP4<CR>		
	RX	#1,SKADISP4:0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0<CR>		
		Actual counter for short keypress events on DI49:0		
		Actual counter for short keypress events on DI50:0		
		Actual counter for short keypress events on DI51:0		
		Actual counter for short keypress events on DI52:0		
		Actual counter for short keypress events on DI53:0		
		Actual counter for short keypress events on DI54:0		
		Actual counter for short keypress events on DI55:0		
		Actual counter for short keypress events on DI56:0		
		Actual counter for short keypress events on DI57:0		
		Actual counter for short keypress events on DI58:0		
		Actual counter for short keypress events on DI59:0		
		Actual counter for short keypress events on DI60:0		
		Actual counter for short keypress events on DI61:0		
		Actual counter for short keypress events on DI62:0		
		Actual counter for short keypress events on DI63:0		
		Actual counter for short keypress events on DI64:0		
<PART>: 1..4, 1=DI1-DI16, 2=DI17-DI32, 3=DI33-DI48, 4=DI49-DI64				
Returns for each digital input the counter for short keypress events. As soon as the module detects a short keypress on a digital input, the counter for the affected digital input is incremented by 1. The parameter <PART> defines the part of the digital inputs. The command returns maximal 16 digital inputs.				
SHORT KEY DIx	ASCII READ COMMAND	#SKDI<DINR><CR> Result: #SKDI<DINR>:<ShortKeyDec>,<ShortKeyHex><CR>	ASCII	
	DINR	1		
	TX	#1,SKDI1<CR>		
	RX	#1,SKDI1:0,0x0<CR>		

		Actual counter for short keypress events on digital input DI1:0		
<DINR>: 1=DI1..64=DI64				
Returns for digital input <DINR> the counter for short keypress events. As soon as the module detects a short keypress on a digital input, the counter for the affected digital input is incremented by 1.				
LONG KEY START ALL DIS PART x	ASCII READ COMMAND	#LKSADISP<PART><CR> Result: #LKSADISP<PART>:<LongKeyStartDInDec>,...,<LongKeyStartDIn+15Dec>, <LongKeyStartDInHex>,...,<LongKeyStartDIn+15Hex><CR>	ASCII	
	PART	4		
	TX	#1,LKSADISP4<CR>		
	RX	#1,LKSADISP4:0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,1,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x1<CR>		
		Actual counter for long keypress start events on DI49:0		
		Actual counter for long keypress start events on DI50:0		
		Actual counter for long keypress start events on DI51:0		
		Actual counter for long keypress start events on DI52:0		
		Actual counter for long keypress start events on DI53:0		
		Actual counter for long keypress start events on DI54:0		
		Actual counter for long keypress start events on DI55:0		
		Actual counter for long keypress start events on DI56:0		
		Actual counter for long keypress start events on DI57:0		
		Actual counter for long keypress start events on DI58:0		
		Actual counter for long keypress start events on DI59:0		
		Actual counter for long keypress start events on DI60:0		
		Actual counter for long keypress start events on DI61:0		
		Actual counter for long keypress start events on DI62:0		
		Actual counter for long keypress start events on DI63:0		
		Actual counter for long keypress start events on DI64:1		
<PART>: 1..4, 1=DI1-DI16, 2=DI17-DI32, 3=DI33-DI48, 4=DI49-DI64				
Returns for each digital input the counter for long keypress start events. As soon as the module detects the start of a long keypress on a digital input, the counter for the affected digital input is incremented by 1. The parameter <PART> defines the part of the digital inputs. The command returns maximal 16 digital inputs.				
LONG KEY START DIx	ASCII READ COMMAND	#LKSDI<DINR><CR> Result: #LKSDI<DINR>:<LongKeyStartDec>,<LongKeyStartHex><CR>	ASCII	
	DINR	1		
	TX	#1,LKSDI1<CR>		
	RX	#1,LKSDI1:0,0x0<CR>		
		Actual counter for long keypress start events on digital input DI1:0		
<DINR>: 1=DI1..64=DI64				
Returns for digital input <DINR> the counter for long keypress start events. As soon as the module detects the start of a long keypress on a digital input, the counter for the affected digital input is incremented by 1.				

LONG KEY END ALL DIS PART x	ASCII READ COMMAND	#LKEADISP<PART><CR> Result: #LKEADISP<PART>:<LongKeyEndDInDec>,...,<LongKeyEndDIn+15Dec>, <LongKeyEndDInHex>,...,<LongKeyEndDIn+15Hex><CR>	ASCII	
	PART	4		
	TX	#1,LKEADISP4<CR>		
	RX	#1,LKEADISP4:0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0, 0x0<CR>		
		Actual counter for long keypress end events on DI49:0		
		Actual counter for long keypress end events on DI50:0		
		Actual counter for long keypress end events on DI51:0		
		Actual counter for long keypress end events on DI52:0		
		Actual counter for long keypress end events on DI53:0		
		Actual counter for long keypress end events on DI54:0		
		Actual counter for long keypress end events on DI55:0		
		Actual counter for long keypress end events on DI56:0		
		Actual counter for long keypress end events on DI57:0		
		Actual counter for long keypress end events on DI58:0		
		Actual counter for long keypress end events on DI59:0		
		Actual counter for long keypress end events on DI60:0		
		Actual counter for long keypress end events on DI61:0		
		Actual counter for long keypress end events on DI62:0		
		Actual counter for long keypress end events on DI63:0		
		Actual counter for long keypress end events on DI64:0		
<PART>: 1..4, 1=DI1-DI16, 2=DI17-DI32, 3=DI33-DI48, 4=DI49-DI64				
Returns for each digital input the counter for long keypress end events. As soon as the module detects the end of a long keypress on a digital input, the counter for the affected digital input is incremented by 1. The parameter <PART> defines the part of the digital inputs. The command returns maximal 16 digital inputs.				
LONG KEY END DIx	ASCII READ COMMAND	#LKEDI<DINR><CR> Result: #LKEDI<DINR>:<LongKeyEndDec>,<LongKeyEndHex><CR>	ASCII	
	DINR	1		
	TX	#1,LKEDI1<CR>		
	RX	#1,LKEDI1:0,0x0<CR>		
		Actual counter for long keypress end events on digital input DI1:0		
<DINR>: 1=DI1..64=DI64				
Returns for digital input <DINR> the counter for long keypress end events. As soon as the module detects the end of a long keypress on a digital input, the counter for the affected digital input is incremented by 1.				
RISE ALL DIS PART x	ASCII READ COMMAND	#RADISP<PART><CR> Result: #RADISP<PART>:<RiseDInDec>,...,<RiseDIn+15Dec>, <RiseDInHex>,...,<RiseDIn+15Hex><CR>	ASCII	
	PART	4		

	TX	#1,RADISP4<CR>		
	RX	#1,RADISP4:0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,1,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x1<CR>		
		Actual counter for rising edges on DI49:0		
		Actual counter for rising edges on DI50:0		
		Actual counter for rising edges on DI51:0		
		Actual counter for rising edges on DI52:0		
		Actual counter for rising edges on DI53:0		
		Actual counter for rising edges on DI54:0		
		Actual counter for rising edges on DI55:0		
		Actual counter for rising edges on DI56:0		
		Actual counter for rising edges on DI57:0		
		Actual counter for rising edges on DI58:0		
		Actual counter for rising edges on DI59:0		
		Actual counter for rising edges on DI60:0		
		Actual counter for rising edges on DI61:0		
		Actual counter for rising edges on DI62:0		
		Actual counter for rising edges on DI63:0		
		Actual counter for rising edges on DI64:1		
<PART>: 1..4, 1=DI1-DI16, 2=DI17-DI32, 3=DI33-DI48, 4=DI49-DI64				
Returns for each digital input the counter for rising edges. As soon as the module detects a rising edge on a digital input, the rising edge counter for the affected digital input is incremented by 1. The parameter <PART> defines the part of the digital inputs. The command returns maximal 16 digital inputs.				
RISE DIx	ASCII READ COMMAND	#RDI<DINR><CR> Result: #RDI<DINR>:<RiseDec>,<RiseHex><CR>	ASCII	
	DINR	1		
	TX	#1,RDI1<CR>		
	RX	#1,RDI1:0,0x0<CR>		
		Actual counter for rising edges on digital input DI1:0		
<DINR>: 1=DI1..64=DI64				
Returns for digital input <DINR> the counter for rising edges. As soon as the module detects a rising edge on a digital input, the rising edge counter for the affected digital input is incremented by 1.				
FALL ALL DIS PART x	ASCII READ COMMAND	#FADISP<PART><CR> Result: #FADISP<PART>:<FallDInDec>,...,<FallDIn+15Dec>, <FallDInHex>,...,<FallDIn+15Hex><CR>	ASCII	
	PART	4		
	TX	#1,FADISP4<CR>		
	RX	#1,FADISP4:0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0<CR>		
		Actual counter for falling edges on DI49:0		
		Actual counter for falling edges on DI50:0		

		Actual counter for falling edges on DI51:0		
		Actual counter for falling edges on DI52:0		
		Actual counter for falling edges on DI53:0		
		Actual counter for falling edges on DI54:0		
		Actual counter for falling edges on DI55:0		
		Actual counter for falling edges on DI56:0		
		Actual counter for falling edges on DI57:0		
		Actual counter for falling edges on DI58:0		
		Actual counter for falling edges on DI59:0		
		Actual counter for falling edges on DI60:0		
		Actual counter for falling edges on DI61:0		
		Actual counter for falling edges on DI62:0		
		Actual counter for falling edges on DI63:0		
		Actual counter for falling edges on DI64:0		
<PART>: 1..4, 1=DI1-DI16, 2=DI17-DI32, 3=DI33-DI48, 4=DI49-DI64				
Returns for each digital input the counter for falling edges. As soon as the module detects a falling edge on a digital input, the falling edge counter for the affected digital input is incremented by 1. The parameter <PART> defines the part of the digital inputs. The command returns maximal 16 digital inputs.				
FALL DIx	ASCII READ COMMAND	#FDI<DINR><CR> Result: #FDI<DINR>:<FallDec>,<FallHex><CR>	ASCII	
	DINR	1		
	TX	#1,FDI1<CR>		
	RX	#1,FDI1:0,0x0<CR>		
		Actual counter for falling edges on digital input DI1:0		
<DINR>: 1=DI1..64=DI64				
Returns for digital input <DINR> the counter for falling edges. As soon as the module detects a falling edge on a digital input, the falling edge counter for the affected digital input is incremented by 1.				
RESET COUNTERS	ASCII WRITE COMMAND	#RC<CR> Result: #OK<CR>	ASCII	NO
	TX	#1,RC<CR>		
	RX	N/A		
Resets all internal counters for digital inputs and events on this digital inputs to 0.				
DIGITAL INPUTS EVENTs				
EVENTS ON	ASCII WRITE COMMAND	#EVTON<CR> Result: #OK<CR>	ASCII	NO
	TX	#1,EVTON<CR>		
	RX	#1,OK<CR>		
Activates event sending of changes on digital inputs Whenever a change is detected on the digital inputs, the IO module sends immediately #<BusAdr>,EVT:DIS:<AllDISasDec>,<AllDISasHex><CR>				

EVENTS OFF	ASCII WRITE COMMAND	#EVTOFF<CR> Result: #OK<CR>	ASCII	NO
	TX	#1,EVTOFF<CR>		
	RX	#1,OK<CR>		
Deactivates event sending of changes on digital inputs Whenever a change is detected on the digital inputs, the IO module sends immediately #<BusAdr>,EVT:DIS:<AllDISasDec>,<AllDISasHex><CR>				

Register NAME Command NAME	MODBUS Register ASCII Command	Register VALUE ASCII Command	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
STATUS DIGITAL INPUTS						
DI1	1x00001 2x00001 I:0	0,0x00 B:00			BIT R/O	
		Actual state of DI1:0=OFF				
Current state of the digital input DIx =0:DI is OFF, =1:DI is ON						
DI2	1x00002 2x00002 I:1	0,0x00 B:00			BIT R/O	
		Actual state of DI2:0=OFF				
DI3	1x00003 2x00003 I:2	0,0x00 B:00			BIT R/O	
		Actual state of DI3:0=OFF				
DI4	1x00004 2x00004 I:3	0,0x00 B:00			BIT R/O	
		Actual state of DI4:0=OFF				
DI5	1x00005 2x00005 I:4	0,0x00 B:00			BIT R/O	
		Actual state of DI5:0=OFF				
DI6	1x00006 2x00006 I:5	0,0x00 B:00			BIT R/O	
		Actual state of DI6:0=OFF				
DI7	1x00007 2x00007 I:6	0,0x00 B:00			BIT R/O	
		Actual state of DI7:0=OFF				
DI8	1x00008 2x00008 I:7	0,0x00 B:00			BIT R/O	
		Actual state of DI8:0=OFF				
DI9	1x00009 2x00009 I:8	0,0x00 B:00			BIT R/O	
		Actual state of DI9:0=OFF				

DI10	1x00010 2x00010 I:9	0,0x00 B:00			BIT R/O	
		Actual state of DI10:0=OFF				
DI11	1x00011 2x00011 I:10	0,0x00 B:00			BIT R/O	
		Actual state of DI11:0=OFF				
DI12	1x00012 2x00012 I:11	0,0x00 B:00			BIT R/O	
		Actual state of DI12:0=OFF				
DI13	1x00013 2x00013 I:12	0,0x00 B:00			BIT R/O	
		Actual state of DI13:0=OFF				
DI14	1x00014 2x00014 I:13	0,0x00 B:00			BIT R/O	
		Actual state of DI14:0=OFF				
DI15	1x00015 2x00015 I:14	0,0x00 B:00			BIT R/O	
		Actual state of DI15:0=OFF				
DI16	1x00016 2x00016 I:15	0,0x00 B:00			BIT R/O	
		Actual state of DI16:0=OFF				
DI17	1x00017 2x00017 I:16	0,0x00 B:00			BIT R/O	
		Actual state of DI17:0=OFF				
DI18	1x00018 2x00018 I:17	0,0x00 B:00			BIT R/O	
		Actual state of DI18:0=OFF				
DI19	1x00019 2x00019 I:18	0,0x00 B:00			BIT R/O	
		Actual state of DI19:0=OFF				
DI20	1x00020 2x00020 I:19	0,0x00 B:00			BIT R/O	
		Actual state of DI20:0=OFF				

DI21	1x00021 2x00021 I:20	0,0x00 B:00			BIT R/O	
		Actual state of DI21:0=OFF				
DI22	1x00022 2x00022 I:21	0,0x00 B:00			BIT R/O	
		Actual state of DI22:0=OFF				
DI23	1x00023 2x00023 I:22	0,0x00 B:00			BIT R/O	
		Actual state of DI23:0=OFF				
DI24	1x00024 2x00024 I:23	0,0x00 B:00			BIT R/O	
		Actual state of DI24:0=OFF				
DI25	1x00025 2x00025 I:24	0,0x00 B:00			BIT R/O	
		Actual state of DI25:0=OFF				
DI26	1x00026 2x00026 I:25	0,0x00 B:00			BIT R/O	
		Actual state of DI26:0=OFF				
DI27	1x00027 2x00027 I:26	0,0x00 B:00			BIT R/O	
		Actual state of DI27:0=OFF				
DI28	1x00028 2x00028 I:27	0,0x00 B:00			BIT R/O	
		Actual state of DI28:0=OFF				
DI29	1x00029 2x00029 I:28	0,0x00 B:00			BIT R/O	
		Actual state of DI29:0=OFF				
DI30	1x00030 2x00030 I:29	0,0x00 B:00			BIT R/O	
		Actual state of DI30:0=OFF				
DI31	1x00031 2x00031 I:30	0,0x00 B:00			BIT R/O	
		Actual state of DI31:0=OFF				

DI32	1x00032 2x00032 I:31	0,0x00 B:00			BIT R/O	
		Actual state of DI32:0=OFF				
DI33	1x00033 2x00033 I:32	0,0x00 B:00			BIT R/O	
		Actual state of DI33:0=OFF				
DI34	1x00034 2x00034 I:33	0,0x00 B:00			BIT R/O	
		Actual state of DI34:0=OFF				
DI35	1x00035 2x00035 I:34	0,0x00 B:00			BIT R/O	
		Actual state of DI35:0=OFF				
DI36	1x00036 2x00036 I:35	0,0x00 B:00			BIT R/O	
		Actual state of DI36:0=OFF				
DI37	1x00037 2x00037 I:36	0,0x00 B:00			BIT R/O	
		Actual state of DI37:0=OFF				
DI38	1x00038 2x00038 I:37	0,0x00 B:00			BIT R/O	
		Actual state of DI38:0=OFF				
DI39	1x00039 2x00039 I:38	0,0x00 B:00			BIT R/O	
		Actual state of DI39:0=OFF				
DI40	1x00040 2x00040 I:39	0,0x00 B:00			BIT R/O	
		Actual state of DI40:0=OFF				
DI41	1x00041 2x00041 I:40	0,0x00 B:00			BIT R/O	
		Actual state of DI41:0=OFF				
DI42	1x00042 2x00042 I:41	0,0x00 B:00			BIT R/O	
		Actual state of DI42:0=OFF				

DI43	1x00043 2x00043 I:42	0,0x00 B:00			BIT R/O	
		Actual state of DI43:0=OFF				
DI44	1x00044 2x00044 I:43	0,0x00 B:00			BIT R/O	
		Actual state of DI44:0=OFF				
DI45	1x00045 2x00045 I:44	0,0x00 B:00			BIT R/O	
		Actual state of DI45:0=OFF				
DI46	1x00046 2x00046 I:45	0,0x00 B:00			BIT R/O	
		Actual state of DI46:0=OFF				
DI47	1x00047 2x00047 I:46	0,0x00 B:00			BIT R/O	
		Actual state of DI47:0=OFF				
DI48	1x00048 2x00048 I:47	0,0x00 B:00			BIT R/O	
		Actual state of DI48:0=OFF				
DI49	1x00049 2x00049 I:48	0,0x00 B:00			BIT R/O	
		Actual state of DI49:0=OFF				
DI50	1x00050 2x00050 I:49	0,0x00 B:00			BIT R/O	
		Actual state of DI50:0=OFF				
DI51	1x00051 2x00051 I:50	0,0x00 B:00			BIT R/O	
		Actual state of DI51:0=OFF				
DI52	1x00052 2x00052 I:51	0,0x00 B:00			BIT R/O	
		Actual state of DI52:0=OFF				
DI53	1x00053 2x00053 I:52	0,0x00 B:00			BIT R/O	
		Actual state of DI53:0=OFF				

DI54	1x00054 2x00054 I:53	0,0x00 B:00			BIT R/O	
		Actual state of DI54:0=OFF				
DI55	1x00055 2x00055 I:54	0,0x00 B:00			BIT R/O	
		Actual state of DI55:0=OFF				
DI56	1x00056 2x00056 I:55	0,0x00 B:00			BIT R/O	
		Actual state of DI56:0=OFF				
DI57	1x00057 2x00057 I:56	0,0x00 B:00			BIT R/O	
		Actual state of DI57:0=OFF				
DI58	1x00058 2x00058 I:57	0,0x00 B:00			BIT R/O	
		Actual state of DI58:0=OFF				
DI59	1x00059 2x00059 I:58	0,0x00 B:00			BIT R/O	
		Actual state of DI59:0=OFF				
DI60	1x00060 2x00060 I:59	0,0x00 B:00			BIT R/O	
		Actual state of DI60:0=OFF				
DI61	1x00061 2x00061 I:60	0,0x00 B:00			BIT R/O	
		Actual state of DI61:0=OFF				
DI62	1x00062 2x00062 I:61	0,0x00 B:00			BIT R/O	
		Actual state of DI62:0=OFF				
DI63	1x00063 2x00063 I:62	0,0x00 B:00			BIT R/O	
		Actual state of DI63:0=OFF				
DI64	1x00064 2x00064 I:63	0,0x00 B:00			BIT R/O	
		Actual state of DI64:0=OFF				

DIGITAL INPUTS: RESET						
RESET COUNTERS	1x10000 2x10000 I:9999	0,0x00 B:00		1:PERFORM RESET	BIT R/W	NO
If this register is written to 1, all internal edge counters and event counters are set to 0. 0 is always returned when reading.						
STATUS REAL DIGITAL INPUTS						
DI1	1x15001 2x14001 I:15000	0,0x00 B:00			BIT R/O	
		Actual state of DI1:0=OFF				
Current state of the digital input DIx with the internal software filter to suppress glitches or spike on thie line =0:DI is OFF, =1:DI is ON						
DI2	1x15002 2x15002 I:15001	0,0x00 B:00			BIT R/O	
		Actual state of DI2:0=OFF				
DI3	1x15003 2x15003 I:15002	0,0x00 B:00			BIT R/O	
		Actual state of DI3:0=OFF				
DI4	1x15004 2x15004 I:15003	0,0x00 B:00			BIT R/O	
		Actual state of DI4:0=OFF				
DI5	1x15005 2x15005 I:15004	0,0x00 B:00			BIT R/O	
		Actual state of DI5:0=OFF				
DI6	1x15006 2x15006 I:15005	0,0x00 B:00			BIT R/O	
		Actual state of DI6:0=OFF				
DI7	1x15007 2x15007 I:15006	0,0x00 B:00			BIT R/O	
		Actual state of DI7:0=OFF				
DI8	1x15008 2x15008 I:15007	0,0x00 B:00			BIT R/O	
		Actual state of DI8:0=OFF				
DI9	1x15009 2x15009 I:15008	0,0x00 B:00			BIT R/O	
		Actual state of DI9:0=OFF				

DI10	1x15010 2x15010 I:15009	0,0x00 B:00			BIT R/O	
		Actual state of DI10:0=OFF				
DI11	1x15011 2x15011 I:15010	0,0x00 B:00			BIT R/O	
		Actual state of DI11:0=OFF				
DI12	1x15012 2x15012 I:15011	0,0x00 B:00			BIT R/O	
		Actual state of DI12:0=OFF				
DI13	1x15013 2x15013 I:15012	0,0x00 B:00			BIT R/O	
		Actual state of DI13:0=OFF				
DI14	1x15014 2x15014 I:15013	0,0x00 B:00			BIT R/O	
		Actual state of DI14:0=OFF				
DI15	1x15015 2x15015 I:15014	0,0x00 B:00			BIT R/O	
		Actual state of DI15:0=OFF				
DI16	1x15016 2x15016 I:15015	0,0x00 B:00			BIT R/O	
		Actual state of DI16:0=OFF				
DI17	1x15017 2x15017 I:15016	0,0x00 B:00			BIT R/O	
		Actual state of DI17:0=OFF				
DI18	1x15018 2x15018 I:15017	0,0x00 B:00			BIT R/O	
		Actual state of DI18:0=OFF				
DI19	1x15019 2x15019 I:15018	0,0x00 B:00			BIT R/O	
		Actual state of DI19:0=OFF				
DI20	1x15020 2x15020 I:15019	0,0x00 B:00			BIT R/O	
		Actual state of DI20:0=OFF				

DI21	1x15021 2x15021 I:15020	0,0x00 B:00			BIT R/O	
		Actual state of DI21:0=OFF				
DI22	1x15022 2x15022 I:15021	0,0x00 B:00			BIT R/O	
		Actual state of DI22:0=OFF				
D23	1x15023 2x15023 I:15022	0,0x00 B:00			BIT R/O	
		Actual state of D23:0=OFF				
DI24	1x15024 2x15024 I:15023	0,0x00 B:00			BIT R/O	
		Actual state of DI24:0=OFF				
DI25	1x15025 2x15025 I:15024	0,0x00 B:00			BIT R/O	
		Actual state of DI25:0=OFF				
DI26	1x15026 2x15026 I:15025	0,0x00 B:00			BIT R/O	
		Actual state of DI26:0=OFF				
DI27	1x15027 2x15027 I:15026	0,0x00 B:00			BIT R/O	
		Actual state of DI27:0=OFF				
DI28	1x15028 2x15028 I:15027	0,0x00 B:00			BIT R/O	
		Actual state of DI28:0=OFF				
DI29	1x15029 2x15029 I:15028	0,0x00 B:00			BIT R/O	
		Actual state of DI29:0=OFF				
DI30	1x15030 2x15030 I:15029	0,0x00 B:00			BIT R/O	
		Actual state of DI30:0=OFF				
DI31	1x15031 2x15031 I:15030	0,0x00 B:00			BIT R/O	
		Actual state of DI31:0=OFF				

DI32	1x15032 2x15032 I:15031	0,0x00 B:00			BIT R/O	
		Actual state of DI32:0=OFF				
D33	1x15033 2x15033 I:15032	0,0x00 B:00			BIT R/O	
		Actual state of D33:0=OFF				
DI34	1x15034 2x15034 I:15033	0,0x00 B:00			BIT R/O	
		Actual state of DI34:0=OFF				
DI35	1x15035 2x15035 I:15034	0,0x00 B:00			BIT R/O	
		Actual state of DI35:0=OFF				
DI36	1x15036 2x15036 I:15035	0,0x00 B:00			BIT R/O	
		Actual state of DI36:0=OFF				
DI37	1x15037 2x15037 I:15036	0,0x00 B:00			BIT R/O	
		Actual state of DI37:0=OFF				
DI38	1x15038 2x15038 I:15037	0,0x00 B:00			BIT R/O	
		Actual state of DI38:0=OFF				
DI39	1x15039 2x15039 I:15038	0,0x00 B:00			BIT R/O	
		Actual state of DI39:0=OFF				
DI40	1x15040 2x15040 I:15039	0,0x00 B:00			BIT R/O	
		Actual state of DI40:0=OFF				
DI41	1x15041 2x15041 I:15040	0,0x00 B:00			BIT R/O	
		Actual state of DI41:0=OFF				
DI42	1x15042 2x15042 I:15041	0,0x00 B:00			BIT R/O	
		Actual state of DI42:0=OFF				

D43	1x15043 2x15043 I:15042	0,0x00 B:00			BIT R/O	
		Actual state of D43:0=OFF				
DI44	1x15044 2x15044 I:15043	0,0x00 B:00			BIT R/O	
		Actual state of DI44:0=OFF				
DI45	1x15045 2x15045 I:15044	0,0x00 B:00			BIT R/O	
		Actual state of DI45:0=OFF				
DI46	1x15046 2x15046 I:15045	0,0x00 B:00			BIT R/O	
		Actual state of DI46:0=OFF				
DI47	1x15047 2x15047 I:15046	0,0x00 B:00			BIT R/O	
		Actual state of DI47:0=OFF				
DI48	1x15048 2x15048 I:15047	0,0x00 B:00			BIT R/O	
		Actual state of DI48:0=OFF				
DI49	1x15049 2x15049 I:15048	0,0x00 B:00			BIT R/O	
		Actual state of DI49:0=OFF				
DI50	1x15050 2x15050 I:15049	0,0x00 B:00			BIT R/O	
		Actual state of DI50:0=OFF				
DI51	1x15051 2x15051 I:15050	0,0x00 B:00			BIT R/O	
		Actual state of DI51:0=OFF				
DI52	1x15052 2x15052 I:15051	0,0x00 B:00			BIT R/O	
		Actual state of DI52:0=OFF				
D53	1x15053 2x15053 I:15052	0,0x00 B:00			BIT R/O	
		Actual state of D53:0=OFF				

DI54	1x15054 2x15054 I:15053	0,0x00 B:00			BIT R/O	
		Actual state of DI54:0=OFF				
DI55	1x15055 2x15055 I:15054	0,0x00 B:00			BIT R/O	
		Actual state of DI55:0=OFF				
DI56	1x15056 2x15056 I:15055	0,0x00 B:00			BIT R/O	
		Actual state of DI56:0=OFF				
DI57	1x15057 2x15057 I:15056	0,0x00 B:00			BIT R/O	
		Actual state of DI57:0=OFF				
DI58	1x15058 2x15058 I:15057	0,0x00 B:00			BIT R/O	
		Actual state of DI58:0=OFF				
DI59	1x15059 2x15059 I:15058	0,0x00 B:00			BIT R/O	
		Actual state of DI59:0=OFF				
DI60	1x15060 2x15060 I:15059	0,0x00 B:00			BIT R/O	
		Actual state of DI60:0=OFF				
DI61	1x15061 2x15061 I:15060	0,0x00 B:00			BIT R/O	
		Actual state of DI61:0=OFF				
DI62	1x15062 2x15062 I:15061	0,0x00 B:00			BIT R/O	
		Actual state of DI62:0=OFF				
DI63	1x15063 2x15063 I:15062	0,0x00 B:00			BIT R/O	
		Actual state of DI63:0=OFF				
DI64	1x15064 2x15064 I:15063	0,0x00 B:00			BIT R/O	
		Actual state of DI64:0=OFF				

STATUS DIGITAL INPUTS						
UNFILTERED DI1	1x15065 2x15065 I:15064	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI1:0=OFF				
Current state of the real digital input DIx without the internal software filter to suppress glitches or spike on thie line =0:DI is OFF, =1:DI is ON						
UNFILTERED DI2	1x15066 2x15066 I:15065	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI2:0=OFF				
UNFILTERED DI3	1x15067 2x15067 I:15066	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI3:0=OFF				
UNFILTERED DI4	1x15068 2x15068 I:15067	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI4:0=OFF				
UNFILTERED DI5	1x15069 2x15069 I:15068	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI5:0=OFF				
UNFILTERED DI6	1x15070 2x15070 I:15069	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI6:0=OFF				
UNFILTERED DI7	1x15071 2x15071 I:15070	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI7:0=OFF				
UNFILTERED DI8	1x15072 2x15072 I:15071	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI8:0=OFF				
UNFILTERED DI9	1x15073 2x15073 I:15072	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI9:0=OFF				
UNFILTERED DI10	1x15074 2x15074 I:15073	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI10:0=OFF				

UNFILTERED DI11	1x15075 2x15075 I:15074	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI11:0=OFF				
UNFILTERED DI12	1x15076 2x15076 I:15075	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI12:0=OFF				
UNFILTERED DI13	1x15077 2x15077 I:15076	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI13:0=OFF				
UNFILTERED DI14	1x15078 2x15078 I:15077	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI14:0=OFF				
UNFILTERED DI15	1x15079 2x15079 I:15078	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI15:0=OFF				
UNFILTERED DI16	1x15080 2x15080 I:15079	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI16:0=OFF				
UNFILTERED DI17	1x15081 2x15081 I:15080	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI17:0=OFF				
UNFILTERED DI18	1x15082 2x15082 I:15081	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI18:0=OFF				
UNFILTERED DI19	1x15083 2x15083 I:15082	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI19:0=OFF				
UNFILTERED DI20	1x15084 2x15084 I:15083	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI20:0=OFF				
UNFILTERED DI21	1x15085 2x15085 I:15084	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI21:0=OFF				

UNFILTERED DI22	1x15086 2x15086 I:15085	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI22:0=OFF				
UNFILTERED DI23	1x15087 2x15087 I:15086	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI23:0=OFF				
UNFILTERED DI24	1x15088 2x15088 I:15087	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI24:0=OFF				
UNFILTERED DI25	1x15089 2x15089 I:15088	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI25:0=OFF				
UNFILTERED DI26	1x15090 2x15090 I:15089	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI26:0=OFF				
UNFILTERED DI27	1x15091 2x15091 I:15090	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI27:0=OFF				
UNFILTERED DI28	1x15092 2x15092 I:15091	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI28:0=OFF				
UNFILTERED DI29	1x15093 2x15093 I:15092	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI29:0=OFF				
UNFILTERED DI30	1x15094 2x15094 I:15093	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI30:0=OFF				
UNFILTERED DI31	1x15095 2x15095 I:15094	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI31:0=OFF				
UNFILTERED DI32	1x15096 2x15096 I:15095	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI32:0=OFF				

UNFILTERED DI33	1x15097 2x15097 I:15096	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI33:0=OFF				
UNFILTERED DI34	1x15098 2x15098 I:15097	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI34:0=OFF				
UNFILTERED DI35	1x15099 2x15099 I:15098	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI35:0=OFF				
UNFILTERED DI36	1x15100 2x15100 I:15099	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI36:0=OFF				
UNFILTERED DI37	1x15101 2x15101 I:15100	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI37:0=OFF				
UNFILTERED DI38	1x15102 2x15102 I:15101	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI38:0=OFF				
UNFILTERED DI39	1x15103 2x15103 I:15102	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI39:0=OFF				
UNFILTERED DI40	1x15104 2x15104 I:15103	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI40:0=OFF				
UNFILTERED DI41	1x15105 2x15105 I:15104	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI41:0=OFF				
UNFILTERED DI42	1x15106 2x15106 I:15105	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI42:0=OFF				
UNFILTERED DI43	1x15107 2x15107 I:15106	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI43:0=OFF				

UNFILTERED DI44	1x15108 2x15108 I:15107	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI44:0=OFF				
UNFILTERED DI45	1x15109 2x15109 I:15108	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI45:0=OFF				
UNFILTERED DI46	1x15110 2x15110 I:15109	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI46:0=OFF				
UNFILTERED DI47	1x15111 2x15111 I:15110	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI47:0=OFF				
UNFILTERED DI48	1x15112 2x15112 I:15111	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI48:0=OFF				
UNFILTERED DI49	1x15113 2x15113 I:15112	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI49:0=OFF				
UNFILTERED DI50	1x15114 2x15114 I:15113	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI50:0=OFF				
UNFILTERED DI51	1x15115 2x15115 I:15114	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI51:0=OFF				
UNFILTERED DI52	1x15116 2x15116 I:15115	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI52:0=OFF				
UNFILTERED DI53	1x15117 2x15117 I:15116	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI53:0=OFF				
UNFILTERED DI54	1x15118 2x15118 I:15117	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI54:0=OFF				

UNFILTERED DI55	1x15119 2x15119 I:15118	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI55:0=OFF				
UNFILTERED DI56	1x15120 2x15120 I:15119	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI56:0=OFF				
UNFILTERED DI57	1x15121 2x15121 I:15120	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI57:0=OFF				
UNFILTERED DI58	1x15122 2x15122 I:15121	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI58:0=OFF				
UNFILTERED DI59	1x15123 2x15123 I:15122	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI59:0=OFF				
UNFILTERED DI60	1x15124 2x15124 I:15123	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI60:0=OFF				
UNFILTERED DI61	1x15125 2x15125 I:15124	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI61:0=OFF				
UNFILTERED DI62	1x15126 2x15126 I:15125	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI62:0=OFF				
UNFILTERED DI63	1x15127 2x15127 I:15126	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI63:0=OFF				
UNFILTERED DI64	1x15128 2x15128 I:15127	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI64:0=OFF				

Register NAME Command NAME	MODBUS Register ASCII Command	Register VALUE ASCII Command	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
DIGITAL INPUTS: DIGITAL INPUT HAS CHANGED IT'S STATE						
DI HAS CHANGED DI1	1x20001 2x20001 I:20000	0,0x00 B:00			BIT R/O	
If the digital input has changed this bit inverts its last state						
DI HAS CHANGED DI2	1x20002 2x20002 I:20001	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI3	1x20003 2x20003 I:20002	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI4	1x20004 2x20004 I:20003	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI5	1x20005 2x20005 I:20004	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI6	1x20006 2x20006 I:20005	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI7	1x20007 2x20007 I:20006	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI8	1x20008 2x20008 I:20007	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI9	1x20009 2x20009 I:20008	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI10	1x20010 2x20010 I:20009	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI11	1x20011 2x20011 I:20010	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI12	1x20012 2x20012 I:20011	0,0x00 B:00			BIT R/O	

DI HAS CHANGED DI13	1x20013 2x20013 I:20012	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI14	1x20014 2x20014 I:20013	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI15	1x20015 2x20015 I:20014	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI16	1x20016 2x20016 I:20015	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI17	1x20017 2x20017 I:20016	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI18	1x20018 2x20018 I:20017	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI19	1x20019 2x20019 I:20018	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI20	1x20020 2x20020 I:20019	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI21	1x20021 2x20021 I:20020	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI22	1x20022 2x20022 I:20021	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI23	1x20023 2x20023 I:20022	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI24	1x20024 2x20024 I:20023	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI25	1x20025 2x20025 I:20024	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI26	1x20026 2x20026 I:20025	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI27	1x20027 2x20027 I:20026	0,0x00 B:00			BIT R/O	

DI HAS CHANGED DI28	1x20028 2x20028 I:20027	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI29	1x20029 2x20029 I:20028	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI30	1x20030 2x20030 I:20029	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI31	1x20031 2x20031 I:20030	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI32	1x20032 2x20032 I:20031	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI33	1x20033 2x20033 I:20032	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI34	1x20034 2x20034 I:20033	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI35	1x20035 2x20035 I:20034	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI36	1x20036 2x20036 I:20035	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI37	1x20037 2x20037 I:20036	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI38	1x20038 2x20038 I:20037	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI39	1x20039 2x20039 I:20038	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI40	1x20040 2x20040 I:20039	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI41	1x20041 2x20041 I:20040	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI42	1x20042 2x20042 I:20041	0,0x00 B:00			BIT R/O	

DI HAS CHANGED DI43	1x20043 2x20043 I:20042	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI44	1x20044 2x20044 I:20043	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI45	1x20045 2x20045 I:20044	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI46	1x20046 2x20046 I:20045	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI47	1x20047 2x20047 I:20046	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI48	1x20048 2x20048 I:20047	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI49	1x20049 2x20049 I:20048	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI50	1x20050 2x20050 I:20049	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI51	1x20051 2x20051 I:20050	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI52	1x20052 2x20052 I:20051	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI53	1x20053 2x20053 I:20052	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI54	1x20054 2x20054 I:20053	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI55	1x20055 2x20055 I:20054	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI56	1x20056 2x20056 I:20055	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI57	1x20057 2x20057 I:20056	0,0x00 B:00			BIT R/O	

DI HAS CHANGED DI58	1x20058 2x20058 I:20057	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI59	1x20059 2x20059 I:20058	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI60	1x20060 2x20060 I:20059	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI61	1x20061 2x20061 I:20060	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI62	1x20062 2x20062 I:20061	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI63	1x20063 2x20063 I:20062	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI64	1x20064 2x20064 I:20063	1,0x01 B:01			BIT R/O	
DIGITAL INPUTS: SHORT KEYPRESS EVENT ON DIGITAL INPUT DETECTED						
SHORT KEYPRESS ON DI1	1x20065 2x20065 I:20064	0,0x00 B:00			BIT R/O	
If a short keypress event was detected on the digital input this bit inverts its last state						
SHORT KEYPRESS ON DI2	1x20066 2x20066 I:20065	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI3	1x20067 2x20067 I:20066	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI4	1x20068 2x20068 I:20067	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI5	1x20069 2x20069 I:20068	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI6	1x20070 2x20070 I:20069	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI7	1x20071 2x20071 I:20070	0,0x00 B:00			BIT R/O	

SHORT KEYPRESS ON DI8	1x20072 2x20072 I:20071	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI9	1x20073 2x20073 I:20072	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI10	1x20074 2x20074 I:20073	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI11	1x20075 2x20075 I:20074	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI12	1x20076 2x20076 I:20075	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI13	1x20077 2x20077 I:20076	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI14	1x20078 2x20078 I:20077	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI15	1x20079 2x20079 I:20078	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI16	1x20080 2x20080 I:20079	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI17	1x20081 2x20081 I:20080	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI18	1x20082 2x20082 I:20081	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI19	1x20083 2x20083 I:20082	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI20	1x20084 2x20084 I:20083	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI21	1x20085 2x20085 I:20084	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI22	1x20086 2x20086 I:20085	0,0x00 B:00			BIT R/O	

SHORT KEYPRESS ON DI23	1x20087 2x20087 I:20086	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI24	1x20088 2x20088 I:20087	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI25	1x20089 2x20089 I:20088	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI26	1x20090 2x20090 I:20089	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI27	1x20091 2x20091 I:20090	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI28	1x20092 2x20092 I:20091	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI29	1x20093 2x20093 I:20092	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI30	1x20094 2x20094 I:20093	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI31	1x20095 2x20095 I:20094	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI32	1x20096 2x20096 I:20095	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI33	1x20097 2x20097 I:20096	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI34	1x20098 2x20098 I:20097	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI35	1x20099 2x20099 I:20098	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI36	1x20100 2x20100 I:20099	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI37	1x20101 2x20101 I:20100	0,0x00 B:00			BIT R/O	

SHORT KEYPRESS ON DI38	1x20102 2x20102 I:20101	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI39	1x20103 2x20103 I:20102	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI40	1x20104 2x20104 I:20103	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI41	1x20105 2x20105 I:20104	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI42	1x20106 2x20106 I:20105	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI43	1x20107 2x20107 I:20106	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI44	1x20108 2x20108 I:20107	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI45	1x20109 2x20109 I:20108	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI46	1x20110 2x20110 I:20109	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI47	1x20111 2x20111 I:20110	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI48	1x20112 2x20112 I:20111	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI49	1x20113 2x20113 I:20112	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI50	1x20114 2x20114 I:20113	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI51	1x20115 2x20115 I:20114	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI52	1x20116 2x20116 I:20115	0,0x00 B:00			BIT R/O	

SHORT KEYPRESS ON DI53	1x20117 2x20117 I:20116	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI54	1x20118 2x20118 I:20117	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI55	1x20119 2x20119 I:20118	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI56	1x20120 2x20120 I:20119	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI57	1x20121 2x20121 I:20120	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI58	1x20122 2x20122 I:20121	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI59	1x20123 2x20123 I:20122	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI60	1x20124 2x20124 I:20123	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI61	1x20125 2x20125 I:20124	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI62	1x20126 2x20126 I:20125	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI63	1x20127 2x20127 I:20126	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI64	1x20128 2x20128 I:20127	1,0x01 B:01			BIT R/O	
DIGITAL INPUTS: LONG KEYPRESS START EVENT ON DIGITAL INPUT DETECTED						
LONG KEYPRESS START ON DI1	1x20129 2x20129 I:20128	0,0x00 B:00			BIT R/O	
If a long keypress start event was detected on the digital input this bit inverts its last state						
LONG KEYPRESS START ON DI2	1x20130 2x20130 I:20129	0,0x00 B:00			BIT R/O	

LONG KEYPRESS START ON DI3	1x20131 2x20131 I:20130	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI4	1x20132 2x20132 I:20131	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI5	1x20133 2x20133 I:20132	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI6	1x20134 2x20134 I:20133	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI7	1x20135 2x20135 I:20134	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI8	1x20136 2x20136 I:20135	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI9	1x20137 2x20137 I:20136	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI10	1x20138 2x20138 I:20137	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI11	1x20139 2x20139 I:20138	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI12	1x20140 2x20140 I:20139	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI13	1x20141 2x20141 I:20140	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI14	1x20142 2x20142 I:20141	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI15	1x20143 2x20143 I:20142	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI16	1x20144 2x20144 I:20143	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI17	1x20145 2x20145 I:20144	0,0x00 B:00			BIT R/O	

LONG KEYPRESS START ON DI18	1x20146 2x20146 I:20145	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI19	1x20147 2x20147 I:20146	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI20	1x20148 2x20148 I:20147	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI21	1x20149 2x20149 I:20148	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI22	1x20150 2x20150 I:20149	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI23	1x20151 2x20151 I:20150	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI24	1x20152 2x20152 I:20151	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI25	1x20153 2x20153 I:20152	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI26	1x20154 2x20154 I:20153	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI27	1x20155 2x20155 I:20154	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI28	1x20156 2x20156 I:20155	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI29	1x20157 2x20157 I:20156	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI30	1x20158 2x20158 I:20157	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI31	1x20159 2x20159 I:20158	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI32	1x20160 2x20160 I:20159	0,0x00 B:00			BIT R/O	

LONG KEYPRESS START ON DI33	1x20161 2x20161 I:20160	1,0x01 B:01			BIT R/O	
LONG KEYPRESS START ON DI34	1x20162 2x20162 I:20161	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI35	1x20163 2x20163 I:20162	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI36	1x20164 2x20164 I:20163	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI37	1x20165 2x20165 I:20164	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI38	1x20166 2x20166 I:20165	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI39	1x20167 2x20167 I:20166	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI40	1x20168 2x20168 I:20167	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI41	1x20169 2x20169 I:20168	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI42	1x20170 2x20170 I:20169	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI43	1x20171 2x20171 I:20170	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI44	1x20172 2x20172 I:20171	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI45	1x20173 2x20173 I:20172	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI46	1x20174 2x20174 I:20173	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI47	1x20175 2x20175 I:20174	0,0x00 B:00			BIT R/O	

LONG KEYPRESS START ON DI48	1x20176 2x20176 I:20175	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI49	1x20177 2x20177 I:20176	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI50	1x20178 2x20178 I:20177	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI51	1x20179 2x20179 I:20178	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI52	1x20180 2x20180 I:20179	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI53	1x20181 2x20181 I:20180	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI54	1x20182 2x20182 I:20181	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI55	1x20183 2x20183 I:20182	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI56	1x20184 2x20184 I:20183	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI57	1x20185 2x20185 I:20184	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI58	1x20186 2x20186 I:20185	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI59	1x20187 2x20187 I:20186	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI60	1x20188 2x20188 I:20187	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI61	1x20189 2x20189 I:20188	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI62	1x20190 2x20190 I:20189	0,0x00 B:00			BIT R/O	

LONG KEYPRESS START ON DI63	1x20191 2x20191 I:20190	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI64	1x20192 2x20192 I:20191	0,0x00 B:00			BIT R/O	
DIGITAL INPUTS: LONG KEYPRESS END EVENT ON DIGITAL INPUT DETECTED						
LONG KEYPRESS END ON DI1	1x20193 2x20193 I:20192	0,0x00 B:00			BIT R/O	
If a long keypress end event was detected on the digital input this bit inverts its last state						
LONG KEYPRESS END ON DI2	1x20194 2x20194 I:20193	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI3	1x20195 2x20195 I:20194	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI4	1x20196 2x20196 I:20195	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI5	1x20197 2x20197 I:20196	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI6	1x20198 2x20198 I:20197	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI7	1x20199 2x20199 I:20198	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI8	1x20200 2x20200 I:20199	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI9	1x20201 2x20201 I:20200	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI10	1x20202 2x20202 I:20201	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI11	1x20203 2x20203 I:20202	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI12	1x20204 2x20204 I:20203	0,0x00 B:00			BIT R/O	

LONG KEYPRESS END ON DI13	1x20205 2x20205 I:20204	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI14	1x20206 2x20206 I:20205	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI15	1x20207 2x20207 I:20206	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI16	1x20208 2x20208 I:20207	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI17	1x20209 2x20209 I:20208	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI18	1x20210 2x20210 I:20209	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI19	1x20211 2x20211 I:20210	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI20	1x20212 2x20212 I:20211	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI21	1x20213 2x20213 I:20212	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI22	1x20214 2x20214 I:20213	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI23	1x20215 2x20215 I:20214	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI24	1x20216 2x20216 I:20215	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI25	1x20217 2x20217 I:20216	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI26	1x20218 2x20218 I:20217	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI27	1x20219 2x20219 I:20218	0,0x00 B:00			BIT R/O	

LONG KEYPRESS END ON DI28	1x20220 2x20220 I:20219	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI29	1x20221 2x20221 I:20220	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI30	1x20222 2x20222 I:20221	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI31	1x20223 2x20223 I:20222	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI32	1x20224 2x20224 I:20223	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI33	1x20225 2x20225 I:20224	1,0x01 B:01			BIT R/O	
LONG KEYPRESS END ON DI34	1x20226 2x20226 I:20225	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI35	1x20227 2x20227 I:20226	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI36	1x20228 2x20228 I:20227	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI37	1x20229 2x20229 I:20228	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI38	1x20230 2x20230 I:20229	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI39	1x20231 2x20231 I:20230	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI40	1x20232 2x20232 I:20231	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI41	1x20233 2x20233 I:20232	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI42	1x20234 2x20234 I:20233	0,0x00 B:00			BIT R/O	

LONG KEYPRESS END ON DI43	1x20235 2x20235 I:20234	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI44	1x20236 2x20236 I:20235	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI45	1x20237 2x20237 I:20236	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI46	1x20238 2x20238 I:20237	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI47	1x20239 2x20239 I:20238	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI48	1x20240 2x20240 I:20239	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI49	1x20241 2x20241 I:20240	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI50	1x20242 2x20242 I:20241	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI51	1x20243 2x20243 I:20242	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI52	1x20244 2x20244 I:20243	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI53	1x20245 2x20245 I:20244	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI54	1x20246 2x20246 I:20245	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI55	1x20247 2x20247 I:20246	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI56	1x20248 2x20248 I:20247	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI57	1x20249 2x20249 I:20248	0,0x00 B:00			BIT R/O	

LONG KEYPRESS END ON DI58	1x20250 2x20250 I:20249	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI59	1x20251 2x20251 I:20250	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI60	1x20252 2x20252 I:20251	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI61	1x20253 2x20253 I:20252	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI62	1x20254 2x20254 I:20253	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI63	1x20255 2x20255 I:20254	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI64	1x20256 2x20256 I:20255	0,0x00 B:00			BIT R/O	
DIGITAL INPUTS: RISING EDGE ON DIGITAL INPUT DETECTED						
RISING EDGE ON DI1	1x20257 2x20257 I:20256	0,0x00 B:00			BIT R/O	
If a rising edge was detected on the digital input this bit inverts its last state						
RISING EDGE ON DI2	1x20258 2x20258 I:20257	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI3	1x20259 2x20259 I:20258	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI4	1x20260 2x20260 I:20259	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI5	1x20261 2x20261 I:20260	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI6	1x20262 2x20262 I:20261	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI7	1x20263 2x20263 I:20262	0,0x00 B:00			BIT R/O	

RISING EDGE ON DI8	1x20264 2x20264 I:20263	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI9	1x20265 2x20265 I:20264	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI10	1x20266 2x20266 I:20265	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI11	1x20267 2x20267 I:20266	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI12	1x20268 2x20268 I:20267	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI13	1x20269 2x20269 I:20268	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI14	1x20270 2x20270 I:20269	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI15	1x20271 2x20271 I:20270	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI16	1x20272 2x20272 I:20271	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI17	1x20273 2x20273 I:20272	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI18	1x20274 2x20274 I:20273	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI19	1x20275 2x20275 I:20274	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI20	1x20276 2x20276 I:20275	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI21	1x20277 2x20277 I:20276	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI22	1x20278 2x20278 I:20277	0,0x00 B:00			BIT R/O	

RISING EDGE ON DI23	1x20279 2x20279 I:20278	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI24	1x20280 2x20280 I:20279	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI25	1x20281 2x20281 I:20280	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI26	1x20282 2x20282 I:20281	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI27	1x20283 2x20283 I:20282	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI28	1x20284 2x20284 I:20283	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI29	1x20285 2x20285 I:20284	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI30	1x20286 2x20286 I:20285	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI31	1x20287 2x20287 I:20286	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI32	1x20288 2x20288 I:20287	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI33	1x20289 2x20289 I:20288	1,0x01 B:01			BIT R/O	
RISING EDGE ON DI34	1x20290 2x20290 I:20289	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI35	1x20291 2x20291 I:20290	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI36	1x20292 2x20292 I:20291	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI37	1x20293 2x20293 I:20292	0,0x00 B:00			BIT R/O	

RISING EDGE ON DI38	1x20294 2x20294 I:20293	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI39	1x20295 2x20295 I:20294	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI40	1x20296 2x20296 I:20295	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI41	1x20297 2x20297 I:20296	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI42	1x20298 2x20298 I:20297	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI43	1x20299 2x20299 I:20298	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI44	1x20300 2x20300 I:20299	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI45	1x20301 2x20301 I:20300	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI46	1x20302 2x20302 I:20301	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI47	1x20303 2x20303 I:20302	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI48	1x20304 2x20304 I:20303	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI49	1x20305 2x20305 I:20304	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI50	1x20306 2x20306 I:20305	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI51	1x20307 2x20307 I:20306	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI52	1x20308 2x20308 I:20307	0,0x00 B:00			BIT R/O	

RISING EDGE ON DI53	1x20309 2x20309 I:20308	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI54	1x20310 2x20310 I:20309	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI55	1x20311 2x20311 I:20310	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI56	1x20312 2x20312 I:20311	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI57	1x20313 2x20313 I:20312	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI58	1x20314 2x20314 I:20313	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI59	1x20315 2x20315 I:20314	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI60	1x20316 2x20316 I:20315	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI61	1x20317 2x20317 I:20316	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI62	1x20318 2x20318 I:20317	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI63	1x20319 2x20319 I:20318	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI64	1x20320 2x20320 I:20319	1,0x01 B:01			BIT R/O	
DIGITAL INPUTS: FALLING EDGE ON DIGITAL INPUT DETECTED						
FALLING EDGE ON DI1	1x20321 2x20321 I:20320	0,0x00 B:00			BIT R/O	
If a falling edge was detected on the digital input this bit inverts its last state						
FALLING EDGE ON DI2	1x20322 2x20322 I:20321	0,0x00 B:00			BIT R/O	

FALLING EDGE ON DI3	1x20323 2x20323 I:20322	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI4	1x20324 2x20324 I:20323	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI5	1x20325 2x20325 I:20324	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI6	1x20326 2x20326 I:20325	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI7	1x20327 2x20327 I:20326	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI8	1x20328 2x20328 I:20327	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI9	1x20329 2x20329 I:20328	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI10	1x20330 2x20330 I:20329	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI11	1x20331 2x20331 I:20330	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI12	1x20332 2x20332 I:20331	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI13	1x20333 2x20333 I:20332	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI14	1x20334 2x20334 I:20333	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI15	1x20335 2x20335 I:20334	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI16	1x20336 2x20336 I:20335	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI17	1x20337 2x20337 I:20336	0,0x00 B:00			BIT R/O	

FALLING EDGE ON DI18	1x20338 2x20338 I:20337	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI19	1x20339 2x20339 I:20338	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI20	1x20340 2x20340 I:20339	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI21	1x20341 2x20341 I:20340	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI22	1x20342 2x20342 I:20341	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI23	1x20343 2x20343 I:20342	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI24	1x20344 2x20344 I:20343	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI25	1x20345 2x20345 I:20344	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI26	1x20346 2x20346 I:20345	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI27	1x20347 2x20347 I:20346	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI28	1x20348 2x20348 I:20347	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI29	1x20349 2x20349 I:20348	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI30	1x20350 2x20350 I:20349	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI31	1x20351 2x20351 I:20350	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI32	1x20352 2x20352 I:20351	0,0x00 B:00			BIT R/O	

FALLING EDGE ON DI33	1x20353 2x20353 I:20352	1,0x01 B:01			BIT R/O	
FALLING EDGE ON DI34	1x20354 2x20354 I:20353	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI35	1x20355 2x20355 I:20354	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI36	1x20356 2x20356 I:20355	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI37	1x20357 2x20357 I:20356	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI38	1x20358 2x20358 I:20357	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI39	1x20359 2x20359 I:20358	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI40	1x20360 2x20360 I:20359	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI41	1x20361 2x20361 I:20360	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI42	1x20362 2x20362 I:20361	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI43	1x20363 2x20363 I:20362	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI44	1x20364 2x20364 I:20363	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI45	1x20365 2x20365 I:20364	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI46	1x20366 2x20366 I:20365	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI47	1x20367 2x20367 I:20366	0,0x00 B:00			BIT R/O	

FALLING EDGE ON DI48	1x20368 2x20368 I:20367	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI49	1x20369 2x20369 I:20368	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI50	1x20370 2x20370 I:20369	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI51	1x20371 2x20371 I:20370	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI52	1x20372 2x20372 I:20371	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI53	1x20373 2x20373 I:20372	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI54	1x20374 2x20374 I:20373	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI55	1x20375 2x20375 I:20374	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI56	1x20376 2x20376 I:20375	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI57	1x20377 2x20377 I:20376	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI58	1x20378 2x20378 I:20377	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI59	1x20379 2x20379 I:20378	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI60	1x20380 2x20380 I:20379	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI61	1x20381 2x20381 I:20380	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI62	1x20382 2x20382 I:20381	0,0x00 B:00			BIT R/O	

FALLING EDGE ON DI63	1x20383 2x20383 I:20382	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI64	1x20384 2x20384 I:20383	1,0x01 B:01			BIT R/O	

Register NAME Command NAME	MODBUS Register ASCII Command	Register VALUE ASCII Command	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
STATUS DIGITAL INPUTS						
DI1	3x00001 4x00001 I:0	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI1:0=OFF				
Current state of the digital input DIx =0:DI is OFF, =1:DI is ON						
DI2	3x00002 4x00002 I:1	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI2:0=OFF				
DI3	3x00003 4x00003 I:2	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI3:0=OFF				
DI4	3x00004 4x00004 I:3	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI4:0=OFF				
DI5	3x00005 4x00005 I:4	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI5:0=OFF				
DI6	3x00006 4x00006 I:5	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI6:0=OFF				
DI7	3x00007 4x00007 I:6	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI7:0=OFF				
DI8	3x00008 4x00008 I:7	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI8:0=OFF				
DI9	3x00009 4x00009 I:8	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI9:0=OFF				

DI10	3x00010 4x00010 I:9	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI10:0=OFF				
DI11	3x00011 4x00011 I:10	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI11:0=OFF				
DI12	3x00012 4x00012 I:11	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI12:0=OFF				
DI13	3x00013 4x00013 I:12	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI13:0=OFF				
DI14	3x00014 4x00014 I:13	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI14:0=OFF				
DI15	3x00015 4x00015 I:14	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI15:0=OFF				
DI16	3x00016 4x00016 I:15	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI16:0=OFF				
DI17	3x00017 4x00017 I:16	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI17:0=OFF				
DI18	3x00018 4x00018 I:17	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI18:0=OFF				
DI19	3x00019 4x00019 I:18	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI19:0=OFF				
DI20	3x00020 4x00020 I:19	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI20:0=OFF				

DI21	3x00021 4x00021 I:20	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI21:0=OFF				
DI22	3x00022 4x00022 I:21	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI22:0=OFF				
DI23	3x00023 4x00023 I:22	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI23:0=OFF				
DI24	3x00024 4x00024 I:23	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI24:1=ON				
DI25	3x00025 4x00025 I:24	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI25:0=OFF				
DI26	3x00026 4x00026 I:25	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI26:0=OFF				
DI27	3x00027 4x00027 I:26	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI27:0=OFF				
DI28	3x00028 4x00028 I:27	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI28:0=OFF				
DI29	3x00029 4x00029 I:28	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI29:0=OFF				
DI30	3x00030 4x00030 I:29	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI30:0=OFF				
DI31	3x00031 4x00031 I:30	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI31:0=OFF				

DI32	3x00032 4x00032 I:31	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI32:0=OFF				
DI33	3x00033 4x00033 I:32	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI33:0=OFF				
DI34	3x00034 4x00034 I:33	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI34:0=OFF				
DI35	3x00035 4x00035 I:34	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI35:0=OFF				
DI36	3x00036 4x00036 I:35	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI36:0=OFF				
DI37	3x00037 4x00037 I:36	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI37:0=OFF				
DI38	3x00038 4x00038 I:37	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI38:0=OFF				
DI39	3x00039 4x00039 I:38	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI39:0=OFF				
DI40	3x00040 4x00040 I:39	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI40:0=OFF				
DI41	3x00041 4x00041 I:40	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI41:0=OFF				
DI42	3x00042 4x00042 I:41	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI42:0=OFF				

DI43	3x00043 4x00043 I:42	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI43:0=OFF				
DI44	3x00044 4x00044 I:43	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI44:0=OFF				
DI45	3x00045 4x00045 I:44	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI45:0=OFF				
DI46	3x00046 4x00046 I:45	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI46:1=ON				
DI47	3x00047 4x00047 I:46	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI47:0=OFF				
DI48	3x00048 4x00048 I:47	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI48:0=OFF				
DI49	3x00049 4x00049 I:48	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI49:0=OFF				
DI50	3x00050 4x00050 I:49	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI50:0=OFF				
DI51	3x00051 4x00051 I:50	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI51:0=OFF				
DI52	3x00052 4x00052 I:51	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI52:0=OFF				
DI53	3x00053 4x00053 I:52	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI53:0=OFF				

DI54	3x00054 4x00054 I:53	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI54:0=OFF				
DI55	3x00055 4x00055 I:54	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI55:0=OFF				
DI56	3x00056 4x00056 I:55	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI56:0=OFF				
DI57	3x00057 4x00057 I:56	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI57:0=OFF				
DI58	3x00058 4x00058 I:57	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI58:0=OFF				
DI59	3x00059 4x00059 I:58	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI59:0=OFF				
DI60	3x00060 4x00060 I:59	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI60:0=OFF				
DI61	3x00061 4x00061 I:60	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI61:0=OFF				
DI62	3x00062 4x00062 I:61	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI62:0=OFF				
DI63	3x00063 4x00063 I:62	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI63:0=OFF				
DI64	3x00064 4x00064 I:63	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI64:0=OFF				

DIGITAL INPUTS: RESET						
RESET COUNTERS	3x10000 4x10000 I:9999	0,0x0000 B:00 00		1:PERFORM RESET	UINT16 R/W	NO
If this register is written to 1, all internal edge counters and event counters are set to 0. 0 is always returned when reading.						
HAS DIS CHANGED	3x10001 4x10001 I:10000	3,0x0003 B:00 03			UINT16 R/O	
		3 event(s)				
As soon as the module registrates an event on one of the available digital inputs, this global event counter is incremented by 1. Possible events are: Detection of a short keypress Detection of the start of a long keypress Detection of the end of a long keypress						
STATUS OF ALL DIS DI1..DI16	3x10002 4x10002 I:10001	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI1:0=OFF				
		Actual state of DI2:0=OFF				
		Actual state of DI3:0=OFF				
		Actual state of DI4:0=OFF				
		Actual state of DI5:0=OFF				
		Actual state of DI6:0=OFF				
		Actual state of DI7:0=OFF				
		Actual state of DI8:0=OFF				
		Actual state of DI9:0=OFF				
		Actual state of DI10:0=OFF				
		Actual state of DI11:0=OFF				
		Actual state of DI12:0=OFF				
		Actual state of DI13:0=OFF				
		Actual state of DI14:0=OFF				
		Actual state of DI15:0=OFF				
		Actual state of DI16:0=OFF				
Actual state of all digital inputs DI1..DI16 Bit 0: =0:DI1 is OFF, =1:DI1 is ON Bit 1: =0:DI2 is OFF, =1:DI2 is ON ... Bit 14: =0:DI15 is OFF, =1:DI15 is ON Bit 15: =0:DI16 is OFF, =1:DI16 is ON						
STATUS OF ALL DIS DI17..DI32	3x10003 4x10003 I:10002	128,0x0080 B:00 80			UINT16 R/O	
		Actual state of DI17:0=OFF				
		Actual state of DI18:0=OFF				
		Actual state of DI19:0=OFF				
		Actual state of DI20:0=OFF				

		Actual state of DI21:0=OFF				
		Actual state of DI22:0=OFF				
		Actual state of DI23:0=OFF				
		Actual state of DI24:1=ON				
		Actual state of DI25:0=OFF				
		Actual state of DI26:0=OFF				
		Actual state of DI27:0=OFF				
		Actual state of DI28:0=OFF				
		Actual state of DI29:0=OFF				
		Actual state of DI30:0=OFF				
		Actual state of DI31:0=OFF				
		Actual state of DI32:0=OFF				
Actual state of all digital inputs DI17..DI32 Bit 0: =0:DI17 is OFF, =1:DI17 is ON Bit 1: =0:DI18 is OFF, =1:DI18 is ON ... Bit 30: =0:DI31 is OFF, =1:DI31 is ON Bit 31: =0:DI32 is OFF, =1:DI32 is ON						
STATUS OF ALL DIS DI33..DI48	3x10004 4x10004 I:10003	128,0x0080 B:00 80			UINT16 R/O	
		Actual state of DI33:0=OFF				
		Actual state of DI34:0=OFF				
		Actual state of DI35:0=OFF				
		Actual state of DI36:0=OFF				
		Actual state of DI37:0=OFF				
		Actual state of DI38:0=OFF				
		Actual state of DI39:0=OFF				
		Actual state of DI40:1=ON				
		Actual state of DI41:0=OFF				
		Actual state of DI42:0=OFF				
		Actual state of DI43:0=OFF				
		Actual state of DI44:0=OFF				
		Actual state of DI45:0=OFF				
		Actual state of DI46:0=OFF				
		Actual state of DI47:0=OFF				
		Actual state of DI48:0=OFF				
Actual state of all digital inputs DI33..DI48 Bit 0: =0:DI33 is OFF, =1:DI33 is ON Bit 1: =0:DI34 is OFF, =1:DI34 is ON ... Bit 30: =0:DI47 is OFF, =1:DI47 is ON Bit 31: =0:DI48 is OFF, =1:DI48 is ON						

STATUS OF ALL DIS DI49..DI64	3x10005 4x10005 I:10004	128,0x0080 B:00 80			UINT16 R/O	
		Actual state of DI49:0=OFF				
		Actual state of DI50:0=OFF				
		Actual state of DI51:0=OFF				
		Actual state of DI52:0=OFF				
		Actual state of DI53:0=OFF				
		Actual state of DI54:0=OFF				
		Actual state of DI55:0=OFF				
		Actual state of DI56:1=ON				
		Actual state of DI57:0=OFF				
		Actual state of DI58:0=OFF				
		Actual state of DI59:0=OFF				
		Actual state of DI60:0=OFF				
		Actual state of DI61:0=OFF				
		Actual state of DI62:0=OFF				
		Actual state of DI63:0=OFF				
		Actual state of DI64:0=OFF				
Actual state of all digital inputs DI49..DI64 Bit 0: =0:DI49 is OFF, =1:DI49 is ON Bit 1: =0:DI50 is OFF, =1:DI50 is ON ... Bit 30: =0:DI63 is OFF, =1:DI63 is ON Bit 31: =0:DI64 is OFF, =1:DI64 is ON						
STATUS REAL DIGITAL INPUTS						
DI1	3x15001 4x15001 I:15000	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI1:0=OFF				
Current state of the digital input DIx with the internal software filter to suppress glitches or spike on thie line =0:DI is OFF, =1:DI is ON						
DI2	3x15002 4x15002 I:15001	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI2:0=OFF				
DI3	3x15003 4x15003 I:15002	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI3:0=OFF				
DI4	3x15004 4x15004 I:15003	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI4:0=OFF				

DI5	3x15005 4x15005 I:15004	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI5:0=OFF				
DI6	3x15006 4x15006 I:15005	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI6:0=OFF				
DI7	3x15007 4x15007 I:15006	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI7:0=OFF				
DI8	3x15008 4x15008 I:15007	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI8:0=OFF				
DI9	3x15009 4x15009 I:15008	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI9:0=OFF				
DI10	3x15010 4x15010 I:15009	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI10:0=OFF				
DI11	3x15011 4x15011 I:15010	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI11:0=OFF				
DI12	3x15012 4x15012 I:15011	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI12:0=OFF				
DI13	3x15013 4x15013 I:15012	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI13:0=OFF				
DI14	3x15014 4x15014 I:15013	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI14:0=OFF				
DI15	3x15015 4x15015 I:15014	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI15:0=OFF				

DI16	3x15016 4x15016 I:15015	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI16:0=OFF				
DI17	3x15017 4x15017 I:15016	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI17:0=OFF				
DI18	3x15018 4x15018 I:15017	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI18:0=OFF				
DI19	3x15019 4x15019 I:15018	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI19:0=OFF				
DI20	3x15020 4x15020 I:15019	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI20:0=OFF				
DI21	3x15021 4x15021 I:15020	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI21:0=OFF				
DI22	3x15022 4x15022 I:15021	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI22:0=OFF				
DI23	3x15023 4x15023 I:15022	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI23:0=OFF				
DI24	3x15024 4x15024 I:15023	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI24:1=ON				
DI25	3x15025 4x15025 I:15024	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI25:0=OFF				
DI26	3x15026 4x15026 I:15025	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI26:0=OFF				

DI27	3x15027 4x15027 I:15026	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI27:0=OFF				
DI28	3x15028 4x15028 I:15027	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI28:0=OFF				
DI29	3x15029 4x15029 I:15028	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI29:0=OFF				
DI30	3x15030 4x15030 I:15029	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI30:0=OFF				
DI31	3x15031 4x15031 I:15030	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI31:0=OFF				
DI32	3x15032 4x15032 I:15031	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI32:0=OFF				
DI33	3x15033 4x15033 I:15032	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI33:0=OFF				
DI34	3x15034 4x15034 I:15033	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI34:0=OFF				
DI35	3x15035 4x15035 I:15034	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI35:0=OFF				
DI36	3x15036 4x15036 I:15035	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI36:0=OFF				
DI37	3x15037 4x15037 I:15036	1,0x0001 B:00 01			UINT16 R/O	

		Actual state of DI37:1=ON				
DI38	3x15038 4x15038 I:15037	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI38:0=OFF				
DI39	3x15039 4x15039 I:15038	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI39:0=OFF				
DI40	3x15040 4x15040 I:15039	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI40:0=OFF				
DI41	3x15041 4x15041 I:15040	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI41:0=OFF				
DI42	3x15042 4x15042 I:15041	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI42:0=OFF				
DI43	3x15043 4x15043 I:15042	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI43:0=OFF				
DI44	3x15044 4x15044 I:15043	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI44:0=OFF				
DI45	3x15045 4x15045 I:15044	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI45:0=OFF				
DI46	3x15046 4x15046 I:15045	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI46:0=OFF				
DI47	3x15047 4x15047 I:15046	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI47:0=OFF				

DI48	3x15048 4x15048 I:15047	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI48:0=OFF				
DI49	3x15049 4x15049 I:15048	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI49:0=OFF				
DI50	3x15050 4x15050 I:15049	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI50:1=ON				
DI51	3x15051 4x15051 I:15050	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI51:0=OFF				
DI52	3x15052 4x15052 I:15051	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI52:0=OFF				
DI53	3x15053 4x15053 I:15052	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI53:0=OFF				
DI54	3x15054 4x15054 I:15053	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI54:0=OFF				
DI55	3x15055 4x15055 I:15054	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI55:0=OFF				
DI56	3x15056 4x15056 I:15055	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI56:0=OFF				
DI57	3x15057 4x15057 I:15056	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI57:0=OFF				
DI58	3x15058 4x15058 I:15057	0,0x0000 B:00 00			UINT16 R/O	

		Actual state of DI58:0=OFF				
DI59	3x15059 4x15059 I:15058	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI59:0=OFF				
DI60	3x15060 4x15060 I:15059	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI60:1=ON				
DI61	3x15061 4x15061 I:15060	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI61:1=ON				
DI62	3x15062 4x15062 I:15061	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI62:1=ON				
DI63	3x15063 4x15063 I:15062	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI63:1=ON				
DI64	3x15064 4x15064 I:15063	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI64:1=ON				
STATUS DIGITAL INPUTS						
UNFILTERED DI1	3x15065 4x15065 I:15064	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI1:0=OFF				
Current state of the real digital input DIx without the internal software filter to suppress glitches or spike on thie line =0:DI is OFF, =1:DI is ON						
UNFILTERED DI2	3x15066 4x15066 I:15065	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI2:0=OFF				
UNFILTERED DI3	3x15067 4x15067 I:15066	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI3:0=OFF				
UNFILTERED DI4	3x15068 4x15068 I:15067	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI4:0=OFF				

UNFILTERED DI5	3x15069 4x15069 I:15068	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI5:0=OFF				
UNFILTERED DI6	3x15070 4x15070 I:15069	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI6:0=OFF				
UNFILTERED DI7	3x15071 4x15071 I:15070	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI7:0=OFF				
UNFILTERED DI8	3x15072 4x15072 I:15071	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI8:0=OFF				
UNFILTERED DI9	3x15073 4x15073 I:15072	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI9:0=OFF				
UNFILTERED DI10	3x15074 4x15074 I:15073	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI10:0=OFF				
UNFILTERED DI11	3x15075 4x15075 I:15074	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI11:0=OFF				
UNFILTERED DI12	3x15076 4x15076 I:15075	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI12:0=OFF				
UNFILTERED DI13	3x15077 4x15077 I:15076	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI13:0=OFF				
UNFILTERED DI14	3x15078 4x15078 I:15077	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI14:0=OFF				
UNFILTERED DI15	3x15079 4x15079 I:15078	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI15:0=OFF				

UNFILTERED DI16	3x15080 4x15080 I:15079	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI16:0=OFF				
UNFILTERED DI17	3x15081 4x15081 I:15080	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI17:0=OFF				
UNFILTERED DI18	3x15082 4x15082 I:15081	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI18:0=OFF				
UNFILTERED DI19	3x15083 4x15083 I:15082	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI19:0=OFF				
UNFILTERED DI20	3x15084 4x15084 I:15083	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI20:0=OFF				
UNFILTERED DI21	3x15085 4x15085 I:15084	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI21:0=OFF				
UNFILTERED DI22	3x15086 4x15086 I:15085	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI22:0=OFF				
UNFILTERED DI23	3x15087 4x15087 I:15086	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI23:0=OFF				
UNFILTERED DI24	3x15088 4x15088 I:15087	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of UNFILTERED DI24:1=ON				
UNFILTERED DI25	3x15089 4x15089 I:15088	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI25:0=OFF				
UNFILTERED DI26	3x15090 4x15090 I:15089	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI26:0=OFF				

UNFILTERED DI27	3x15091 4x15091 I:15090	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI27:0=OFF				
UNFILTERED DI28	3x15092 4x15092 I:15091	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI28:0=OFF				
UNFILTERED DI29	3x15093 4x15093 I:15092	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI29:0=OFF				
UNFILTERED DI30	3x15094 4x15094 I:15093	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI30:0=OFF				
UNFILTERED DI31	3x15095 4x15095 I:15094	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI31:0=OFF				
UNFILTERED DI32	3x15096 4x15096 I:15095	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI32:0=OFF				
UNFILTERED DI33	3x15097 4x15097 I:15096	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI33:0=OFF				
UNFILTERED DI34	3x15098 4x15098 I:15097	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI34:0=OFF				
UNFILTERED DI35	3x15099 4x15099 I:15098	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI35:0=OFF				
UNFILTERED DI36	3x15100 4x15100 I:15099	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI36:0=OFF				
UNFILTERED DI37	3x15101 4x15101 I:15100	0,0x0000 B:00 00			UINT16 R/O	

		Actual state of UNFILTERED DI37:0=OFF				
UNFILTERED DI38	3x15102 4x15102 I:15101	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI38:0=OFF				
UNFILTERED DI39	3x15103 4x15103 I:15102	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of UNFILTERED DI39:1=ON				
UNFILTERED DI40	3x15104 4x15104 I:15103	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI40:0=OFF				
UNFILTERED DI41	3x15105 4x15105 I:15104	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI41:0=OFF				
UNFILTERED DI42	3x15106 4x15106 I:15105	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI42:0=OFF				
UNFILTERED DI43	3x15107 4x15107 I:15106	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI43:0=OFF				
UNFILTERED DI44	3x15108 4x15108 I:15107	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI44:0=OFF				
UNFILTERED DI45	3x15109 4x15109 I:15108	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI45:0=OFF				
UNFILTERED DI46	3x15110 4x15110 I:15109	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI46:0=OFF				
UNFILTERED DI47	3x15111 4x15111 I:15110	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI47:0=OFF				

UNFILTERED DI48	3x15112 4x15112 I:15111	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI48:0=OFF				
UNFILTERED DI49	3x15113 4x15113 I:15112	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI49:0=OFF				
UNFILTERED DI50	3x15114 4x15114 I:15113	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI50:0=OFF				
UNFILTERED DI51	3x15115 4x15115 I:15114	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI51:0=OFF				
UNFILTERED DI52	3x15116 4x15116 I:15115	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI52:0=OFF				
UNFILTERED DI53	3x15117 4x15117 I:15116	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI53:0=OFF				
UNFILTERED DI54	3x15118 4x15118 I:15117	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of UNFILTERED DI54:1=ON				
UNFILTERED DI55	3x15119 4x15119 I:15118	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI55:0=OFF				
UNFILTERED DI56	3x15120 4x15120 I:15119	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI56:0=OFF				
UNFILTERED DI57	3x15121 4x15121 I:15120	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI57:0=OFF				
UNFILTERED DI58	3x15122 4x15122 I:15121	0,0x0000 B:00 00			UINT16 R/O	

		Actual state of UNFILTERED DI58:0=OFF				
UNFILTERED DI59	3x15123 4x15123 I:15122	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI59:0=OFF				
UNFILTERED DI60	3x15124 4x15124 I:15123	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI60:0=OFF				
UNFILTERED DI61	3x15125 4x15125 I:15124	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI61:0=OFF				
UNFILTERED DI62	3x15126 4x15126 I:15125	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI62:0=OFF				
UNFILTERED DI63	3x15127 4x15127 I:15126	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI63:0=OFF				
UNFILTERED DI64	3x15128 4x15128 I:15127	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI64:0=OFF				

Register NAME Command NAME	MODBUS Register ASCII Command	Register VALUE ASCII Command	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
DIGITAL INPUTS						
STATUS DI1 A	3x05001 4x05001 I:5000	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
Status for the digital input DIx Bit 0-4: Lower 5 bits of CHANGE COUNTER Bit 5-9: Lower 5 bits of RISING EDGE COUNTER Bit 10-14: Lower 5 bits of FALLING EDGE COUNTER Bit 15: Current Status of DIx =0: DIx si OFF, =1: DIx is ON						
STATUS DI1 B	3x05002 4x05002 I:5001	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
Status for the digital input DIx Bit 0-4: Lower 5 bits of SHORT KEYPRESS EVENTS Bit 5-9: Lower 5 bits of LONG KEYPRESS START EVENTS Bit 10-14: Lower 5 bits of LONG KEYPRESS END EVENTS Bit 15: Current Status of DIx =0: DIx si OFF, =1: DIx is ON						
STATUS DI2 A	3x05003 4x05003 I:5002	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI2 B	3x05004 4x05004 I:5003	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI3 A	3x05005 4x05005 I:5004	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI3 B	3x05006 4x05006 I:5005	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI4 A	3x05007 4x05007 I:5006	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI4 B	3x05008 4x05008 I:5007	0,0x0000 B:00 00			UINT16 R/O	

		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI5 A	3x05009 4x05009 I:5008	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI5 B	3x05010 4x05010 I:5009	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI6 A	3x05011 4x05011 I:5010	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI6 B	3x05012 4x05012 I:5011	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI7 A	3x05013 4x05013 I:5012	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI7 B	3x05014 4x05014 I:5013	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI8 A	3x05015 4x05015 I:5014	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI8 B	3x05016 4x05016 I:5015	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI9 A	3x05017 4x05017 I:5016	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI9 B	3x05018 4x05018 I:5017	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI10 A	3x05019 4x05019 I:5018	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				

STATUS DI10 B	3x05020 4x05020 I:5019	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI11 A	3x05021 4x05021 I:5020	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI11 B	3x05022 4x05022 I:5021	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI12 A	3x05023 4x05023 I:5022	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI12 B	3x05024 4x05024 I:5023	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI13 A	3x05025 4x05025 I:5024	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI13 B	3x05026 4x05026 I:5025	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI14 A	3x05027 4x05027 I:5026	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI14 B	3x05028 4x05028 I:5027	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI15 A	3x05029 4x05029 I:5028	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI15 B	3x05030 4x05030 I:5029	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				

STATUS DI16 A	3x05031 4x05031 I:5030	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI16 B	3x05032 4x05032 I:5031	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI17 A	3x05033 4x05033 I:5032	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI17 B	3x05034 4x05034 I:5033	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI18 A	3x05035 4x05035 I:5034	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI18 B	3x05036 4x05036 I:5035	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI19 A	3x05037 4x05037 I:5036	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI19 B	3x05038 4x05038 I:5037	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI20 A	3x05039 4x05039 I:5038	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI20 B	3x05040 4x05040 I:5039	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI21 A	3x05041 4x05041 I:5040	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				

STATUS DI21 B	3x05042 4x05042 I:5041	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI22 A	3x05043 4x05043 I:5042	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI22 B	3x05044 4x05044 I:5043	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI23 A	3x05045 4x05045 I:5044	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI23 B	3x05046 4x05046 I:5045	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI24 A	3x05047 4x05047 I:5046	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI24 B	3x05048 4x05048 I:5047	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI25 A	3x05049 4x05049 I:5048	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI25 B	3x05050 4x05050 I:5049	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI26 A	3x05051 4x05051 I:5050	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI26 B	3x05052 4x05052 I:5051	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				

STATUS DI27 A	3x05053 4x05053 I:5052	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI27 B	3x05054 4x05054 I:5053	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI28 A	3x05055 4x05055 I:5054	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI28 B	3x05056 4x05056 I:5055	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI29 A	3x05057 4x05057 I:5056	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI29 B	3x05058 4x05058 I:5057	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI30 A	3x05059 4x05059 I:5058	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI30 B	3x05060 4x05060 I:5059	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI31 A	3x05061 4x05061 I:5060	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI31 B	3x05062 4x05062 I:5061	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI32 A	3x05063 4x05063 I:5062	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				

STATUS DI32 B	3x05064 4x05064 I:5063	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI33 A	3x05065 4x05065 I:5064	1058,0x0422 B:04 22			UINT16 R/O	
		DI:1,CC:2,REC:1,FEC:1				
STATUS DI33 B	3x05066 4x05066 I:5065	1056,0x0420 B:04 20			UINT16 R/O	
		DI:1,SKE:0,LKSE:1,LKEE:1				
STATUS DI34 A	3x05067 4x05067 I:5066	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI34 B	3x05068 4x05068 I:5067	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI35 A	3x05069 4x05069 I:5068	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI35 B	3x05070 4x05070 I:5069	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI36 A	3x05071 4x05071 I:5070	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI36 B	3x05072 4x05072 I:5071	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI37 A	3x05073 4x05073 I:5072	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI37 B	3x05074 4x05074 I:5073	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				

STATUS DI38 A	3x05075 4x05075 I:5074	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI38 B	3x05076 4x05076 I:5075	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI39 A	3x05077 4x05077 I:5076	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI39 B	3x05078 4x05078 I:5077	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI40 A	3x05079 4x05079 I:5078	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI40 B	3x05080 4x05080 I:5079	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI41 A	3x05081 4x05081 I:5080	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI41 B	3x05082 4x05082 I:5081	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI42 A	3x05083 4x05083 I:5082	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI42 B	3x05084 4x05084 I:5083	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI43 A	3x05085 4x05085 I:5084	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				

STATUS DI43 B	3x05086 4x05086 I:5085	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI44 A	3x05087 4x05087 I:5086	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI44 B	3x05088 4x05088 I:5087	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI45 A	3x05089 4x05089 I:5088	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI45 B	3x05090 4x05090 I:5089	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI46 A	3x05091 4x05091 I:5090	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI46 B	3x05092 4x05092 I:5091	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI47 A	3x05093 4x05093 I:5092	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI47 B	3x05094 4x05094 I:5093	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI48 A	3x05095 4x05095 I:5094	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI48 B	3x05096 4x05096 I:5095	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				

STATUS DI49 A	3x05097 4x05097 I:5096	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI49 B	3x05098 4x05098 I:5097	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI50 A	3x05099 4x05099 I:5098	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI50 B	3x05100 4x05100 I:5099	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI51 A	3x05101 4x05101 I:5100	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI51 B	3x05102 4x05102 I:5101	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI52 A	3x05103 4x05103 I:5102	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI52 B	3x05104 4x05104 I:5103	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI53 A	3x05105 4x05105 I:5104	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI53 B	3x05106 4x05106 I:5105	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI54 A	3x05107 4x05107 I:5106	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				

STATUS DI54 B	3x05108 4x05108 I:5107	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI55 A	3x05109 4x05109 I:5108	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI55 B	3x05110 4x05110 I:5109	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI56 A	3x05111 4x05111 I:5110	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI56 B	3x05112 4x05112 I:5111	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI57 A	3x05113 4x05113 I:5112	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI57 B	3x05114 4x05114 I:5113	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI58 A	3x05115 4x05115 I:5114	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI58 B	3x05116 4x05116 I:5115	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI59 A	3x05117 4x05117 I:5116	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI59 B	3x05118 4x05118 I:5117	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				

STATUS DI60 A	3x05119 4x05119 I:5118	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI60 B	3x05120 4x05120 I:5119	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI61 A	3x05121 4x05121 I:5120	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI61 B	3x05122 4x05122 I:5121	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI62 A	3x05123 4x05123 I:5122	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI62 B	3x05124 4x05124 I:5123	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI63 A	3x05125 4x05125 I:5124	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI63 B	3x05126 4x05126 I:5125	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI64 A	3x05127 4x05127 I:5126	1057,0x0421 B:04 21			UINT16 R/O	
		DI:1,CC:1,REC:1,FEC:1				
STATUS DI64 B	3x05128 4x05128 I:5127	1,0x0001 B:00 01			UINT16 R/O	
		DI:0,SKE:1,LKSE:0,LKEE:0				
STATUS						
FILTER PATTERN DI1	3x05129 4x05129 I:5128	0,0x00000000 B:00 00 00 00			UINT32 R/O	
The internal pattern for corresponding digital input for AC/DC filtering. The internal used state is created out of this internal pattern via oversampling.						

FILTER PATTERN DI2	3x05131 4x05131 I:5130	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI3	3x05133 4x05133 I:5132	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI4	3x05135 4x05135 I:5134	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI5	3x05137 4x05137 I:5136	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI6	3x05139 4x05139 I:5138	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI7	3x05141 4x05141 I:5140	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI8	3x05143 4x05143 I:5142	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI9	3x05145 4x05145 I:5144	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI10	3x05147 4x05147 I:5146	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI11	3x05149 4x05149 I:5148	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI12	3x05151 4x05151 I:5150	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI13	3x05153 4x05153 I:5152	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI14	3x05155 4x05155 I:5154	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI15	3x05157 4x05157 I:5156	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI16	3x05159 4x05159 I:5158	0,0x00000000 B:00 00 00 00			UINT32 R/O	

FILTER PATTERN DI17	3x05161 4x05161 I:5160	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI18	3x05163 4x05163 I:5162	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI19	3x05165 4x05165 I:5164	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI20	3x05167 4x05167 I:5166	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI21	3x05169 4x05169 I:5168	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI22	3x05171 4x05171 I:5170	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI23	3x05173 4x05173 I:5172	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI24	3x05175 4x05175 I:5174	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI25	3x05177 4x05177 I:5176	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI26	3x05179 4x05179 I:5178	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI27	3x05181 4x05181 I:5180	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI28	3x05183 4x05183 I:5182	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI29	3x05185 4x05185 I:5184	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI30	3x05187 4x05187 I:5186	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI31	3x05189 4x05189 I:5188	0,0x00000000 B:00 00 00 00			UINT32 R/O	

FILTER PATTERN DI32	3x05191 4x05191 I:5190	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI33	3x05193 4x05193 I:5192	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI34	3x05195 4x05195 I:5194	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI35	3x05197 4x05197 I:5196	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI36	3x05199 4x05199 I:5198	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI37	3x05201 4x05201 I:5200	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI38	3x05203 4x05203 I:5202	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI39	3x05205 4x05205 I:5204	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI40	3x05207 4x05207 I:5206	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI41	3x05209 4x05209 I:5208	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI42	3x05211 4x05211 I:5210	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI43	3x05213 4x05213 I:5212	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI44	3x05215 4x05215 I:5214	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI45	3x05217 4x05217 I:5216	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI46	3x05219 4x05219 I:5218	0,0x00000000 B:00 00 00 00			UINT32 R/O	

(C) Copyright 2020-2022 by RESI Informatik & Automation GmbH & DI HC Sigl,MSc

FILTER PATTERN DI47	3x05221 4x05221 I:5220	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI48	3x05223 4x05223 I:5222	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI49	3x05225 4x05225 I:5224	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI50	3x05227 4x05227 I:5226	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI51	3x05229 4x05229 I:5228	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI52	3x05231 4x05231 I:5230	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI53	3x05233 4x05233 I:5232	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI54	3x05235 4x05235 I:5234	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI55	3x05237 4x05237 I:5236	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI56	3x05239 4x05239 I:5238	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI57	3x05241 4x05241 I:5240	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI58	3x05243 4x05243 I:5242	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI59	3x05245 4x05245 I:5244	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI60	3x05247 4x05247 I:5246	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI61	3x05249 4x05249 I:5248	0,0x00000000 B:00 00 00 00			UINT32 R/O	

(C) Copyright 2020-2022 by RESI Informatik & Automation GmbH & DI HC Sigl,MSc

FILTER PATTERN DI62	3x05251 4x05251 I:5250	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI63	3x05253 4x05253 I:5252	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI64	3x05255 4x05255 I:5254	0,0x00000000 B:00 00 00 00			UINT32 R/O	
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI1						
RISE DI1	3x07001 4x07001 I:7000	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
Counter for rising edges on the digital input DIx. If the module detects a rising edge on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.						
FALL DI1	3x07002 4x07002 I:7001	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
Counter for falling edges on the digital input DIx. If the module detects a falling edge on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.						
CHANGE DI1	3x07003 4x07003 I:7002	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
Counter for events on the digital input DIx. If the module detects an event on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0. The following events are available: Detecion of a short keypress Detection of the start of a long keypress Detection of the end of a long keypress						
SHORT KEYPRESS DI1	3x07004 4x07004 I:7003	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
Counter for short keypress events on the digital input DIx. If the module detects a short keypress on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.						
LONG KEYPRESS START DI1	3x07005 4x07005 I:7004	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
Counter for start events of long keypress actions on the digital input DIx. If the module detects the start of a long keypress action on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.						

LONG KEYPRESS END DI1	3x07006 4x07006 I:7005	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
Counter for end events of long keypress actions on the digital input DIx. If the module detects the end of a long keypress action on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.						
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI2						
RISE DI2	3x07011 4x07011 I:7010	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI2	3x07012 4x07012 I:7011	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI2	3x07013 4x07013 I:7012	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI2	3x07014 4x07014 I:7013	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI2	3x07015 4x07015 I:7014	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI2	3x07016 4x07016 I:7015	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI3						
RISE DI3	3x07021 4x07021 I:7020	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI3	3x07022 4x07022 I:7021	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI3	3x07023 4x07023 I:7022	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

SHORT KEYPRESS DI3	3x07024 4x07024 I:7023	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI3	3x07025 4x07025 I:7024	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI3	3x07026 4x07026 I:7025	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI4						
RISE DI4	3x07031 4x07031 I:7030	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI4	3x07032 4x07032 I:7031	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI4	3x07033 4x07033 I:7032	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI4	3x07034 4x07034 I:7033	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI4	3x07035 4x07035 I:7034	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI4	3x07036 4x07036 I:7035	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI5						
RISE DI5	3x07041 4x07041 I:7040	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI5	3x07042 4x07042 I:7041	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

CHANGE DI5	3x07043 4x07043 I:7042	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI5	3x07044 4x07044 I:7043	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI5	3x07045 4x07045 I:7044	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI5	3x07046 4x07046 I:7045	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI6						
RISE DI6	3x07051 4x07051 I:7050	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI6	3x07052 4x07052 I:7051	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI6	3x07053 4x07053 I:7052	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI6	3x07054 4x07054 I:7053	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI6	3x07055 4x07055 I:7054	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI6	3x07056 4x07056 I:7055	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI7						
RISE DI7	3x07061 4x07061 I:7060	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

FALL DI7	3x07062 4x07062 I:7061	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI7	3x07063 4x07063 I:7062	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI7	3x07064 4x07064 I:7063	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI7	3x07065 4x07065 I:7064	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI7	3x07066 4x07066 I:7065	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI8						
RISE DI8	3x07071 4x07071 I:7070	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI8	3x07072 4x07072 I:7071	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI8	3x07073 4x07073 I:7072	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI8	3x07074 4x07074 I:7073	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI8	3x07075 4x07075 I:7074	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI8	3x07076 4x07076 I:7075	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI9						

RISE DI9	3x07081 4x07081 I:7080	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI9	3x07082 4x07082 I:7081	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI9	3x07083 4x07083 I:7082	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI9	3x07084 4x07084 I:7083	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI9	3x07085 4x07085 I:7084	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI9	3x07086 4x07086 I:7085	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI10						
RISE DI10	3x07091 4x07091 I:7090	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI10	3x07092 4x07092 I:7091	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI10	3x07093 4x07093 I:7092	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI10	3x07094 4x07094 I:7093	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI10	3x07095 4x07095 I:7094	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

LONG KEYPRESS END DI10	3x07096 4x07096 I:7095	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI11						
RISE DI11	3x07101 4x07101 I:7100	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI11	3x07102 4x07102 I:7101	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI11	3x07103 4x07103 I:7102	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI11	3x07104 4x07104 I:7103	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI11	3x07105 4x07105 I:7104	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI11	3x07106 4x07106 I:7105	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI12						
RISE DI12	3x07111 4x07111 I:7110	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI12	3x07112 4x07112 I:7111	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI12	3x07113 4x07113 I:7112	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI12	3x07114 4x07114 I:7113	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

LONG KEYPRESS START DI12	3x07115 4x07115 I:7114	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI12	3x07116 4x07116 I:7115	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI13						
RISE DI13	3x07121 4x07121 I:7120	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI13	3x07122 4x07122 I:7121	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI13	3x07123 4x07123 I:7122	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI13	3x07124 4x07124 I:7123	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI13	3x07125 4x07125 I:7124	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI13	3x07126 4x07126 I:7125	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI14						
RISE DI14	3x07131 4x07131 I:7130	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI14	3x07132 4x07132 I:7131	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI14	3x07133 4x07133 I:7132	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

SHORT KEYPRESS DI14	3x07134 4x07134 I:7133	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI14	3x07135 4x07135 I:7134	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI14	3x07136 4x07136 I:7135	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI15						
RISE DI15	3x07141 4x07141 I:7140	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI15	3x07142 4x07142 I:7141	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI15	3x07143 4x07143 I:7142	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI15	3x07144 4x07144 I:7143	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI15	3x07145 4x07145 I:7144	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI15	3x07146 4x07146 I:7145	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI16						
RISE DI16	3x07151 4x07151 I:7150	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI16	3x07152 4x07152 I:7151	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

CHANGE DI16	3x07153 4x07153 I:7152	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI16	3x07154 4x07154 I:7153	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI16	3x07155 4x07155 I:7154	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI16	3x07156 4x07156 I:7155	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI17						
RISE DI17	3x07161 4x07161 I:7160	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI17	3x07162 4x07162 I:7161	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI17	3x07163 4x07163 I:7162	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI17	3x07164 4x07164 I:7163	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI17	3x07165 4x07165 I:7164	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI17	3x07166 4x07166 I:7165	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI18						
RISE DI18	3x07171 4x07171 I:7170	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

FALL DI18	3x07172 4x07172 I:7171	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI18	3x07173 4x07173 I:7172	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI18	3x07174 4x07174 I:7173	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI18	3x07175 4x07175 I:7174	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI18	3x07176 4x07176 I:7175	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI19						
RISE DI19	3x07181 4x07181 I:7180	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI19	3x07182 4x07182 I:7181	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI19	3x07183 4x07183 I:7182	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI19	3x07184 4x07184 I:7183	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI19	3x07185 4x07185 I:7184	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI19	3x07186 4x07186 I:7185	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI20						

RISE DI20	3x07191 4x07191 I:7190	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI20	3x07192 4x07192 I:7191	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI20	3x07193 4x07193 I:7192	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI20	3x07194 4x07194 I:7193	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI20	3x07195 4x07195 I:7194	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI20	3x07196 4x07196 I:7195	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI21						
RISE DI21	3x07201 4x07201 I:7200	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI21	3x07202 4x07202 I:7201	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI21	3x07203 4x07203 I:7202	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI21	3x07204 4x07204 I:7203	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI21	3x07205 4x07205 I:7204	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

LONG KEYPRESS END DI21	3x07206 4x07206 I:7205	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI22						
RISE DI22	3x07211 4x07211 I:7210	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI22	3x07212 4x07212 I:7211	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI22	3x07213 4x07213 I:7212	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI22	3x07214 4x07214 I:7213	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI22	3x07215 4x07215 I:7214	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI22	3x07216 4x07216 I:7215	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI23						
RISE DI23	3x07221 4x07221 I:7220	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI23	3x07222 4x07222 I:7221	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI23	3x07223 4x07223 I:7222	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI23	3x07224 4x07224 I:7223	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

LONG KEYPRESS START DI23	3x07225 4x07225 I:7224	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI23	3x07226 4x07226 I:7225	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI24						
RISE DI24	3x07231 4x07231 I:7230	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI24	3x07232 4x07232 I:7231	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI24	3x07233 4x07233 I:7232	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI24	3x07234 4x07234 I:7233	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI24	3x07235 4x07235 I:7234	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI24	3x07236 4x07236 I:7235	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI25						
RISE DI25	3x07241 4x07241 I:7240	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI25	3x07242 4x07242 I:7241	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI25	3x07243 4x07243 I:7242	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)				
SHORT KEYPRESS DI25	3x07244 4x07244 I:7243	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI25	3x07245 4x07245 I:7244	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI25	3x07246 4x07246 I:7245	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI26						
RISE DI26	3x07251 4x07251 I:7250	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI26	3x07252 4x07252 I:7251	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI26	3x07253 4x07253 I:7252	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI26	3x07254 4x07254 I:7253	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI26	3x07255 4x07255 I:7254	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI26	3x07256 4x07256 I:7255	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI27						
RISE DI27	3x07261 4x07261 I:7260	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

FALL DI27	3x07262 4x07262 I:7261	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI27	3x07263 4x07263 I:7262	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI27	3x07264 4x07264 I:7263	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI27	3x07265 4x07265 I:7264	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI27	3x07266 4x07266 I:7265	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI28						
RISE DI28	3x07271 4x07271 I:7270	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI28	3x07272 4x07272 I:7271	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI28	3x07273 4x07273 I:7272	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI28	3x07274 4x07274 I:7273	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI28	3x07275 4x07275 I:7274	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI28	3x07276 4x07276 I:7275	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI29						
RISE DI29	3x07281 4x07281 I:7280	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI29	3x07282 4x07282 I:7281	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI29	3x07283 4x07283 I:7282	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI29	3x07284 4x07284 I:7283	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI29	3x07285 4x07285 I:7284	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI29	3x07286 4x07286 I:7285	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI30						
RISE DI30	3x07291 4x07291 I:7290	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI30	3x07292 4x07292 I:7291	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI30	3x07293 4x07293 I:7292	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI30	3x07294 4x07294 I:7293	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI30	3x07295 4x07295 I:7294	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)				
LONG KEYPRESS END DI30	3x07296 4x07296 I:7295	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI31						
RISE DI31	3x07301 4x07301 I:7300	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI31	3x07302 4x07302 I:7301	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI31	3x07303 4x07303 I:7302	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI31	3x07304 4x07304 I:7303	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI31	3x07305 4x07305 I:7304	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI31	3x07306 4x07306 I:7305	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI32						
RISE DI32	3x07311 4x07311 I:7310	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI32	3x07312 4x07312 I:7311	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI32	3x07313 4x07313 I:7312	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

SHORT KEYPRESS DI32	3x07314 4x07314 I:7313	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI32	3x07315 4x07315 I:7314	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI32	3x07316 4x07316 I:7315	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI33						
RISE DI33	3x07321 4x07321 I:7320	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI33	3x07322 4x07322 I:7321	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI33	3x07323 4x07323 I:7322	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
SHORT KEYPRESS DI33	3x07324 4x07324 I:7323	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI33	3x07325 4x07325 I:7324	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS END DI33	3x07326 4x07326 I:7325	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI34						
RISE DI34	3x07331 4x07331 I:7330	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI34	3x07332 4x07332 I:7331	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)				
CHANGE DI34	3x07333 4x07333 I:7332	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI34	3x07334 4x07334 I:7333	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI34	3x07335 4x07335 I:7334	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI34	3x07336 4x07336 I:7335	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI35						
RISE DI35	3x07341 4x07341 I:7340	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI35	3x07342 4x07342 I:7341	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI35	3x07343 4x07343 I:7342	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI35	3x07344 4x07344 I:7343	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI35	3x07345 4x07345 I:7344	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI35	3x07346 4x07346 I:7345	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI36						

RISE DI36	3x07351 4x07351 I:7350	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI36	3x07352 4x07352 I:7351	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI36	3x07353 4x07353 I:7352	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI36	3x07354 4x07354 I:7353	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI36	3x07355 4x07355 I:7354	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI36	3x07356 4x07356 I:7355	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI37						
RISE DI37	3x07361 4x07361 I:7360	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI37	3x07362 4x07362 I:7361	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI37	3x07363 4x07363 I:7362	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI37	3x07364 4x07364 I:7363	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI37	3x07365 4x07365 I:7364	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

LONG KEYPRESS END DI37	3x07366 4x07366 I:7365	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI38						
RISE DI38	3x07371 4x07371 I:7370	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI38	3x07372 4x07372 I:7371	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI38	3x07373 4x07373 I:7372	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI38	3x07374 4x07374 I:7373	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI38	3x07375 4x07375 I:7374	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI38	3x07376 4x07376 I:7375	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI39						
RISE DI39	3x07381 4x07381 I:7380	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI39	3x07382 4x07382 I:7381	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI39	3x07383 4x07383 I:7382	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI39	3x07384 4x07384 I:7383	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)				
LONG KEYPRESS START DI39	3x07385 4x07385 I:7384	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI39	3x07386 4x07386 I:7385	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI40						
RISE DI40	3x07391 4x07391 I:7390	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI40	3x07392 4x07392 I:7391	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI40	3x07393 4x07393 I:7392	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI40	3x07394 4x07394 I:7393	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI40	3x07395 4x07395 I:7394	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI40	3x07396 4x07396 I:7395	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI41						
RISE DI41	3x07401 4x07401 I:7400	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI41	3x07402 4x07402 I:7401	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

CHANGE DI41	3x07403 4x07403 I:7402	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI41	3x07404 4x07404 I:7403	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI41	3x07405 4x07405 I:7404	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI41	3x07406 4x07406 I:7405	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI42						
RISE DI42	3x07411 4x07411 I:7410	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI42	3x07412 4x07412 I:7411	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI42	3x07413 4x07413 I:7412	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI42	3x07414 4x07414 I:7413	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI42	3x07415 4x07415 I:7414	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI42	3x07416 4x07416 I:7415	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI43						
RISE DI43	3x07421 4x07421 I:7420	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)				
FALL DI43	3x07422 4x07422 I:7421	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI43	3x07423 4x07423 I:7422	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI43	3x07424 4x07424 I:7423	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI43	3x07425 4x07425 I:7424	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI43	3x07426 4x07426 I:7425	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI44						
RISE DI44	3x07431 4x07431 I:7430	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI44	3x07432 4x07432 I:7431	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI44	3x07433 4x07433 I:7432	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI44	3x07434 4x07434 I:7433	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI44	3x07435 4x07435 I:7434	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI44	3x07436 4x07436 I:7435	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI45						
RISE DI45	3x07441 4x07441 I:7440	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI45	3x07442 4x07442 I:7441	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI45	3x07443 4x07443 I:7442	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI45	3x07444 4x07444 I:7443	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI45	3x07445 4x07445 I:7444	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI45	3x07446 4x07446 I:7445	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI46						
RISE DI46	3x07451 4x07451 I:7450	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI46	3x07452 4x07452 I:7451	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI46	3x07453 4x07453 I:7452	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI46	3x07454 4x07454 I:7453	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

LONG KEYPRESS START DI46	3x07455 4x07455 I:7454	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI46	3x07456 4x07456 I:7455	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI47						
RISE DI47	3x07461 4x07461 I:7460	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI47	3x07462 4x07462 I:7461	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI47	3x07463 4x07463 I:7462	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI47	3x07464 4x07464 I:7463	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI47	3x07465 4x07465 I:7464	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI47	3x07466 4x07466 I:7465	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI48						
RISE DI48	3x07471 4x07471 I:7470	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI48	3x07472 4x07472 I:7471	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI48	3x07473 4x07473 I:7472	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)				
SHORT KEYPRESS DI48	3x07474 4x07474 I:7473	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI48	3x07475 4x07475 I:7474	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI48	3x07476 4x07476 I:7475	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI49						
RISE DI49	3x07481 4x07481 I:7480	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI49	3x07482 4x07482 I:7481	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI49	3x07483 4x07483 I:7482	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI49	3x07484 4x07484 I:7483	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI49	3x07485 4x07485 I:7484	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI49	3x07486 4x07486 I:7485	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI50						
RISE DI50	3x07491 4x07491 I:7490	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

FALL DI50	3x07492 4x07492 I:7491	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI50	3x07493 4x07493 I:7492	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI50	3x07494 4x07494 I:7493	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI50	3x07495 4x07495 I:7494	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI50	3x07496 4x07496 I:7495	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI51						
RISE DI51	3x07501 4x07501 I:7500	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI51	3x07502 4x07502 I:7501	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI51	3x07503 4x07503 I:7502	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI51	3x07504 4x07504 I:7503	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI51	3x07505 4x07505 I:7504	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI51	3x07506 4x07506 I:7505	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI52						
RISE DI52	3x07511 4x07511 I:7510	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI52	3x07512 4x07512 I:7511	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI52	3x07513 4x07513 I:7512	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI52	3x07514 4x07514 I:7513	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI52	3x07515 4x07515 I:7514	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI52	3x07516 4x07516 I:7515	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI53						
RISE DI53	3x07521 4x07521 I:7520	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI53	3x07522 4x07522 I:7521	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI53	3x07523 4x07523 I:7522	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI53	3x07524 4x07524 I:7523	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI53	3x07525 4x07525 I:7524	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)				
LONG KEYPRESS END DI53	3x07526 4x07526 I:7525	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI54						
RISE DI54	3x07531 4x07531 I:7530	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI54	3x07532 4x07532 I:7531	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI54	3x07533 4x07533 I:7532	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI54	3x07534 4x07534 I:7533	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI54	3x07535 4x07535 I:7534	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI54	3x07536 4x07536 I:7535	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI55						
RISE DI55	3x07541 4x07541 I:7540	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI55	3x07542 4x07542 I:7541	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI55	3x07543 4x07543 I:7542	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

SHORT KEYPRESS DI55	3x07544 4x07544 I:7543	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI55	3x07545 4x07545 I:7544	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI55	3x07546 4x07546 I:7545	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI56						
RISE DI56	3x07551 4x07551 I:7550	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI56	3x07552 4x07552 I:7551	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI56	3x07553 4x07553 I:7552	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI56	3x07554 4x07554 I:7553	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI56	3x07555 4x07555 I:7554	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI56	3x07556 4x07556 I:7555	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI57						
RISE DI57	3x07561 4x07561 I:7560	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI57	3x07562 4x07562 I:7561	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)				
CHANGE DI57	3x07563 4x07563 I:7562	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI57	3x07564 4x07564 I:7563	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI57	3x07565 4x07565 I:7564	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI57	3x07566 4x07566 I:7565	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI58						
RISE DI58	3x07571 4x07571 I:7570	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI58	3x07572 4x07572 I:7571	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI58	3x07573 4x07573 I:7572	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI58	3x07574 4x07574 I:7573	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI58	3x07575 4x07575 I:7574	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI58	3x07576 4x07576 I:7575	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI59						

RISE DI59	3x07581 4x07581 I:7580	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI59	3x07582 4x07582 I:7581	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI59	3x07583 4x07583 I:7582	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI59	3x07584 4x07584 I:7583	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI59	3x07585 4x07585 I:7584	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI59	3x07586 4x07586 I:7585	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI60						
RISE DI60	3x07591 4x07591 I:7590	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI60	3x07592 4x07592 I:7591	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI60	3x07593 4x07593 I:7592	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI60	3x07594 4x07594 I:7593	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI60	3x07595 4x07595 I:7594	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

LONG KEYPRESS END DI60	3x07596 4x07596 I:7595	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI61						
RISE DI61	3x07601 4x07601 I:7600	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI61	3x07602 4x07602 I:7601	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI61	3x07603 4x07603 I:7602	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI61	3x07604 4x07604 I:7603	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI61	3x07605 4x07605 I:7604	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI61	3x07606 4x07606 I:7605	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI62						
RISE DI62	3x07611 4x07611 I:7610	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI62	3x07612 4x07612 I:7611	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI62	3x07613 4x07613 I:7612	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI62	3x07614 4x07614 I:7613	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)				
LONG KEYPRESS START DI62	3x07615 4x07615 I:7614	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI62	3x07616 4x07616 I:7615	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI63						
RISE DI63	3x07621 4x07621 I:7620	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI63	3x07622 4x07622 I:7621	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI63	3x07623 4x07623 I:7622	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI63	3x07624 4x07624 I:7623	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI63	3x07625 4x07625 I:7624	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI63	3x07626 4x07626 I:7625	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI64						
RISE DI64	3x07631 4x07631 I:7630	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI64	3x07632 4x07632 I:7631	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				

CHANGE DI64	3x07633 4x07633 I:7632	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI64	3x07634 4x07634 I:7633	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI64	3x07635 4x07635 I:7634	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI64	3x07636 4x07636 I:7635	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				