Register NAME	MODBUS Register	Register VALUE	DATA TYPE	DO WRITE
ASCII COMMANDS				
HEART BEAT	ASCII READ COMMAND	#HB<CR> Result: #HB<CR>	ASCII	
	TX	#255,HB<CR>		
	RX	#255,HB<CR>		
Sends an Heartbeat to test the communication				
GET VERSION	ASCII READ COMMAND	#VERSION<CR> Result: #VERSION:<VersionHi>,<VersionMed>,<VersionLo><CR>	ASCII	
	TX	#255,VERSION<CR>		
	RX	#255,VERSION:1.1.0<CR>		
		Actual SW version:1.1.0		
Returns the version number of the module VersionHi: Version number high (1..255) VersionMed: Version number medium (1..255) VersionLo: Version number low (1..255)				
GET TYPE	ASCII READ COMMAND	#TYPE<CR> Result: #TYPE:<Type><CR>	ASCII	
	TX	#255,TYPE<CR>		
	RX	#255,TYPE:RESI-C4-A-32DI12RO16AIOX<CR>		
		Actual module type:RESI-C4-A-32DI12RO16AIOX		
Returns the actual module type				
GET FEATURES	ASCII READ COMMAND	#FTRS<CR> Result: #FTRS:<Type><CR>	ASCII	
	TX	#255,FTRS<CR>		
	RX	#255,FTRS:RESI-C4-A-32DI12RO16AIOX,RS485,DI:32,RO:12,AIOX:16<CR>		
		Actual module type:N/A		
		Number of digital inputs:N/A		
		Type of digital inputs:N/A		
Returns the actual module features				
GET OWNER	ASCII READ COMMAND	#OWNER<CR> Result: #OWNER:<Owner><CR>	ASCII	
	TX	#255,OWNER<CR>		
	RX	#255,OWNER:RESI<CR>		
		Actual owner:RESI		
Returns the actual owner of the module				

GET CREATOR	ASCII READ COMMAND	#CREATOR<CR> Result: #CREATOR:<Creator><CR>	ASCII	
	TX	#255,CREATOR<CR>		
	RX	#255,CREATOR:DI HC SIGL,MSC<CR>		
		Actual creator:DI HC SIGL,MSC		
Returns the actual creator of the module				
GET COPYRIGHT	ASCII READ COMMAND	#COPYRIGHT<CR> Result: #COPYRIGHT:<Copyright><CR>	ASCII	
	TX	#255,COPYRIGHT<CR>		
	RX	#255,COPYRIGHT:2015-24 BY RESI AND DI HC SIGL,MSC WWW.RESI.CC<CR>		
		Actual copyright:2015-24 BY RESI AND DI HC SIGL,MSC WWW.RESI.CC		
Returns the actual copyright of the module				
GET SERIAL NUMBER	ASCII READ COMMAND	#SN<CR> Result: #SN:<Serial><CR>	ASCII	
	TX	#255,SN<CR>		
	RX	#255,SN:39002C000253554637303820<CR>		
		Actual serial number:39002C000253554637303820		
Returns the actual serial number of the module				
SET BOX NAME	ASCII WRITE COMMAND	#SETBOXNAME:<BOXNAME><CR> Result: #OK<CR>	ASCII	YES
	BOXNAME	MYBOX		
	TX	#255,SETBOXNAME:MYBOX<CR>		
	RX	N/A		
Sets a new box name for the controller				
GET BOX NAME	ASCII READ COMMAND	#BOXNAME<CR> Result: #BOXNAME:<BoxName><CR>	ASCII	
	TX	#255,BOXNAME<CR>		
	RX	#255,BOXNAME:MYBOX<CR>		
		Actual box name:MYBOX		
Returns the actual box name of the module. If no box name is defined, the value NONAME is returned				
GET INTERNAL STATUS	ASCII READ COMMAND	#INTSTAT<CR> Result: #INTSTAT:<Status><CR>	ASCII	
	TX	#255,INTSTAT<CR>		
	RX	#255,INTSTAT:I2C1:0,I2C2:0<CR>		
		Actual internal status:I2C1		
Returns the device specific internal status				

GET DIP SWITCH	ASCII READ COMMAND	#GDIP<CR> Result: #GDIP:<DIPSwitchDec>,<DIPSwitchHex><CR>	ASCII	
	TX	#255,GDIP<CR>		
	RX	#255,GDIP:255,0xFF<CR>		
		Actual DIP SWITCH settings:11111111		

Returns the actual setting of the Dip switches as decimal number and as hexadecimal number.
DIPSwitchDec
DIPSwitchHex
The current value of the DIP switches:
Bit 0: DIP Switch 1 (=0:OFF, =1:ON)
Bit 1: DIP Switch 2 (=0:OFF, =1:ON)
Bit 2: DIP Switch 3 (=0:OFF, =1:ON)
Bit 3: DIP Switch 4 (=0:OFF, =1:ON)
Bit 4: DIP Switch 5 (=0:OFF, =1:ON)
Bit 5: DIP Switch 6 (=0:OFF, =1:ON)
Bit 6: DIP Switch 7(=0:OFF, =1:ON)
Bit 7: DIP Switch 8 (=0:OFF, =1:ON)

SYSTEM COMMANDS

RESET	ASCII WRITE COMMAND	#RST<CR> Result: #OK<CR>	ASCII	NO
	TX	#255,RST<CR>		
	RX	N/A		

Executes a software reset (Reboot) of the module. Be aware that you will losse all serial interfaces in USB!!!

FACTORY RESET	ASCII WRITE COMMAND	#FRST<CR> Result: #OK<CR>	ASCII	NO
	TX	#255,FRST<CR>		
	RX	N/A		

Executes a factory reset of the module

WATCHDOG TIMER	ASCII WRITE COMMAND	#WD:<WDTIME><CR> Result: #OK<CR>	ASCII	NO
	WDTIME	10		
	TX	#255,WD:10<CR>		
	RX	N/A		

Enables or disables the WATCHDOG Timer for the Raspberry Pi module.
WDTIME:
1..3600000: Time for Watchdog in Milliseconds (Maximum 60 Minutes)
=0: No Watchdog is generated
HINT: The Watchdog is internally handled every 10ms, so every value below 10 will reset immediately the Raspberry Pi computer.

SET IO WATCHDOG TIMER	ASCII WRITE COMMAND	#SIOWATCHDOG:<IOWDTIME><CR> Result: #OK<CR>	ASCII	NO
	IOWDTIME	10		
	TX	#255,SIOWATCHDOG:10<CR>		

	RX	N/A		
Sets a new time for the internal IO WATCHDOG Timer. <IOWDTIME> is a time in 100ms. =0: No IO Watchdog is used HINT: The Watchdog is internally handled every 100ms, if the Timer reaches 0, all internal IOS will be set to a preconfigured state. Every ASCII command or MODBUS request will reset this timer.				
GET IO WATCHDOG TIMER	ASCII READ COMMAND	#GIOWATCHDOG<CR> Result: #GIOWATCHDOG::<IOWDTIME><CR>	ASCII	
	TX	#255,GIOWATCHDOG<CR>		
	RX	#255,GIOWATCHDOG:0,0x0<CR>		
Returns the actual time for the internal IO WATCHDOG Timer. <IOWDTIME> is a time in 100ms. =0: No IO Watchdog is used HINT: The Watchdog is internally handled every 100ms, if the Timer reaches 0, all internal IOS will be set to a preconfigured state. Every ASCII command or MODBUS request will reset this timer.				
CPU PARAMETERS				
GET CPU VOLTAGE	ASCII READ COMMAND	#GCPUTEMP<CR> Result: #GCPUTEMP:<CPUTemp><CR>	ASCII	
	TX	#255,GCPUTEMP<CR>		
	RX	#255,GCPUTEMP:52.3058<CR>		
		Actual internal temperature of CPU:52.3058°C		
Current internal temperature of CPU in ° Celsius.				
GET CPU VOLTAGE	ASCII READ COMMAND	#GCPUVOLT<CR> Result: #GCPUVOLT:<CPUVoltage><CR>	ASCII	
	TX	#255,GCPUVOLT<CR>		
	RX	#255,GCPUVOLT:3.3430<CR>		
		Actual supply voltage of CPU:3.3430V		
Current internal supply voltage of CPU in Volt.				
GET CPU BACKUP	ASCII READ COMMAND	#GCPUBACK<CR> Result: #GCPUBACK:<CPUBackupVoltage><CR>	ASCII	
	TX	#255,GCPUBACK<CR>		
	RX	#255,GCPUBACK:3.1871<CR>		
		Actual backup voltage of CPU for RTC:3.1871V		
Current internal backup voltage of CPU for the RTC in Volt.				

Register NAME	MODBUS Register	Register VALUE	DATA TYPE	DO WRITE
ASCII COMMANDS:REAL TIME CLOCK				
GET REAL TIME CLOCK	ASCII READ COMMAND	#GRTC<CR> Result: #GRTC:YMD,<YEAR>,<MONTH>,<DAY>,HMS,<HOUR>,<MINUTE>,<SECOND>,<WEEKDAY> ,DOK,<DATEOK>,TOK,<TIMEOK><CR>	ASCII	
	TX	#255,GRTC<CR>		
	RX	#255,GRTC:YMD,24,1,1,HMS,0,3,3,MON,DOK,1,TOK,1<CR>		
		Actual date DD.MM.YYYY:1.1.2024		
		Actual time HH.MM.SS (24h):00:03:03		
		Actual Weekday:MON		
		Battery buffered date is ok:YES		
		Battery buffered time is ok:YES		
Shows current RTC time of battery backup RTC on module				
ASCII COMMANDS:REAL TIME CLOCK				
SET REAL TIME CLOCK	ASCII WRITE COMMAND	#SRTC:YMD,<YEAR>,<MONTH>,<DAY>,HMS,<HOUR>,<MINUTE>,<SECOND>, <WEEKDAY><CR> Result: #OK<CR>	ASCII	YES
	YEAR	2024		
	MONTH	03		
	DAY	12		
	HOUR	15		
	MINUTE	01		
	SECOND	22		
	WEEKDAY	TUE		
	TX	#255,SRTC:YMD,24,03,12,HMS,15,01,22,TUE<CR>		
	RX	N/A		
Executes a software reset (Reboot) of the module.				

Register NAME	MODBUS Register	Register VALUE	DATA TYPE	DO WRITE
ASCII COMMANDS:FRAM				
GET FRAMSIZE	ASCII READ COMMAND	#GFRAMSIZE<CR> Result: #GFRAMSIZE:<FRAMType>,<FRAMSize>,<UsedSizeDEC>,<UsedSizeHEX><CR>	ASCII	
	TX	#255,GFRAMSIZE<CR>		
	RX	#255,GFRAMSIZE:FM25L16B_G,2kB,2,0x2<CR>		
Reads the actual type and size of the used FRAM. The <UsedSize> describes the internal used space in bytes of the FRAM				
ASCII COMMANDS:FRAM				
GET FRAM16	ASCII READ COMMAND	#GFRAM16:<INDEX><CR> Result: #GFRAM16:<INDEXDEC>,<VALUEDEC>,<INDEXHEX>,<VALUEHEX><CR> or #GFRAM16:<INDEXDEC>,ERR,<INDEXHEX>,ERR<CR>	ASCII	
	INDEX	350		
	TX	#255,GFRAM16:350<CR>		
	RX	#255,GFRAM16:350,0,0x15E,0x0<CR>		
		FRAM Index in bytes:350		
		FRAM Value in decimal:0		
Reads the actual UINT16 value (2 bytes) of FRAM memory <INDEX>. <INDEX> is a BYTE index in the FRAM strogae starting with 0.				
GET FRAM32	ASCII READ COMMAND	#GFRAM32:<INDEX><CR> Result: #GFRAM32:<INDEXDEC>,<VALUEDEC>,<INDEXHEX>,<VALUEHEX><CR> or #GFRAM32:<INDEXDEC>,ERR,<INDEXHEX>,ERR<CR>	ASCII	
	INDEX	350		
	TX	#255,GFRAM32:350<CR>		
	RX	#255,GFRAM32:350,0,0x15E,0x0<CR>		
		FRAM Index in bytes:350		
		FRAM Value in decimal:0		
Reads the actual UINT32 value 4 bytes) of FRAM memory <INDEX>. <INDEX> is a BYTE index in the FRAM strogae starting with 0.				
GET FRAMDBL	ASCII READ COMMAND	#GFRAMDBL:<INDEX><CR> Result: #GFRAMDBL:<INDEXDEC>,<VALUEDBL>,<INDEXHEX>,<VALUEDBL><CR> or #GFRAMDBL:<INDEXDEC>,ERR,<INDEXHEX>,ERR<CR>	ASCII	
	INDEX	400		
	TX	#255,GFRAMDBL:400<CR>		
	RX	#255,GFRAMDBL:400,0,0x190,0<CR>		
		FRAM Index in bytes:400		
		FRAM Value in decimal:0		
Reads the actual DOUBLE value 8 bytes) of FRAM memory <INDEX>. <INDEX> is a BYTE index in the FRAM strogae starting with 0.				
ASCII COMMANDS:FRAM				

SET FRAM16	ASCII WRITE COMMAND	#SFRAM16:<INDEX>,<VALUE><CR> Result: #SFRAM16:OK<CR> or #SFRAM16:ERR<CR>	ASCII	YES
	INDEX	350		
	VALUE	1234		
	TX	#255,SFRAM16:350,1234<CR>		
	RX	N/A		
Writes a new UINT16 value (2 byte) into FRAM memory <INDEX>. <INDEX> is a BYTE index in the FRAM strogae starting with 0.				
SET FRAM32	ASCII WRITE COMMAND	#SFRAM32:<INDEX>,<VALUE><CR> Result: #SFRAM32:OK<CR> or #SFRAM32:ERR<CR>	ASCII	YES
	INDEX	350		
	VALUE	123456		
	TX	#255,SFRAM32:350,123456<CR>		
	RX	N/A		
Writes a new UINT32 value (4 byte) into FRAM memory <INDEX>. <INDEX> is a BYTE index in the FRAM strogae starting with 0.				
SET FRAMDBL	ASCII WRITE COMMAND	#SFRAMDBL:<INDEX>,<DOUBLEVALUE><CR> Result: #SFRAMDBL:OK<CR> or #SFRAMDBL:ERR<CR>	ASCII	YES
	INDEX	400		
	DOUBLEVALUE	3,1415926		
	TX	#255,SFRAMDBL:400,3.1415926<CR>		
	RX	N/A		
Writes a new DOUBLE value (8 byte) into FRAM memory <INDEX>. <INDEX> is a BYTE index in the FRAM strogae starting with 0.				

Register NAME	MODBUS Register	Register VALUE	DATA TYPE	DO WRITE
LED STATUS:LED1:GREEN				
GET LED1	ASCII READ COMMAND	#GLED1<CR> Result: #GLED1:<LEDMode>,<LEDStateDec>,<LEDStateHex><CR>	ASCII	
	TX	#255,GLED1<CR>		
	RX	#255,GLED1:OFF,0,0x0<CR>		
		Actual LED state:OFF LED ist currently 0		
Returns the actual state of the LED1:GREEN on the cover of module				
LED COMMANDS:LED1:GREEN				
SET LED1 OFF	ASCII WRITE COMMAND	#SL1OFF<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL1OFF<CR>		
	RX	N/A		
Sets the current state of the LED1:GREEN on the cover of module to OFF				
SET LED1 ON	ASCII WRITE COMMAND	#SL1ON<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL1ON<CR>		
	RX	N/A		
Sets the current state of the LED1:GREEN on the cover of module to ON				
SET LED1 INVERT	ASCII WRITE COMMAND	#SL1INV<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL1INV<CR>		
	RX	N/A		
Inverts the current state of the LED1:GREEN on the cover of module from ON to OFF or from OFF to ON				
SET LED1 PULSE	ASCII WRITE COMMAND	#SL1PULSE:<PULSETIME><CR> Result: #OK<CR>	ASCII	YES
	PULSETIME	1000		
	TX	#255,SL1PULSE:1000<CR>		
	RX	N/A		
Sets the current state of the LED1:GREEN on the cover of module to PULSE and defines the one time pulse duration in Milliseconds between 1 and 60000				
SET LED1 BLINK	ASCII WRITE COMMAND	#SL1BLINK:<BLINKTIME><CR> Result: #OK<CR>	ASCII	YES
	BLINKTIME	1000		
	TX	#255,SL1BLINK:1000<CR>		
	RX	N/A		

Sets the current state of the LED1:GREEN on the cover of module to PULSE and defines the one time pulse duration in Milliseconds between 1 and 60000				
SET LED1 FLASH	ASCII WRITE COMMAND	#SL1FLASH:<ONTIME>,<OFFTIME><CR> Result: #OK<CR>	ASCII	YES
	ONTIME	200		
	OFFTIME	3000		
	TX	#255,SL1FLASH:200,3000<CR>		
	RX	N/A		
Sets the current state of the LED1:GREEN on the cover of module to FLASH and defines the on and off intervals in Milliseconds between 20 and 600000				

Register NAME	MODBUS Register	Register VALUE	DATA TYPE	DO WRITE
LED STATUS:LED2:WHITE				
GET LED2	ASCII READ COMMAND	#GLED2<CR> Result: #GLED2:<LEDMode>,<LEDStateDec>,<LEDStateHex><CR>	ASCII	
	TX	#255,GLED2<CR>		
	RX	#255,GLED2:OFF,0,0x0<CR>		
		Actual LED state:OFF LED ist currently 0		
Returns the actual state of the LED2:WHITE on the cover of module				
LED COMMANDS:LED2:WHITE				
SET LED2 OFF	ASCII WRITE COMMAND	#SL2OFF<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL2OFF<CR>		
	RX	N/A		
Sets the current state of the LED2:WHITE on the cover of module to OFF				
SET LED2 ON	ASCII WRITE COMMAND	#SL2ON<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL2ON<CR>		
	RX	N/A		
Sets the current state of the LED2:WHITE on the cover of module to ON				
SET LED2 INVERT	ASCII WRITE COMMAND	#SL2INV<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL2INV<CR>		
	RX	N/A		
Inverts the current state of the LED2:WHITE on the cover of module from ON to OFF or from OFF to ON				
SET LED2 PULSE	ASCII WRITE COMMAND	#SL2PULSE:<PULSETIME><CR> Result: #OK<CR>	ASCII	YES
	PULSETIME	1000		
	TX	#255,SL2PULSE:1000<CR>		
	RX	N/A		
Sets the current state of the LED2:WHITE on the cover of module to PULSE and defines the one time pulse duration in Milliseconds between 1 and 60000				
SET LED2 BLINK	ASCII WRITE COMMAND	#SL2BLINK:<BLINKTIME><CR> Result: #OK<CR>	ASCII	YES
	BLINKTIME	1000		
	TX	#255,SL2BLINK:1000<CR>		
	RX	N/A		

Sets the current state of the LED2:WHITE on the cover of module to PULSE and defines the one time pulse duration in Milliseconds between 1 and 60000				
SET LED2 FLASH	ASCII WRITE COMMAND	#SL2FLASH:<ONTIME>,<OFFTIME><CR> Result: #OK<CR>	ASCII	YES
	ONTIME	200		
	OFFTIME	3000		
	TX	#255,SL2FLASH:200,3000<CR>		
	RX	N/A		
Sets the current state of the LED2:WHITE on the cover of module to FLASH and defines the on and off intervals in Milliseconds between 20 and 600000				

Register NAME	MODBUS Register	Register VALUE	DATA TYPE	DO WRITE
LED STATUS:LED3:RED				
GET LED3	ASCII READ COMMAND	#GLED3<CR> Result: #GLED3:<LEDMode>,<LEDStateDec>,<LEDStateHex><CR>	ASCII	
	TX	#255,GLED3<CR>		
	RX	#255,GLED3:OFF,0,0x0<CR>		
		Actual LED state:OFF LED ist currently 0		
Returns the actual state of the LED3:RED on the cover of module				
LED COMMANDS:LED3:RED				
SET LED3 OFF	ASCII WRITE COMMAND	#SL3OFF<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL3OFF<CR>		
	RX	N/A		
Sets the current state of the LED3:RED on the cover of module to OFF				
SET LED3 ON	ASCII WRITE COMMAND	#SL3ON<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL3ON<CR>		
	RX	N/A		
Sets the current state of the LED3:RED on the cover of module to ON				
SET LED3 INVERT	ASCII WRITE COMMAND	#SL3INV<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL3INV<CR>		
	RX	N/A		
Inverts the current state of the LED3:RED on the cover of module from ON to OFF or from OFF to ON				
SET LED3 PULSE	ASCII WRITE COMMAND	#SL3PULSE:<PULSETIME><CR> Result: #OK<CR>	ASCII	YES
	PULSETIME	1000		
	TX	#255,SL3PULSE:1000<CR>		
	RX	N/A		
Sets the current state of the LED3:RED on the cover of module to PULSE and defines the one time pulse duration in Milliseconds between 1 and 60000				
SET LED3 BLINK	ASCII WRITE COMMAND	#SL3BLINK:<BLINKTIME><CR> Result: #OK<CR>	ASCII	YES
	BLINKTIME	1000		
	TX	#255,SL3BLINK:1000<CR>		
	RX	N/A		

Sets the current state of the LED3:RED on the cover of module to PULSE and defines the one time pulse duration in Milliseconds between 1 and 60000				
SET LED3 FLASH	ASCII WRITE COMMAND	#SL3FLASH:<ONTIME>,<OFFTIME><CR> Result: #OK<CR>	ASCII	YES
	ONTIME	200		
	OFFTIME	3000		
	TX	#255,SL3FLASH:200,3000<CR>		
	RX	N/A		
Sets the current state of the LED3:RED on the cover of module to FLASH and defines the on and off intervals in Milliseconds between 20 and 600000				

Register NAME	MODBUS Register	Register VALUE	DATA TYPE	DO WRITE
LED STATUS:LED4:YELLOW				
GET LED4	ASCII READ COMMAND	#GLED4<CR> Result: #GLED4:<LEDMode>,<LEDStateDec>,<LEDStateHex><CR>	ASCII	
	TX	#255,GLED4<CR>		
	RX	#255,GLED4:OFF,0,0x0<CR>		
		Actual LED state:OFF LED ist currently 0		
Returns the actual state of the LED4:YELLOW on the cover of module				
LED COMMANDS:LED4:YELLOW				
SET LED4 OFF	ASCII WRITE COMMAND	#SL4OFF<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL4OFF<CR>		
	RX	N/A		
Sets the current state of the LED4:YELLOW on the cover of module to OFF				
SET LED4 ON	ASCII WRITE COMMAND	#SL4ON<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL4ON<CR>		
	RX	N/A		
Sets the current state of the LED4:YELLOW on the cover of module to ON				
SET LED4 INVERT	ASCII WRITE COMMAND	#SL4INV<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL4INV<CR>		
	RX	N/A		
Inverts the current state of the LED4:YELLOW on the cover of module from ON to OFF or from OFF to ON				
SET LED4 PULSE	ASCII WRITE COMMAND	#SL4PULSE:<PULSETIME><CR> Result: #OK<CR>	ASCII	YES
	PULSETIME	1000		
	TX	#255,SL4PULSE:1000<CR>		
	RX	N/A		
Sets the current state of the LED4:YELLOW on the cover of module to PULSE and defines the one time pulse duration in Milliseconds between 1 and 60000				
SET LED4 BLINK	ASCII WRITE COMMAND	#SL4BLINK:<BLINKTIME><CR> Result: #OK<CR>	ASCII	YES
	BLINKTIME	1000		
	TX	#255,SL4BLINK:1000<CR>		
	RX	N/A		

Sets the current state of the LED4:YELLOW on the cover of module to PULSE and defines the one time pulse duration in Milliseconds between 1 and 60000				
SET LED4 FLASH	ASCII WRITE COMMAND	#SL4FLASH:<ONTIME>,<OFFTIME><CR> Result: #OK<CR>	ASCII	YES
	ONTIME	200		
	OFFTIME	3000		
	TX	#255,SL4FLASH:200,3000<CR>		
	RX	N/A		
Sets the current state of the LED4:YELLOW on the cover of module to FLASH and defines the on and off intervals in Milliseconds between 20 and 600000				

Register NAME	MODBUS Register	Register VALUE	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
PRODUCT INFO						
HW_GROUP	3x65201 4x65201 I:65200	50176,0xC400 B:C4 00			UINT16 R/O	
This is the group of hardware of the current product						
HW_TYPE	3x65202 4x65202 I:65201	1,0x0001 B:00 01			UINT16 R/O	
This is the type of hardware of the current product						
SW_VERSION	3x65203 4x65203 I:65202	272,0x0110 B:01 10			UINT16 R/O	
		SW VERSION:0.1.0				
This is the current software version of the firmware						
SW_AUTHOR	3x65204 4x65204 I:65203	21321,0x5349 B:53 49			UINT16 R/O	
This is the current software author of the firmware						
MANUFACTURER	3x65205 4x65205 I:65204	1380275017,0x52455349 B:52 45 53 49			UINT32 R/O	
This is the current software author of the firmware						
NUMBER OF DIGITAL INPUTS	3x65207 4x65207 I:65206	0,0x0000 B:00 00			UINT16 R/O	
		Number of DIS:0				
This is the current software version of the firmware						
NUMBER OF DIGITAL OUTPUTS	3x65208 4x65208 I:65207	0,0x0000 B:00 00			UINT16 R/O	
		Number of DOS:0				
This is the current software version of the firmware						
NUMBER OF ANALOG INPUTS	3x65209 4x65209 I:65208	0,0x0000 B:00 00			UINT16 R/O	
		Number of AIS:0				
This is the current software version of the firmware						
NUMBER OF ANALOG OUTPUTS	3x65210 4x65210 I:65209	0,0x0000 B:00 00			UINT16 R/O	
		Number of AOS:0				

This is the current software version of the firmware						
NUMBER OF UNIVERSAL IN/OUTPUTS	3x65211 4x65211 I:65210	0,0x0000 B:00 00			UINT16 R/O	
		Number of AIOX:0				
This is the current software version of the firmware						
NUMBER OF SPECIAL INPUTS	3x65212 4x65212 I:65211	0,0x0000 B:00 00			UINT16 R/O	
		Number of special inputs:0				
This is the current software version of the firmware						
NUMBER OF SPECIAL OUTPUTS	3x65213 4x65213 I:65212	0,0x0000 B:00 00			UINT16 R/O	
		Number of special outputs:0				
This is the current software version of the firmware						
FEATURE1	3x65214 4x65214 I:65213	2,0x0002 B:00 02			UINT16 R/O	
		Feature:RS485				
This is the feature list of the controller: 0:NONE, 1:RS232, 2:RS485, 3:KNX, 4:DALI, 5:MBUS, 6:LORA, 7:LTE, 8:2xETHERNET						
FEATURE2	3x65215 4x65215 I:65214	0,0x0000 B:00 00			UINT16 R/O	
		Feature:NONE				
FEATURE3	3x65216 4x65216 I:65215	0,0x0000 B:00 00			UINT16 R/O	
		Feature:NONE				
FEATURE4	3x65217 4x65217 I:65216	0,0x0000 B:00 00			UINT16 R/O	
		Feature:NONE				
FEATURE5	3x65218 4x65218 I:65217	0,0x0000 B:00 00			UINT16 R/O	
		Feature:NONE				
FEATURE6	3x65219 4x65219 I:65218	0,0x0000 B:00 00			UINT16 R/O	
		Feature:NONE				
FEATURE7	3x65220 4x65220 I:65219	0,0x0000 B:00 00			UINT16 R/O	

		Feature:NONE				
FEATURE8	3x65221 4x65221 I:65220	0,0x0000 B:00 00			UINT16 R/O	
		Feature:NONE				
MODBUS WATCHDOG						
MODBUS WATCHDOG TIME	3x65222 4x65222 I:65221	0,0x0000 B:00 00		50	UINT16 R/W	NO
		Actual watchdog time in 1/100s:0 -> 0,0s				
Writing a value onto this register defines a new time for the internal communication watchdog timer. The value is a timespan in 1/100s. =0: The communication watchdog is disabled =1..65535: Communication watchdog will be trigegred after x 1/100s pause on communication line In case of an communication watchdog, the module sets all outputs to the states defined in the configuration output registers Reading this register will return the current stored time from the internal FRAM						
RASPBERRY PI WATCHDOG TIMER	3x65223 4x65223 I:65222	0,0x0000 B:00 00		50	UINT16 R/W	NO
		remaining watchdog time in 10ms:0 -> 0,000s				
Enables or disables the WATCHDOG Timer for the Raspberry Pi module. 1..65535: Time for Watchdog in x10 Milliseconds (Maximum 655,35 seconds) =0: No Watchdog is generated						
FRAM						
GET FRAM TYPE	3x65224 4x65224 I:65223	2,0x0002 B:00 02			UINT16 R/O	
		FRAM size & type:FM25L16 2kB				
Returns the current ytpe of the FRAM and its total size =2:FM25L16B_G, 2kB =64:FM25V05, 64kB =128:FM25V10, 128kB						
GET FRAM USED BYTES	3x65225 4x65225 I:65224	166,0x00A6 B:00 A6			UINT16 R/O	
		FRAM used bytes:166				
Returns the amount of used bytes from system in FRAM						
RTC REAL TIME CLOCK						
RTC YEAR	3x65231 4x65231 I:65230	24,0x0018 B:00 18		24	UINT16 R/W	NO
		Actual RTC year:24				
Returns the actual year of the internal real time clock in the range of 24 to 99. Writing to this register prepares the setting of a new time.						

RTC MONTH	3x65232 4x65232 I:65231	2,0x0002 B:00 02		1	UINT16 R/W	NO
		Actual RTC month:2				
Returns the actual month of the internal real time clock in the range of 1 to 12 Writing to this register prepares the setting of a new time.						
RTC DAY	3x65233 4x65233 I:65232	29,0x001D B:00 1D		1	UINT16 R/W	NO
		Actual RTC day:29				
Returns the actual day of the internal real time clock in the range of 1 to 31 Writing to this register prepares the setting of a new time.						
RTC HOUR	3x65234 4x65234 I:65233	18,0x0012 B:00 12		12	UINT16 R/W	NO
		Actual RTC month:18				
Returns the actual hour of the internal real time clock in the range of 0 to 23 Writing to this register prepares the setting of a new time.						
RTC MINUTE	3x65235 4x65235 I:65234	0,0x0000 B:00 00		45	UINT16 R/W	NO
		Actual RTC hour:0				
Returns the actual minute of the internal real time clock in the range of 0 to 59 Writing to this register prepares the setting of a new time.						
RTC SECOND	3x65236 4x65236 I:65235	23,0x0017 B:00 17		30	UINT16 R/W	NO
		Actual RTC second:23				
Returns the actual second of the internal real time clock in the range of 0 to 59 Writing to this register prepares the setting of a new time.						
RTC DAY OF WEEK	3x65237 4x65237 I:65236	4,0x0004 B:00 04		5:FRIDAY	UINT16 R/W	NO
		Actual RTC week day:THU		SELECT DAY OF WEEK		
Returns the actual day of week in the range 1 to 7 1:MON, 2:TUE, 3:WED, 4:THU, 5:FRI, 6:SAT, 7:SUN Writing to this register writes a new date and time and weekday to the RTC						
DIP SWITCH STATUS						
DIP SWITCH	3x65501 4x65501 I:65500	85,0x0055 B:00 55			UINT16 R/O	

Returns the actual setting of the Dip switches. Bit 0: DIP Switch 1 (=0:OFF, =1:ON) Bit 1: DIP Switch 2 (=0:OFF, =1:ON) Bit 2: DIP Switch 3 (=0:OFF, =1:ON) Bit 3: DIP Switch 4 (=0:OFF, =1:ON) Bit 4: DIP Switch 5 (=0:OFF, =1:ON) Bit 5: DIP Switch 6 (=0:OFF, =1:ON) Bit 6: DIP Switch 7 (=0:OFF, =1:ON) Bit 7: DIP Switch 8 (=0:OFF, =1:ON)						
CPU DATA						
SERIAL1	3x65521 4x65521 I:65520	34,0x0022 B:00 22			UINT16 R/O	
Serial number of module as 96 bit unsigned integer number						
SERIAL2	3x65522 4x65522 I:65521	24,0x0018 B:00 18			UINT16 R/O	
SERIAL3	3x65523 4x65523 I:65522	22291,0x5713 B:57 13			UINT16 R/O	
SERIAL4	3x65524 4x65524 I:65523	20547,0x5043 B:50 43			UINT16 R/O	
SERIAL5	3x65525 4x65525 I:65524	13361,0x3431 B:34 31			UINT16 R/O	
SERIAL6	3x65526 4x65526 I:65525	8246,0x2036 B:20 36			UINT16 R/O	
		SERIAL:220018001357435031343620				
Serial number of module as 96 bit unsigned integer number						
CPU TEMPERATURE	3x65527 4x65527 I:65526	5061,0x13C5 B:13 C5			UINT16 R/O	
		Actual internal temperature of CPU:50,61°C				
Current internal temperature of CPU in ° Celsius multiplied by 100.						
CPU VOLTAGE	3x65528 4x65528 I:65527	333,0x014D B:01 4D			UINT16 R/O	
		Actual supply voltage of CPU:3,33V				
Current internal supply voltage of CPU in Volt multiplied by 1000.						
CPU BACKUP	3x65529 4x65529 I:65528	311,0x0137 B:01 37			UINT16 R/O	
		Actual backup voltage of CPU for RTC:3,11V				
Current internal backup voltage of CPU for RTC in Volt multiplied by 1000.						
RESETs						

RASPBERRY PI RESET	3x65534 4x65534 I:65533	0,0x00 B:00 00		1:PERFORM RASPBERRY PI RESET	BIT R/W	NO
Resets the Raspberry Pi						
RASPBERRY PI RESET	3x65534 4x65534 I:65533	0,0x0000 B:00 00		1:PERFORM RASPBERRY PI RESET	UINT16 R/W	NO
Resets the Raspberry Pi						
FACTORY RESET	1x65535 2x65535 I:65534	0,0x00 B:00		1:PERFORM FACTORY RESET	BIT R/W	NO
Performs a factory reset of all internal saved parameters						
FACTORY RESET	3x65535 4x65535 I:65534	0,0x0000 B:00 00		1:PERFORM FACTORY RESET	UINT16 R/W	NO
Performs a factory reset of all internal saved parameters						
RESET	1x65536 2x65536 I:65535	0,0x00 B:00		1:PERFORM ARM RESET	BIT R/W	NO
Performs a software reset, whenever 1 is written to this register. If the host writes to this register 1, the module executes a soft reset (reboot). Be aware that you will losse all serial interfaces in USB!!!						
RESET	3x65536 4x65535 I:65535	0,0x0000 B:00 00		1:PERFORM ARM RESET	UINT16 R/W	NO
Performs a software reset, whenever 1 is written to this register. If the host writes to this register 1, the module executes a soft reset (reboot). Be aware that you will losse all serial interfaces in USB!!!						

Register NAME	MODBUS Register	Register VALUE	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
DIP SWITCH STATUS						
DIP SWITCH DIP1	1x65001 2x65001 I:65000	????			BIT R/O	
Returns the actual setting of the Dip switches. =0: DIP is OFF =1: DIP is ON						
DIP SWITCH DIP2	1x65002 2x65002 I:65001	????			BIT R/O	
DIP SWITCH DIP3	1x65003 2x65003 I:65002	????			BIT R/O	
DIP SWITCH DIP4	1x65004 2x65004 I:65003	????			BIT R/O	
DIP SWITCH DIP5	1x65005 2x65005 I:65004	????			BIT R/O	
DIP SWITCH DIP6	1x65006 2x65006 I:65005	????			BIT R/O	
DIP SWITCH DIP7	1x65007 2x65007 I:65006	????			BIT R/O	
DIP SWITCH DIP8	1x65008 2x65008 I:65007	????			BIT R/O	
LED1:GREEN						
LED1:GREEN SET TO OFF	1x65009 2x65009 I:65008	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil sets the LED to OFF						
LED1:GREEN SET TO ON	1x65010 2x65010 I:65009	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil sets the LED to ON						
LED1:GREEN INVERT LED STATE	1x65011 2x65011 I:65010	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil inverts the actual LED state						
LED1:GREEN BLINK	1x65012 2x65012 I:65011	????		N/A:DO NOTHING	BIT W/O	NO

Writing 1 to this coil start symmetrical blinking of LED with last defined time						
LED1:GREEN FLASH	1x65013 2x65013 I:65012	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with last defined times						
LED1:GREEN PULSE	1x65014 2x65014 I:65013	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with last defined time						
LED1:GREEN BLINK 5s	1x65015 2x65015 I:65014	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 5s ON-5s OFF cycle						
LED1:GREEN BLINK 1s	1x65016 2x65016 I:65015	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 1s ON-1s OFF cycle						
LED1:GREEN BLINK 250ms	1x65017 2x65017 I:65016	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 250ms ON-250ms OFF cycle						
LED1:GREEN BLINK 50ms	1x65018 2x65018 I:65017	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 50ms ON-50ms OFF cycle						
LED1:GREEN FLASH 5s-1s	1x65019 2x65019 I:65018	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 5s ON-1s OFF cycle						
LED1:GREEN FLASH 1s-250ms	1x65020 2x65020 I:65019	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 1s ON-250ms OFF cycle						
LED1:GREEN FLASH 500ms-100ms	1x65021 2x65021 I:65020	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 500ms ON-100ms OFF cycle						
LED1:GREEN FLASH 300ms-50ms	1x65022 2x65022 I:65021	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 300ms ON-50ms OFF cycle						
LED1:GREEN PULSE 1s	1x65023 2x65023 I:65022	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 1s ON						

LED1:GREEN PULSE 500ms	1x65024 2x65024 I:65023	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 500ms ON						
LED1:GREEN PULSE 250ms	1x65025 2x65025 I:65024	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 250ms ON						
LED1:GREEN PULSE 100ms	1x65026 2x65026 I:65025	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 100ms ON						
LED1:GREEN PULSE 20ms	1x65027 2x65027 I:65026	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 20ms ON						
LED2:WHITE						
LED2:WHITE SET TO OFF	1x65029 2x65029 I:65028	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil sets the LED to OFF						
LED2:WHITE SET TO ON	1x65030 2x65030 I:65029	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil sets the LED to ON						
LED2:WHITE INVERT LED STATE	1x65031 2x65031 I:65030	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil inverts the actual LED state						
LED2:WHITE BLINK	1x65032 2x65032 I:65031	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with last defined time						
LED2:WHITE FLASH	1x65033 2x65033 I:65032	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with last defined times						
LED2:WHITE PULSE	1x65034 2x65034 I:65033	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with last defined time						
LED2:WHITE BLINK 5s	1x65035 2x65035 I:65034	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 5s ON-5s OFF cycle						

LED2:WHITE BLINK 1s	1x65036 2x65036 I:65035	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 1s ON-1s OFF cycle						
LED2:WHITE BLINK 250ms	1x65037 2x65037 I:65036	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 250ms ON-250ms OFF cycle						
LED2:WHITE BLINK 50ms	1x65038 2x65038 I:65037	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 50ms ON-50ms OFF cycle						
LED2:WHITE FLASH 5s-1s	1x65039 2x65039 I:65038	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 5s ON-1s OFF cycle						
LED2:WHITE FLASH 1s-250ms	1x65040 2x65040 I:65039	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 1s ON-250ms OFF cycle						
LED2:WHITE FLASH 500ms-100ms	1x65041 2x65041 I:65040	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 500ms ON-100ms OFF cycle						
LED2:WHITE FLASH 300ms-50ms	1x65042 2x65042 I:65041	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 300ms ON-50ms OFF cycle						
LED2:WHITE PULSE 1s	1x65043 2x65043 I:65042	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 1s ON						
LED2:WHITE PULSE 500ms	1x65044 2x65044 I:65043	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 500ms ON						
LED2:WHITE PULSE 250ms	1x65045 2x65045 I:65044	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 250ms ON						
LED2:WHITE PULSE 100ms	1x65046 2x65046 I:65045	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 100ms ON						

LED2:WHITE PULSE 20ms	1x65047 2x65047 I:65046	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 20ms ON						
LED3:RED						
LED3:RED SET TO OFF	1x65049 2x65049 I:65048	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil sets the LED to OFF						
LED3:RED SET TO ON	1x65050 2x65050 I:65049	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil sets the LED to ON						
LED3:RED INVERT LED STATE	1x65051 2x65051 I:65050	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil inverts the actual LED state						
LED3:RED BLINK	1x65052 2x65052 I:65051	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with last defined time						
LED3:RED FLASH	1x65053 2x65053 I:65052	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with last defined times						
LED3:RED PULSE	1x65054 2x65054 I:65053	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with last defined time						
LED3:RED BLINK 5s	1x65055 2x65055 I:65054	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 5s ON-5s OFF cycle						
LED3:RED BLINK 1s	1x65056 2x65056 I:65055	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 1s ON-1s OFF cycle						
LED3:RED BLINK 250ms	1x65057 2x65057 I:65056	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 250ms ON-250ms OFF cycle						
LED3:RED BLINK 50ms	1x65058 2x65058 I:65057	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 50ms ON-50ms OFF cycle						

LED3:RED FLASH 5s-1s	1x65059 2x65059 I:65058	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 5s ON-1s OFF cycle						
LED3:RED FLASH 1s-250ms	1x65060 2x65060 I:65059	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 1s ON-250ms OFF cycle						
LED3:RED FLASH 500ms-100ms	1x65061 2x65061 I:65060	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 500ms ON-100ms OFF cycle						
LED3:RED FLASH 300ms-50ms	1x65062 2x65062 I:65061	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 300ms ON-50ms OFF cycle						
LED3:RED PULSE 1s	1x65063 2x65063 I:65062	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 1s ON						
LED3:RED PULSE 500ms	1x65064 2x65064 I:65063	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 500ms ON						
LED3:RED PULSE 250ms	1x65065 2x65065 I:65064	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 250ms ON						
LED3:RED PULSE 100ms	1x65066 2x65066 I:65065	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 100ms ON						
LED3:RED PULSE 20ms	1x65067 2x65067 I:65066	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 20ms ON						
LED4:YELLOW						
LED4:YELLOW SET TO OFF	1x65069 2x65069 I:65068	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil sets the LED to OFF						
LED4:YELLOW SET TO ON	1x65070 2x65070 I:65069	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil sets the LED to ON						

LED4:YELLOW INVERT LED STATE	1x65071 2x65071 I:65070	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil inverts the actual LED state						
LED4:YELLOW BLINK	1x65072 2x65072 I:65071	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with last defined time						
LED4:YELLOW FLASH	1x65073 2x65073 I:65072	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with last defined times						
LED4:YELLOW PULSE	1x65074 2x65074 I:65073	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with last defined time						
LED4:YELLOW BLINK 5s	1x65075 2x65075 I:65074	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 5s ON-5s OFF cycle						
LED4:YELLOW BLINK 1s	1x65076 2x65076 I:65075	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 1s ON-1s OFF cycle						
LED4:YELLOW BLINK 250ms	1x65077 2x65077 I:65076	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 250ms ON-250ms OFF cycle						
LED4:YELLOW BLINK 50ms	1x65078 2x65078 I:65077	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 50ms ON-50ms OFF cycle						
LED4:YELLOW FLASH 5s-1s	1x65079 2x65079 I:65078	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 5s ON-1s OFF cycle						
LED4:YELLOW FLASH 1s-250ms	1x65080 2x65080 I:65079	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 1s ON-250ms OFF cycle						
LED4:YELLOW FLASH 500ms-100ms	1x65081 2x65081 I:65080	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 500ms ON-100ms OFF cycle						

LED4:YELLOW FLASH 300ms-50ms	1x65082 2x65082 I:65081	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 300ms ON-50ms OFF cycle						
LED4:YELLOW PULSE 1s	1x65083 2x65083 I:65082	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 1s ON						
LED4:YELLOW PULSE 500ms	1x65084 2x65084 I:65083	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 500ms ON						
LED4:YELLOW PULSE 250ms	1x65085 2x65085 I:65084	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 250ms ON						
LED4:YELLOW PULSE 100ms	1x65086 2x65086 I:65085	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 100ms ON						
LED4:YELLOW PULSE 20ms	1x65087 2x65087 I:65086	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 20ms ON						
DIP SWITCH STATUS						
DIP SWITCH	3x65501 4x65501 I:65500	????			UINT16 R/O	
Returns the actual setting of the Dip switches. Bit 0: DIP Switch 1 (=0:OFF, =1:ON) Bit 1: DIP Switch 2 (=0:OFF, =1:ON) Bit 2: DIP Switch 3 (=0:OFF, =1:ON) Bit 3: DIP Switch 4 (=0:OFF, =1:ON) Bit 4: DIP Switch 5 (=0:OFF, =1:ON) Bit 5: DIP Switch 6 (=0:OFF, =1:ON) Bit 6: DIP Switch 7 (=0:OFF, =1:ON) Bit 7: DIP Switch 8 (=0:OFF, =1:ON)						
LED1:GREEN						
LED1:GREEN STATE	3x65502 4x65502 I:65501	????		1:SET TO ON	UINT16 R/W	NO
		State of LED:????				

Returns the actual state of the LED Writing to this register will set a new state for the LED 0: Switch LED permanent OFF 1: Switch LED permanent ON 2: Invert last state of LED 3: Start symmetrical blinking of LED with TIME1 ON and TIME1 OFF 4: Start asymmetrical flashing of LED with TIME1 ON and TIME2 OFF 5: Start one time pulse of LED with TIME1 ON and inifinite OFF						
LED1:GREEN TIME1	3x65503 4x65503 I:65502	????		1000	UINT16 R/W	YES
		Actual time 1 in ms:0				
Returns the actual time1 for blink,flash and pulse ON time in Milliseconds Writing to this register sets a new time in the range 20-65534ms						
LED1:GREEN TIME2	3x65504 4x65504 I:65503	????		2000	UINT16 R/W	YES
		Actual time 2 in ms:0				
Returns the actual time2 for blink and flash OFF time in Milliseconds Writing to this register sets a new time in the range 20-65534ms						
LED2:WHITE						
LED2:WHITE STATE	3x65505 4x65505 I:65504	????		1:SET TO ON	UINT16 R/W	NO
		State of LED:????				
Returns the actual state of the LED Writing to this register will set a new state for the LED 0: Switch LED permanent OFF 1: Switch LED permanent ON 2: Invert last state of LED 3: Start symmetrical blinking of LED with TIME1 ON and TIME1 OFF 4: Start asymmetrical flashing of LED with TIME1 ON and TIME2 OFF 5: Start one time pulse of LED with TIME1 ON and inifinite OFF						
LED2:WHITE TIME1	3x65506 4x65506 I:65505	????		1000	UINT16 R/W	YES
		Actual time 1 in ms:0				
Returns the actual time1 for blink,flash and pulse ON time in Milliseconds Writing to this register sets a new time in the range 20-65534ms						
LED2:WHITE TIME2	3x65507 4x65507 I:65506	????		2000	UINT16 R/W	YES
		Actual time 2 in ms:0				
Returns the actual time2 for blink and flash OFF time in Milliseconds Writing to this register sets a new time in the range 20-65534ms						
LED3:RED						
LED3:RED STATE	3x65508 4x65508 I:65507	????		1:SET TO ON	UINT16 R/W	NO

		State of LED:????				
Returns the actual state of the LED Writing to this register will set a new state for the LED 0: Switch LED permanent OFF 1: Switch LED permanent ON 2: Invert last state of LED 3: Start symmetrical blinking of LED with TIME1 ON and TIME1 OFF 4: Start asymmetrical flashing of LED with TIME1 ON and TIME2 OFF 5: Start one time pulse of LED with TIME1 ON and inifinite OFF						
LED3:RED TIME1	3x65509 4x65509 I:65508	????		1000	UINT16 R/W	YES
		Actual time 1 in ms:0				
Returns the actual time1 for blink,flash and pulse ON time in Milliseconds Writing to this register sets a new time in the range 20-65534ms						
LED3:RED TIME2	3x65510 4x65510 I:65509	????		2000	UINT16 R/W	YES
		Actual time 2 in ms:0				
Returns the actual time2 for blink and flash OFF time in Milliseconds Writing to this register sets a new time in the range 20-65534ms						
LED4:YELLOW						
LED4:YELLOW STATE	3x65511 4x65511 I:65510	????		1:SET TO ON	UINT16 R/W	NO
		State of LED:????				
Returns the actual state of the LED Writing to this register will set a new state for the LED 0: Switch LED permanent OFF 1: Switch LED permanent ON 2: Invert last state of LED 3: Start symmetrical blinking of LED with TIME1 ON and TIME1 OFF 4: Start asymmetrical flashing of LED with TIME1 ON and TIME2 OFF 5: Start one time pulse of LED with TIME1 ON and inifinite OFF						
LED4:YELLOW TIME1	3x65512 4x65512 I:65511	????		1000	UINT16 R/W	YES
		Actual time 1 in ms:0				
Returns the actual time1 for blink,flash and pulse ON time in Milliseconds Writing to this register sets a new time in the range 20-65534ms						
LED4:YELLOW TIME2	3x65513 4x65513 I:65512	????		2000	UINT16 R/W	YES
		Actual time 2 in ms:0				
Returns the actual time2 for blink and flash OFF time in Milliseconds Writing to this register sets a new time in the range 20-65534ms						

Command NAME	ASCII command type	ASCII command structure	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
ASCII COMMANDS						
DIGITAL INPUTS						
GET DIGITAL INPUTS	ASCII READ COMMAND	#GDIS<CR> Result: #GDIS:<DISDec>,<DISHex><CR>			ASCII	
	TX	#255,GDIS<CR>				
	RX	#255,GDIS:0,0x0<CR>				
		Actual status of digital inputs:0000.0000.0000.0000.0000.0000.0000.0000				
Returns the actual state of all digital inputs as decimal number and as hexadecimal number. DISDec, DISHex The current state of all digital inputs: Bit 0: State of DI1 (=0:OFF, =1:ON) Bit 1: State of DI2 (=0:OFF, =1:ON) Bit 2: State of DI3 (=0:OFF, =1:ON) ... Bit 29: State of DI30 (=0:OFF, =1:ON) Bit 30: State of DI31 (=0:OFF, =1:ON) Bit 31: State of DI32 (=0:OFF, =1:ON)							
GET DIGITAL INPUT DIx	ASCII READ COMMAND	#GDI<DINR><CR> Result: #GDI<DINR>:<DIxDec>,<DIxHex><CR>			ASCII	
	DINR	1				
	TX	#255,GDI1<CR>				
	RX	#255,GDI1:0,0x0<CR>				
		Actual status of digital input DI1:0=OFF				
<DINR>: 1=DI1..32=DI32						
Returns the actual state of the digital input DIx as decimal number and as hexadecimal number. DIxDec, DIxHex: The current state of the digital input x: =0: Digital input is OFF =1: Digital input is ON						
GET ALL CHANGES	ASCII READ COMMAND	#GAC<CR> Result: #GAC:<ChangesDec>,<ChangesHex><CR>			ASCII	
	TX	#255,GAC<CR>				
	RX	#255,GAC:0,0x0<CR>				
		Actual change counter:0				
Returns the counter for changes on all digital inputs. As soon as the module detects a short keypress or long key press or long key release event, this counter is incremented by 1. If this values has changed sience the last polling request, the host knows, that at least one digital input has changed its state.						

CHANGE ALL DIS PART x	ASCII READ COMMAND	#CADISP<PART><CR> Result: #CADISP<PART>:<ChangeDInDec>,...,<ChangeDIn+15Dec>, <ChangeDInHex>,...,<ChangeDIn+15Hex><CR>	ASCII	
	PART	2		
	TX	#255,CADISP2<CR>		
	RX	#255,CADISP2:0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0<CR>		
		Actual counter for changes on DI17:0		
		Actual counter for changes on DI18:0		
		Actual counter for changes on DI19:0		
		Actual counter for changes on DI20:0		
		Actual counter for changes on DI21:0		
		Actual counter for changes on DI22:0		
		Actual counter for changes on DI23:0		
		Actual counter for changes on DI24:0		
		Actual counter for changes on DI25:0		
		Actual counter for changes on DI26:0		
		Actual counter for changes on DI27:0		
		Actual counter for changes on DI28:0		
		Actual counter for changes on DI29:0		
		Actual counter for changes on DI30:0		
		Actual counter for changes on DI31:0		
		Actual counter for changes on DI32:0		
<PART>: 1..2, 1=DI1-DI16, 2=DI17-DI32				
Returns for each digital input the counter for changes. As soon as the module detects a signal change on a digital input, the change counter for the affected digital input is incremented by 1. A signal change can be: Detection of a short keypress Detection of the start of a long keypress Detection of a release of a long keypress The parameter <PART> defines the part of the digital inputs. The command returns maximal 16 digital inputs.				
CHANGE DIx	ASCII READ COMMAND	#CDI<DINR><CR> Result: #CDI<DINR>:<ChangesDec>,<ChangesHex><CR>	ASCII	
	DINR	1		
	TX	#255,CDI1<CR>		
	RX	#255,CDI1:0,0x0<CR>		
		Actual counter for changes on digital input DI1:0		
<DINR>: 1=DI1..32=DI32				
Returns for digital input <DINR> the counter for signal changes. As soon as the module detects a signal change on a digital input, the change counter for the affected digital input is incremented by 1. A signal change can be: Detection of a short keypress Detection of the start of a long keypress Detection of a release of a long keypress				

SHORT KEY ALL DIS PART x	ASCII READ COMMAND	#SKADISP<PART><CR> Result: #SKADISP<PART>:<ShortKeyDInDec>,...,<ShortKeyDIn+15Dec>, <ShortKeyDInHex>,...,<ShortKeyDIn+15Hex><CR>	ASCII	
	PART	2		
	TX	#255,SKADISP2<CR>		
	RX	#255,SKADISP2:0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0<CR>		
		Actual counter for short keypress events on DI17:0		
		Actual counter for short keypress events on DI18:0		
		Actual counter for short keypress events on DI19:0		
		Actual counter for short keypress events on DI20:0		
		Actual counter for short keypress events on DI21:0		
		Actual counter for short keypress events on DI22:0		
		Actual counter for short keypress events on DI23:0		
		Actual counter for short keypress events on DI24:0		
		Actual counter for short keypress events on DI25:0		
		Actual counter for short keypress events on DI26:0		
		Actual counter for short keypress events on DI27:0		
		Actual counter for short keypress events on DI28:0		
		Actual counter for short keypress events on DI29:0		
		Actual counter for short keypress events on DI30:0		
		Actual counter for short keypress events on DI31:0		
		Actual counter for short keypress events on DI32:0		
<PART>: 1..2, 1=DI1-DI16, 2=DI17-DI32				
Returns for each digital input the counter for short keypress events. As soon as the module detects a short keypress on a digital input, the counter for the affected digital input is incremented by 1. The parameter <PART> defines the part of the digital inputs. The command returns maximal 16 digital inputs.				
SHORT KEY DIx	ASCII READ COMMAND	#SKDI<DINR><CR> Result: #SKDI<DINR>:<ShortKeyDec>,<ShortKeyHex><CR>	ASCII	
	DINR	1		
	TX	#255,SKDI1<CR>		
	RX	#255,SKDI1:0,0x0<CR>		
		Actual counter for short keypress events on digital input DI1:0		
<DINR>: 1=DI1..32=DI32				
Returns for digital input <DINR> the counter for short keypress events. As soon as the module detects a short keypress on a digital input, the counter for the affected digital input is incremented by 1.				
LONG KEY START ALL DIS PART x	ASCII READ COMMAND	#LKSADISP<PART><CR> Result: #LKSADISP<PART>:<LongKeyStartDInDec>,...,<LongKeyStartDIn+15Dec>, <LongKeyStartDInHex>,...,<LongKeyStartDIn+15Hex><CR>	ASCII	
	PART	2		
	TX	#255,LKSADISP2<CR>		

	RX	#255,LKSADISP2:0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0<CR>		
		Actual counter for long keypress start events on DI17:0		
		Actual counter for long keypress start events on DI18:0		
		Actual counter for long keypress start events on DI19:0		
		Actual counter for long keypress start events on DI20:0		
		Actual counter for long keypress start events on DI21:0		
		Actual counter for long keypress start events on DI22:0		
		Actual counter for long keypress start events on DI23:0		
		Actual counter for long keypress start events on DI24:0		
		Actual counter for long keypress start events on DI25:0		
		Actual counter for long keypress start events on DI26:0		
		Actual counter for long keypress start events on DI27:0		
		Actual counter for long keypress start events on DI28:0		
		Actual counter for long keypress start events on DI29:0		
		Actual counter for long keypress start events on DI30:0		
		Actual counter for long keypress start events on DI31:0		
		Actual counter for long keypress start events on DI32:0		
<PART>: 1..2, 1=DI1-DI16, 2=DI17-DI32				
Returns for each digital input the counter for long keypress start events. As soon as the module detects the start of a long keypress on a digital input, the counter for the affected digital input is incremented by 1. The parameter <PART> defines the part of the digital inputs. The command returns maximal 16 digital inputs.				
LONG KEY START DIx	ASCII READ COMMAND	#LKSDI<DINR><CR> Result: #LKSDI<DINR>:<LongKeyStartDec>,<LongKeyStartHex><CR>	ASCII	
	DINR	1		
	TX	#255,LKSDI1<CR>		
	RX	#255,LKSDI1:0,0x0<CR>		
		Actual counter for long keypress start events on digital input DI1:0		
<DINR>: 1=DI1..32=DI32				
Returns for digital input <DINR> the counter for long keypress start events. As soon as the module detects the start of a long keypress on a digital input, the counter for the affected digital input is incremented by 1.				
LONG KEY END ALL DIS PART x	ASCII READ COMMAND	#LKEADISP<PART><CR> Result: #LKEADISP<PART>:<LongKeyEndDInDec>,...,<LongKeyEndDIn+15Dec>, <LongKeyEndDInHex>,...,<LongKeyEndDIn+15Hex><CR>	ASCII	
	PART	2		
	TX	#255,LKEADISP2<CR>		
	RX	#255,LKEADISP2:0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0<CR>		
		Actual counter for long keypress end events on DI17:0		
		Actual counter for long keypress end events on DI18:0		
		Actual counter for long keypress end events on DI19:0		
		Actual counter for long keypress end events on DI20:0		

		Actual counter for long keypress end events on DI21:0		
		Actual counter for long keypress end events on DI22:0		
		Actual counter for long keypress end events on DI23:0		
		Actual counter for long keypress end events on DI24:0		
		Actual counter for long keypress end events on DI25:0		
		Actual counter for long keypress end events on DI26:0		
		Actual counter for long keypress end events on DI27:0		
		Actual counter for long keypress end events on DI28:0		
		Actual counter for long keypress end events on DI29:0		
		Actual counter for long keypress end events on DI30:0		
		Actual counter for long keypress end events on DI31:0		
		Actual counter for long keypress end events on DI32:0		
<PART>: 1..2, 1=DI1-DI16, 2=DI17-DI32				
Returns for each digital input the counter for long keypress end events. As soon as the module detects the end of a long keypress on a digital input, the counter for the affected digital input is incremented by 1. The parameter <PART> defines the part of the digital inputs. The command returns maximal 16 digital inputs.				
LONG KEY END DIx	ASCII READ COMMAND	#LKEDI<DINR><CR> Result: #LKEDI<DINR>:<LongKeyEndDec>,<LongKeyEndHex><CR>	ASCII	
	DINR	1		
	TX	#255,LKEDI1<CR>		
	RX	#255,LKEDI1:0,0x0<CR>		
		Actual counter for long keypress end events on digital input DI1:0		
<DINR>: 1=DI1..32=DI32				
Returns for digital input <DINR> the counter for long keypress end events. As soon as the module detects the end of a long keypress on a digital input, the counter for the affected digital input is incremented by 1.				
RISE ALL DIS PART x	ASCII READ COMMAND	#RADISP<PART><CR> Result: #RADISP<PART>:<RiseDInDec>,...,<RiseDIn+15Dec>, <RiseDInHex>,...,<RiseDIn+15Hex><CR>	ASCII	
	PART	2		
	TX	#255,RADISP2<CR>		
	RX	#255,RADISP2:0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0<CR>		
		Actual counter for rising edges on DI17:0		
		Actual counter for rising edges on DI18:0		
		Actual counter for rising edges on DI19:0		
		Actual counter for rising edges on DI20:0		
		Actual counter for rising edges on DI21:0		
		Actual counter for rising edges on DI22:0		
		Actual counter for rising edges on DI23:0		
		Actual counter for rising edges on DI24:0		
		Actual counter for rising edges on DI25:0		
		Actual counter for rising edges on DI26:0		

		Actual counter for rising edges on DI27:0		
		Actual counter for rising edges on DI28:0		
		Actual counter for rising edges on DI29:0		
		Actual counter for rising edges on DI30:0		
		Actual counter for rising edges on DI31:0		
		Actual counter for rising edges on DI32:0		
<PART>: 1..2, 1=DI1-DI16, 2=DI17-DI32				
Returns for each digital input the counter for rising edges. As soon as the module detects a rising edge on a digital input, the rising edge counter for the affected digital input is incremented by 1. The parameter <PART> defines the part of the digital inputs. The command returns maximal 16 digital inputs.				
RISE DIx	ASCII READ COMMAND	#RDI<DINR><CR> Result: #RDI<DINR>:<RiseDec>,<RiseHex><CR>	ASCII	
	DINR	1		
	TX	#255,RDI1<CR>		
	RX	#255,RDI1:0,0x0<CR>		
		Actual counter for rising edges on digital input DI1:0		
<DINR>: 1=DI1..32=DI32				
Returns for digital input <DINR> the counter for rising edges. As soon as the module detects a rising edge on a digital input, the rising edge counter for the affected digital input is incremented by 1.				
FALL ALL DIS PART x	ASCII READ COMMAND	#FADISP<PART><CR> Result: #FADISP<PART>:<FallDInDec>,...,<FallDIn+15Dec>, <FallDInHex>,...,<FallDIn+15Hex><CR>	ASCII	
	PART	2		
	TX	#255,FADISP2<CR>		
	RX	#255,FADISP2:0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0<CR>		
		Actual counter for falling edges on DI17:0		
		Actual counter for falling edges on DI18:0		
		Actual counter for falling edges on DI19:0		
		Actual counter for falling edges on DI20:0		
		Actual counter for falling edges on DI21:0		
		Actual counter for falling edges on DI22:0		
		Actual counter for falling edges on DI23:0		
		Actual counter for falling edges on DI24:0		
		Actual counter for falling edges on DI25:0		
		Actual counter for falling edges on DI26:0		
		Actual counter for falling edges on DI27:0		
		Actual counter for falling edges on DI28:0		
		Actual counter for falling edges on DI29:0		
		Actual counter for falling edges on DI30:0		
		Actual counter for falling edges on DI31:0		
		Actual counter for falling edges on DI32:0		

<PART>: 1..2, 1=DI1-DI16, 2=DI17-DI32				
Returns for each digital input the counter for falling edges. As soon as the module detects a falling edge on a digital input, the falling edge counter for the affected digital input is incremented by 1. The parameter <PART> defines the part of the digital inputs. The command returns maximal 16 digital inputs.				
FALL DIx	ASCII READ COMMAND	#FDI<DINR><CR> Result: #FDI<DINR>:<FallDec>,<FallHex><CR>	ASCII	
	DINR	1		
	TX	#255,FDI1<CR>		
	RX	#255,FDI1:0,0x0<CR>		
		Actual counter for falling edges on digital input DI1:0		
<DINR>: 1=DI1..32=DI32				
Returns for digital input <DINR> the counter for falling edges. As soon as the module detects a falling edge on a digital input, the falling edge counter for the affected digital input is incremented by 1.				
RESET COUNTERS	ASCII WRITE COMMAND	#RC<CR> Result: #OK<CR>	ASCII	NO
	TX	#255,RC<CR>		
	RX	N/A		
Resets all internal counters for digital inputs and events on this digital inputs to 0.				
DIGITAL INPUTS EVENTs				
EVENTS ON	ASCII WRITE COMMAND	#EVTON<CR> Result: #OK<CR>	ASCII	NO
	TX	#255,EVTON<CR>		
	RX	#1,OK<CR>		
Activates event sending of changes on digital inputs Whenever a change is detected on the digital inputs, the IO module sends immediately #<BusAdr>,EVT:DIS:<AllDISasDec>,<AllDISasHex><CR>				
EVENTS OFF	ASCII WRITE COMMAND	#EVTOFF<CR> Result: #OK<CR>	ASCII	NO
	TX	#255,EVTOFF<CR>		
	RX	#1,OK<CR>		
Deactivates event sending of changes on digital inputs Whenever a change is detected on the digital inputs, the IO module sends immediately #<BusAdr>,EVT:DIS:<AllDISasDec>,<AllDISasHex><CR>				

Command NAME	ASCII command type	ASCII command structure	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
ASCII COMMANDS						
DIGITAL OUTPUTS						
UPDATE DIGITAL INPUTS AND OUTPUTS	ASCII WRITE COMMAND	#UDIOS:<OutAllDOS><CR> Result: #UDIOS:<InAllDISDec>,<InAllDISHex><CR>			ASCII	YES
	DO1	0:OFF				
	DO2	0:OFF				
	DO3	0:OFF				
	DO4	0:OFF				
	DO5	0:OFF				
	DO6	0:OFF				
	DO7	0:OFF				
	DO8	0:OFF				
	DO9	0:OFF				
	DO10	0:OFF				
	DO11	0:OFF				
	DO12	0:OFF				
	TX	#255,UDIOS:0<CR>				
	RX	#255,UDIOS:2147483648,0x80000000<CR>				
		Actual status of digital inputs:1000.0000.0000.0000.0000.0000.0000.0000				
Sets all digital outputs to the new state OutAllDOS and gives back the current status of all digital inputs InAllDIS as decimal and hexadecimal value OutAllDOS: The new state for all digital outputs Bit 0: State of DO1 (=0:OFF, =1:ON) Bit 1: State of DO2 (=0:OFF, =1:ON) Bit 2: State of DO3 (=0:OFF, =1:ON) ... Bit 9: State of DO10 (=0:OFF, =1:ON) Bit 10: State of DO11 (=0:OFF, =1:ON) Bit 11: State of DO12 (=0:OFF, =1:ON) InAllDIS: The current state for all digital inputs Bit 0: State of DI1 (=0:OFF, =1:ON) Bit 1: State of DI2 (=0:OFF, =1:ON) Bit 2: State of DI3 (=0:OFF, =1:ON) ... Bit 29: State of DI30 (=0:OFF, =1:ON) Bit 30: State of DI31 (=0:OFF, =1:ON) Bit 31: State of DI32 (=0:OFF, =1:ON)						
SET DIGITAL OUTPUTS	ASCII WRITE COMMAND	#SDOS:<OutAllDOS><CR> Result: #OK<CR>			ASCII	YES
	DO1	0:OFF				

	DO2	0:OFF		
	DO3	0:OFF		
	DO4	0:OFF		
	DO5	0:OFF		
	DO6	0:OFF		
	DO7	0:OFF		
	DO8	0:OFF		
	DO9	0:OFF		
	DO10	0:OFF		
	DO11	0:OFF		
	DO12	0:OFF		
	TX	#255,SDOS:0<CR>		
	RX	#255,OK<CR>		

Sets all digital outputs to the new state OutAllDOS
The new state for all digital outputs
Bit 0: State of DO1 (=0:OFF, =1:ON)
Bit 1: State of DO2 (=0:OFF, =1:ON)
Bit 2: State of DO3 (=0:OFF, =1:ON)
...
Bit 9: State of DO10 (=0:OFF, =1:ON)
Bit 10: State of DO11 (=0:OFF, =1:ON)
Bit 11: State of DO12 (=0:OFF, =1:ON)

SET DIGITAL OUTPUT DOx	ASCII WRITE COMMAND	#SDO<DONR>:<Out><CR> Result: #OK<CR>	ASCII	NO
	DONR	2		
	DOx	0:OFF		
	TX	#255,SDO2:0<CR>		
	RX	N/A		

<DONR>: 1=DO1..12=DO12

Sets the new state for digital output DOx. The state is defined with <Out>.
Out
The new state of the digital output DOx:
=0: digital output is OFF
=1: digital output is ON

GET DIGITAL OUTPUTS	ASCII READ COMMAND	#GDOS<CR> Result: #GDOS:<DOSDec>,<DOSHex><CR>	ASCII	
	TX	#255,GDOS<CR>		
	RX	#255,GDOS:0,0x0<CR>		
		Actual status of digital outputs:0000.0000.0000		

Returns the actual state of the digital outputs as decimal number and as hexadecimal number.
DOSDec, DOSHex
The current state of the digital outputs:
Bit 0: State of DO1 (=0:OFF, =1:ON)
Bit 1: State of DO2 (=0:OFF, =1:ON)
Bit 2: State of DO3 (=0:OFF, =1:ON)
...
Bit 9: State of DO10 (=0:OFF, =1:ON)
Bit 10: State of DO11 (=0:OFF, =1:ON)
Bit 11: State of DO12 (=0:OFF, =1:ON)

GET DIGITAL OUTPUT DOx	ASCII READ COMMAND	#GDO<DONR><CR> Result: #GDO<DONR>:<DOxDec>,<DOxHex><CR>	ASCII	
	DONR	2		
	TX	#255,GDO2<CR>		
	RX	#255,GDO2:0,0x0<CR>		
		Actual status of digital output DO2:0=OFF		

Returns the actual state of the digital output DOx as decimal number and as hexadecimal number.
DOxDec, DOxHex
The current state of the digital output DOx:
=0: relay output is OFF
=1: relay output is ON

DIGITAL OUTPUTS: PULSE OUTPUT

PULSE DOx	ASCII WRITE COMMAND	#PDO<DONR>:<Time><CR> Result: #OK<CR>	ASCII	YES
	DONR	2		
	TIME	200		
	TX	#255,PDO2:200<CR>		
	RX	#255,OK<CR>		

<DONR>: 1=DO1..12=DO12

<Time>: 0..65535*100ms

This command switches the digital output DOx on for the pulse duration <PulseTimeIn100ms>*100ms.
PulseTimeIn100ms: A duration in 100ms units.
The corresponding digital output is switched on for this time period.

GET PULSE TIMER DOx	ASCII READ COMMAND	#GPT<DONR><CR> Result: #GPT:<TimeDec>,<TimeHex><CR>	ASCII	
	DONR	2		
	TX	#255,GPT2<CR>		
	RX	#255,GPT2:19812,0x4D64<CR>		
		Actual pulse time for DO2:19,8s		

<DONR>: 1=DO1..12=DO12

Returns the remaining timer value of the pulse for digital output DOx in ms.
PulseTimeInMSDec, PulseTimeInMSHex
The remaining time of the pulse in Milliseconds

Command NAME	ASCII command type	ASCII command structure	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
ASCII COMMANDS						
DIGITAL OUTPUTS						
INITIAL & WATCHDOG STATE FOR DIGITAL OUTPUTS						
SET INITIAL & WATCHDOG STATE FOR DIGITAL OUTPUTS	ASCII WRITE COMMAND	#SCDOS:<OutAllDOS><CR> Result: #OK<CR>			ASCII	YES
	DO1	0:OFF				
	DO2	0:OFF				
	DO3	0:OFF				
	DO4	0:OFF				
	DO5	0:OFF				
	DO6	0:OFF				
	DO7	0:OFF				
	DO8	0:OFF				
	DO9	0:OFF				
	DO10	0:OFF				
	DO11	0:OFF				
	DO12	0:OFF				
	TX	#255,SCDOS:0<CR>				
	RX	#255,OK<CR>				
This command sets all digital outputs to a new state for controller restart and watchdog function. The state is saved in FRAM. This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured. OutAllDOS The new state for the digital outputs: Bit 0: New state of DO1 (=0:OFF, =1:ON) Bit 1: New state of DO2 (=0:OFF, =1:ON) ... Bit 10: New state of DO11 (=0:OFF, =1:ON) Bit 11: New state of DO12 (=0:OFF, =1:ON)						
GET INITIAL & WATCHDOG STATE FOR DIGITAL OUTPUTS	ASCII READ COMMAND	#GCDOS<CR> Result: #GCDOS:<DOSDec>,<DOSHex><CR>			ASCII	
	TX	#255,GCDOS<CR>				
	RX	#255,GCDOS:0,0x0<CR>				
		Init & watchdog configuration for digital outputs:				
		DO1-DO12:0000.0000.0000				

Returns the actual initial and watchdog state of the digital outputs as decimal number and as hexadecimal number. This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured

DOSDec, DOSHex
The current state of the digital outputs:
Bit 0: State of DO1 (=0:OFF, =1:ON)
Bit 1: State of DO2 (=0:OFF, =1:ON)
...
Bit 10: State of DO11 (=0:OFF, =1:ON)
Bit 11: State of DO12 (=0:OFF, =1:ON)

Register NAME Command NAME	MODBUS Register ASCII Command	Register VALUE ASCII Command	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
STATUS DIGITAL INPUTS						
DI1	1x00001 2x00001 I:0	0,0x00 B:00			BIT R/O	
		Actual state of DI1:0=OFF				
Current state of the digital input DIx =0:DI is OFF, =1:DI is ON						
DI2	1x00002 2x00002 I:1	0,0x00 B:00			BIT R/O	
		Actual state of DI2:0=OFF				
DI3	1x00003 2x00003 I:2	0,0x00 B:00			BIT R/O	
		Actual state of DI3:0=OFF				
DI4	1x00004 2x00004 I:3	0,0x00 B:00			BIT R/O	
		Actual state of DI4:0=OFF				
DI5	1x00005 2x00005 I:4	0,0x00 B:00			BIT R/O	
		Actual state of DI5:0=OFF				
DI6	1x00006 2x00006 I:5	0,0x00 B:00			BIT R/O	
		Actual state of DI6:0=OFF				
DI7	1x00007 2x00007 I:6	0,0x00 B:00			BIT R/O	
		Actual state of DI7:0=OFF				
DI8	1x00008 2x00008 I:7	0,0x00 B:00			BIT R/O	
		Actual state of DI8:0=OFF				
DI9	1x00009 2x00009 I:8	0,0x00 B:00			BIT R/O	
		Actual state of DI9:0=OFF				

DI10	1x00010 2x00010 I:9	0,0x00 B:00			BIT R/O	
		Actual state of DI10:0=OFF				
DI11	1x00011 2x00011 I:10	0,0x00 B:00			BIT R/O	
		Actual state of DI11:0=OFF				
DI12	1x00012 2x00012 I:11	0,0x00 B:00			BIT R/O	
		Actual state of DI12:0=OFF				
DI13	1x00013 2x00013 I:12	0,0x00 B:00			BIT R/O	
		Actual state of DI13:0=OFF				
DI14	1x00014 2x00014 I:13	0,0x00 B:00			BIT R/O	
		Actual state of DI14:0=OFF				
DI15	1x00015 2x00015 I:14	0,0x00 B:00			BIT R/O	
		Actual state of DI15:0=OFF				
DI16	1x00016 2x00016 I:15	0,0x00 B:00			BIT R/O	
		Actual state of DI16:0=OFF				
DI17	1x00017 2x00017 I:16	0,0x00 B:00			BIT R/O	
		Actual state of DI17:0=OFF				
DI18	1x00018 2x00018 I:17	0,0x00 B:00			BIT R/O	
		Actual state of DI18:0=OFF				
DI19	1x00019 2x00019 I:18	0,0x00 B:00			BIT R/O	
		Actual state of DI19:0=OFF				
DI20	1x00020 2x00020 I:19	0,0x00 B:00			BIT R/O	
		Actual state of DI20:0=OFF				

DI21	1x00021 2x00021 I:20	0,0x00 B:00			BIT R/O	
		Actual state of DI21:0=OFF				
DI22	1x00022 2x00022 I:21	0,0x00 B:00			BIT R/O	
		Actual state of DI22:0=OFF				
DI23	1x00023 2x00023 I:22	0,0x00 B:00			BIT R/O	
		Actual state of DI23:0=OFF				
DI24	1x00024 2x00024 I:23	0,0x00 B:00			BIT R/O	
		Actual state of DI24:0=OFF				
DI25	1x00025 2x00025 I:24	0,0x00 B:00			BIT R/O	
		Actual state of DI25:0=OFF				
DI26	1x00026 2x00026 I:25	0,0x00 B:00			BIT R/O	
		Actual state of DI26:0=OFF				
DI27	1x00027 2x00027 I:26	0,0x00 B:00			BIT R/O	
		Actual state of DI27:0=OFF				
DI28	1x00028 2x00028 I:27	0,0x00 B:00			BIT R/O	
		Actual state of DI28:0=OFF				
DI29	1x00029 2x00029 I:28	0,0x00 B:00			BIT R/O	
		Actual state of DI29:0=OFF				
DI30	1x00030 2x00030 I:29	0,0x00 B:00			BIT R/O	
		Actual state of DI30:0=OFF				
DI31	1x00031 2x00031 I:30	0,0x00 B:00			BIT R/O	
		Actual state of DI31:0=OFF				

DI32	1x00032 2x00032 I:31	1,0x01 B:01			BIT R/O	
		Actual state of DI32:1=ON				
STATUS DIGITAL OUTPUTS						
DO1	1x00033 2x00033 I:32	0,0x00 B:00		1	BIT R/W	NO
		Actual state of DO1:0=OFF		ENTER NEW STATE (0 or 1)		
Current state of the digital output DOx =0:DO is OFF, =1:DO is ON Writing on this register changes the state of the digital output						
DO2	1x00034 2x00034 I:33	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO2:0=OFF		ENTER NEW STATE (0 or 1)		
DO3	1x00035 2x00035 I:34	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO3:0=OFF		ENTER NEW STATE (0 or 1)		
DO4	1x00036 2x00036 I:35	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO4:0=OFF		ENTER NEW STATE (0 or 1)		
DO5	1x00037 2x00037 I:36	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO5:0=OFF		ENTER NEW STATE (0 or 1)		
DO6	1x00038 2x00038 I:37	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO6:0=OFF		ENTER NEW STATE (0 or 1)		
DO7	1x00039 2x00039 I:38	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO7:0=OFF		ENTER NEW STATE (0 or 1)		
DO8	1x00040 2x00040 I:39	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO8:0=OFF		ENTER NEW STATE (0 or 1)		
DO9	1x00041 2x00041 I:40	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO9:0=OFF		ENTER NEW STATE (0 or 1)		

DO10	1x00042 2x00042 I:41	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO10:0=OFF		ENTER NEW STATE (0 or 1)		
DO11	1x00043 2x00043 I:42	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO11:0=OFF		ENTER NEW STATE (0 or 1)		
DO12	1x00044 2x00044 I:43	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO12:0=OFF		ENTER NEW STATE (0 or 1)		
DIGITAL INPUTS: RESET						
RESET COUNTERS	1x10000 2x10000 I:9999	0,0x00 B:00		1:PERFORM RESET	BIT R/W	NO
If this register is written to 1, all internal edge counters and event counters are set to 0. 0 is always returned when reading.						
STATUS REAL DIGITAL INPUTS						
DI1	1x15001 2x14001 I:15000	0,0x00 B:00			BIT R/O	
		Actual state of DI1:0=OFF				
Current state of the digital input DIx with the internal software filter to suppress glitches or spike on thie line =0:DI is OFF, =1:DI is ON						
DI2	1x15002 2x15002 I:15001	0,0x00 B:00			BIT R/O	
		Actual state of DI2:0=OFF				
DI3	1x15003 2x15003 I:15002	0,0x00 B:00			BIT R/O	
		Actual state of DI3:0=OFF				
DI4	1x15004 2x15004 I:15003	0,0x00 B:00			BIT R/O	
		Actual state of DI4:0=OFF				
DI5	1x15005 2x15005 I:15004	0,0x00 B:00			BIT R/O	
		Actual state of DI5:0=OFF				
DI6	1x15006 2x15006 I:15005	0,0x00 B:00			BIT R/O	
		Actual state of DI6:0=OFF				

DI7	1x15007 2x15007 I:15006	0,0x00 B:00			BIT R/O	
		Actual state of DI7:0=OFF				
DI8	1x15008 2x15008 I:15007	0,0x00 B:00			BIT R/O	
		Actual state of DI8:0=OFF				
DI9	1x15009 2x15009 I:15008	0,0x00 B:00			BIT R/O	
		Actual state of DI9:0=OFF				
DI10	1x15010 2x15010 I:15009	0,0x00 B:00			BIT R/O	
		Actual state of DI10:0=OFF				
DI11	1x15011 2x15011 I:15010	0,0x00 B:00			BIT R/O	
		Actual state of DI11:0=OFF				
DI12	1x15012 2x15012 I:15011	0,0x00 B:00			BIT R/O	
		Actual state of DI12:0=OFF				
DI13	1x15013 2x15013 I:15012	0,0x00 B:00			BIT R/O	
		Actual state of DI13:0=OFF				
DI14	1x15014 2x15014 I:15013	0,0x00 B:00			BIT R/O	
		Actual state of DI14:0=OFF				
DI15	1x15015 2x15015 I:15014	0,0x00 B:00			BIT R/O	
		Actual state of DI15:0=OFF				
DI16	1x15016 2x15016 I:15015	0,0x00 B:00			BIT R/O	
		Actual state of DI16:0=OFF				
DI17	1x15017 2x15017 I:15016	0,0x00 B:00			BIT R/O	
		Actual state of DI17:0=OFF				

DI18	1x15018 2x15018 I:15017	0,0x00 B:00			BIT R/O	
		Actual state of DI18:0=OFF				
DI19	1x15019 2x15019 I:15018	0,0x00 B:00			BIT R/O	
		Actual state of DI19:0=OFF				
DI20	1x15020 2x15020 I:15019	0,0x00 B:00			BIT R/O	
		Actual state of DI20:0=OFF				
DI21	1x15021 2x15021 I:15020	0,0x00 B:00			BIT R/O	
		Actual state of DI21:0=OFF				
DI22	1x15022 2x15022 I:15021	0,0x00 B:00			BIT R/O	
		Actual state of DI22:0=OFF				
D23	1x15023 2x15023 I:15022	0,0x00 B:00			BIT R/O	
		Actual state of D23:0=OFF				
DI24	1x15024 2x15024 I:15023	0,0x00 B:00			BIT R/O	
		Actual state of DI24:0=OFF				
DI25	1x15025 2x15025 I:15024	0,0x00 B:00			BIT R/O	
		Actual state of DI25:0=OFF				
DI26	1x15026 2x15026 I:15025	0,0x00 B:00			BIT R/O	
		Actual state of DI26:0=OFF				
DI27	1x15027 2x15027 I:15026	0,0x00 B:00			BIT R/O	
		Actual state of DI27:0=OFF				
DI28	1x15028 2x15028 I:15027	0,0x00 B:00			BIT R/O	
		Actual state of DI28:0=OFF				

DI29	1x15029 2x15029 I:15028	0,0x00 B:00			BIT R/O	
		Actual state of DI29:0=OFF				
DI30	1x15030 2x15030 I:15029	0,0x00 B:00			BIT R/O	
		Actual state of DI30:0=OFF				
DI31	1x15031 2x15031 I:15030	0,0x00 B:00			BIT R/O	
		Actual state of DI31:0=OFF				
DI32	1x15032 2x15032 I:15031	1,0x01 B:01			BIT R/O	
		Actual state of DI32:1=ON				
STATUS DIGITAL INPUTS						
UNFILTERED DI1	1x15033 2x15033 I:15032	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI1:0=OFF				
Current state of the real digital input DIx without the internal software filter to suppress glitches or spike on thie line =0:DI is OFF, =1:DI is ON						
UNFILTERED DI2	1x15034 2x15034 I:15033	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI2:0=OFF				
UNFILTERED DI3	1x15035 2x15035 I:15034	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI3:0=OFF				
UNFILTERED DI4	1x15036 2x15036 I:15035	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI4:0=OFF				
UNFILTERED DI5	1x15037 2x15037 I:15036	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI5:0=OFF				
UNFILTERED DI6	1x15038 2x15038 I:15037	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI6:0=OFF				
UNFILTERED DI7	1x15039 2x15039 I:15038	0,0x00 B:00			BIT R/O	

		Actual state of UNFILTERED DI7:0=OFF				
UNFILTERED DI8	1x15040 2x15040 I:15039	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI8:0=OFF				
UNFILTERED DI9	1x15041 2x15041 I:15040	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI9:0=OFF				
UNFILTERED DI10	1x15042 2x15042 I:15041	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI10:0=OFF				
UNFILTERED DI11	1x15043 2x15043 I:15042	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI11:0=OFF				
UNFILTERED DI12	1x15044 2x15044 I:15043	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI12:0=OFF				
UNFILTERED DI13	1x15045 2x15045 I:15044	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI13:0=OFF				
UNFILTERED DI14	1x15046 2x15046 I:15045	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI14:0=OFF				
UNFILTERED DI15	1x15047 2x15047 I:15046	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI15:0=OFF				
UNFILTERED DI16	1x15048 2x15048 I:15047	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI16:0=OFF				
UNFILTERED DI17	1x15049 2x15049 I:15048	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI17:0=OFF				
UNFILTERED DI18	1x15050 2x15050 I:15049	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI18:0=OFF				

UNFILTERED DI19	1x15051 2x15051 I:15050	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI19:0=OFF				
UNFILTERED DI20	1x15052 2x15052 I:15051	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI20:0=OFF				
UNFILTERED DI21	1x15053 2x15053 I:15052	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI21:0=OFF				
UNFILTERED DI22	1x15054 2x15054 I:15053	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI22:0=OFF				
UNFILTERED DI23	1x15055 2x15055 I:15054	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI23:0=OFF				
UNFILTERED DI24	1x15056 2x15056 I:15055	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI24:0=OFF				
UNFILTERED DI25	1x15057 2x15057 I:15056	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI25:0=OFF				
UNFILTERED DI26	1x15058 2x15058 I:15057	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI26:0=OFF				
UNFILTERED DI27	1x15059 2x15059 I:15058	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI27:0=OFF				
UNFILTERED DI28	1x15060 2x15060 I:15059	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI28:0=OFF				
UNFILTERED DI29	1x15061 2x15061 I:15060	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI29:0=OFF				

UNFILTERED DI30	1x15062 2x15062 I:15061	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI30:0=OFF				
UNFILTERED DI31	1x15063 2x15063 I:15062	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI31:0=OFF				
UNFILTERED DI32	1x15064 2x15064 I:15063	1,0x01 B:01			BIT R/O	
		Actual state of UNFILTERED DI32:1=ON				
STATUS DIGITAL OUTPUTS						
DO1	1x16001 2x16001 I:16000	0,0x00 B:00		1	BIT R/W	NO
		Actual state of DO1:0=OFF		ENTER NEW STATE (0 or 1)		
Current state of the digital output DOx =0:DO is OFF, =1:DO is ON Writing on this register changes the state of the digital output						
DO2	1x16002 2x16002 I:16001	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO2:0=OFF		ENTER NEW STATE (0 or 1)		
DO3	1x16003 2x16003 I:16002	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO3:0=OFF		ENTER NEW STATE (0 or 1)		
DO4	1x16004 2x16004 I:16003	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO4:0=OFF		ENTER NEW STATE (0 or 1)		
DO5	1x16005 2x16005 I:16004	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO5:0=OFF		ENTER NEW STATE (0 or 1)		
DO6	1x16006 2x16006 I:16005	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO6:0=OFF		ENTER NEW STATE (0 or 1)		
DO7	1x16007 2x16007 I:16006	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO7:0=OFF		ENTER NEW STATE (0 or 1)		

DO8	1x16008 2x16008 I:16007	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO8:0=OFF		ENTER NEW STATE (0 or 1)		
DO9	1x16009 2x16009 I:16008	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO9:0=OFF		ENTER NEW STATE (0 or 1)		
DO10	1x16010 2x16010 I:16009	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO10:0=OFF		ENTER NEW STATE (0 or 1)		
DO11	1x16011 2x16011 I:16010	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO11:0=OFF		ENTER NEW STATE (0 or 1)		
DO12	1x16012 2x16012 I:16011	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO12:0=OFF		ENTER NEW STATE (0 or 1)		

Register NAME Command NAME	MODBUS Register ASCII Command	Register VALUE ASCII Command	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
DIGITAL INPUTS: DIGITAL INPUT HAS CHANGED IT'S STATE						
DI HAS CHANGED DI1	1x20001 2x20001 I:20000	0,0x00 B:00			BIT R/O	
If the digital input has changed this bit inverts its last state						
DI HAS CHANGED DI2	1x20002 2x20002 I:20001	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI3	1x20003 2x20003 I:20002	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI4	1x20004 2x20004 I:20003	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI5	1x20005 2x20005 I:20004	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI6	1x20006 2x20006 I:20005	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI7	1x20007 2x20007 I:20006	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI8	1x20008 2x20008 I:20007	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI9	1x20009 2x20009 I:20008	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI10	1x20010 2x20010 I:20009	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI11	1x20011 2x20011 I:20010	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI12	1x20012 2x20012 I:20011	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI13	1x20013 2x20013 I:20012	0,0x00 B:00			BIT R/O	

DI HAS CHANGED DI14	1x20014 2x20014 I:20013	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI15	1x20015 2x20015 I:20014	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI16	1x20016 2x20016 I:20015	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI17	1x20017 2x20017 I:20016	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI18	1x20018 2x20018 I:20017	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI19	1x20019 2x20019 I:20018	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI20	1x20020 2x20020 I:20019	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI21	1x20021 2x20021 I:20020	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI22	1x20022 2x20022 I:20021	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI23	1x20023 2x20023 I:20022	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI24	1x20024 2x20024 I:20023	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI25	1x20025 2x20025 I:20024	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI26	1x20026 2x20026 I:20025	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI27	1x20027 2x20027 I:20026	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI28	1x20028 2x20028 I:20027	0,0x00 B:00			BIT R/O	

DI HAS CHANGED DI29	1x20029 2x20029 I:20028	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI30	1x20030 2x20030 I:20029	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI31	1x20031 2x20031 I:20030	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI32	1x20032 2x20032 I:20031	1,0x01 B:01			BIT R/O	
DIGITAL INPUTS: SHORT KEYPRESS EVENT ON DIGITAL INPUT DETECTED						
SHORT KEYPRESS ON DI1	1x20033 2x20033 I:20032	0,0x00 B:00			BIT R/O	
If a short keypress event was detected on the digital input this bit inverts its last state						
SHORT KEYPRESS ON DI2	1x20034 2x20034 I:20033	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI3	1x20035 2x20035 I:20034	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI4	1x20036 2x20036 I:20035	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI5	1x20037 2x20037 I:20036	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI6	1x20038 2x20038 I:20037	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI7	1x20039 2x20039 I:20038	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI8	1x20040 2x20040 I:20039	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI9	1x20041 2x20041 I:20040	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI10	1x20042 2x20042 I:20041	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI11	1x20043 2x20043 I:20042	0,0x00 B:00			BIT R/O	

SHORT KEYPRESS ON DI12	1x20044 2x20044 I:20043	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI13	1x20045 2x20045 I:20044	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI14	1x20046 2x20046 I:20045	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI15	1x20047 2x20047 I:20046	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI16	1x20048 2x20048 I:20047	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI17	1x20049 2x20049 I:20048	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI18	1x20050 2x20050 I:20049	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI19	1x20051 2x20051 I:20050	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI20	1x20052 2x20052 I:20051	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI21	1x20053 2x20053 I:20052	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI22	1x20054 2x20054 I:20053	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI23	1x20055 2x20055 I:20054	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI24	1x20056 2x20056 I:20055	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI25	1x20057 2x20057 I:20056	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI26	1x20058 2x20058 I:20057	0,0x00 B:00			BIT R/O	

SHORT KEYPRESS ON DI27	1x20059 2x20059 I:20058	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI28	1x20060 2x20060 I:20059	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI29	1x20061 2x20061 I:20060	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI30	1x20062 2x20062 I:20061	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI31	1x20063 2x20063 I:20062	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI32	1x20064 2x20064 I:20063	0,0x00 B:00			BIT R/O	
DIGITAL INPUTS: LONG KEYPRESS START EVENT ON DIGITAL INPUT DETECTED						
LONG KEYPRESS START ON DI1	1x20065 2x20065 I:20064	0,0x00 B:00			BIT R/O	
If a long keypress start event was detected on the digital input this bit inverts its last state						
LONG KEYPRESS START ON DI2	1x20066 2x20066 I:20065	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI3	1x20067 2x20067 I:20066	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI4	1x20068 2x20068 I:20067	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI5	1x20069 2x20069 I:20068	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI6	1x20070 2x20070 I:20069	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI7	1x20071 2x20071 I:20070	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI8	1x20072 2x20072 I:20071	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI9	1x20073 2x20073 I:20072	0,0x00 B:00			BIT R/O	

LONG KEYPRESS START ON DI10	1x20074 2x20074 I:20073	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI11	1x20075 2x20075 I:20074	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI12	1x20076 2x20076 I:20075	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI13	1x20077 2x20077 I:20076	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI14	1x20078 2x20078 I:20077	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI15	1x20079 2x20079 I:20078	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI16	1x20080 2x20080 I:20079	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI17	1x20081 2x20081 I:20080	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI18	1x20082 2x20082 I:20081	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI19	1x20083 2x20083 I:20082	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI20	1x20084 2x20084 I:20083	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI21	1x20085 2x20085 I:20084	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI22	1x20086 2x20086 I:20085	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI23	1x20087 2x20087 I:20086	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI24	1x20088 2x20088 I:20087	0,0x00 B:00			BIT R/O	

LONG KEYPRESS START ON DI25	1x20089 2x20089 I:20088	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI26	1x20090 2x20090 I:20089	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI27	1x20091 2x20091 I:20090	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI28	1x20092 2x20092 I:20091	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI29	1x20093 2x20093 I:20092	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI30	1x20094 2x20094 I:20093	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI31	1x20095 2x20095 I:20094	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI32	1x20096 2x20096 I:20095	1,0x01 B:01			BIT R/O	
DIGITAL INPUTS: LONG KEYPRESS END EVENT ON DIGITAL INPUT DETECTED						
LONG KEYPRESS END ON DI1	1x20097 2x20097 I:20096	0,0x00 B:00			BIT R/O	
If a long keypress end event was detected on the digital input this bit inverts its last state						
LONG KEYPRESS END ON DI2	1x20098 2x20098 I:20097	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI3	1x20099 2x20099 I:20098	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI4	1x20100 2x20100 I:20099	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI5	1x20101 2x20101 I:20100	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI6	1x20102 2x20102 I:20101	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI7	1x20103 2x20103 I:20102	0,0x00 B:00			BIT R/O	

LONG KEYPRESS END ON DI8	1x20104 2x20104 I:20103	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI9	1x20105 2x20105 I:20104	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI10	1x20106 2x20106 I:20105	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI11	1x20107 2x20107 I:20106	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI12	1x20108 2x20108 I:20107	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI13	1x20109 2x20109 I:20108	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI14	1x20110 2x20110 I:20109	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI15	1x20111 2x20111 I:20110	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI16	1x20112 2x20112 I:20111	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI17	1x20113 2x20113 I:20112	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI18	1x20114 2x20114 I:20113	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI19	1x20115 2x20115 I:20114	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI20	1x20116 2x20116 I:20115	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI21	1x20117 2x20117 I:20116	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI22	1x20118 2x20118 I:20117	0,0x00 B:00			BIT R/O	

LONG KEYPRESS END ON DI23	1x20119 2x20119 I:20118	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI24	1x20120 2x20120 I:20119	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI25	1x20121 2x20121 I:20120	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI26	1x20122 2x20122 I:20121	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI27	1x20123 2x20123 I:20122	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI28	1x20124 2x20124 I:20123	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI29	1x20125 2x20125 I:20124	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI30	1x20126 2x20126 I:20125	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI31	1x20127 2x20127 I:20126	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI32	1x20128 2x20128 I:20127	0,0x00 B:00			BIT R/O	
DIGITAL INPUTS: RISING EDGE ON DIGITAL INPUT DETECTED						
RISING EDGE ON DI1	1x20129 2x20129 I:20128	0,0x00 B:00			BIT R/O	
If a rising edge was detected on the digital input this bit inverts its last state						
RISING EDGE ON DI2	1x20130 2x20130 I:20129	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI3	1x20131 2x20131 I:20130	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI4	1x20132 2x20132 I:20131	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI5	1x20133 2x20133 I:20132	0,0x00 B:00			BIT R/O	

RISING EDGE ON DI6	1x20134 2x20134 I:20133	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI7	1x20135 2x20135 I:20134	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI8	1x20136 2x20136 I:20135	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI9	1x20137 2x20137 I:20136	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI10	1x20138 2x20138 I:20137	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI11	1x20139 2x20139 I:20138	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI12	1x20140 2x20140 I:20139	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI13	1x20141 2x20141 I:20140	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI14	1x20142 2x20142 I:20141	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI15	1x20143 2x20143 I:20142	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI16	1x20144 2x20144 I:20143	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI17	1x20145 2x20145 I:20144	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI18	1x20146 2x20146 I:20145	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI19	1x20147 2x20147 I:20146	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI20	1x20148 2x20148 I:20147	0,0x00 B:00			BIT R/O	

RISING EDGE ON DI21	1x20149 2x20149 I:20148	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI22	1x20150 2x20150 I:20149	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI23	1x20151 2x20151 I:20150	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI24	1x20152 2x20152 I:20151	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI25	1x20153 2x20153 I:20152	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI26	1x20154 2x20154 I:20153	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI27	1x20155 2x20155 I:20154	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI28	1x20156 2x20156 I:20155	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI29	1x20157 2x20157 I:20156	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI30	1x20158 2x20158 I:20157	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI31	1x20159 2x20159 I:20158	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI32	1x20160 2x20160 I:20159	1,0x01 B:01			BIT R/O	
DIGITAL INPUTS: FALLING EDGE ON DIGITAL INPUT DETECTED						
FALLING EDGE ON DI1	1x20161 2x20161 I:20160	0,0x00 B:00			BIT R/O	
If a falling edge was detected on the digital input this bit inverts its last state						
FALLING EDGE ON DI2	1x20162 2x20162 I:20161	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI3	1x20163 2x20163 I:20162	0,0x00 B:00			BIT R/O	

FALLING EDGE ON DI4	1x20164 2x20164 I:20163	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI5	1x20165 2x20165 I:20164	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI6	1x20166 2x20166 I:20165	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI7	1x20167 2x20167 I:20166	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI8	1x20168 2x20168 I:20167	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI9	1x20169 2x20169 I:20168	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI10	1x20170 2x20170 I:20169	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI11	1x20171 2x20171 I:20170	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI12	1x20172 2x20172 I:20171	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI13	1x20173 2x20173 I:20172	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI14	1x20174 2x20174 I:20173	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI15	1x20175 2x20175 I:20174	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI16	1x20176 2x20176 I:20175	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI17	1x20177 2x20177 I:20176	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI18	1x20178 2x20178 I:20177	0,0x00 B:00			BIT R/O	

FALLING EDGE ON DI19	1x20179 2x20179 I:20178	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI20	1x20180 2x20180 I:20179	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI21	1x20181 2x20181 I:20180	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI22	1x20182 2x20182 I:20181	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI23	1x20183 2x20183 I:20182	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI24	1x20184 2x20184 I:20183	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI25	1x20185 2x20185 I:20184	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI26	1x20186 2x20186 I:20185	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI27	1x20187 2x20187 I:20186	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI28	1x20188 2x20188 I:20187	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI29	1x20189 2x20189 I:20188	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI30	1x20190 2x20190 I:20189	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI31	1x20191 2x20191 I:20190	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI32	1x20192 2x20192 I:20191	0,0x00 B:00			BIT R/O	

Register NAME Command NAME	MODBUS Register ASCII Command	Register VALUE ASCII Command	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
STATUS DIGITAL INPUTS						
DI1	3x00001 4x00001 I:0	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI1:0=OFF				
Current state of the digital input DIx =0:DI is OFF, =1:DI is ON						
DI2	3x00002 4x00002 I:1	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI2:0=OFF				
DI3	3x00003 4x00003 I:2	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI3:0=OFF				
DI4	3x00004 4x00004 I:3	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI4:0=OFF				
DI5	3x00005 4x00005 I:4	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI5:0=OFF				
DI6	3x00006 4x00006 I:5	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI6:0=OFF				
DI7	3x00007 4x00007 I:6	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI7:0=OFF				
DI8	3x00008 4x00008 I:7	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI8:0=OFF				
DI9	3x00009 4x00009 I:8	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI9:0=OFF				

DI10	3x00010 4x00010 I:9	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI10:0=OFF				
DI11	3x00011 4x00011 I:10	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI11:0=OFF				
DI12	3x00012 4x00012 I:11	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI12:0=OFF				
DI13	3x00013 4x00013 I:12	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI13:0=OFF				
DI14	3x00014 4x00014 I:13	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI14:0=OFF				
DI15	3x00015 4x00015 I:14	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI15:0=OFF				
DI16	3x00016 4x00016 I:15	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI16:0=OFF				
DI17	3x00017 4x00017 I:16	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI17:0=OFF				
DI18	3x00018 4x00018 I:17	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI18:0=OFF				
DI19	3x00019 4x00019 I:18	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI19:0=OFF				
DI20	3x00020 4x00020 I:19	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI20:0=OFF				

DI21	3x00021 4x00021 I:20	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI21:0=OFF				
DI22	3x00022 4x00022 I:21	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI22:0=OFF				
DI23	3x00023 4x00023 I:22	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI23:0=OFF				
DI24	3x00024 4x00024 I:23	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI24:0=OFF				
DI25	3x00025 4x00025 I:24	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI25:0=OFF				
DI26	3x00026 4x00026 I:25	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI26:0=OFF				
DI27	3x00027 4x00027 I:26	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI27:0=OFF				
DI28	3x00028 4x00028 I:27	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI28:0=OFF				
DI29	3x00029 4x00029 I:28	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI29:0=OFF				
DI30	3x00030 4x00030 I:29	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI30:0=OFF				
DI31	3x00031 4x00031 I:30	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI31:0=OFF				

DI32	3x00032 4x00032 I:31	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI32:1=ON				
STATUS DIGITAL OUTPUTS						
DO1	3x00033 4x00033 I:32	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual state of DO1:0=OFF		ENTER NEW STATE (0 or 1)		
Current state of the digital output DOx =0:DO is OFF, =1:DO is ON Writing on this register changes the state of the digital output						
DO2	3x00034 4x00034 I:33	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO2:0=OFF		ENTER NEW STATE (0 or 1)		
DO3	3x00035 4x00035 I:34	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO3:0=OFF		ENTER NEW STATE (0 or 1)		
DO4	3x00036 4x00036 I:35	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO4:0=OFF		ENTER NEW STATE (0 or 1)		
DO5	3x00037 4x00037 I:36	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO5:0=OFF		ENTER NEW STATE (0 or 1)		
DO6	3x00038 4x00038 I:37	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO6:0=OFF		ENTER NEW STATE (0 or 1)		
DO7	3x00039 4x00039 I:38	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO7:0=OFF		ENTER NEW STATE (0 or 1)		
DO8	3x00040 4x00040 I:39	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO8:0=OFF		ENTER NEW STATE (0 or 1)		
DO9	3x00041 4x00041 I:40	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO9:0=OFF		ENTER NEW STATE (0 or 1)		

DO10	3x00042 4x00042 I:41	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO10:0=OFF		ENTER NEW STATE (0 or 1)		
DO11	3x00043 4x00043 I:42	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO11:0=OFF		ENTER NEW STATE (0 or 1)		
DO12	3x00044 4x00044 I:43	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO12:0=OFF		ENTER NEW STATE (0 or 1)		
DIGITAL INPUTS: RESET						
RESET COUNTERS	3x10000 4x10000 I:9999	0,0x0000 B:00 00		1:PERFORM RESET	UINT16 R/W	NO
If this register is written to 1, all internal edge counters and event counters are set to 0. 0 is always returned when reading.						
HAS DIS CHANGED	3x10001 4x10001 I:10000	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
As soon as the module registrates an event on one of the available digital inputs, this global event counter is incremented by 1. Possible events are: Detection of a short keypress Detection of the start of a long keypress Detection of the end of a long keypress						
STATUS OF ALL DIS DI1..DI16	3x10002 4x10002 I:10001	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI1:0=OFF				
		Actual state of DI2:0=OFF				
		Actual state of DI3:0=OFF				
		Actual state of DI4:0=OFF				
		Actual state of DI5:0=OFF				
		Actual state of DI6:0=OFF				
		Actual state of DI7:0=OFF				
		Actual state of DI8:0=OFF				
		Actual state of DI9:0=OFF				
		Actual state of DI10:0=OFF				
		Actual state of DI11:0=OFF				
		Actual state of DI12:0=OFF				
		Actual state of DI13:0=OFF				
		Actual state of DI14:0=OFF				
		Actual state of DI15:0=OFF				
		Actual state of DI16:0=OFF				

Actual state of all digital inputs DI1..DI12 Bit 0: =0:DI1 is OFF, =1:DI1 is ON Bit 1: =0:DI2 is OFF, =1:DI2 is ON ... Bit 14: =0:DI15 is OFF, =1:DI15 is ON Bit 15: =0:DI16 is OFF, =1:DI16 is ON						
STATUS OF ALL DIS DI17..DI32	3x10003 4x10003 I:10002	32768,0x8000 B:80 00			UINT16 R/O	
		Actual state of DI17:0=OFF				
		Actual state of DI18:0=OFF				
		Actual state of DI19:0=OFF				
		Actual state of DI20:0=OFF				
		Actual state of DI21:0=OFF				
		Actual state of DI22:0=OFF				
		Actual state of DI23:0=OFF				
		Actual state of DI24:0=OFF				
		Actual state of DI25:0=OFF				
		Actual state of DI26:0=OFF				
		Actual state of DI27:0=OFF				
		Actual state of DI28:0=OFF				
		Actual state of DI29:0=OFF				
		Actual state of DI30:0=OFF				
		Actual state of DI31:0=OFF				
		Actual state of DI32:1=ON				
Actual state of all digital inputs DI1..DI12 Bit 0: =0:DI17 is OFF, =1:DI17 is ON Bit 1: =0:DI18 is OFF, =1:DI18 is ON ... Bit 14: =0:DI31 is OFF, =1:DI31 is ON Bit 15: =0:DI32 is OFF, =1:DI32 is ON						
STATUS OF DIGITAL OUTPUTS						
STATUS OF ALL DOS DO1-DO12	3x10004 4x10004 I:10003	0,0x0000 B:00 00		0x0FFF	UINT16 R/W	NO
		Actual state of DO1:0=OFF		1		
		Actual state of DO2:0=OFF		1		
		Actual state of DO3:0=OFF		1		
		Actual state of DO4:0=OFF		1		
		Actual state of DO5:0=OFF		1		
		Actual state of DO6:0=OFF		1		
		Actual state of DO7:0=OFF		1		
		Actual state of DO8:0=OFF		1		
		Actual state of DO9:0=OFF		1		
		Actual state of DO10:0=OFF		1		
		Actual state of DO11:0=OFF		1		

		Actual state of DO12:0=OFF		1		
Actual state of all digital outputs Bit 0: =0:DO1 is OFF, =1:DO1 is ON Bit 1: =0:DO2 is OFF, =1:DO2 is ON ... Bit 10: =0:DO11 is OFF, =1:DO11 is ON Bit 11: =0:DO12 is OFF, =1:DO12 is ON Write on this register sets all digital outputs to a new state						
STATUS OF DIGITAL OUTPUTS						
REAL STATUS OF ALL DOS DO1-DO12	3x10501 4x10501 I:10500	0,0x0000 B:00 00			UINT16 R/O	
		Real state of DO1:0=OFF				
		Real state of DO2:0=OFF				
		Real state of DO3:0=OFF				
		Real state of DO4:0=OFF				
		Real state of DO5:0=OFF				
		Real state of DO6:0=OFF				
		Real state of DO7:0=OFF				
		Real state of DO8:0=OFF				
		Real state of DO9:0=OFF				
		Real state of DO10:0=OFF				
		Real state of DO11:0=OFF				
		Real state of DO12:0=OFF				
Actual state of all digital outputs in the DO chips Bit 0: =0:DO1 is OFF, =1:DO1 is ON Bit 1: =0:DO2 is OFF, =1:DO2 is ON ... Bit 10: =0:DO11 is OFF, =1:DO11 is ON Bit 11: =0:DO12 is OFF, =1:DO12 is ON Write on this register sets all digital outputs to a new state						
STATUS REAL DIGITAL INPUTS						
DI1	1x15001 2x15001 I:15000	0,0x0000 B:00			UINT16 R/O	
		Actual state of DI1:0=OFF				
Current state of the digital input DIx with the internal software filter to suppress glitches or spike on thie line =0:DI is OFF, =1:DI is ON						
DI2	1x15002 2x15002 I:15001	1,0x0001 B:00			UINT16 R/O	
		Actual state of DI2:1=ON				
DI3	1x15003 2x15003 I:15002	2,0x0002 B:00			UINT16 R/O	

		Actual state of DI3:2=ON				
DI4	1x15004 2x15004 I:15003	3,0x0003 B:00			UINT16 R/O	
		Actual state of DI4:3=ON				
DI5	1x15005 2x15005 I:15004	4,0x0004 B:00			UINT16 R/O	
		Actual state of DI5:4=ON				
DI6	1x15006 2x15006 I:15005	5,0x0005 B:00			UINT16 R/O	
		Actual state of DI6:5=ON				
DI7	1x15007 2x15007 I:15006	6,0x0006 B:00			UINT16 R/O	
		Actual state of DI7:6=ON				
DI8	1x15008 2x15008 I:15007	7,0x0007 B:00			UINT16 R/O	
		Actual state of DI8:7=ON				
DI9	1x15009 2x15009 I:15008	0,0x0000 B:00			UINT16 R/O	
		Actual state of DI9:0=OFF				
DI10	1x15010 2x15010 I:15009	1,0x0001 B:00			UINT16 R/O	
		Actual state of DI10:1=ON				
DI11	1x15011 2x15011 I:15010	2,0x0002 B:00			UINT16 R/O	
		Actual state of DI11:2=ON				
DI12	1x15012 2x15012 I:15011	3,0x0003 B:00			UINT16 R/O	
		Actual state of DI12:3=ON				
DI13	1x15013 2x15013 I:15012	4,0x0004 B:00			UINT16 R/O	
		Actual state of DI13:4=ON				
DI14	1x15014 2x15014 I:15013	5,0x0005 B:00			UINT16 R/O	
		Actual state of DI14:5=ON				

DI15	1x15015 2x15015 I:15014	6,0x0006 B:00			UINT16 R/O	
		Actual state of DI15:6=ON				
DI16	1x15016 2x15016 I:15015	7,0x0007 B:00			UINT16 R/O	
		Actual state of DI16:7=ON				
DI17	1x15017 2x15017 I:15016	0,0x0000 B:00			UINT16 R/O	
		Actual state of DI17:0=OFF				
DI18	1x15018 2x15018 I:15017	1,0x0001 B:00			UINT16 R/O	
		Actual state of DI18:1=ON				
DI19	1x15019 2x15019 I:15018	2,0x0002 B:00			UINT16 R/O	
		Actual state of DI19:2=ON				
DI20	1x15020 2x15020 I:15019	0,0x0000 B:00			UINT16 R/O	
		Actual state of DI20:0=OFF				
DI21	1x15021 2x15021 I:15020	1,0x0001 B:00			UINT16 R/O	
		Actual state of DI21:1=ON				
DI22	1x15022 2x15022 I:15021	2,0x0002 B:00			UINT16 R/O	
		Actual state of DI22:2=ON				
D23	1x15023 2x15023 I:15022	3,0x0003 B:00			UINT16 R/O	
		Actual state of D23:3=ON				
DI24	1x15024 2x15024 I:15023	4,0x0004 B:00			UINT16 R/O	
		Actual state of DI24:4=ON				
DI25	1x15025 2x15025 I:15024	5,0x0005 B:00			UINT16 R/O	
		Actual state of DI25:5=ON				

DI26	1x15026 2x15026 I:15025	6,0x0006 B:00			UINT16 R/O	
		Actual state of DI26:6=ON				
DI27	1x15027 2x15027 I:15026	7,0x0007 B:00			UINT16 R/O	
		Actual state of DI27:7=ON				
DI28	1x15028 2x15028 I:15027	0,0x0000 B:00			UINT16 R/O	
		Actual state of DI28:0=OFF				
DI29	1x15029 2x15029 I:15028	1,0x0001 B:00			UINT16 R/O	
		Actual state of DI29:1=ON				
DI30	1x15030 2x15030 I:15029	2,0x0002 B:00			UINT16 R/O	
		Actual state of DI30:2=ON				
DI31	1x15031 2x15031 I:15030	3,0x0003 B:00			UINT16 R/O	
		Actual state of DI31:3=ON				
DI32	1x15032 2x15032 I:15031	260,0x0104 B:01			UINT16 R/O	
		Actual state of DI32:260=ON				
STATUS DIGITAL INPUTS						
UNFILTERED DI1	1x15033 2x15033 I:15032	5,0x0005 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI1:5=ON				
Current state of the real digital input DIx without the internal software filter to suppress glitches or spike on thie line =0:DI is OFF, =1:DI is ON						
UNFILTERED DI2	1x15034 2x15034 I:15033	6,0x0006 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI2:6=ON				
UNFILTERED DI3	1x15035 2x15035 I:15034	7,0x0007 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI3:7=ON				
UNFILTERED DI4	1x15036 2x15036 I:15035	0,0x0000 B:00			UINT16 R/O	

		Actual state of UNFILTERED DI4:0=OFF				
UNFILTERED DI5	1x15037 2x15037 I:15036	1,0x0001 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI5:1=ON				
UNFILTERED DI6	1x15038 2x15038 I:15037	2,0x0002 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI6:2=ON				
UNFILTERED DI7	1x15039 2x15039 I:15038	0,0x0000 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI7:0=OFF				
UNFILTERED DI8	1x15040 2x15040 I:15039	1,0x0001 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI8:1=ON				
UNFILTERED DI9	1x15041 2x15041 I:15040	2,0x0002 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI9:2=ON				
UNFILTERED DI10	1x15042 2x15042 I:15041	3,0x0003 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI10:3=ON				
UNFILTERED DI11	1x15043 2x15043 I:15042	4,0x0004 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI11:4=ON				
UNFILTERED DI12	1x15044 2x15044 I:15043	5,0x0005 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI12:5=ON				
UNFILTERED DI13	1x15045 2x15045 I:15044	6,0x0006 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI13:6=ON				
UNFILTERED DI14	1x15046 2x15046 I:15045	7,0x0007 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI14:7=ON				
UNFILTERED DI15	1x15047 2x15047 I:15046	0,0x0000 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI15:0=OFF				

UNFILTERED DI16	1x15048 2x15048 I:15047	1,0x0001 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI16:1=ON				
UNFILTERED DI17	1x15049 2x15049 I:15048	2,0x0002 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI17:2=ON				
UNFILTERED DI18	1x15050 2x15050 I:15049	3,0x0003 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI18:3=ON				
UNFILTERED DI19	1x15051 2x15051 I:15050	4,0x0004 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI19:4=ON				
UNFILTERED DI20	1x15052 2x15052 I:15051	5,0x0005 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI20:5=ON				
UNFILTERED DI21	1x15053 2x15053 I:15052	6,0x0006 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI21:6=ON				
UNFILTERED DI22	1x15054 2x15054 I:15053	7,0x0007 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI22:7=ON				
UNFILTERED DI23	1x15055 2x15055 I:15054	0,0x0000 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI23:0=OFF				
UNFILTERED DI24	1x15056 2x15056 I:15055	1,0x0001 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI24:1=ON				
UNFILTERED DI25	1x15057 2x15057 I:15056	2,0x0002 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI25:2=ON				
UNFILTERED DI26	1x15058 2x15058 I:15057	0,0x0000 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI26:0=OFF				

UNFILTERED DI27	1x15059 2x15059 I:15058	1,0x0001 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI27:1=ON				
UNFILTERED DI28	1x15060 2x15060 I:15059	2,0x0002 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI28:2=ON				
UNFILTERED DI29	1x15061 2x15061 I:15060	3,0x0003 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI29:3=ON				
UNFILTERED DI30	1x15062 2x15062 I:15061	4,0x0004 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI30:4=ON				
UNFILTERED DI31	1x15063 2x15063 I:15062	5,0x0005 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI31:5=ON				
UNFILTERED DI32	1x15064 2x15064 I:15063	262,0x0106 B:01			UINT16 R/O	
		Actual state of UNFILTERED DI32:262=ON				
STATUS DIGITAL OUTPUTS						
DO1	3x16001 4x16001 I:16000	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual state of DO1:0=OFF		ENTER NEW STATE (0 or 1)		
Current state of the digital output DOx =0:DO is OFF, =1:DO is ON Writing on this register changes the state of the digital output						
DO2	3x16002 4x16002 I:16001	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO2:0=OFF		ENTER NEW STATE (0 or 1)		
DO3	3x16003 4x16003 I:16002	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO3:0=OFF		ENTER NEW STATE (0 or 1)		
DO4	3x16004 4x16004 I:16003	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO4:0=OFF		ENTER NEW STATE (0 or 1)		

DO5	3x16005 4x16005 I:16004	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO5:0=OFF		ENTER NEW STATE (0 or 1)		
DO6	3x16006 4x16006 I:16005	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO6:0=OFF		ENTER NEW STATE (0 or 1)		
DO7	3x16007 4x16007 I:16006	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO7:0=OFF		ENTER NEW STATE (0 or 1)		
DO8	3x16008 4x16008 I:16007	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO8:0=OFF		ENTER NEW STATE (0 or 1)		
DO9	3x16009 4x16009 I:16008	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO9:0=OFF		ENTER NEW STATE (0 or 1)		
DO10	3x16010 4x16010 I:16009	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO10:0=OFF		ENTER NEW STATE (0 or 1)		
DO11	3x16011 4x16011 I:16010	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO11:0=OFF		ENTER NEW STATE (0 or 1)		
DO12	3x16012 4x16012 I:16011	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO12:0=OFF		ENTER NEW STATE (0 or 1)		

Register NAME Command NAME	MODBUS Register ASCII Command	Register VALUE ASCII Command	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
PULSE TIME FOR DIGITAL OUTPUTS						
PULSE TIME DO1	3x20001 4x20001 I:20000	0,0x0000 B:00 00	200	20,0	UINT16 R/W	YES
Generate a pulse on digital output x in 100ms units (0,1 to 6553,5 Seconds selectable) If you write onto this register, the digital output will be switched on for the desired time in 100ms units.						
PULSE TIME DO2	3x20002 4x20002 I:20001	0,0x0000 B:00 00	300	30,0	UINT16 R/W	NO
PULSE TIME DO3	3x20003 4x20003 I:20002	0,0x0000 B:00 00	400	40,0	UINT16 R/W	NO
PULSE TIME DO4	3x20004 4x20004 I:20003	0,0x0000 B:00 00	500	50,0	UINT16 R/W	NO
PULSE TIME DO5	3x20005 4x20005 I:20004	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO6	3x20006 4x20006 I:20005	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO7	3x20007 4x20007 I:20006	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO8	3x20008 4x20008 I:20007	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO9	3x20009 4x20009 I:20008	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO10	3x20010 4x20010 I:20009	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO11	3x20011 4x20011 I:20010	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO12	3x20012 4x20012 I:20011	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE STATUS FOR DIGITAL OUTPUTS						

PULSE TIMER DO1	3x21001 4x21001 I:21000	19503,0x00004C2F B:00 00 4C 2F			UINT32 R/O	
		19,5 seconds				
Remaining time of the pulse on digital output x in Milliseconds.						
PULSE TIMER DO2	3x21003 4x21003 I:21002	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO3	3x21005 4x21005 I:21004	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO4	3x21007 4x21007 I:21006	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO5	3x21009 4x21009 I:21008	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO6	3x21011 4x21011 I:21010	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO7	3x21013 4x21013 I:21012	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO8	3x21015 4x21015 I:21014	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO9	3x21017 4x21017 I:21016	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO10	3x21019 4x21019 I:21018	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO11	3x21021 4x21021 I:21020	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				

PULSE TIMER DO12	3x21023 4x21023 I:21022	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE STATUS FOR DIGITAL OUTPUTS						
PULSE TIMER DO1	3x21025 4x21025 I:21024	19003,0x00004A3B B:4A 3B 00 00			UINT32R R/O	
		19,0 seconds				
Remaining time of the pulse on digital output x in Milliseconds.						
PULSE TIMER DO2	3x21027 4x21027 I:21026	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO3	3x21029 4x21029 I:21028	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO4	3x21031 4x21031 I:21030	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO5	3x21033 4x21033 I:21032	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO6	3x21035 4x21035 I:21034	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO7	3x21037 4x21037 I:21036	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO8	3x21039 4x21039 I:21038	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO9	3x21041 4x21041 I:21040	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO10	3x21043 4x21043 I:21042	0,0x00000000 B:00 00 00 00			UINT32R R/O	

		0,0 seconds				
PULSE TIMER DO11	3x21045 4x21045 I:21044	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO12	3x21047 4x21047 I:21046	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				

Register NAME Command NAME	MODBUS Register ASCII Command	Register VALUE ASCII Command	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
DIGITAL INPUTS						
STATUS DI1 A	3x05001 4x05001 I:5000	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
Status for the digital input DIx Bit 0-4: Lower 5 bits of CHANGE COUNTER Bit 5-9: Lower 5 bits of RISING EDGE COUNTER Bit 10-14: Lower 5 bits of FALLING EDGE COUNTER Bit 15: Current Status of DIx =0: DIx si OFF, =1: DIx is ON						
STATUS DI1 B	3x05002 4x05002 I:5001	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
Status for the digital input DIx Bit 0-4: Lower 5 bits of SHORT KEYPRESS EVENTS Bit 5-9: Lower 5 bits of LONG KEYPRESS START EVENTS Bit 10-14: Lower 5 bits of LONG KEYPRESS END EVENTS Bit 15: Current Status of DIx =0: DIx si OFF, =1: DIx is ON						
STATUS DI2 A	3x05003 4x05003 I:5002	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI2 B	3x05004 4x05004 I:5003	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI3 A	3x05005 4x05005 I:5004	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI3 B	3x05006 4x05006 I:5005	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI4 A	3x05007 4x05007 I:5006	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI4 B	3x05008 4x05008 I:5007	0,0x0000 B:00 00			UINT16 R/O	

		DI:0,SKE:0,LKSE:0,LKEE:0			
STATUS DI5 A	3x05009 4x05009 I:5008	0,0x0000 B:00 00		UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0			
STATUS DI5 B	3x05010 4x05010 I:5009	0,0x0000 B:00 00		UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0			
STATUS DI6 A	3x05011 4x05011 I:5010	0,0x0000 B:00 00		UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0			
STATUS DI6 B	3x05012 4x05012 I:5011	0,0x0000 B:00 00		UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0			
STATUS DI7 A	3x05013 4x05013 I:5012	0,0x0000 B:00 00		UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0			
STATUS DI7 B	3x05014 4x05014 I:5013	0,0x0000 B:00 00		UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0			
STATUS DI8 A	3x05015 4x05015 I:5014	0,0x0000 B:00 00		UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0			
STATUS DI8 B	3x05016 4x05016 I:5015	0,0x0000 B:00 00		UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0			
STATUS DI9 A	3x05017 4x05017 I:5016	0,0x0000 B:00 00		UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0			
STATUS DI9 B	3x05018 4x05018 I:5017	0,0x0000 B:00 00		UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0			
STATUS DI10 A	3x05019 4x05019 I:5018	0,0x0000 B:00 00		UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0			

STATUS DI10 B	3x05020 4x05020 I:5019	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI11 A	3x05021 4x05021 I:5020	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI11 B	3x05022 4x05022 I:5021	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI12 A	3x05023 4x05023 I:5022	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI12 B	3x05024 4x05024 I:5023	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI13 A	3x05025 4x05025 I:5024	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI13 B	3x05026 4x05026 I:5025	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI14 A	3x05027 4x05027 I:5026	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI14 B	3x05028 4x05028 I:5027	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI15 A	3x05029 4x05029 I:5028	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI15 B	3x05030 4x05030 I:5029	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				

STATUS DI16 A	3x05031 4x05031 I:5030	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI16 B	3x05032 4x05032 I:5031	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI17 A	3x05033 4x05033 I:5032	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI17 B	3x05034 4x05034 I:5033	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI18 A	3x05035 4x05035 I:5034	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI18 B	3x05036 4x05036 I:5035	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI19 A	3x05037 4x05037 I:5036	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI19 B	3x05038 4x05038 I:5037	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI20 A	3x05039 4x05039 I:5038	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI20 B	3x05040 4x05040 I:5039	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI21 A	3x05041 4x05041 I:5040	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				

STATUS DI21 B	3x05042 4x05042 I:5041	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI22 A	3x05043 4x05043 I:5042	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI22 B	3x05044 4x05044 I:5043	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI23 A	3x05045 4x05045 I:5044	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI23 B	3x05046 4x05046 I:5045	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI24 A	3x05047 4x05047 I:5046	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI24 B	3x05048 4x05048 I:5047	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI25 A	3x05049 4x05049 I:5048	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI25 B	3x05050 4x05050 I:5049	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI26 A	3x05051 4x05051 I:5050	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI26 B	3x05052 4x05052 I:5051	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				

STATUS DI27 A	3x05053 4x05053 I:5052	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI27 B	3x05054 4x05054 I:5053	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI28 A	3x05055 4x05055 I:5054	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI28 B	3x05056 4x05056 I:5055	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI29 A	3x05057 4x05057 I:5056	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI29 B	3x05058 4x05058 I:5057	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI30 A	3x05059 4x05059 I:5058	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI30 B	3x05060 4x05060 I:5059	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI31 A	3x05061 4x05061 I:5060	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI31 B	3x05062 4x05062 I:5061	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI32 A	3x05063 4x05063 I:5062	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				

STATUS DI32 B	3x05064 4x05064 I:5063	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS						
FILTER PATTERN DI1	3x05065 4x05065 I:5064	0,0x00000000 B:00 00 00 00			UINT32 R/O	
The internal pattern for corresponding digital input for AC/DC filtering. The internal used state is created out of this internal pattern via oversampling.						
FILTER PATTERN DI2	3x05067 4x05067 I:5066	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI3	3x05069 4x05069 I:5068	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI4	3x05071 4x05071 I:5070	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI5	3x05073 4x05073 I:5072	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI6	3x05075 4x05075 I:5074	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI7	3x05077 4x05077 I:5076	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI8	3x05079 4x05079 I:5078	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI9	3x05081 4x05081 I:5080	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI10	3x05083 4x05083 I:5082	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI11	3x05085 4x05085 I:5084	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI12	3x05087 4x05087 I:5086	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI13	3x05089 4x05089 I:5088	0,0x00000000 B:00 00 00 00			UINT32 R/O	

FILTER PATTERN DI14	3x05091 4x05091 I:5090	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI15	3x05093 4x05093 I:5092	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI16	3x05095 4x05095 I:5094	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI17	3x05097 4x05097 I:5096	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI18	3x05099 4x05099 I:5098	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI19	3x05101 4x05101 I:5100	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI20	3x05103 4x05103 I:5102	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI21	3x05105 4x05105 I:5104	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI22	3x05107 4x05107 I:5106	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI23	3x05109 4x05109 I:5108	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI24	3x05111 4x05111 I:5110	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI25	3x05113 4x05113 I:5112	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI26	3x05115 4x05115 I:5114	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI27	3x05117 4x05117 I:5116	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI28	3x05119 4x05119 I:5118	0,0x00000000 B:00 00 00 00			UINT32 R/O	

FILTER PATTERN DI29	3x05121 4x05121 I:5120	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI30	3x05123 4x05123 I:5122	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI31	3x05125 4x05125 I:5124	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI32	3x05127 4x05127 I:5126	0,0x00000000 B:00 00 00 00			UINT32 R/O	
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI1						
RISE DI1	3x07001 4x07001 I:7000	3,0x0003 B:00 03			UINT16 R/O	
		3 event(s)				
Counter for rising edges on the digital input DIx. If the module detects a rising edge on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.						
FALL DI1	3x07002 4x07002 I:7001	3,0x0003 B:00 03			UINT16 R/O	
		3 event(s)				
Counter for falling edges on the digital input DIx. If the module detects a falling edge on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.						
CHANGE DI1	3x07003 4x07003 I:7002	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
Counter for events on the digital input DIx. If the module detects an event on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0. The following events are available: Detecion of a short keypress Detection of the start of a long keypress Detection of the end of a long keypress						
SHORT KEYPRESS DI1	3x07004 4x07004 I:7003	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
Counter for short keypress events on the digital input DIx. If the module detects a short keypress on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.						
LONG KEYPRESS START DI1	3x07005 4x07005 I:7004	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				

Counter for start events of long keypress actions on the digital input DIx. If the module detects the start of a long keypress action on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.						
LONG KEYPRESS END DI1	3x07006 4x07006 I:7005	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
Counter for end events of long keypress actions on the digital input DIx. If the module detects the end of a long keypress action on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.						
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI2						
RISE DI2	3x07011 4x07011 I:7010	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI2	3x07012 4x07012 I:7011	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI2	3x07013 4x07013 I:7012	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI2	3x07014 4x07014 I:7013	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI2	3x07015 4x07015 I:7014	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI2	3x07016 4x07016 I:7015	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI3						
RISE DI3	3x07021 4x07021 I:7020	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI3	3x07022 4x07022 I:7021	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI3	3x07023 4x07023 I:7022	6,0x0006 B:00 06			UINT16 R/O	

		6 event(s)				
SHORT KEYPRESS DI3	3x07024 4x07024 I:7023	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI3	3x07025 4x07025 I:7024	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI3	3x07026 4x07026 I:7025	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI4						
RISE DI4	3x07031 4x07031 I:7030	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI4	3x07032 4x07032 I:7031	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI4	3x07033 4x07033 I:7032	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI4	3x07034 4x07034 I:7033	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI4	3x07035 4x07035 I:7034	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI4	3x07036 4x07036 I:7035	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI5						
RISE DI5	3x07041 4x07041 I:7040	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI5	3x07042 4x07042 I:7041	4,0x0004 B:00 04			UINT16 R/O	

		4 event(s)				
CHANGE DI5	3x07043 4x07043 I:7042	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI5	3x07044 4x07044 I:7043	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI5	3x07045 4x07045 I:7044	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI5	3x07046 4x07046 I:7045	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI6						
RISE DI6	3x07051 4x07051 I:7050	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI6	3x07052 4x07052 I:7051	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI6	3x07053 4x07053 I:7052	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI6	3x07054 4x07054 I:7053	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI6	3x07055 4x07055 I:7054	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI6	3x07056 4x07056 I:7055	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI7						
RISE DI7	3x07061 4x07061 I:7060	4,0x0004 B:00 04			UINT16 R/O	

		4 event(s)				
FALL DI7	3x07062 4x07062 I:7061	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI7	3x07063 4x07063 I:7062	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI7	3x07064 4x07064 I:7063	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI7	3x07065 4x07065 I:7064	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI7	3x07066 4x07066 I:7065	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI8						
RISE DI8	3x07071 4x07071 I:7070	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI8	3x07072 4x07072 I:7071	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI8	3x07073 4x07073 I:7072	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI8	3x07074 4x07074 I:7073	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI8	3x07075 4x07075 I:7074	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI8	3x07076 4x07076 I:7075	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				

DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI9						
RISE DI9	3x07081 4x07081 I:7080	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI9	3x07082 4x07082 I:7081	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI9	3x07083 4x07083 I:7082	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI9	3x07084 4x07084 I:7083	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI9	3x07085 4x07085 I:7084	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI9	3x07086 4x07086 I:7085	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI10						
RISE DI10	3x07091 4x07091 I:7090	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI10	3x07092 4x07092 I:7091	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI10	3x07093 4x07093 I:7092	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI10	3x07094 4x07094 I:7093	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI10	3x07095 4x07095 I:7094	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				

LONG KEYPRESS END DI10	3x07096 4x07096 I:7095	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI11						
RISE DI11	3x07101 4x07101 I:7100	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI11	3x07102 4x07102 I:7101	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI11	3x07103 4x07103 I:7102	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI11	3x07104 4x07104 I:7103	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI11	3x07105 4x07105 I:7104	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI11	3x07106 4x07106 I:7105	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI12						
RISE DI12	3x07111 4x07111 I:7110	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI12	3x07112 4x07112 I:7111	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI12	3x07113 4x07113 I:7112	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI12	3x07114 4x07114 I:7113	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				

LONG KEYPRESS START DI12	3x07115 4x07115 I:7114	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI12	3x07116 4x07116 I:7115	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI13						
RISE DI13	3x07121 4x07121 I:7120	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI13	3x07122 4x07122 I:7121	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI13	3x07123 4x07123 I:7122	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI13	3x07124 4x07124 I:7123	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI13	3x07125 4x07125 I:7124	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI13	3x07126 4x07126 I:7125	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI14						
RISE DI14	3x07131 4x07131 I:7130	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI14	3x07132 4x07132 I:7131	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI14	3x07133 4x07133 I:7132	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				

SHORT KEYPRESS DI14	3x07134 4x07134 I:7133	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI14	3x07135 4x07135 I:7134	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI14	3x07136 4x07136 I:7135	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI15						
RISE DI15	3x07141 4x07141 I:7140	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI15	3x07142 4x07142 I:7141	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI15	3x07143 4x07143 I:7142	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI15	3x07144 4x07144 I:7143	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI15	3x07145 4x07145 I:7144	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI15	3x07146 4x07146 I:7145	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI16						
RISE DI16	3x07151 4x07151 I:7150	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI16	3x07152 4x07152 I:7151	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				

CHANGE DI16	3x07153 4x07153 I:7152	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI16	3x07154 4x07154 I:7153	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI16	3x07155 4x07155 I:7154	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI16	3x07156 4x07156 I:7155	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI17						
RISE DI17	3x07161 4x07161 I:7160	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI17	3x07162 4x07162 I:7161	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI17	3x07163 4x07163 I:7162	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI17	3x07164 4x07164 I:7163	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI17	3x07165 4x07165 I:7164	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI17	3x07166 4x07166 I:7165	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI18						
RISE DI18	3x07171 4x07171 I:7170	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				

FALL DI18	3x07172 4x07172 I:7171	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI18	3x07173 4x07173 I:7172	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI18	3x07174 4x07174 I:7173	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI18	3x07175 4x07175 I:7174	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI18	3x07176 4x07176 I:7175	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI19						
RISE DI19	3x07181 4x07181 I:7180	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI19	3x07182 4x07182 I:7181	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI19	3x07183 4x07183 I:7182	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI19	3x07184 4x07184 I:7183	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI19	3x07185 4x07185 I:7184	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI19	3x07186 4x07186 I:7185	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI20						

RISE DI20	3x07191 4x07191 I:7190	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI20	3x07192 4x07192 I:7191	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI20	3x07193 4x07193 I:7192	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI20	3x07194 4x07194 I:7193	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI20	3x07195 4x07195 I:7194	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI20	3x07196 4x07196 I:7195	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI21						
RISE DI21	3x07201 4x07201 I:7200	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI21	3x07202 4x07202 I:7201	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI21	3x07203 4x07203 I:7202	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI21	3x07204 4x07204 I:7203	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI21	3x07205 4x07205 I:7204	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				

LONG KEYPRESS END DI21	3x07206 4x07206 I:7205	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI22						
RISE DI22	3x07211 4x07211 I:7210	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI22	3x07212 4x07212 I:7211	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI22	3x07213 4x07213 I:7212	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI22	3x07214 4x07214 I:7213	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI22	3x07215 4x07215 I:7214	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI22	3x07216 4x07216 I:7215	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI23						
RISE DI23	3x07221 4x07221 I:7220	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI23	3x07222 4x07222 I:7221	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI23	3x07223 4x07223 I:7222	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI23	3x07224 4x07224 I:7223	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				

LONG KEYPRESS START DI23	3x07225 4x07225 I:7224	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI23	3x07226 4x07226 I:7225	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI24						
RISE DI24	3x07231 4x07231 I:7230	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI24	3x07232 4x07232 I:7231	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI24	3x07233 4x07233 I:7232	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI24	3x07234 4x07234 I:7233	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI24	3x07235 4x07235 I:7234	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI24	3x07236 4x07236 I:7235	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI25						
RISE DI25	3x07241 4x07241 I:7240	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI25	3x07242 4x07242 I:7241	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI25	3x07243 4x07243 I:7242	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				

SHORT KEYPRESS DI25	3x07244 4x07244 I:7243	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI25	3x07245 4x07245 I:7244	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI25	3x07246 4x07246 I:7245	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI26						
RISE DI26	3x07251 4x07251 I:7250	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI26	3x07252 4x07252 I:7251	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI26	3x07253 4x07253 I:7252	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI26	3x07254 4x07254 I:7253	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI26	3x07255 4x07255 I:7254	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI26	3x07256 4x07256 I:7255	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI27						
RISE DI27	3x07261 4x07261 I:7260	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI27	3x07262 4x07262 I:7261	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				

CHANGE DI27	3x07263 4x07263 I:7262	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI27	3x07264 4x07264 I:7263	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI27	3x07265 4x07265 I:7264	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI27	3x07266 4x07266 I:7265	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI28						
RISE DI28	3x07271 4x07271 I:7270	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI28	3x07272 4x07272 I:7271	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI28	3x07273 4x07273 I:7272	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI28	3x07274 4x07274 I:7273	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI28	3x07275 4x07275 I:7274	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI28	3x07276 4x07276 I:7275	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI129						
RISE DI129	3x07281 4x07281 I:7280	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				

FALL DI129	3x07282 4x07282 I:7281	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI129	3x07283 4x07283 I:7282	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI129	3x07284 4x07284 I:7283	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI129	3x07285 4x07285 I:7284	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI129	3x07286 4x07286 I:7285	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI30						
RISE DI30	3x07291 4x07291 I:7290	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI30	3x07292 4x07292 I:7291	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI30	3x07293 4x07293 I:7292	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI30	3x07294 4x07294 I:7293	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI30	3x07295 4x07295 I:7294	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI30	3x07296 4x07296 I:7295	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI31						

RISE DI31	3x07301 4x07301 I:7300	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI31	3x07302 4x07302 I:7301	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI31	3x07303 4x07303 I:7302	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI31	3x07304 4x07304 I:7303	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI31	3x07305 4x07305 I:7304	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI31	3x07306 4x07306 I:7305	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI32						
RISE DI32	3x07311 4x07311 I:7310	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI32	3x07312 4x07312 I:7311	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI32	3x07313 4x07313 I:7312	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI32	3x07314 4x07314 I:7313	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI32	3x07315 4x07315 I:7314	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				

LONG KEYPRESS END DI32	3x07316 4x07316 I:7315	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				

Register NAME Command NAME	MODBUS Register ASCII Command	Register VALUE ASCII Command	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
INITIAL & WATCHDOG STATUS FOR ALL DIGITAL OUTPUTS						
FRAM INTIAL & WATCHDOG STATUS OF DO1-DO12	3x59001 4x59001 I:59000	????		0x0FFF	UINT16 R/W	NO
		Actual init & watchdog state of DO1:0=OFF		1		
		Actual init & watchdog state of DO2:0=OFF		1		
		Actual init & watchdog state of DO3:0=OFF		1		
		Actual init & watchdog state of DO4:0=OFF		1		
		Actual init & watchdog state of DO5:0=OFF		1		
		Actual init & watchdog state of DO6:0=OFF		1		
		Actual init & watchdog state of DO7:0=OFF		1		
		Actual init & watchdog state of DO8:0=OFF		1		
		Actual init & watchdog state of DO9:0=OFF		1		
		Actual init & watchdog state of DO10:0=OFF		1		
		Actual init & watchdog state of DO11:0=OFF		1		
		Actual init & watchdog state of DO12:0=OFF		1		

Current FRAM setting of initial and watchdog state of all digital outputs. This state is used after power on and after a communcation watchdog timeout, if a watchdog time is confgured
Bit 0: =0:DO1 is OFF, =1:DO1 is ON
Bit 1: =0:DO2 is OFF, =1:DO2 is ON
...
Bit 10: =0:DO11 is OFF, =1:DO11 is ON
Bit 11: =0:DO12 is OFF, =1:DO12 is ON

Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FRAM