

Register NAME	MODBUS Register	Register VALUE	DATA TYPE	DO WRITE
<b>ASCII COMMANDS</b>				
HEART BEAT	ASCII READ COMMAND	#HB<CR> Result: #HB<CR>	ASCII	
	TX	#255,HB<CR>		
	RX	#255,HB<CR>		
Sends an Heartbeat to test the communication				
GET VERSION	ASCII READ COMMAND	#VERSION<CR> Result: #VERSION:<VersionHi>,<VersionMed>,<VersionLo><CR>	ASCII	
	TX	#255,VERSION<CR>		
	RX	#255,VERSION:1.1.0<CR>		
		Actual SW version:1.1.0		
Returns the version number of the module VersionHi: Version number high (1..255) VersionMed: Version number medium (1..255) VersionLo: Version number low (1..255)				
GET TYPE	ASCII READ COMMAND	#TYPE<CR> Result: #TYPE:<Type><CR>	ASCII	
	TX	#255,TYPE<CR>		
	RX	#255,TYPE:RESI-C4-A-64DI60DO16AIOX<CR>		
		Actual module type:RESI-C4-A-64DI60DO16AIOX		
Returns the actual module type				
GET FEATURES	ASCII READ COMMAND	#FTRS<CR> Result: #FTRS:<Type><CR>	ASCII	
	TX	#255,FTRS<CR>		
	RX	#255,FTRS:RESI-C4-A-64DI60DO16AIOX,RS485,DI:64,DO:60,AIOX:16,DOIC:MAX14915<CR>		
		Actual module type:N/A		
		Number of digital inputs:N/A		
		Type of digital inputs:N/A		
Returns the actual module features				
GET OWNER	ASCII READ COMMAND	#OWNER<CR> Result: #OWNER:<Owner><CR>	ASCII	
	TX	#255,OWNER<CR>		
	RX	#255,OWNER:RESI<CR>		
		Actual owner:RESI		
Returns the actual owner of the module				

GET CREATOR	ASCII READ COMMAND	#CREATOR<CR> Result: #CREATOR:<Creator><CR>	ASCII	
	TX	#255,CREATOR<CR>		
	RX	#255,CREATOR:DI HC SIGL,MSC<CR>		
		Actual creator:DI HC SIGL,MSC		
Returns the actual creator of the module				
GET COPYRIGHT	ASCII READ COMMAND	#COPYRIGHT<CR> Result: #COPYRIGHT:<Copyright><CR>	ASCII	
	TX	#255,COPYRIGHT<CR>		
	RX	#255,COPYRIGHT:2015-24 BY RESI AND DI HC SIGL,MSC WWW.RESI.CC<CR>		
		Actual copyright:2015-24 BY RESI AND DI HC SIGL,MSC WWW.RESI.CC		
Returns the actual copyright of the module				
GET SERIAL NUMBER	ASCII READ COMMAND	#SN<CR> Result: #SN:<Serial><CR>	ASCII	
	TX	#255,SN<CR>		
	RX	#255,SN:39002C000253554637303820<CR>		
		Actual serial number:39002C000253554637303820		
Returns the actual serial number of the module				
SET BOX NAME	ASCII WRITE COMMAND	#SETBOXNAME:<BOXNAME><CR> Result: #OK<CR>	ASCII	YES
	BOXNAME	MYBOX		
	TX	#255,SETBOXNAME:MYBOX<CR>		
	RX	#255,OK<CR>		
Sets a new box name for the controller				
GET BOX NAME	ASCII READ COMMAND	#BOXNAME<CR> Result: #BOXNAME:<BoxName><CR>	ASCII	
	TX	#255,BOXNAME<CR>		
	RX	#255,BOXNAME:MYBOX<CR>		
		Actual box name:MYBOX		
Returns the actual box name of the module. If no box name is defined, the value NONAME is returned				
GET INTERNAL STATUS	ASCII READ COMMAND	#INTSTAT<CR> Result: #INTSTAT:<Status><CR>	ASCII	
	TX	#255,INTSTAT<CR>		
	RX	#255,INTSTAT:I2C1:0,I2C2:0<CR>		
		Actual internal status:I2C1		
Returns the device specific internal status				

GET DIP SWITCH	ASCII READ COMMAND	#GDIP<CR> Result: #GDIP:<DIPSwitchDec>,<DIPSwitchHex><CR>	ASCII	
	TX	#255,GDIP<CR>		
	RX	#255,GDIP:0,0x0<CR>		
		Actual DIP SWITCH settings:00000000		
Returns the actual setting of the Dip switches as decimal number and as hexadecimal number. DIPSwitchDec DIPSwitchHex The current value of the DIP switches: Bit 0: DIP Switch 1 (=0:OFF, =1:ON) Bit 1: DIP Switch 2 (=0:OFF, =1:ON) Bit 2: DIP Switch 3 (=0:OFF, =1:ON) Bit 3: DIP Switch 4 (=0:OFF, =1:ON) Bit 4: DIP Switch 5 (=0:OFF, =1:ON) Bit 5: DIP Switch 6 (=0:OFF, =1:ON) Bit 6: DIP Switch 7(=0:OFF, =1:ON) Bit 7: DIP Switch 8 (=0:OFF, =1:ON)				
<b>SYSTEM COMMANDS</b>				
RESET	ASCII WRITE COMMAND	#RST<CR> Result: #OK<CR>	ASCII	NO
	TX	#255,RST<CR>		
	RX	N/A		
Executes a software reset (Reboot) of the module. Be aware that you will lose all serial interfaces in USB!!!				
FACTORY RESET	ASCII WRITE COMMAND	#FRST<CR> Result: #OK<CR>	ASCII	NO
	TX	#255,FRST<CR>		
	RX	N/A		
Executes a factory reset of the module				
WATCHDOG TIMER	ASCII WRITE COMMAND	#WD:<WDTIME><CR> Result: #OK<CR>	ASCII	YES
	WDTIME	0		
	TX	#255,WD:0<CR>		
	RX	#255,OK<CR>		
Enables or disables the WATCHDOG Timer for the Raspberry Pi module. WDTIME: 1..3600000: Time for Watchdog in Milliseconds (Maximum 60 Minutes) =0: No Watchdog is generated HINT: The Watchdog is internally handled every 10ms, so every value below 10 will reset immediately the Raspberry Pi computer.				
SET IO WATCHDOG TIMER	ASCII WRITE COMMAND	#SIOWATCHDOG:<IOWDTIME><CR> Result: #OK<CR>	ASCII	YES
	IOWDTIME	0		
	TX	#255,SIOWATCHDOG:0<CR>		

	RX	#255,OK<CR>		
Sets a new time for the internal IO WATCHDOG Timer. <IOWDTIME> is a time in 100ms. =0: No IO Watchdog is used HINT: The Watchdog is internally handled every 100ms, if the Timer reaches 0, all internal IOS will be set to a preconfigured state. Every ASCII command or MODBUS request will reset this timer.				
GET IO WATCHDOG TIMER	ASCII READ COMMAND	#GIOWATCHDOG<CR> Result: #GIOWATCHDOG::<IOWDTIME><CR>	ASCII	
	TX	#255,GIOWATCHDOG<CR>		
	RX	#255,GIOWATCHDOG:0,0x0<CR>		
Returns the actual time for the internal IO WATCHDOG Timer. <IOWDTIME> is a time in 100ms. =0: No IO Watchdog is used HINT: The Watchdog is internally handled every 100ms, if the Timer reaches 0, all internal IOS will be set to a preconfigured state. Every ASCII command or MODBUS request will reset this timer.				
<b>CPU PARAMETERS</b>				
GET CPU VOLTAGE	ASCII READ COMMAND	#GCPUTEMP<CR> Result: #GCPUTEMP:<CPUTemp><CR>	ASCII	
	TX	#255,GCPUTEMP<CR>		
	RX	#255,GCPUTEMP:41.4092<CR>		
		Actual internal temperature of CPU:41.4092°C		
Current internal temperature of CPU in ° Celsius.				
GET CPU VOLTAGE	ASCII READ COMMAND	#GCPUVOLT<CR> Result: #GCPUVOLT:<CPUVoltage><CR>	ASCII	
	TX	#255,GCPUVOLT<CR>		
	RX	#255,GCPUVOLT:3.3453<CR>		
		Actual supply voltage of CPU:3.3453V		
Current internal supply voltage of CPU in Volt.				
GET CPU BACKUP	ASCII READ COMMAND	#GCPUBACK<CR> Result: #GCPUBACK:<CPUBackupVoltage><CR>	ASCII	
	TX	#255,GCPUBACK<CR>		
	RX	#255,GCPUBACK:3.1871<CR>		
		Actual backup voltage of CPU for RTC:3.1871V		
Current internal backup voltage of CPU for the RTC in Volt.				

Register NAME	MODBUS Register	Register VALUE	DATA TYPE	DO WRITE
<b>ASCII COMMANDS:REAL TIME CLOCK</b>				
GET REAL TIME CLOCK	ASCII READ COMMAND	#GRTC<CR> Result: #GRTC:YMD,<YEAR>,<MONTH>,<DAY>,HMS,<HOUR>,<MINUTE>,<SECOND>,<WEEKDAY> ,DOK,<DATEOK>,TOK,<TIMEOK><CR>	ASCII	
	TX	#255,GRTC<CR>		
	RX	#255,GRTC:YMD,24,1,5,HMS,15,34,47,FRI,DOK,1,TOK,1<CR>		
		Actual date DD.MM.YYYY:5.1.2024		
		Actual time HH.MM.SS (24h):15:34:47		
		Actual Weekday:FRI		
		Battery buffered date is ok:YES		
		Battery buffered time is ok:YES		
Shows current RTC time of battery backup RTC on module				
<b>ASCII COMMANDS:REAL TIME CLOCK</b>				
SET REAL TIME CLOCK	ASCII WRITE COMMAND	#SRTC:YMD,<YEAR>,<MONTH>,<DAY>,HMS,<HOUR>,<MINUTE>,<SECOND>,<WEEKDAY><CR> Result: #OK<CR>	ASCII	YES
	YEAR	2024		
	MONTH	03		
	DAY	12		
	HOUR	14		
	MINUTE	42		
	SECOND	33		
	WEEKDAY	TUE		
	TX	#255,SRTC:YMD,24,03,12,HMS,14,42,33,TUE<CR>		
	RX	#255,OK<CR>		
Executes a software reset (Reboot) of the module.				

Register NAME	MODBUS Register	Register VALUE	DATA TYPE	DO WRITE
<b>ASCII COMMANDS:FRAM</b>				
GET FRAMSIZE	ASCII READ COMMAND	#GFRAMSIZE <CR> Result: #GFRAMSIZE:<FRAMType>,<FRAMSize>,<UsedSizeDEC>,<UsedSizeHEX> <CR>	ASCII	
	TX	#255,GFRAMSIZE <CR>		
	RX	#255,GFRAMSIZE:FM25L16B_G,2kB,210,0xD2 <CR>		
Reads the actual type and size of the used FRAM. The <UsedSize> describes the internal used space in bytes of the FRAM				
<b>ASCII COMMANDS:FRAM</b>				
GET FRAM16	ASCII READ COMMAND	#GFRAM16:<INDEX> <CR> Result: #GFRAM16:<INDEXDEC>,<VALUEDEC>,<INDEXHEX>,<VALUEHEX> <CR> or #GFRAM16:<INDEXDEC>,<ERR>,<INDEXHEX>,<ERR><CR>	ASCII	
	INDEX	350		
	TX	#255,GFRAM16:350 <CR>		
	RX	#255,GFRAM16:350,0,0x15E,0x0 <CR>		
		FRAM Index in bytes:350		
		FRAM Value in decimal:0		
Reads the actual UINT16 value (2 bytes) of FRAM memory <INDEX>. <INDEX> is a BYTE index in the FRAM strogae starting with 0.				
GET FRAM32	ASCII READ COMMAND	#GFRAM32:<INDEX> <CR> Result: #GFRAM32:<INDEXDEC>,<VALUEDEC>,<INDEXHEX>,<VALUEHEX> <CR> or #GFRAM32:<INDEXDEC>,<ERR>,<INDEXHEX>,<ERR><CR>	ASCII	
	INDEX	350		
	TX	#255,GFRAM32:350 <CR>		
	RX	#255,GFRAM32:350,0,0x15E,0x0 <CR>		
		FRAM Index in bytes:350		
		FRAM Value in decimal:0		
Reads the actual UINT32 value 4 bytes) of FRAM memory <INDEX>. <INDEX> is a BYTE index in the FRAM strogae starting with 0.				
GET FRAMDBL	ASCII READ COMMAND	#GFRAMDBL:<INDEX> <CR> Result: #GFRAMDBL:<INDEXDEC>,<VALUEDBL>,<INDEXHEX>,<VALUEDBL> <CR> or #GFRAMDBL:<INDEXDEC>,<ERR>,<INDEXHEX>,<ERR><CR>	ASCII	
	INDEX	400		
	TX	#255,GFRAMDBL:400 <CR>		
	RX	#255,GFRAMDBL:400,0,0x190,0 <CR>		
		FRAM Index in bytes:400		
		FRAM Value in decimal:0		
Reads the actual DOUBLE value 8 bytes) of FRAM memory <INDEX>. <INDEX> is a BYTE index in the FRAM strogae starting with 0.				
<b>ASCII COMMANDS:FRAM</b>				

SET FRAM16	ASCII WRITE COMMAND	#SFRAM16:<INDEX>,<VALUE><CR> Result: #SFRAM16:OK<CR> or #SFRAM16:ERR<CR>	ASCII	YES
	INDEX	350		
	VALUE	1234		
	TX	#255,SFRAM16:350,1234<CR>		
	RX	#255,SFRAM16:OK<CR>		
Writes a new UINT16 value (2 byte) into FRAM memory <INDEX>. <INDEX> is a BYTE index in the FRAM strogae starting with 0.				
SET FRAM32	ASCII WRITE COMMAND	#SFRAM32:<INDEX>,<VALUE><CR> Result: #SFRAM32:OK<CR> or #SFRAM32:ERR<CR>	ASCII	YES
	INDEX	350		
	VALUE	123456		
	TX	#255,SFRAM32:350,123456<CR>		
	RX	#255,SFRAM32:OK<CR>		
Writes a new UINT32 value (4 byte) into FRAM memory <INDEX>. <INDEX> is a BYTE index in the FRAM strogae starting with 0.				
SET FRAMDBL	ASCII WRITE COMMAND	#SFRAMDBL:<INDEX>,<DOUBLEVALUE><CR> Result: #SFRAMDBL:OK<CR> or #SERAMDBL:ERR<CR>	ASCII	YES
	INDEX	400		
	DOUBLEVALUE	3,1415926		
	TX	#255,SFRAMDBL:400,3.1415926<CR>		
	RX	#255,SFRAMDBL:OK<CR>		
Writes a new DOUBLE value (8 byte) into FRAM memory <INDEX>. <INDEX> is a BYTE index in the FRAM strogae starting with 0.				

Register NAME	MODBUS Register	Register VALUE	DATA TYPE	DO WRITE
<b>LED STATUS:LED1:GREEN</b>				
GET LED1	ASCII READ COMMAND	#GLED1<CR> Result: #GLED1:<LEDMode>,<LEDStateDec>,<LEDStateHex><CR>	ASCII	
	TX	#255,GLED1<CR>		
	RX	#255,GLED1:OFF,0,0x0<CR>		
		Actual LED state:OFF LED ist currently 0		
Returns the actual state of the LED1:GREEN on the cover of module				
<b>LED COMMANDS:LED1:GREEN</b>				
SET LED1 OFF	ASCII WRITE COMMAND	#SL1OFF<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL1OFF<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED1:GREEN on the cover of module to OFF				
SET LED1 ON	ASCII WRITE COMMAND	#SL1ON<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL1ON<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED1:GREEN on the cover of module to ON				
SET LED1 INVERT	ASCII WRITE COMMAND	#SL1INV<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL1INV<CR>		
	RX	#255,OK<CR>		
Inverts the current state of the LED1:GREEN on the cover of module from ON to OFF or from OFF to ON				
SET LED1 PULSE	ASCII WRITE COMMAND	#SL1PULSE:<PULSETIME><CR> Result: #OK<CR>	ASCII	YES
	PULSETIME	1000		
	TX	#255,SL1PULSE:1000<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED1:GREEN on the cover of module to PULSE and defines the one time pulse duration in Milliseconds between 1 and 60000				
SET LED1 BLINK	ASCII WRITE COMMAND	#SL1BLINK:<BLINKTIME><CR> Result: #OK<CR>	ASCII	YES
	BLINKTIME	1000		
	TX	#255,SL1BLINK:1000<CR>		
	RX	#255,OK<CR>		



Sets the current state of the LED1:GREEN on the cover of module to PULSE and defines the one time pulse duration in Milliseconds between 1 and 60000

SET LED1 FLASH	ASCII WRITE COMMAND	#SL1FLASH:<ONTIME>,<OFFTIME><CR> Result: #OK<CR>	ASCII	YES
	ONTIME	200		
	OFFTIME	3000		
	TX	#255,SL1FLASH:200,3000<CR>		
	RX	#255,OK<CR>		

Sets the current state of the LED1:GREEN on the cover of module to FLASH and defines the on and off intervals in Milliseconds between 20 and 600000

Register NAME	MODBUS Register	Register VALUE	DATA TYPE	DO WRITE
<b>LED STATUS:LED2:WHITE</b>				
GET LED2	ASCII READ COMMAND	#GLED2<CR> Result: #GLED2:<LEDMode>,<LEDStateDec>,<LEDStateHex><CR>	ASCII	
	TX	#255,GLED2<CR>		
	RX	#255,GLED2:BLINK,0,0x0<CR>		
		Actual LED state:BLINK LED ist currently 0		
Returns the actual state of the LED2:WHITE on the cover of module				
<b>LED COMMANDS:LED2:WHITE</b>				
SET LED2 OFF	ASCII WRITE COMMAND	#SL2OFF<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL2OFF<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED2:WHITE on the cover of module to OFF				
SET LED2 ON	ASCII WRITE COMMAND	#SL2ON<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL2ON<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED2:WHITE on the cover of module to ON				
SET LED2 INVERT	ASCII WRITE COMMAND	#SL2INV<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL2INV<CR>		
	RX	#255,OK<CR>		
Inverts the current state of the LED2:WHITE on the cover of module from ON to OFF or from OFF to ON				
SET LED2 PULSE	ASCII WRITE COMMAND	#SL2PULSE:<PULSETIME><CR> Result: #OK<CR>	ASCII	YES
	PULSETIME	1000		
	TX	#255,SL2PULSE:1000<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED2:WHITE on the cover of module to PULSE and defines the one time pulse duration in Milliseconds between 1 and 60000				
SET LED2 BLINK	ASCII WRITE COMMAND	#SL2BLINK:<BLINKTIME><CR> Result: #OK<CR>	ASCII	YES
	BLINKTIME	1000		
	TX	#255,SL2BLINK:1000<CR>		
	RX	#255,OK<CR>		

Sets the current state of the LED2:WHITE on the cover of module to PULSE and defines the one time pulse duration in Milliseconds between 1 and 60000

SET LED2 FLASH	ASCII WRITE COMMAND	#SL2FLASH:<ONTIME>,<OFFTIME><CR> Result: #OK<CR>	ASCII	YES
	ONTIME	200		
	OFFTIME	3000		
	TX	#255,SL2FLASH:200,3000<CR>		
	RX	#255,OK<CR>		

Sets the current state of the LED2:WHITE on the cover of module to FLASH and defines the on and off intervals in Milliseconds between 20 and 600000

Register NAME	MODBUS Register	Register VALUE	DATA TYPE	DO WRITE
<b>LED STATUS:LED3:RED</b>				
GET LED3	ASCII READ COMMAND	#GLED3<CR> Result: #GLED3:<LEDMode>,<LEDStateDec>,<LEDStateHex><CR>	ASCII	
	TX	#255,GLED3<CR>		
	RX	#255,GLED3:ON,1,0x1<CR>		
		Actual LED state:ON LED ist currently 1		
Returns the actual state of the LED3:RED on the cover of module				
<b>LED COMMANDS:LED3:RED</b>				
SET LED3 OFF	ASCII WRITE COMMAND	#SL3OFF<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL3OFF<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED3:RED on the cover of module to OFF				
SET LED3 ON	ASCII WRITE COMMAND	#SL3ON<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL3ON<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED3:RED on the cover of module to ON				
SET LED3 INVERT	ASCII WRITE COMMAND	#SL3INV<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL3INV<CR>		
	RX	#255,OK<CR>		
Inverts the current state of the LED3:RED on the cover of module from ON to OFF or from OFF to ON				
SET LED3 PULSE	ASCII WRITE COMMAND	#SL3PULSE:<PULSETIME><CR> Result: #OK<CR>	ASCII	YES
	PULSETIME	1000		
	TX	#255,SL3PULSE:1000<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED3:RED on the cover of module to PULSE and defines the one time pulse duration in Milliseconds between 1 and 60000				
SET LED3 BLINK	ASCII WRITE COMMAND	#SL3BLINK:<BLINKTIME><CR> Result: #OK<CR>	ASCII	YES
	BLINKTIME	1000		
	TX	#255,SL3BLINK:1000<CR>		
	RX	#255,OK<CR>		

Sets the current state of the LED3:RED on the cover of module to PULSE and defines the one time pulse duration in Milliseconds between 1 and 60000

SET LED3 FLASH	ASCII WRITE COMMAND	#SL3FLASH:<ONTIME>,<OFFTIME><CR> Result: #OK<CR>	ASCII	YES
	ONTIME	200		
	OFFTIME	3000		
	TX	#255,SL3FLASH:200,3000<CR>		
	RX	#255,OK<CR>		

Sets the current state of the LED3:RED on the cover of module to FLASH and defines the on and off intervals in Milliseconds between 20 and 600000

Register NAME	MODBUS Register	Register VALUE	DATA TYPE	DO WRITE
<b>LED STATUS:LED4:YELLOW</b>				
GET LED4	ASCII READ COMMAND	#GLED4<CR> Result: #GLED4:<LEDMode>,<LEDStateDec>,<LEDStateHex><CR>	ASCII	
	TX	#255,GLED4<CR>		
	RX	#255,GLED4:OFF,0,0x0<CR>		
		Actual LED state:OFF LED ist currently 0		
Returns the actual state of the LED4:YELLOW on the cover of module				
<b>LED COMMANDS:LED4:YELLOW</b>				
SET LED4 OFF	ASCII WRITE COMMAND	#SL4OFF<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL4OFF<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED4:YELLOW on the cover of module to OFF				
SET LED4 ON	ASCII WRITE COMMAND	#SL4ON<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL4ON<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED4:YELLOW on the cover of module to ON				
SET LED4 INVERT	ASCII WRITE COMMAND	#SL4INV<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL4INV<CR>		
	RX	#255,OK<CR>		
Inverts the current state of the LED4:YELLOW on the cover of module from ON to OFF or from OFF to ON				
SET LED4 PULSE	ASCII WRITE COMMAND	#SL4PULSE:<PULSETIME><CR> Result: #OK<CR>	ASCII	YES
	PULSETIME	1000		
	TX	#255,SL4PULSE:1000<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED4:YELLOW on the cover of module to PULSE and defines the one time pulse duration in Milliseconds between 1 and 60000				
SET LED4 BLINK	ASCII WRITE COMMAND	#SL4BLINK:<BLINKTIME><CR> Result: #OK<CR>	ASCII	YES
	BLINKTIME	1000		
	TX	#255,SL4BLINK:1000<CR>		
	RX	#255,OK<CR>		

Sets the current state of the LED4:YELLOW on the cover of module to PULSE and defines the one time pulse duration in Milliseconds between 1 and 60000

SET LED4 FLASH	ASCII WRITE COMMAND	#SL4FLASH:<ONTIME>,<OFFTIME><CR> Result: #OK<CR>	ASCII	YES
	ONTIME	200		
	OFFTIME	3000		
	TX	#255,SL4FLASH:200,3000<CR>		
	RX	#255,OK<CR>		

Sets the current state of the LED4:YELLOW on the cover of module to FLASH and defines the on and off intervals in Milliseconds between 20 and 600000

Register NAME	MODBUS Register	Register VALUE	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
<b>PRODUCT INFO</b>						
HW_GROUP	3x65201 4x65201 I:65200	50176,0xC400 B:C4 00			UINT16 R/O	
This is the group of hardware of the current product						
HW_TYPE	3x65202 4x65202 I:65201	1,0x0001 B:00 01			UINT16 R/O	
This is the type of hardware of the current product						
SW_VERSION	3x65203 4x65203 I:65202	272,0x0110 B:01 10			UINT16 R/O	
SW VERSION:0.1.0						
This is the current software version of the firmware						
SW_AUTHOR	3x65204 4x65204 I:65203	21321,0x5349 B:53 49			UINT16 R/O	
This is the current software author of the firmware						
MANUFACTURER	3x65205 4x65205 I:65204	1380275017,0x52455349 B:52 45 53 49			UINT32 R/O	
This is the current software author of the firmware						
NUMBER OF DIGITAL INPUTS	3x65207 4x65207 I:65206	0,0x0000 B:00 00			UINT16 R/O	
Number of DIS:0						
This is the current software version of the firmware						
NUMBER OF DIGITAL OUTPUTS	3x65208 4x65208 I:65207	0,0x0000 B:00 00			UINT16 R/O	
Number of DOS:0						
This is the current software version of the firmware						
NUMBER OF ANALOG INPUTS	3x65209 4x65209 I:65208	0,0x0000 B:00 00			UINT16 R/O	
Number of AIS:0						
This is the current software version of the firmware						
NUMBER OF ANALOG OUTPUTS	3x65210 4x65210 I:65209	0,0x0000 B:00 00			UINT16 R/O	
Number of AOS:0						



This is the current software version of the firmware						
NUMBER OF UNIVERSAL IN/OUTPUTS	3x65211 4x65211 I:65210	0,0x0000 B:00 00			UINT16 R/O	
		Number of AIOX:0				
This is the current software version of the firmware						
NUMBER OF SPECIAL INPUTS	3x65212 4x65212 I:65211	0,0x0000 B:00 00			UINT16 R/O	
		Number of special inputs:0				
This is the current software version of the firmware						
NUMBER OF SPECIAL OUTPUTS	3x65213 4x65213 I:65212	0,0x0000 B:00 00			UINT16 R/O	
		Number of special outputs:0				
This is the current software version of the firmware						
FEATURE1	3x65214 4x65214 I:65213	2,0x0002 B:00 02			UINT16 R/O	
		Feature:RS485				
This is the feature list of the controller: 0:NONE, 1:RS232, 2:RS485, 3:KNX, 4:DALI, 5:MBUS, 6:LORA, 7:LTE, 8:2xETHERNET						
FEATURE2	3x65215 4x65215 I:65214	0,0x0000 B:00 00			UINT16 R/O	
		Feature:NONE				
FEATURE3	3x65216 4x65216 I:65215	0,0x0000 B:00 00			UINT16 R/O	
		Feature:NONE				
FEATURE4	3x65217 4x65217 I:65216	0,0x0000 B:00 00			UINT16 R/O	
		Feature:NONE				
FEATURE5	3x65218 4x65218 I:65217	0,0x0000 B:00 00			UINT16 R/O	
		Feature:NONE				
FEATURE6	3x65219 4x65219 I:65218	0,0x0000 B:00 00			UINT16 R/O	
		Feature:NONE				
FEATURE7	3x65220 4x65220 I:65219	0,0x0000 B:00 00			UINT16 R/O	

		Feature:NONE				
FEATURE8	3x65221 4x65221 l:65220	0,0x0000 B:00 00			UINT16 R/O	
		Feature:NONE				
<b>MODBUS WATCHDOG</b>						
MODBUS WATCHDOG TIME	3x65222 4x65222 l:65221	0,0x0000 B:00 00		50	UINT16 R/W	NO
		Actual watchdog time in 1/100s:0 -> 0,0s				
<p>Writing a value onto this register defines a new time for the internal communication watchdog timer. The value is a timespan in 1/100s.          =0: The communication watchdog is disabled          =1..65535: Communication watchdog will be triggered after x 1/100s pause on communication line</p> <p>In case of an communication watchdog, the module sets all outputs to the states defined in the configuration output registers</p> <p>Reading this register will return the current stored time from the internal FRAM</p>						
RASPBERRY PI WATCHDOG TIMER	3x65223 4x65223 l:65222	0,0x0000 B:00 00		50	UINT16 R/W	NO
		remaining watchdog time in 10ms:0 -> 0,000s				
<p>Enables or disables the WATCHDOG Timer for the Raspberry Pi module.          1..65535: Time for Watchdog in x10 Milliseconds (Maximum 655,35 seconds)          =0: No Watchdog is generated</p>						
<b>FRAM</b>						
GET FRAM TYPE	3x65224 4x65224 l:65223	2,0x0002 B:00 02			UINT16 R/O	
		FRAM size & type:FM25L16 2kB				
<p>Returns the current type of the FRAM and its total size          =2:FM25L16B_G, 2kB          =64:FM25V05, 64kB          =128:FM25V10, 128kB</p>						
GET FRAM USED BYTES	3x65225 4x65225 l:65224	166,0x00A6 B:00 A6			UINT16 R/O	
		FRAM used bytes:166				
Returns the amount of used bytes from system in FRAM						
<b>RTC REAL TIME CLOCK</b>						
RTC YEAR	3x65231 4x65231 l:65230	24,0x0018 B:00 18		24	UINT16 R/W	NO
		Actual RTC year:24				
<p>Returns the actual year of the internal real time clock in the range of 24 to 99.          Writing to this register prepares the setting of a new time.</p>						

RTC MONTH	3x65232 4x65232 l:65231	2,0x0002 B:00 02		1		UINT16 R/W	NO
		Actual RTC month:2					
Returns the actual month of the internal real time clock in the range of 1 to 12 Writing to this register prepares the setting of a new time.							
RTC DAY	3x65233 4x65233 l:65232	29,0x001D B:00 1D		1		UINT16 R/W	NO
		Actual RTC day:29					
Returns the actual day of the internal real time clock in the range of 1 to 31 Writing to this register prepares the setting of a new time.							
RTC HOUR	3x65234 4x65234 l:65233	18,0x0012 B:00 12		12		UINT16 R/W	NO
		Actual RTC month:18					
Returns the actual hour of the internal real time clock in the range of 0 to 23 Writing to this register prepares the setting of a new time.							
RTC MINUTE	3x65235 4x65235 l:65234	0,0x0000 B:00 00		45		UINT16 R/W	NO
		Actual RTC hour:0					
Returns the actual minute of the internal real time clock in the range of 0 to 59 Writing to this register prepares the setting of a new time.							
RTC SECOND	3x65236 4x65236 l:65235	23,0x0017 B:00 17		30		UINT16 R/W	NO
		Actual RTC second:23					
Returns the actual second of the internal real time clock in the range of 0 to 59 Writing to this register prepares the setting of a new time.							
RTC DAY OF WEEK	3x65237 4x65237 l:65236	4,0x0004 B:00 04		5:FRIDAY		UINT16 R/W	NO
		Actual RTC week day:THU		SELECT DAY OF WEEK			
Returns the actual day of week in the range 1 to 7 1:MON, 2:TUE, 3:WED, 4:THU, 5:FRI, 6:SAT, 7:SUN Writing to this register writes a new date and time and weekday to the RTC							
<b>DIP SWITCH STATUS</b>							
DIP SWITCH	3x65501 4x65501 l:65500	85,0x0055 B:00 55				UINT16 R/O	

Returns the actual setting of the Dip switches.

Bit 0: DIP Switch 1 (=0:OFF, =1:ON)  
 Bit 1: DIP Switch 2 (=0:OFF, =1:ON)  
 Bit 2: DIP Switch 3 (=0:OFF, =1:ON)  
 Bit 3: DIP Switch 4 (=0:OFF, =1:ON)  
 Bit 4: DIP Switch 5 (=0:OFF, =1:ON)  
 Bit 5: DIP Switch 6 (=0:OFF, =1:ON)  
 Bit 6: DIP Switch 7 (=0:OFF, =1:ON)  
 Bit 7: DIP Switch 8 (=0:OFF, =1:ON)

**CPU DATA**

SERIAL1	3x65521 4x65521 I:65520	34,0x0022 B:00 22		UINT16 R/O	
Serial number of module as 96 bit unsigned integer number					
SERIAL2	3x65522 4x65522 I:65521	24,0x0018 B:00 18		UINT16 R/O	
SERIAL3	3x65523 4x65523 I:65522	22291,0x5713 B:57 13		UINT16 R/O	
SERIAL4	3x65524 4x65524 I:65523	20547,0x5043 B:50 43		UINT16 R/O	
SERIAL5	3x65525 4x65525 I:65524	13361,0x3431 B:34 31		UINT16 R/O	
SERIAL6	3x65526 4x65526 I:65525	8246,0x2036 B:20 36		UINT16 R/O	
		SERIAL:220018001357435031343620			
Serial number of module as 96 bit unsigned integer number					
CPU TEMPERATURE	3x65527 4x65527 I:65526	5061,0x13C5 B:13 C5		UINT16 R/O	
		Actual internal temperature of CPU:50,61°C			
Current internal temperature of CPU in ° Celsius multiplied by 100.					
CPU VOLTAGE	3x65528 4x65528 I:65527	333,0x014D B:01 4D		UINT16 R/O	
		Actual supply voltage of CPU:3,33V			
Current internal supply voltage of CPU in Volt multiplied by 1000.					
CPU BACKUP	3x65529 4x65529 I:65528	311,0x0137 B:01 37		UINT16 R/O	
		Actual backup voltage of CPU for RTC:3,11V			
Current internal backup voltage of CPU for RTC in Volt multiplied by 1000.					

**RESETs**

RASPBERRY PI RESET	3x65534 4x65534 I:65533	0,0x00 B:00 00		1:PERFORM RASPBERRY PI RESET	BIT R/W	NO
Resets the Raspberry Pi						
RASPBERRY PI RESET	3x65534 4x65534 I:65533	0,0x0000 B:00 00		1:PERFORM RASPBERRY PI RESET	UINT16 R/W	NO
Resets the Raspberry Pi						
FACTORY RESET	1x65535 2x65535 I:65534	0,0x00 B:00		1:PERFORM FACTORY RESET	BIT R/W	NO
Performs a factory reset of all internal saved parameters						
FACTORY RESET	3x65535 4x65535 I:65534	0,0x0000 B:00 00		1:PERFORM FACTORY RESET	UINT16 R/W	NO
Performs a factory reset of all internal saved parameters						
RESET	1x65536 2x65536 I:65535	0,0x00 B:00		1:PERFORM ARM RESET	BIT R/W	NO
Performs a software reset, whenever 1 is written to this register. If the host writes to this register 1, the module executes a soft reset (reboot). Be aware that you will lose all serial interfaces in USB!!!						
RESET	3x65536 4x65535 I:65535	0,0x0000 B:00 00		1:PERFORM ARM RESET	UINT16 R/W	NO
Performs a software reset, whenever 1 is written to this register. If the host writes to this register 1, the module executes a soft reset (reboot). Be aware that you will lose all serial interfaces in USB!!!						

Register NAME	MODBUS Register	Register VALUE	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
<b>DIP SWITCH STATUS</b>						
DIP SWITCH DIP1	1x65001 2x65001 I:65000	????			BIT R/O	
Returns the actual setting of the Dip switches. =0: DIP is OFF =1: DIP is ON						
DIP SWITCH DIP2	1x65002 2x65002 I:65001	????			BIT R/O	
DIP SWITCH DIP3	1x65003 2x65003 I:65002	????			BIT R/O	
DIP SWITCH DIP4	1x65004 2x65004 I:65003	????			BIT R/O	
DIP SWITCH DIP5	1x65005 2x65005 I:65004	????			BIT R/O	
DIP SWITCH DIP6	1x65006 2x65006 I:65005	????			BIT R/O	
DIP SWITCH DIP7	1x65007 2x65007 I:65006	????			BIT R/O	
DIP SWITCH DIP8	1x65008 2x65008 I:65007	????			BIT R/O	
<b>LED1:GREEN</b>						
LED1:GREEN SET TO OFF	1x65009 2x65009 I:65008	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil sets the LED to OFF						
LED1:GREEN SET TO ON	1x65010 2x65010 I:65009	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil sets the LED to ON						
LED1:GREEN INVERT LED STATE	1x65011 2x65011 I:65010	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil inverts the actual LED state						
LED1:GREEN BLINK	1x65012 2x65012 I:65011	????		N/A:DO NOTHING	BIT W/O	NO

Writing 1 to this coil start symmetrical blinking of LED with last defined time						
LED1:GREEN FLASH	1x65013 2x65013 I:65012	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with last defined times						
LED1:GREEN PULSE	1x65014 2x65014 I:65013	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with last defined time						
LED1:GREEN BLINK 5s	1x65015 2x65015 I:65014	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 5s ON-5s OFF cycle						
LED1:GREEN BLINK 1s	1x65016 2x65016 I:65015	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 1s ON-1s OFF cycle						
LED1:GREEN BLINK 250ms	1x65017 2x65017 I:65016	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 250ms ON-250ms OFF cycle						
LED1:GREEN BLINK 50ms	1x65018 2x65018 I:65017	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 50ms ON-50ms OFF cycle						
LED1:GREEN FLASH 5s-1s	1x65019 2x65019 I:65018	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 5s ON-1s OFF cycle						
LED1:GREEN FLASH 1s-250ms	1x65020 2x65020 I:65019	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 1s ON-250ms OFF cycle						
LED1:GREEN FLASH 500ms-100ms	1x65021 2x65021 I:65020	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 500ms ON-100ms OFF cycle						
LED1:GREEN FLASH 300ms-50ms	1x65022 2x65022 I:65021	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 300ms ON-50ms OFF cycle						
LED1:GREEN PULSE 1s	1x65023 2x65023 I:65022	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 1s ON						

LED1:GREEN PULSE 500ms	1x65024 2x65024 I:65023	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 500ms ON						
LED1:GREEN PULSE 250ms	1x65025 2x65025 I:65024	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 250ms ON						
LED1:GREEN PULSE 100ms	1x65026 2x65026 I:65025	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 100ms ON						
LED1:GREEN PULSE 20ms	1x65027 2x65027 I:65026	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 20ms ON						
<b>LED2:WHITE</b>						
LED2:WHITE SET TO OFF	1x65029 2x65029 I:65028	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil sets the LED to OFF						
LED2:WHITE SET TO ON	1x65030 2x65030 I:65029	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil sets the LED to ON						
LED2:WHITE INVERT LED STATE	1x65031 2x65031 I:65030	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil inverts the actual LED state						
LED2:WHITE BLINK	1x65032 2x65032 I:65031	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with last defined time						
LED2:WHITE FLASH	1x65033 2x65033 I:65032	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with last defined times						
LED2:WHITE PULSE	1x65034 2x65034 I:65033	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with last defined time						
LED2:WHITE BLINK 5s	1x65035 2x65035 I:65034	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 5s ON-5s OFF cycle						



LED2:WHITE BLINK 1s	1x65036 2x65036 I:65035	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 1s ON-1s OFF cycle						
LED2:WHITE BLINK 250ms	1x65037 2x65037 I:65036	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 250ms ON-250ms OFF cycle						
LED2:WHITE BLINK 50ms	1x65038 2x65038 I:65037	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 50ms ON-50ms OFF cycle						
LED2:WHITE FLASH 5s-1s	1x65039 2x65039 I:65038	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 5s ON-1s OFF cycle						
LED2:WHITE FLASH 1s-250ms	1x65040 2x65040 I:65039	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 1s ON-250ms OFF cycle						
LED2:WHITE FLASH 500ms-100ms	1x65041 2x65041 I:65040	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 500ms ON-100ms OFF cycle						
LED2:WHITE FLASH 300ms-50ms	1x65042 2x65042 I:65041	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 300ms ON-50ms OFF cycle						
LED2:WHITE PULSE 1s	1x65043 2x65043 I:65042	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 1s ON						
LED2:WHITE PULSE 500ms	1x65044 2x65044 I:65043	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 500ms ON						
LED2:WHITE PULSE 250ms	1x65045 2x65045 I:65044	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 250ms ON						
LED2:WHITE PULSE 100ms	1x65046 2x65046 I:65045	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 100ms ON						

LED2:WHITE PULSE 20ms	1x65047 2x65047 I:65046	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 20ms ON						
<b>LED3:RED</b>						
LED3:RED SET TO OFF	1x65049 2x65049 I:65048	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil sets the LED to OFF						
LED3:RED SET TO ON	1x65050 2x65050 I:65049	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil sets the LED to ON						
LED3:RED INVERT LED STATE	1x65051 2x65051 I:65050	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil inverts the actual LED state						
LED3:RED BLINK	1x65052 2x65052 I:65051	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with last defined time						
LED3:RED FLASH	1x65053 2x65053 I:65052	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with last defined times						
LED3:RED PULSE	1x65054 2x65054 I:65053	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with last defined time						
LED3:RED BLINK 5s	1x65055 2x65055 I:65054	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 5s ON-5s OFF cycle						
LED3:RED BLINK 1s	1x65056 2x65056 I:65055	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 1s ON-1s OFF cycle						
LED3:RED BLINK 250ms	1x65057 2x65057 I:65056	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 250ms ON-250ms OFF cycle						
LED3:RED BLINK 50ms	1x65058 2x65058 I:65057	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 50ms ON-50ms OFF cycle						

LED3:RED FLASH 5s-1s	1x65059 2x65059 I:65058	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 5s ON-1s OFF cycle						
LED3:RED FLASH 1s-250ms	1x65060 2x65060 I:65059	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 1s ON-250ms OFF cycle						
LED3:RED FLASH 500ms-100ms	1x65061 2x65061 I:65060	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 500ms ON-100ms OFF cycle						
LED3:RED FLASH 300ms-50ms	1x65062 2x65062 I:65061	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 300ms ON-50ms OFF cycle						
LED3:RED PULSE 1s	1x65063 2x65063 I:65062	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 1s ON						
LED3:RED PULSE 500ms	1x65064 2x65064 I:65063	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 500ms ON						
LED3:RED PULSE 250ms	1x65065 2x65065 I:65064	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 250ms ON						
LED3:RED PULSE 100ms	1x65066 2x65066 I:65065	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 100ms ON						
LED3:RED PULSE 20ms	1x65067 2x65067 I:65066	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 20ms ON						
<b>LED4:YELLOW</b>						
LED4:YELLOW SET TO OFF	1x65069 2x65069 I:65068	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil sets the LED to OFF						
LED4:YELLOW SET TO ON	1x65070 2x65070 I:65069	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil sets the LED to ON						

LED4:YELLOW INVERT LED STATE	1x65071 2x65071 I:65070	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil inverts the actual LED state						
LED4:YELLOW BLINK	1x65072 2x65072 I:65071	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with last defined time						
LED4:YELLOW FLASH	1x65073 2x65073 I:65072	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with last defined times						
LED4:YELLOW PULSE	1x65074 2x65074 I:65073	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with last defined time						
LED4:YELLOW BLINK 5s	1x65075 2x65075 I:65074	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 5s ON-5s OFF cycle						
LED4:YELLOW BLINK 1s	1x65076 2x65076 I:65075	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 1s ON-1s OFF cycle						
LED4:YELLOW BLINK 250ms	1x65077 2x65077 I:65076	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 250ms ON-250ms OFF cycle						
LED4:YELLOW BLINK 50ms	1x65078 2x65078 I:65077	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 50ms ON-50ms OFF cycle						
LED4:YELLOW FLASH 5s-1s	1x65079 2x65079 I:65078	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 5s ON-1s OFF cycle						
LED4:YELLOW FLASH 1s-250ms	1x65080 2x65080 I:65079	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 1s ON-250ms OFF cycle						
LED4:YELLOW FLASH 500ms-100ms	1x65081 2x65081 I:65080	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 500ms ON-100ms OFF cycle						

LED4:YELLOW FLASH 300ms-50ms	1x65082 2x65082 I:65081	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 300ms ON-50ms OFF cycle						
LED4:YELLOW PULSE 1s	1x65083 2x65083 I:65082	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 1s ON						
LED4:YELLOW PULSE 500ms	1x65084 2x65084 I:65083	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 500ms ON						
LED4:YELLOW PULSE 250ms	1x65085 2x65085 I:65084	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 250ms ON						
LED4:YELLOW PULSE 100ms	1x65086 2x65086 I:65085	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 100ms ON						
LED4:YELLOW PULSE 20ms	1x65087 2x65087 I:65086	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 20ms ON						
<b>DIP SWITCH STATUS</b>						
DIP SWITCH	3x65501 4x65501 I:65500	????			UINT16 R/O	
Returns the actual setting of the Dip switches. Bit 0: DIP Switch 1 (=0:OFF, =1:ON) Bit 1: DIP Switch 2 (=0:OFF, =1:ON) Bit 2: DIP Switch 3 (=0:OFF, =1:ON) Bit 3: DIP Switch 4 (=0:OFF, =1:ON) Bit 4: DIP Switch 5 (=0:OFF, =1:ON) Bit 5: DIP Switch 6 (=0:OFF, =1:ON) Bit 6: DIP Switch 7 (=0:OFF, =1:ON) Bit 7: DIP Switch 8 (=0:OFF, =1:ON)						
<b>LED1:GREEN</b>						
LED1:GREEN STATE	3x65502 4x65502 I:65501	????		1:SET TO ON	UINT16 R/W	NO
		State of LED:????				

Returns the actual state of the LED

Writing to this register will set a new state for the LED

0: Switch LED permanent OFF

1: Switch LED permanent ON

2: Invert last state of LED

3: Start symmetrical blinking of LED with TIME1 ON and TIME1 OFF

4: Start asymmetrical flashing of LED with TIME1 ON and TIME2 OFF

5: Start one time pulse of LED with TIME1 ON and infinite OFF

LED1:GREEN TIME1	3x65503 4x65503 1:65502	????		1000	UINT16 R/W	YES
		Actual time 1 in ms:0				

Returns the actual time1 for blink,flash and pulse ON time in Milliseconds

Writing to this register sets a new time in the range 20-65534ms

LED1:GREEN TIME2	3x65504 4x65504 1:65503	????		2000	UINT16 R/W	YES
		Actual time 2 in ms:0				

Returns the actual time2 for blink and flash OFF time in Milliseconds

Writing to this register sets a new time in the range 20-65534ms

**LED2:WHITE**

LED2:WHITE STATE	3x65505 4x65505 1:65504	????		1:SET TO ON	UINT16 R/W	NO
		State of LED:????				

Returns the actual state of the LED

Writing to this register will set a new state for the LED

0: Switch LED permanent OFF

1: Switch LED permanent ON

2: Invert last state of LED

3: Start symmetrical blinking of LED with TIME1 ON and TIME1 OFF

4: Start asymmetrical flashing of LED with TIME1 ON and TIME2 OFF

5: Start one time pulse of LED with TIME1 ON and infinite OFF

LED2:WHITE TIME1	3x65506 4x65506 1:65505	????		1000	UINT16 R/W	YES
		Actual time 1 in ms:0				

Returns the actual time1 for blink,flash and pulse ON time in Milliseconds

Writing to this register sets a new time in the range 20-65534ms

LED2:WHITE TIME2	3x65507 4x65507 1:65506	????		2000	UINT16 R/W	YES
		Actual time 2 in ms:0				

Returns the actual time2 for blink and flash OFF time in Milliseconds

Writing to this register sets a new time in the range 20-65534ms

**LED3:RED**

LED3:RED STATE	3x65508 4x65508 1:65507	????		1:SET TO ON	UINT16 R/W	NO
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		State of LED:????				
Returns the actual state of the LED Writing to this register will set a new state for the LED 0: Switch LED permanent OFF 1: Switch LED permanent ON 2: Invert last state of LED 3: Start symmetrical blinking of LED with TIME1 ON and TIME1 OFF 4: Start asymmetrical flashing of LED with TIME1 ON and TIME2 OFF 5: Start one time pulse of LED with TIME1 ON and infinite OFF						
LED3:RED TIME1	3x65509 4x65509 1:65508	????		1000	UINT16 R/W	YES
		Actual time 1 in ms:0				
Returns the actual time1 for blink,flash and pulse ON time in Milliseconds Writing to this register sets a new time in the range 20-65534ms						
LED3:RED TIME2	3x65510 4x65510 1:65509	????		2000	UINT16 R/W	YES
		Actual time 2 in ms:0				
Returns the actual time2 for blink and flash OFF time in Milliseconds Writing to this register sets a new time in the range 20-65534ms						
<b>LED4:YELLOW</b>						
LED4:YELLOW STATE	3x65511 4x65511 1:65510	????		1:SET TO ON	UINT16 R/W	NO
		State of LED:????				
Returns the actual state of the LED Writing to this register will set a new state for the LED 0: Switch LED permanent OFF 1: Switch LED permanent ON 2: Invert last state of LED 3: Start symmetrical blinking of LED with TIME1 ON and TIME1 OFF 4: Start asymmetrical flashing of LED with TIME1 ON and TIME2 OFF 5: Start one time pulse of LED with TIME1 ON and infinite OFF						
LED4:YELLOW TIME1	3x65512 4x65512 1:65511	????		1000	UINT16 R/W	YES
		Actual time 1 in ms:0				
Returns the actual time1 for blink,flash and pulse ON time in Milliseconds Writing to this register sets a new time in the range 20-65534ms						
LED4:YELLOW TIME2	3x65513 4x65513 1:65512	????		2000	UINT16 R/W	YES
		Actual time 2 in ms:0				
Returns the actual time2 for blink and flash OFF time in Milliseconds Writing to this register sets a new time in the range 20-65534ms						

Command NAME	ASCII command type	ASCII command structure	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
<b>ASCII COMMANDS</b>						
<b>DIGITAL INPUTS</b>						
GET DIGITAL INPUTS	ASCII READ COMMAND	#GDIS<CR> Result: #GDIS:<DISADec>,<DISBDec>,<DISAHex>,<DISBHex><CR>			ASCII	
	TX	#255,GDIS<CR>				
	RX	#255,GDIS:0,2147483648,0x0,0x80000000<CR>				
		Actual status of digital inputs:				
		DI1-32:0000.0000.0000.0000.0000.0000.0000.0000				
		DI33-64:1000.0000.0000.0000.0000.0000.0000.0000				
<DISADec>,<DISAHex>:DI1-32 <DISBDec>,<DISBHex>:DI33-64  Returns the actual state of all digital inputs as decimal number and as hexadecimal number. DISADec, DISAHex The current state of all digital inputs: Bit 0: State of DI1 (=0:OFF, =1:ON) Bit 1: State of DI2 (=0:OFF, =1:ON) Bit 2: State of DI3 (=0:OFF, =1:ON) ... Bit 29: State of DI30 (=0:OFF, =1:ON) Bit 30: State of DI31 (=0:OFF, =1:ON) Bit 31: State of DI32 (=0:OFF, =1:ON)						
DISBDec, DISBHex The current state of all digital inputs: Bit 0: State of DI33 (=0:OFF, =1:ON) Bit 1: State of DI34 (=0:OFF, =1:ON) Bit 2: State of DI35 (=0:OFF, =1:ON) ... Bit 29: State of DI62 (=0:OFF, =1:ON) Bit 30: State of DI63 (=0:OFF, =1:ON) Bit 31: State of DI64 (=0:OFF, =1:ON)						
GET DIGITAL INPUT DIx	ASCII READ COMMAND	#GDI<DINR><CR> Result: #GDI<DINR>:<DIxDec>,<DIxHex><CR>			ASCII	
	DINR	1				
	TX	#255,GDI1<CR>				
	RX	#255,GDI1:0,0x0<CR>				
		Actual status of digital input DI1:0=OFF				
<DINR>: 1=DI1..64=DI64						









LONG KEY END ALL DIS PART x	ASCII READ COMMAND	#LKEADISP<PART><CR> Result: #LKEADISP<PART>:<LongKeyEndDInDec>,...,<LongKeyEndDIn+15Dec>, <LongKeyEndDInHex>,...,<LongKeyEndDIn+15Hex><CR>	ASCII	
	PART	2		
	TX	#255,LKEADISP2<CR>		
	RX	#255,LKEADISP2:0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0<CR>		
		Actual counter for long keypress end events on DI17:0		
		Actual counter for long keypress end events on DI18:0		
		Actual counter for long keypress end events on DI19:0		
		Actual counter for long keypress end events on DI20:0		
		Actual counter for long keypress end events on DI21:0		
		Actual counter for long keypress end events on DI22:0		
		Actual counter for long keypress end events on DI23:0		
		Actual counter for long keypress end events on DI24:0		
		Actual counter for long keypress end events on DI25:0		
		Actual counter for long keypress end events on DI26:0		
		Actual counter for long keypress end events on DI27:0		
		Actual counter for long keypress end events on DI28:0		
		Actual counter for long keypress end events on DI29:0		
		Actual counter for long keypress end events on DI30:0		
		Actual counter for long keypress end events on DI31:0		
		Actual counter for long keypress end events on DI32:0		
<PART>: 1..4, 1=DI1-DI16, 2=DI17-DI32, 3=DI33-DI48, 4=DI49-DI64				
Returns for each digital input the counter for long keypress end events. As soon as the module detects the end of a long keypress on a digital input, the counter for the affected digital input is incremented by 1.				
The parameter <PART> defines the part of the digital inputs. The command returns maximal 16 digital inputs.				
LONG KEY END DIx	ASCII READ COMMAND	#LKEDI<DINR><CR> Result: #LKEDI<DINR>:<LongKeyEndDec>,<LongKeyEndHex><CR>	ASCII	
	DINR	1		
	TX	#255,LKEDI1<CR>		
	RX	#255,LKEDI1:0,0x0<CR>		
		Actual counter for long keypress end events on digital input DI1:0		
<DINR>: 1=DI1..64=DI64				
Returns for digital input <DINR> the counter for long keypress end events.				
As soon as the module detects the end of a long keypress on a digital input, the counter for the affected digital input is incremented by 1.				
RISE ALL DIS PART x	ASCII READ COMMAND	#RADISP<PART><CR> Result: #RADISP<PART>:<RiseDInDec>,...,<RiseDIn+15Dec>, <RiseDInHex>,...,<RiseDIn+15Hex><CR>	ASCII	
	PART	2		
	TX	#255,RADISP2<CR>		



		Actual counter for falling edges on DI21:0		
		Actual counter for falling edges on DI22:0		
		Actual counter for falling edges on DI23:0		
		Actual counter for falling edges on DI24:0		
		Actual counter for falling edges on DI25:0		
		Actual counter for falling edges on DI26:0		
		Actual counter for falling edges on DI27:0		
		Actual counter for falling edges on DI28:0		
		Actual counter for falling edges on DI29:0		
		Actual counter for falling edges on DI30:0		
		Actual counter for falling edges on DI31:0		
		Actual counter for falling edges on DI32:0		

<PART>: 1..4, 1=DI1-DI16, 2=DI17-DI32, 3=DI33-DI48, 4=DI49-DI64

Returns for each digital input the counter for falling edges. As soon as the module detects a falling edge on a digital input, the falling edge counter for the affected digital input is incremented by 1.

The parameter <PART> defines the part of the digital inputs. The command returns maximal 16 digital inputs.

FALL DIx	ASCII READ COMMAND	#FDI<DINR> <CR> Result: #FDI<DINR>:<FallDec>,<FallHex> <CR>	ASCII	
	DINR	1		
	TX	#255,FDI1<CR>		
	RX	#255,FDI1:1,0x1<CR>		
		Actual counter for falling edges on digital input DI1:1		

<DINR>: 1=DI1..64=DI64

Returns for digital input <DINR> the counter for falling edges.

As soon as the module detects a falling edge on a digital input, the falling edge counter for the affected digital input is incremented by 1.

RESET COUNTERS	ASCII WRITE COMMAND	#RC <CR> Result: #OK <CR>	ASCII	NO
	TX	#255,RC <CR>		
	RX	N/A		

Resets all internal counters for digital inputs and events on this digital inputs to 0.

#### DIGITAL INPUTS EVENTS

EVENTS ON	ASCII WRITE COMMAND	#EVTON <CR> Result: #OK <CR>	ASCII	NO
	TX	#255,EVTON <CR>		
	RX	#1,OK <CR>		

Activates event sending of changes on digital inputs

Whenever a change is detected on the digital inputs, the IO module sends immediately

#<BusAdr>,<EVT:DIS:<AllDISasDec>,<AllDISasHex> <CR>

EVENTS OFF	ASCII WRITE COMMAND	#EVTOFF <CR> Result: #OK <CR>	ASCII	NO
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	<b>TX</b>	#255,EVTOFF<CR>		
	<b>RX</b>	#1,OK<CR>		
Deactivates event sending of changes on digital inputs				
Whenever a change is detected on the digital inputs, the IO module sends immediately #<BusAdr>,EVT:DIS:<AIIISasDec>,<AIIISasHex><CR>				

Command NAME	ASCII command type	ASCII command structure	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
<b>ASCII COMMANDS</b>						
<b>DIGITAL OUTPUTS</b>						
UPDATE DIGITAL INPUTS AND OUTPUTS	ASCII WRITE COMMAND	#UDIOS:<OutAllDOSa>,<OutAllDOSb> <CR> Result: #UDIOS:<InAllDISADec>,<InAllDISBDec>,<InAllDISAHex>,<InAllDISBHex> <CR>			ASCII	YES
	DO1	1:ON				
	DO2	1:ON				
	DO3	1:ON				
	DO4	1:ON				
	DO5	1:ON				
	DO6	1:ON				
	DO7	1:ON				
	DO8	1:ON				
	DO9	1:ON				
	DO10	1:ON				
	DO11	1:ON				
	DO12	1:ON				
	DO13	1:ON				
	DO14	1:ON				
	DO15	1:ON				
	DO16	1:ON				
	DO17	1:ON				
	DO18	1:ON				
	DO19	1:ON				
	DO20	1:ON				
	DO21	1:ON				
	DO22	1:ON				
	DO23	1:ON				
	DO24	1:ON				
	DO25	1:ON				
	DO26	1:ON				
	DO27	1:ON				
	DO28	1:ON				
	DO29	1:ON				
	DO30	1:ON				
	DO31	1:ON				
	DO32	1:ON				
	OutAllDOSa	0xFFFFFFFF	4294967295			



	DO33	1:ON		
	DO34	1:ON		
	DO35	1:ON		
	DO36	1:ON		
	DO37	1:ON		
	DO38	1:ON		
	DO39	1:ON		
	DO40	1:ON		
	DO41	1:ON		
	DO42	1:ON		
	DO43	1:ON		
	DO44	1:ON		
	DO45	1:ON		
	DO46	1:ON		
	DO47	1:ON		
	DO48	1:ON		
	DO49	1:ON		
	DO50	1:ON		
	DO51	1:ON		
	DO52	1:ON		
	DO53	1:ON		
	DO54	1:ON		
	DO55	1:ON		
	DO56	1:ON		
	DO57	1:ON		
	DO58	1:ON		
	DO59	1:ON		
	DO60	1:ON		
	OutAllDOS	0x0FFFFFFF	268435455	
	TX	#255,UDIOS:0xFFFFFFFF,0xFFFFFFFF <CR>		
	RX	#255,UDIOS:0,2147483648,0x0,0x80000000 <CR>		
		Actual status of digital inputs:		
		DI1-DI32:0000.0000.0000.0000.0000.0000.0000.0000		
		DI33-DI64:0000.0000.0000.0000.0000.0000.0000.0000		

Sets all digital outputs to the new state OutAllDOS and gives back the current status of all digital inputs InAllDIS as decimal and hexadecimal value

OutAllDOSA: The new state for all digital outputs

Bit 0: State of DO1 (=0:OFF, =1:ON)

Bit 1: State of DO2 (=0:OFF, =1:ON)

Bit 2: State of DO3 (=0:OFF, =1:ON)

...

Bit 29: State of DO30 (=0:OFF, =1:ON)

Bit 30: State of DO31 (=0:OFF, =1:ON)

Bit 31: State of DO32 (=0:OFF, =1:ON)

OutAllDOSB: The new state for all digital outputs

Bit 0: State of DO33 (=0:OFF, =1:ON)

Bit 1: State of DO34 (=0:OFF, =1:ON)

Bit 2: State of DO45 (=0:OFF, =1:ON)

...

Bit 25: State of DO58 (=0:OFF, =1:ON)

Bit 26: State of DO59 (=0:OFF, =1:ON)

Bit 27: State of DO60 (=0:OFF, =1:ON)

InAllDISA: The current state for all digital inputs

Bit 0: State of DI1 (=0:OFF, =1:ON)

Bit 1: State of DI2 (=0:OFF, =1:ON)

Bit 2: State of DI3 (=0:OFF, =1:ON)

...

Bit 29: State of DI30 (=0:OFF, =1:ON)

Bit 30: State of DI31 (=0:OFF, =1:ON)

Bit 31: State of DI32 (=0:OFF, =1:ON)

InAllDISB: The current state for all digital inputs

Bit 0: State of DI33 (=0:OFF, =1:ON)

Bit 1: State of DI34 (=0:OFF, =1:ON)

Bit 2: State of DI35 (=0:OFF, =1:ON)

...

Bit 29: State of DI62 (=0:OFF, =1:ON)

Bit 30: State of DI63 (=0:OFF, =1:ON)

Bit 31: State of DI64 (=0:OFF, =1:ON)

SET DIGITAL OUTPUTS	ASCII WRITE COMMAND	#SDOS:<OutAllDOSA>,<OutAllDOSB><CR> Result: #OK<CR>	ASCII	YES
	DO1	1:ON		
	DO2	1:ON		
	DO3	1:ON		
	DO4	1:ON		
	DO5	1:ON		
	DO6	1:ON		
	DO7	1:ON		
	DO8	1:ON		
	DO9	1:ON		
	DO10	1:ON		
	DO11	1:ON		
	DO12	1:ON		

	DO13	1:ON		
	DO14	1:ON		
	DO15	1:ON		
	DO16	1:ON		
	DO17	1:ON		
	DO18	1:ON		
	DO19	1:ON		
	DO20	1:ON		
	DO21	1:ON		
	DO22	1:ON		
	DO23	1:ON		
	DO24	1:ON		
	DO25	1:ON		
	DO26	1:ON		
	DO27	1:ON		
	DO28	1:ON		
	DO29	1:ON		
	DO30	1:ON		
	DO31	1:ON		
	DO32	1:ON		
	OutAllDOS	0xFFFFFFFF	4294967295	
	DO33	1:ON		
	DO34	1:ON		
	DO35	1:ON		
	DO36	1:ON		
	DO37	1:ON		
	DO38	1:ON		
	DO39	1:ON		
	DO40	1:ON		
	DO41	1:ON		
	DO42	1:ON		
	DO43	1:ON		
	DO44	1:ON		
	DO45	1:ON		
	DO46	1:ON		
	DO47	1:ON		
	DO48	1:ON		
	DO49	1:ON		
	DO50	1:ON		
	DO51	1:ON		
	DO52	1:ON		
	DO53	1:ON		

	DO54	1:ON		
	DO55	1:ON		
	DO56	1:ON		
	DO57	1:ON		
	DO58	1:ON		
	DO59	1:ON		
	DO60	1:ON		
	OutAllDOSB	0x0FFFFFFF	268435455	
	TX	#255,SDOS:0xFFFFFFFF,0xFFFFFFFF <CR>		
	RX	#255,OK <CR>		

Sets all digital outputs to the new state OutAllDOSA and OutAllDOSB

OutAllDOSA:The new state for all digital outputs

Bit 0: State of DO1 (=0:OFF, =1:ON)

Bit 1: State of DO2 (=0:OFF, =1:ON)

Bit 2: State of DO3 (=0:OFF, =1:ON)

...

Bit 29: State of DO30 (=0:OFF, =1:ON)

Bit 30: State of DO31 (=0:OFF, =1:ON)

Bit 31: State of DO32 (=0:OFF, =1:ON)

OutAllDOSB:The new state for all digital outputs

Bit 0: State of DO33 (=0:OFF, =1:ON)

Bit 1: State of DO34 (=0:OFF, =1:ON)

Bit 2: State of DO35 (=0:OFF, =1:ON)

...

Bit 25: State of DO58 (=0:OFF, =1:ON)

Bit 26: State of DO59 (=0:OFF, =1:ON)

Bit 27: State of DO60 (=0:OFF, =1:ON)

SET DIGITAL OUTPUT DOx	ASCII WRITE COMMAND	#SDO<DONR>:<Out> <CR> Result: #OK<CR>	ASCII	NO
	DONR	2		
	DOx	0:OFF		
	TX	#255,SDO2:0<CR>		
	RX	N/A		

<DONR>: 1=DO1..60=DO60

Sets the new state for digital output DOx. The state is defined with <Out>.

Out

The new state of the digital output DOx:

=0: digital output is OFF

=1: digital output is ON

GET DIGITAL OUTPUTS	ASCII READ COMMAND	#GDOS<CR> Result: #GDOS:<DOSADec>,<DOSBDec>,<DOSAHex>,<DOSBHex> <CR>	ASCII	
	TX	#255,GDOS <CR>		
	RX	#255,GDOS:4294967295,268435455,0xFFFFFFFF,0xFFFFFFFF <CR>		
		Actual status of digital outputs:		

		DO1-DO32:1111.1111.1111.1111.1111.1111.1111.1111		
		DO33-DO60:1111.1111.1111.1111.1111.1111.1111.1111		

Returns the actual state of the digital outputs as decimal number and as hexadecimal number.

DOSADec, DOSAHex

The current state of the digital outputs:

Bit 0: State of DO1 (=0:OFF, =1:ON)

Bit 1: State of DO2 (=0:OFF, =1:ON)

Bit 2: State of DO3 (=0:OFF, =1:ON)

...

Bit 29: State of DO30 (=0:OFF, =1:ON)

Bit 30: State of DO31 (=0:OFF, =1:ON)

Bit 31: State of DO32 (=0:OFF, =1:ON)

DOSBDec, DOSBHex

The current state of the digital outputs:

Bit 0: State of DO33 (=0:OFF, =1:ON)

Bit 1: State of DO34 (=0:OFF, =1:ON)

Bit 2: State of DO35 (=0:OFF, =1:ON)

...

Bit 25: State of DO58 (=0:OFF, =1:ON)

Bit 26: State of DO59 (=0:OFF, =1:ON)

Bit 27: State of DO60 (=0:OFF, =1:ON)

GET DIGITAL OUTPUT DOx	ASCII READ COMMAND	#GDO<DONR> <CR> Result: #GDO<DONR>:<DOxDec>,<DOxHex> <CR>	ASCII	
	DONR	2		
	TX	#255,GDO2 <CR>		
	RX	#255,GDO2:1,0x1 <CR>		
		Actual status of digital output DO2:1=ON		

Returns the actual state of the digital output DOx as decimal number and as hexadecimal number.

DOxDec, DOxHex

The current state of the digital output DOx:

=0: relay output is OFF

=1: relay output is ON

#### DIGITAL OUTPUTS: PULSE OUTPUT

PULSE DOx	ASCII WRITE COMMAND	#PDO<DONR>:<Time> <CR> Result: #OK <CR>	ASCII	YES
	DONR	2		
	TIME	200		
	TX	#255,PDO2:200 <CR>		
	RX	#255,OK <CR>		

<DONR>: 1=DO1..60=DO60

<Time>: 0..65535\*100ms

This command switches the digital output DOx on for the pulse duration <PulseTimeIn100ms>\*100ms.

PulseTimeIn100ms: A duration in 100ms units.

The corresponding digital output is switched on for this time period.

GET PULSE TIMER DOx	ASCII	#GPT<DONR><CR>	ASCII	
	READ	Result:		
	COMMAND	#GPT:<TimeDec>,<TimeHex><CR>		
	DONR	2		
	TX	#255,GPT2<CR>		
	RX	#255,GPT2:19813,0x4D65<CR>		
		Actual pulse time for DO2:19,8s		

<DONR>: 1=DO1..60=DO60

Returns the remaining timer value of the pulse for digital output DOx in ms.

PulseTimeInMSDec, PulseTimeInMSHex

The remaining time of the pulse in Milliseconds

Command NAME	ASCII command type	ASCII command structure	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
<b>ASCII COMMANDS</b>						
<b>DIGITAL OUTPUTS</b>						
<b>DIGITAL OUTPUTS: ENABLE OPEN WIRE DETECTION WHILE ON</b>						
SET DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE ON	ASCII WRITE COMMAND	#SDOEOWDONS:<OpenWireDOSA>,<OpenWireDOSB><CR> Result: #OK<CR>			ASCII	YES
	DO1	1:ENABLE				
	DO2	1:ENABLE				
	DO3	1:ENABLE				
	DO4	1:ENABLE				
	DO5	1:ENABLE				
	DO6	1:ENABLE				
	DO7	1:ENABLE				
	DO8	1:ENABLE				
	DO9	1:ENABLE				
	DO10	1:ENABLE				
	DO11	1:ENABLE				
	DO12	1:ENABLE				
	DO13	1:ENABLE				
	DO14	1:ENABLE				
	DO15	1:ENABLE				
	DO16	1:ENABLE				
	DO17	1:ENABLE				
	DO18	1:ENABLE				
	DO19	1:ENABLE				
	DO20	1:ENABLE				
	DO21	1:ENABLE				
	DO22	1:ENABLE				
	DO23	1:ENABLE				
	DO24	1:ENABLE				
	DO25	1:ENABLE				
	DO26	1:ENABLE				
	DO27	1:ENABLE				
	DO28	1:ENABLE				
	DO29	1:ENABLE				
	DO30	1:ENABLE				
	DO31	1:ENABLE				
	DO32	1:ENABLE				
	OpenWireDOSA	0xFFFFFFFF	4294967295			

	DO33	1:ENABLE		
	DO34	1:ENABLE		
	DO35	1:ENABLE		
	DO36	1:ENABLE		
	DO37	1:ENABLE		
	DO38	1:ENABLE		
	DO39	1:ENABLE		
	DO40	1:ENABLE		
	DO41	1:ENABLE		
	DO42	1:ENABLE		
	DO43	1:ENABLE		
	DO44	1:ENABLE		
	DO45	1:ENABLE		
	DO46	1:ENABLE		
	DO47	1:ENABLE		
	DO48	1:ENABLE		
	DO49	1:ENABLE		
	DO50	1:ENABLE		
	DO51	1:ENABLE		
	DO52	1:ENABLE		
	DO53	1:ENABLE		
	DO54	1:ENABLE		
	DO55	1:ENABLE		
	DO56	1:ENABLE		
	DO57	1:ENABLE		
	DO58	1:ENABLE		
	DO59	1:ENABLE		
	DO60	1:ENABLE		
	OpenWireDOSB	0x0FFFFFFF	268435455	
	TX	#255,SDOEOWDONS:0xFFFFFFFF,0xFFFFFFFF <CR>		
	RX	#255,OK <CR>		

Sets the open wire mode for all digital outputs to the new mode OpenWireDOSA and OpenWireDOSB. This enables the diagnostic of open wire while the digital output is ON.

OpenWireDOSA: The new state for all digital outputs

Bit 0: New mode for DO1 (=0:DISABLED, =1:ENABLED)

Bit 1: New mode for DO2 (=0:DISABLED, =1:ENABLED)

...

Bit 30: New mode for DO21 (=0:DISABLED, =1:ENABLED)

Bit 31: New mode for DO32 (=0:DISABLED, =1:ENABLED)

OpenWireDOSB: The new state for all digital outputs

Bit 0: New mode for DO33 (=0:DISABLED, =1:ENABLED)

Bit 1: New mode for DO34 (=0:DISABLED, =1:ENABLED)

...

Bit 26: New mode for DO59 (=0:DISABLED, =1:ENABLED)

Bit 27: New mode for DO60 (=0:DISABLED, =1:ENABLED)



SET DIGITAL OUTPUT ENABLE OPEN WIRE DETECTION WHILE ON	ASCII WRITE COMMAND	#SDOEOWDON<DONR>:<OpenWireDOx><CR> Result: #OK<CR>	ASCII	YES
	DONR	2		
	DOx	1:ENABLE		
	TX	#255,SDOEOWDON2:1<CR>		
	RX	#255,OK<CR>		
<DONR>: 1=DO1..60=DO60				
<OpenWireDOx>: 0=DISABLE..1=ENABLE				
Sets the open wire mode for digital output DOx to the new mode OpenWireDOx. This enables the diagnostic of open wire while the digital output is ON. The new mode of the digital output DOx: =0: diagnostic mode for digital output is DISABLED =1: diagnostic mode for digital output is ENABLED				
GET DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE ON	ASCII READ COMMAND	#GDOEOWDONS<CR> Result: #GDOEOWDONS:<OpenWireDOSADec>,<OpenWireDOSBDec>, <OpenWireDOSAHex>,<OpenWireDOSBHex><CR>	ASCII	
	TX	#255,GDOEOWDONS<CR>		
	RX	#255,GDOEOWDONS:4294967295,268435455,0xFFFFFFFF,0xFFFFFFFF<CR>		
		Actual mode for open wire diagnostic while ON of digital outputs:		
		DO1-DO32:1111.1111.1111.1111.1111.1111.1111.1111		
		DO33-DO60:1111.1111.1111.1111.1111.1111.1111		
Returns the actual mode for open wire diagnostic while digital output is ON as decimal number and as hexadecimal number. OpenWireDOSADec, OpenWireDOASHex The current mode for open wire diagnostic while ON of the digital outputs: Bit 0: Open wire diagnostic mode of DO1 (=0:DISABLED, =1:ENABLED) Bit 1: Open wire diagnostic mode of DO2 (=0:DISABLED, =1:ENABLED) ... Bit 30: Open wire diagnostic mode of DO31 (=0:DISABLED, =1:ENABLED) Bit 31: Open wire diagnostic mode of DO32 (=0:DISABLED, =1:ENABLED)				
OpenWireDOSBDec, OpenWireDOBSHex The current mode for open wire diagnostic while ON of the digital outputs: Bit 0: Open wire diagnostic mode of DO33 (=0:DISABLED, =1:ENABLED) Bit 1: Open wire diagnostic mode of DO34 (=0:DISABLED, =1:ENABLED) ... Bit 26: Open wire diagnostic mode of DO59 (=0:DISABLED, =1:ENABLED) Bit 27: Open wire diagnostic mode of DO60 (=0:DISABLED, =1:ENABLED)				
GET DIGITAL OUTPUT ENABLE OPEN WIRE DETECTION WHILE ON	ASCII READ COMMAND	#GDOEOWDON<DONR><CR> Result: #GDOEOWDON<DONR>:<OpenWireDOxDec>,<OpenWireDOxHex><CR>	ASCII	
	DONR	2		
	TX	#255,GDOEOWDON2<CR>		
	RX	#255,GDOEOWDON2:1,0x1<CR>		
		Actual open wire diagnostic mode while ON of digital output DO2:1=ENABLED		
<DONR>: 1=DO1..60=DO60				

Returns the actual open wire diagnostic mode while ON of the digital output DOx as decimal number and as hexadecimal number.

OpenWireDOxDec, OpenWireDOxHex

The current diagnostic mode of the digital output DOx:

=0: open wire diagnostic mode for digital output is DISABLED

=1: open wire diagnostic mode for digital output is ENABLED

#### DIGITAL OUTPUTS: ENABLE OPEN WIRE DETECTION WHILE OFF

SET DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE OFF	ASCII WRITE COMMAND	#SDOEOWDOFFS:<OpenWireDOSA>,<OpenWireDOSB> <CR> Result: #OK<CR>	ASCII	YES
	DO1	1:ENABLE		
	DO2	1:ENABLE		
	DO3	1:ENABLE		
	DO4	1:ENABLE		
	DO5	1:ENABLE		
	DO6	1:ENABLE		
	DO7	1:ENABLE		
	DO8	1:ENABLE		
	DO9	1:ENABLE		
	DO10	1:ENABLE		
	DO11	1:ENABLE		
	DO12	1:ENABLE		
	DO13	1:ENABLE		
	DO14	1:ENABLE		
	DO15	1:ENABLE		
	DO16	1:ENABLE		
	DO17	1:ENABLE		
	DO18	1:ENABLE		
	DO19	1:ENABLE		
	DO20	1:ENABLE		
	DO21	1:ENABLE		
	DO22	1:ENABLE		
	DO23	1:ENABLE		
	DO24	1:ENABLE		
	DO25	1:ENABLE		
	DO26	1:ENABLE		
	DO27	1:ENABLE		
	DO28	1:ENABLE		
	DO29	1:ENABLE		
	DO30	1:ENABLE		
	DO31	1:ENABLE		
	DO32	1:ENABLE		
	OpenWireDOSA	0xFFFFFFFF   4294967295		
	DO33	1:ENABLE		
	DO34	1:ENABLE		
	DO35	1:ENABLE		

	DO36	1:ENABLE		
	DO37	1:ENABLE		
	DO38	1:ENABLE		
	DO39	1:ENABLE		
	DO40	1:ENABLE		
	DO41	1:ENABLE		
	DO42	1:ENABLE		
	DO43	1:ENABLE		
	DO44	1:ENABLE		
	DO45	1:ENABLE		
	DO46	1:ENABLE		
	DO47	1:ENABLE		
	DO48	1:ENABLE		
	DO49	1:ENABLE		
	DO50	1:ENABLE		
	DO51	1:ENABLE		
	DO52	1:ENABLE		
	DO53	1:ENABLE		
	DO54	1:ENABLE		
	DO55	1:ENABLE		
	DO56	1:ENABLE		
	DO57	1:ENABLE		
	DO58	1:ENABLE		
	DO59	1:ENABLE		
	DO60	1:ENABLE		
	OpenWireDOSB	0x0FFFFFFF	268435455	
	TX	#255,SDOEOWDOFFS:0xFFFFFFFF,0xFFFFFFFF <CR>		
	RX	#255,OK <CR>		
Sets the open wire mode for all digital outputs to the new mode OpenWireDOSA and OpenWireDOSB. This enables the diagnostic of open wire while the digital output is OFF. OpenWireDOSA: The new state for all digital outputs Bit 0: New mode for DO1 (=0:DISABLED, =1:ENABLED) Bit 1: New mode for DO2 (=0:DISABLED, =1:ENABLED) ... Bit 30: New mode for DO21 (=0:DISABLED, =1:ENABLED) Bit 31: New mode for DO32 (=0:DISABLED, =1:ENABLED)  OpenWireDOSB: The new state for all digital outputs Bit 0: New mode for DO33 (=0:DISABLED, =1:ENABLED) Bit 1: New mode for DO34 (=0:DISABLED, =1:ENABLED) ... Bit 26: New mode for DO59 (=0:DISABLED, =1:ENABLED) Bit 27: New mode for DO60 (=0:DISABLED, =1:ENABLED)				
SET DIGITAL OUTPUT ENABLE OPEN WIRE DETECTION WHILE OFF	ASCII WRITE COMMAND	#SDOEOWDOFF <DONR> : <OpenWireDOx> <CR> Result: #OK <CR>		ASCII
	DONR	2		YES
	DOx	1:ENABLE		

	TX	#255,SDOEOWDOFF2:1<CR>		
	RX	#255,OK<CR>		
<DONR>: 1=DO1..60=DO60				
<OpenWireDOx>: 0=DISABLE..1=ENABLE				
Sets the open wire mode for digital output DOx to the new mode OpenWireDOx. This enables the diagnostic of open wire while the digital output is OFF. The new mode of the digital output DOx: =0: diagnostic mode for digital output is DISABLED =1: diagnostic mode for digital output is ENABLED				
GET DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE OFF	ASCII READ COMMAND	#GDOEOWDOFFS<CR> Result: #GDOEOWDOFFS:<OpenWireDOSADec>,<OpenWireDOSBDec>, <OpenWireDOSASHex>,<OpenWireDOSBHex><CR>	ASCII	
	TX	#255,GDOEOWDOFFS<CR>		
	RX	#255,GDOEOWDOFFS:4294967295,268435455,0xFFFFFFFF,0xFFFFFFFF<CR>		
		Actual mode for open wire diagnostic while OFF of digital outputs:		
		DO1-DO32:1111.1111.1111.1111.1111.1111.1111.1111		
		DO33-DO60:1111.1111.1111.1111.1111.1111.1111		
Returns the actual mode for open wire diagnostic while digital output is OFF as decimal number and as hexadecimal number. OpenWireDOSADec, OpenWireDOASHex The current mode for open wire diagnostic while OFF of the digital outputs: Bit 0: Open wire diagnostic mode of DO1 (=0:DISABLED, =1:ENABLED) Bit 1: Open wire diagnostic mode of DO2 (=0:DISABLED, =1:ENABLED) ... Bit 30: Open wire diagnostic mode of DO31 (=0:DISABLED, =1:ENABLED) Bit 31: Open wire diagnostic mode of DO32 (=0:DISABLED, =1:ENABLED)				
OpenWireDOSBDec, OpenWireDOBSHex The current mode for open wire diagnostic while OFF of the digital outputs: Bit 0: Open wire diagnostic mode of DO33 (=0:DISABLED, =1:ENABLED) Bit 1: Open wire diagnostic mode of DO34 (=0:DISABLED, =1:ENABLED) ... Bit 26: Open wire diagnostic mode of DO59 (=0:DISABLED, =1:ENABLED) Bit 27: Open wire diagnostic mode of DO60 (=0:DISABLED, =1:ENABLED)				
GET DIGITAL OUTPUT ENABLE OPEN WIRE DETECTION WHILE OFF	ASCII READ COMMAND	#GDOEOWDOFF<DONR><CR> Result: #GDOEOWDOFF<DONR>:<OpenWireDOxDec>,<OpenWireDOxHex><CR>	ASCII	
	DONR	2		
	TX	#255,GDOEOWDOFF2<CR>		
	RX	#255,GDOEOWDOFF2:1,0x1<CR>		
		Actual open wire diagnostic mode while OFF of digital output DO2:1=ENABLED		
<DONR>: 1=DO1..60=DO60				
Returns the actual open wire diagnostic mode while OFF of the digital output DOx as decimal number and as hexadecimal number. OpenWireDOxDec, OpenWireDOxHex The current diagnostic mode of the digital output DOx: =0: open wire diagnostic mode for digital output is DISABLED =1: open wire diagnostic mode for digital output is ENABLED				
<b>DIGITAL OUTPUTS: ENABLE SHORTCUT TO VDD DETECTION WHILE OFF</b>				

SET DIGITAL OUTPUTS ENABLE SHORTCUT TO VDD DETECTION WHILE OFF	ASCII WRITE COMMAND	#SDOESVDDS:<ShortCutDOSA>,<ShortCutDOSB> <CR> Result: #OK<CR>	ASCII	YES
	DO1	1:ENABLE		
	DO2	1:ENABLE		
	DO3	1:ENABLE		
	DO4	1:ENABLE		
	DO5	1:ENABLE		
	DO6	1:ENABLE		
	DO7	1:ENABLE		
	DO8	1:ENABLE		
	DO9	1:ENABLE		
	DO10	1:ENABLE		
	DO11	1:ENABLE		
	DO12	1:ENABLE		
	DO13	1:ENABLE		
	DO14	1:ENABLE		
	DO15	1:ENABLE		
	DO16	1:ENABLE		
	DO17	1:ENABLE		
	DO18	1:ENABLE		
	DO19	1:ENABLE		
	DO20	1:ENABLE		
	DO21	1:ENABLE		
	DO22	1:ENABLE		
	DO23	1:ENABLE		
	DO24	1:ENABLE		
	DO25	1:ENABLE		
	DO26	1:ENABLE		
	DO27	1:ENABLE		
	DO28	1:ENABLE		
	DO29	1:ENABLE		
	DO30	1:ENABLE		
	DO31	1:ENABLE		
	DO32	1:ENABLE		
	ShortCutDOSA	0xFFFFFFFF   4294967295		
	DO33	1:ENABLE		
	DO34	1:ENABLE		
	DO35	1:ENABLE		
	DO36	1:ENABLE		
	DO37	1:ENABLE		
	DO38	1:ENABLE		
	DO39	1:ENABLE		
	DO40	1:ENABLE		
	DO41	1:ENABLE		
	DO42	1:ENABLE		

	DO43	1:ENABLE		
	DO44	1:ENABLE		
	DO45	1:ENABLE		
	DO46	1:ENABLE		
	DO47	1:ENABLE		
	DO48	1:ENABLE		
	DO49	1:ENABLE		
	DO50	1:ENABLE		
	DO51	1:ENABLE		
	DO52	1:ENABLE		
	DO53	1:ENABLE		
	DO54	1:ENABLE		
	DO55	1:ENABLE		
	DO56	1:ENABLE		
	DO57	1:ENABLE		
	DO58	1:ENABLE		
	DO59	1:ENABLE		
	DO60	1:ENABLE		
	ShortCutDOSB	0x0FFFFFFF	268435455	
	TX	#255,SDOESVDDS:0xFFFFFFFF,0xFFFFFFFF<CR>		
	RX	#255,OK<CR>		

Sets the shortcut to VDD detection mode for all digital outputs to the new mode ShortCutDOSA and ShortCutDOSB. This enables the diagnostic of shortcut to VDD while the digital output is OFF.

ShortCutDOSA: The new state for all digital outputs

Bit 0: New mode for DO1 (=0:DISABLED, =1:ENABLED)

Bit 1: New mode for DO2 (=0:DISABLED, =1:ENABLED)

...

Bit 30: New mode for DO21 (=0:DISABLED, =1:ENABLED)

Bit 31: New mode for DO32 (=0:DISABLED, =1:ENABLED)

ShortCutDOSB: The new state for all digital outputs

Bit 0: New mode for DO33 (=0:DISABLED, =1:ENABLED)

Bit 1: New mode for DO34 (=0:DISABLED, =1:ENABLED)

...

Bit 26: New mode for DO59 (=0:DISABLED, =1:ENABLED)

Bit 27: New mode for DO60 (=0:DISABLED, =1:ENABLED)

SET DIGITAL OUTPUT ENABLE SHORTCUT TO VDD DETECTION WHILE OFF	ASCII WRITE COMMAND	#SDOESVDD<DONR>:<ShortCutDOx><CR> Result: #OK<CR>	ASCII	YES
	DONR	2		
	DOx	0:DISABLE		
	TX	#255,SDOESVDD2:0<CR>		
	RX	#255,OK<CR>		

<DONR>: 1=DO1..60=DO60

<ShortCutDOx>: 0=DISABLE..1=ENABLE

Sets the shortcut to VDD mode for digital output DOx to the new mode ShortCutDOx. This enables the diagnostic of shortcut to VDD while the digital output is OFF.

The new mode of the digital output DOx:

=0: diagnostic mode for digital output is DISABLED

=1: diagnostic mode for digital output is ENABLED

GET DIGITAL OUTPUTS ENABLE SHORT CUT TO VDD DETECTION WHILE OFF	ASCII READ COMMAND	#GDOESVDDS<CR> Result: #GDOESDDS:<ShortCutDOSADec>,<ShortCutDOSBDec>, <ShortCutDOSAHex>,<ShortCutDOSBHex><CR>	ASCII	
	TX	#255,GDOESVDDS<CR>		
	RX	#255,GDOESVDDS:4294967293,268435455,0xFFFFFDD,0xFFFFF<CR>		
		Actual mode for shortcut to VDD diagnostic while OFF of digital outputs:		
		DO1-DO32:1111.1111.1111.1111.1111.1111.1101		
		DO33-DO60:1111.1111.1111.1111.1111.1111		

Returns the actual mode for shortcut to VDD diagnostic while digital output is OFF as decimal number and as hexadecimal number.

ShortCutDOSADec, ShortCutDOSAHex

The current mode for shortcut diagnostic while OFF of the digital outputs:

Bit 0: Open wire diagnostic mode of DO1 (=0:DISABLED, =1:ENABLED)

Bit 1: Open wire diagnostic mode of DO2 (=0:DISABLED, =1:ENABLED)

...

Bit 30: Open wire diagnostic mode of DO31 (=0:DISABLED, =1:ENABLED)

Bit 31: Open wire diagnostic mode of DO32 (=0:DISABLED, =1:ENABLED)

ShortCutDOSBDec, ShortCutDOSBHex

The current mode for open wire diagnostic while OFF of the digital outputs:

Bit 0: Open wire diagnostic mode of DO33 (=0:DISABLED, =1:ENABLED)

Bit 1: Open wire diagnostic mode of DO34 (=0:DISABLED, =1:ENABLED)

...

Bit 26: Open wire diagnostic mode of DO59 (=0:DISABLED, =1:ENABLED)

Bit 27: Open wire diagnostic mode of DO60 (=0:DISABLED, =1:ENABLED)

GET DIGITAL OUTPUT ENABLE SHORT CUT TO VDD DETECTION WHILE OFF	ASCII READ COMMAND	#GDOESVDD<DONR><CR> Result: #GDOESVDD<DONR>:<ShortCutDOxDec>,<ShortCutDOxHex><CR>	ASCII	
	DONR	2		
	TX	#255,GDOESVDD2<CR>		
	RX	#255,GDOESVDD2:0,0x0<CR>		
		Actual shortcut to VDD diagnostic mode while OFF of digital output DO2:0=DISABLED		

<DONR>: 1=DO1..60=DO60

Returns the actual short cut to VDD diagnostic mode while OFF of the digital output DOx as decimal number and as hexadecimal number.

ShortCutDOxDec, ShortCutDOxHex

The current diagnostic mode of the digital output DOx:

=0: open wire diagnostic mode for digital output is DISABLED

=1: open wire diagnostic mode for digital output is ENABLED

#### DIGITAL OUTPUTS: SPI STATUS

GET SPI STATUS DIGITAL OUTPUT GROUPS	ASCII READ COMMAND	#GSSDOGS<CR> Result: #GSSDOGS:<SPIDOGSDec>,<SPIDOGSHex><CR>	ASCII	
	TX	#255,GSSDOGS<CR>		
	RX	#255,GSSDOGS:0,0x0<CR>		
		Actual SPI status of digital output groups:0000.0000		

digital output group #1, chip #1:DO1-DO7  
 digital output group #2, chip #2:DO8-DO15  
 digital output group #3, chip #3:DO16-DO22  
 digital output group #4, chip #4:DO23-DO30  
 digital output group #5, chip #5:DO31-DO38  
 digital output group #6, chip #6:DO39-DO45  
 digital output group #7, chip #7:DO46-DO53  
 digital output group #8, chip #8:DO54-DO60

Returns the actual SPI communication state of the corresponding output group as decimal number and as hexadecimal number.

SPIDOGSDec,SPIDOGSHex

The current SPI communication state of the digital output group:

Bit 0: SPI communication state for digital output group #1 (=0:NO FAULT, =1:FAULT)

Bit 1: SPI communication state for digital output group #2 (=0:NO FAULT, =1:FAULT)

Bit 2: SPI communication state for digital output group #3 (=0:NO FAULT, =1:FAULT)

Bit 3: SPI communication state for digital output group #4 (=0:NO FAULT, =1:FAULT)

Bit 4: SPI communication state for digital output group #5 (=0:NO FAULT, =1:FAULT)

Bit 5: SPI communication state for digital output group #6 (=0:NO FAULT, =1:FAULT)

Bit 6: SPI communication state for digital output group #7 (=0:NO FAULT, =1:FAULT)

Bit 7: SPI communication state for digital output group #8 (=0:NO FAULT, =1:FAULT)

GET SPI STATUS DIGITAL OUTPUT GROUPx	ASCII READ COMMAND DOGRP	#GSSDOG<DOGRP><CR> Result: #GSSDOG<DOGRP>:<SPIDOGxDec>,<SPIDOGxHex><CR> 8	ASCII	
	TX	#255,GSSDOG8<CR>		
	RX	#255,GSSDOG8:0,0x0<CR>		
		Actual SPI status of digital output group DOG8:0=NO FAULT		

<DOGRP>: 1=CHIP1..8=CHIP8

digital output group #1, chip #1:DO1-DO7  
 digital output group #2, chip #2:DO8-DO15  
 digital output group #3, chip #3:DO16-DO22  
 digital output group #4, chip #4:DO23-DO30  
 digital output group #5, chip #5:DO31-DO38  
 digital output group #6, chip #6:DO39-DO45  
 digital output group #7, chip #7:DO46-DO53  
 digital output group #8, chip #8:DO54-DO60

Returns the actual SPI communication state of the digital output group DOGRP as decimal number and as hexadecimal number.

SPIDOGxDec, SPIDOGxHex

The current SPI communication state of the digital output group DOGRP:

=0: SPI communication state for output group is OK (NO FAULT)

=1: SPI communication state for output group is FAULT

#### DIGITAL OUTPUTS: INTERRUPT STATUS

GET DIGITAL OUTPUTS INTERRUPT STATUS	ASCII READ COMMAND	#GDOINTS<CR> Result: #GDOINTS:<InterruptStatusADec>,<InterruptStatusBDec>, <InterruptStatusAHex>,<InterruptStatusBHex><CR>	ASCII	
	TX	#255,GDOINTS<CR>		
	RX	#255,GDOINTS:1077952576,1077938184,0x40404040,0x40400808<CR>		
		Actual interrupt status of all digital output groups:		
		CHIP #1:0100.0000		
		CHIP #2:0100.0000		
		CHIP #3:0100.0000		



	CHIP #4:0100.0000		
	CHIP #5:0000.1000		
	CHIP #6:0000.1000		
	CHIP #7:0100.0000		
	CHIP #8:0100.0000		

digital output group #1, chip #1:DO1-DO7  
digital output group #2, chip #2:DO8-DO15  
digital output group #3, chip #3:DO16-DO22  
digital output group #4, chip #4:DO23-DO30  
digital output group #5, chip #5:DO31-DO38  
digital output group #6, chip #6:DO39-DO45  
digital output group #7, chip #7:DO46-DO53  
digital output group #8, chip #8:DO54-DO60

Returns the actual interrupt state of all output groups as decimal number and as hexadecimal number.  
InterruptStatusADec,InterruptStatusAHex: The current interrupt state of digital output groups 1-4 (CHIP1-4):  
For each chip 8 bits are used: CHIP#1:Bits 0-7, CHIP#2:Bits 8-15, CHIP#3:Bits 16-23, CHIP#4:Bits 24-31  
InterruptStatusBDec,InterruptStatusBHex: The current interrupt state of digital output groups 5-8 (CHIP5-8):  
For each chip 8 bits are used: CHIP#5:Bits 0-7, CHIP#6:Bits 8-15, CHIP#7:Bits 16-23, CHIP#8:Bits 24-31

Bit 0: Overload detected (0=OK,1=FAULT)  
Bit 1: Current limit detected(0=OK,1=FAULT)  
Bit 2: Open wire while OFF detected (0=OK,1=FAULT)  
Bit 3: Open wire while ON detected (0=OK,1=FAULT)  
Bit 4: Short to VDD while ON detected (0=OK,1=FAULT)  
Bit 5: Thermal error detected-shutdown (0=OK,1=FAULT)  
Bit 6: Supply error detected (0=OK,1=FAULT)  
Bit 7: Communication error detected (0=OK,1=FAULT)

GET DIGITAL OUTPUT GROUPx INTERRUPT STATUS	ASCII READ COMMAND	#GDOINT<DOGRP> <CR> Result: #GDOINT<DOGRP>:<InterruptStatusDec>,<InterruptStatusHex> <CR>	ASCII	
	DOGRP	8		
	TX	#255,GDOINT8<CR>		
	RX	#255,GDOINT8:64,0x40<CR>		
		Actual interrupt status of digital output group 8:0100.0000		

<DOGRP>: 1=CHIP1..8=CHIP8

digital output group #1, chip #1:DO1-DO7  
digital output group #2, chip #2:DO8-DO15  
digital output group #3, chip #3:DO16-DO22  
digital output group #4, chip #4:DO23-DO30  
digital output group #5, chip #5:DO31-DO38  
digital output group #6, chip #6:DO39-DO45  
digital output group #7, chip #7:DO46-DO53  
digital output group #8, chip #8:DO54-DO60

Returns the actual interrupt state of the digital output group DOGRP as decimal number and as hexadecimal number.

InterruptStatusDec, InterruptStatusHex

Bit 0:Overload detected (0=OK,1=FAULT)

Bit 1:Current limit detected(0=OK,1=FAULT)

Bit 2:Open wire while OFF detected (0=OK,1=FAULT)

Bit 3:Open wire while ON detected (0=OK,1=FAULT)

Bit 4:Short to VDD while ON detected (0=OK,1=FAULT)

Bit 5:Thermal error detected-shutdown (0=OK,1=FAULT)

Bit 6:Supply error detected (0=OK,1=FAULT)

Bit 7:Communication error detected (0=OK,1=FAULT)

### DIGITAL OUTPUTS: GLOBAL ERRORS

GET DIGITAL OUTPUTS GLOBAL ERRORS	ASCII READ COMMAND	#GDOERRS<CR> Result: #GDOERRS:<GlobalErrorsADec>,<GlobalErrorsBDec>, <GlobalErrorsAHex>,<GlobalErrorsBHex><CR>	ASCII	
	<b>TX</b>	#255,GDOERRS<CR>		
	<b>RX</b>	#255,GDOERRS:471604252,471597056,0x1C1C1C1C,0x1C1C0000<CR>		
		Actual global errors of all digital output groups:		
		CHIP #1:0001.1100		
		CHIP #2:0001.1100		
		CHIP #3:0001.1100		
		CHIP #4:0001.1100		
		CHIP #5:0000.0000		
		CHIP #6:0000.0000		
		CHIP #7:0001.1100		
		CHIP #8:0001.1100		

digital output group #1, chip #1:DO1-DO7

digital output group #2, chip #2:DO8-DO15

digital output group #3, chip #3:DO16-DO22

digital output group #4, chip #4:DO23-DO30

digital output group #5, chip #5:DO31-DO38

digital output group #6, chip #6:DO39-DO45

digital output group #7, chip #7:DO46-DO53

digital output group #8, chip #8:DO54-DO60

Returns the actual current global error state of all output groups as decimal number and as hexadecimal number.

GlobalErrorADec,GlobalErrorAHex: The current global error state of digital output groups 1-4 (CHIP1-4):

For each chip 8 bits are used: CHIP#1:Bits 0-7, CHIP#2:Bits 8-15, CHIP#3:Bits 16-23, CHIP#4:Bits 24-31

GlobalErrorBDec,GlobalErrorBHex: The current current global error state of digital output groups 5-8 (CHIP5-8):

For each chip 8 bits are used: CHIP#5:Bits 0-7, CHIP#6:Bits 8-15, CHIP#7:Bits 16-23, CHIP#8:Bits 24-31

Bit 0: Internal under voltage detected (0=OK,1=FAULT)

Bit 1: VA under voltage detected (<2.3V) (0=OK,1=FAULT)

Bit 2: VDD not good detected (<17V) (0=OK,1=FAULT)

Bit 3: VDD warning detected (<12V) (0=OK,1=FAULT)

Bit 4: VDD under voltage detected (<8V) (0=OK,1=FAULT)

Bit 5: Thermal shutdown (0=OK,1=FAULT)

Bit 6: Synchronisation error detected (0=OK,1=FAULT)

Bit 7: Watchdog error detected (0=OK,1=FAULT)

GET DIGITAL OUTPUT GROUPx GLOBAL ERRORS	ASCII READ COMMAND	#GDOERR<DOGRP><CR> Result: #GDOERR<DOGRP>:<GlobalErrorsDec>,<GlobalErrorsHex><CR>	ASCII	
	DOGRP	8		
	TX	#255,GDOERR8<CR>		
	RX	#255,GDOERR8:28,0x1C<CR>		
		Actual global errors of digital output group 8:0001.1100		
<DOGRP>: 1=CHIP1..8=CHIP8				
digital output group #1, chip #1:DO1-DO7 digital output group #2, chip #2:DO8-DO15 digital output group #3, chip #3:DO16-DO22 digital output group #4, chip #4:DO23-DO30 digital output group #5, chip #5:DO31-DO38 digital output group #6, chip #6:DO39-DO45 digital output group #7, chip #7:DO46-DO53 digital output group #8, chip #8:DO54-DO60				
Returns the actual interrupt state of the digital output group DOGRP as decimal number and as hexadecimal number. InterruptStatusDec, InterruptStatusHex Bit 0:Internal under voltage detected (0=OK,1=FAULT) Bit 1:VA under voltage detected (<2.3V) (0=OK,1=FAULT) Bit 2:VDD not good detected (<17V) (0=OK,1=FAULT) Bit 3:VDD warning detected (<12V) (0=OK,1=FAULT) Bit 4:VDD under voltage detected (<8V) (0=OK,1=FAULT) Bit 5:Thermal shutdown (0=OK,1=FAULT) Bit 6:Synchronisation error detected (0=OK,1=FAULT) Bit 7:Watchdog error detected (0=OK,1=FAULT)				
<b>DIGITAL OUTPUTS: THERMAL OVERLOAD DETECTION</b>				
GET DIGITAL OUTPUTS THERMAL OVERLOAD DETECTION	ASCII READ COMMAND	#GDOTOS<CR> Result: #GDOTOS:<StatusDOSADec>,<StatusDOSBDec>, <StatusDOSAHex>02.03.24<StatusDOSBHex><CR>	ASCII	
	TX	#255,GDOTOS<CR>		
	RX	#255,GDOTOS:0,0,0x0,0x0<CR>		
		Actual thermal overload detection status of digital outputs: DO1-DO32:0000.0000.0000.0000.0000.0000.0000.0000 DO33-DO60:0000.0000.0000.0000.0000.0000.0000.0000		
Returns the actual state of the thermal overload detection for all digital outputs as decimal number and as hexadecimal number. StatusDOSADec, StatusDOSAHex The current mode for open wire diagnostic while OFF of the digital outputs: Bit 0: Thermal overload detected on DO1 (=0:NO, =1:YES) Bit 1: Thermal overload detected on DO2 (=0:NO, =1:YES) ... Bit 30: Thermal overload detected on DO31 (=0:NO, =1:YES) Bit 31: Thermal overload detected on DO32 (=0:NO, =1:YES)				

StatusDOSBDec, StatusDOSBHex

The current mode for open wire diagnostic while OFF of the digital outputs:

Bit 0: Thermal overload detected on DO33 (=0:NO, =1:YES)

Bit 1: Thermal overload detected on DO34 (=0:NO, =1:YES)

...

Bit 26: Thermal overload detected on DO59 (=0:NO, =1:YES)

Bit 27: Thermal overload detected on DO60 (=0:NO, =1:YES)

GET DIGITAL OUTPUT DOx THERMAL OVERLOAD DETECTION	ASCII READ COMMAND DONR	#GDOTO<DONR> <CR> Result: #GDOTO<DONR>:<StatusDOxDec>,<StatusDOxHex> <CR> 2	ASCII	
	TX	#255,GDOTO2<CR>		
	RX	#255,GDOTO2:0,0x0<CR>		
		Thermal overload detected on DO2:0=NO		

<DONR>: 1=DO1..60=DO60

Returns the actual state of the thermal overload detection for digital output DOx as decimal number and as hexadecimal number.

StatusDOxDec, StatusDOxHex

The current detection state for digital output DOx:

=0: digital output is OK

=1: FAULT detected on digital output

#### DIGITAL OUTPUTS: CURRENT LIMIT DETECTION

GET DIGITAL OUTPUTS CURRENT LIMIT DETECTION	ASCII READ COMMAND	#GDOCLS<CR> Result: #GDOCLS:<StatusDOSADec>,<StatusDOSBDec>, <StatusDOSAHex>,<StatusDOSBHex><CR>	ASCII	
	TX	#255,GDOCLS<CR>		
	RX	#255,GDOCLS:0,0,0x0,0x0<CR>		
		Actual current limit detection status of digital outputs:		
		DO1-DO32:0000.0000.0000.0000.0000.0000.0000.0000		
		DO33-DO60:0000.0000.0000.0000.0000.0000.0000.0000		

Returns the actual state of the current limit detection for all digital outputs as decimal number and as hexadecimal number.

StatusDOSADec, StatusDOSAHex

The current detection state of the digital outputs:

Bit 0: Current limit reached for DO1 (=0:NO, =1:YES)

Bit 1: Current limit reached for DO2 (=0:NO, =1:YES)

...

Bit 30: Current limit reached for DO31 (=0:NO, =1:YES)

Bit 31: Current limit reached for DO32 (=0:NO, =1:YES)

StatusDOSBDec, StatusDOSBHex

The current detection state of the digital outputs:

Bit 0: Current limit reached for DO33 (=0:NO, =1:YES)

Bit 1: Current limit reached for DO34 (=0:NO, =1:YES)

...

Bit 26: Current limit reached for DO59 (=0:NO, =1:YES)

Bit 27: Current limit reached for DO60 (=0:NO, =1:YES)

GET DIGITAL OUTPUT DOx CURRENT LIMIT DETECTION	ASCII READ COMMAND DONR	#GDOCL<DONR> <CR> Result: #GDOCL<DONR>:<StatusDOxDec>,<StatusDOxHex> <CR> 2	ASCII	
--	----------------------------------	--	-------	--

	TX	#255,GDOCL2<CR>		
	RX	#255,GDOCL2:0,0x0<CR>		
		Actual current limit detection status of DO2:0=OK		
<DONR>: 1=DO1..60=DO60				
Returns the actual state of the current limit detection for digital output DOx as decimal number and as hexadecimal number. StatusDOxDec, StatusDOxHex The current detection state for digital output DOx: =0: digital output is OK =1: FAULT detected on digital output				
<b>DIGITAL OUTPUTS: OPEN WIRE DETECTION WHILE ON</b>				
GET DIGITAL OUTPUTS OPEN WIRE FAULT DETECTION WHILE ON	ASCII READ COMMAND	#GDOOWFONS<CR> Result: #GDOOWFONS:<StatusDOSADec>,<StatusDOSBDec>, <StatusDOSAHex>,<StatusDOSBHex><CR>	ASCII	
	TX	#255,GDOOWFONS<CR>		
	RX	#255,GDOOWFONS:3221225472,8191,0xC0000000,0x1FFF<CR>		
		Actual open wire fault detection status while ON of digital outputs:		
		DO1-DO32:1100.0000.0000.0000.0000.0000.0000.0000		
		DO33-DO60:0000.0000.0000.0000.1111.1111.1111		
Returns the actual state of the open wire fault detection while ON for all digital outputs as decimal number and as hexadecimal number. StatusDOSADec, StatusDOSAHex The current detection state of the digital outputs: Bit 0: State of DO1 (=0:OK, =1:FAULT) Bit 1: State of DO2 (=0:OK, =1:FAULT) ... Bit 30: State of DO31 (=0:OK, =1:FAULT) Bit 31: State of DO32 (=0:OK, =1:FAULT)				
StatusDOSBDec, StatusDOSBHex The current detection state of the digital outputs: Bit 0: State of DO33 (=0:OK, =1:FAULT) Bit 1: State of DO34 (=0:OK, =1:FAULT) ... Bit 26: State of DO59 (=0:OK, =1:FAULT) Bit 27: State of DO60 (=0:OK, =1:FAULT)				
GET DIGITAL OUTPUT DOx OPEN WIRE FAULT DETECTION WHILE ON	ASCII READ COMMAND	#GDOOWFON<DONR> <CR> Result: #GDOOWFON<DONR>:<StatusDOxDec>,<StatusDOxHex> <CR>	ASCII	
	DONR	2		
	TX	#255,GDOOWFON2<CR>		
	RX	#255,GDOOWFON2:0,0x0<CR>		
		Actual open wire fault detection status while ON of DO2:0=OK		
<DONR>: 1=DO1..60=DO60				
Returns the actual state of the open wire fault detection while ON for digital output DOx as decimal number and as hexadecimal number. StatusDOxDec, StatusDOxHex The current detection state for digital output DOx: =0: digital output is OK =1: FAULT detected on digital output				
<b>DIGITAL OUTPUTS: OPEN WIRE DETECTION WHILE OFF</b>				

GET DIGITAL OUTPUTS OPEN WIRE FAULT DETECTION WHILE OFF	ASCII READ COMMAND	#GDOOWFOFFS<CR> Result: #GDOOWFOFFS:<StatusDOSADec>,<StatusDOSBDec>, <StatusDOSAHex>,<StatusDOSBHex><CR>	ASCII	
	TX	#255,GDOOWFOFFS<CR>		
	RX	#255,GDOOWFOFFS:0,0,0x0,0x0<CR>		
		Actual open wire fault detection status while OFF of digital outputs:		
		DO1-DO32:0000.0000.0000.0000.0000.0000.0000.0000		
		DO33-DO60:0000.0000.0000.0000.0000.0000.0000.0000		
Returns the actual state of the open wire fault detection while OFF for all digital outputs as decimal number and as hexadecimal number. StatusDOSADec, StatusDOSAHex The current detection state of the digital outputs: Bit 0: State of DO1 (=0:OK, =1:FAULT) Bit 1: State of DO2 (=0:OK, =1:FAULT) ... Bit 30: State of DO31 (=0:OK, =1:FAULT) Bit 31: State of DO32 (=0:OK, =1:FAULT)				
StatusDOSBDec, StatusDOSBHex The current detection state of the digital outputs: Bit 0: State of DO33 (=0:OK, =1:FAULT) Bit 1: State of DO34 (=0:OK, =1:FAULT) ... Bit 26: State of DO59 (=0:OK, =1:FAULT) Bit 27: State of DO60 (=0:OK, =1:FAULT)				
GET DIGITAL OUTPUT DOx OPEN WIRE FAULT DETECTION WHILE OFF	ASCII READ COMMAND	#GDOOWFOFF<DONR><CR> Result: #GDOOWFOFF<DONR>:<StatusDOxDec>,<StatusDOxHex><CR>	ASCII	
	DONR	2		
	TX	#255,GDOOWFOFF2<CR>		
	RX	#255,GDOOWFOFF2:0,0x0<CR>		
		Actual open wire fault detection status while OFF of DO2:0=OK		
<DONR>: 1=DO1..60=DO60				
Returns the actual state of the open wire fault detection while OFF for digital output DOx as decimal number and as hexadecimal number. StatusDOxDec, StatusDOxHex The current detection state for digital output DOx: =0: digital output is OK =1: FAULT detected on digital output				
<b>DIGITAL OUTPUTS: SHORTCUT TO VDD DETECTION WHILE OFF</b>				
GET DIGITAL OUTPUTS SHORTCUT TO VDD FAULT DETECTION WHILE OFF	ASCII READ COMMAND	#GDOSVDDS<CR> Result: #GDOSVDDS:<StatusDOSADec>,<StatusDOSBDec>, <StatusDOSAHex>,<StatusDOSBHex><CR>	ASCII	
	TX	#255,GDOSVDDS<CR>		
	RX	#255,GDOSVDDS:0,0,0x0,0x0<CR>		
		Actual shortcut to VDD fault detection status while OFF of digital outputs:		
		DO1-DO32:0000.0000.0000.0000.0000.0000.0000.0000		
		DO33-DO60:0000.0000.0000.0000.0000.0000.0000.0000		

Returns the actual state of the shortcut to VDD fault detection while OFF for all digital outputs as decimal number and as hexadecimal number.

StatusDOSADec, StatusDOSAHex

The current detection state of the digital outputs:

Bit 0: State of DO1 (=0:OK, =1:FAULT)

Bit 1: State of DO2 (=0:OK, =1:FAULT)

...

Bit 30: State of DO31 (=0:OK, =1:FAULT)

Bit 31: State of DO32 (=0:OK, =1:FAULT)

StatusDOSBDec, StatusDOSBHex

The current detection state of the digital outputs:

Bit 0: State of DO33 (=0:OK, =1:FAULT)

Bit 1: State of DO34 (=0:OK, =1:FAULT)

...

Bit 26: State of DO59 (=0:OK, =1:FAULT)

Bit 27: State of DO60 (=0:OK, =1:FAULT)

GET DIGITAL OUTPUT DOx SHORTCUT TO VDD FAULT DETECTION WHILE OFF	ASCII READ COMMAND DONR	#GDOSVDD<DONR> <CR> Result: #GDOSVDD<DONR>:<StatusDOxDec>,<StatusDOxHex> <CR> 2	ASCII	
	TX	#255,GDOSVDD2<CR>		
	RX	#255,GDOSVDD2:0,0x0<CR>		
		Actual shortcut to VDD fault detection status while OFF of DO2:0=OK		

<DONR>: 1=DO1..60=DO60

Returns the actual state of the shortcut to VDD fault detection while OFF for digital output DOx as decimal number and as hexadecimal number.

StatusDOxDec, StatusDOxHex

The current detection state for digital output DOx:

=0: digital output is OK

=1: FAULT detected on digital output

GET DIGITAL OUTPUTS CHIPSET NAME	ASCII READ COMMAND	#GDOCHIPSET <CR> Result: #GDOCHIPSET:<ChipSetName>	ASCII	
	TX	#255,GDOCHIPSET <CR>		
	RX	#255,GDOCHIPSET:MAX14915 <CR>		
		Actual name of chipset for digital outputs:MAX14915		

Returns the actual name of the chipset of the digital outputs

Command NAME	ASCII command type	ASCII command structure	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
<b>ASCII COMMANDS</b>						
<b>DIGITAL OUTPUTS</b>						
<b>INITIAL &amp; WATCHDOG STATE FOR DIGITAL OUTPUTS</b>						
SET INITIAL & WATCHDOG STATE FOR DIGITAL OUTPUTS	ASCII WRITE COMMAND	#SCDOS:<OutAllDOSA>,<OutAllDOSB><CR> Result: #OK<CR>			ASCII	YES
	DO1	1:ON				
	DO2	1:ON				
	DO3	1:ON				
	DO4	1:ON				
	DO5	1:ON				
	DO6	1:ON				
	DO7	1:ON				
	DO8	1:ON				
	DO9	1:ON				
	DO10	1:ON				
	DO11	1:ON				
	DO12	1:ON				
	DO13	1:ON				
	DO14	1:ON				
	DO15	1:ON				
	DO16	1:ON				
	DO17	1:ON				
	DO18	1:ON				
	DO19	1:ON				
	DO20	1:ON				
	DO21	1:ON				
	DO22	1:ON				
	DO23	1:ON				
	DO24	1:ON				
	DO25	1:ON				
	DO26	1:ON				
	DO27	1:ON				
	DO28	1:ON				
	DO29	1:ON				
	DO30	1:ON				
	DO31	1:ON				
	DO32	1:ON				
	OutAllDOSA	0xFFFFFFFF	4294967295			



	DO33	1:ON		
	DO34	1:ON		
	DO35	1:ON		
	DO36	1:ON		
	DO37	1:ON		
	DO38	1:ON		
	DO39	1:ON		
	DO40	1:ON		
	DO41	1:ON		
	DO42	1:ON		
	DO43	1:ON		
	DO44	1:ON		
	DO45	1:ON		
	DO46	1:ON		
	DO47	1:ON		
	DO48	1:ON		
	DO49	1:ON		
	DO50	1:ON		
	DO51	1:ON		
	DO52	1:ON		
	DO53	1:ON		
	DO54	1:ON		
	DO55	1:ON		
	DO56	1:ON		
	DO57	1:ON		
	DO58	1:ON		
	DO59	1:ON		
	DO60	1:ON		
	OutAllDOSB	0x0FFFFFFF	268435455	
	TX	#255,SCDOS:0xFFFFFFFF,0xFFFFFFFF <CR>		
	RX	#255,OK <CR>		

This command sets all digital outputs to a new state for controller restart and watchdog function. The state is saved in FRAM.  
This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured.

#### OutAllDOSA

The new state for the digital outputs:

Bit 0: New state of DO1 (=0:OFF, =1:ON)

Bit 1: New state of DO2 (=0:OFF, =1:ON)

...

Bit 30: New state of DO31 (=0:OFF, =1:ON)

Bit 31: New state of DO32 (=0:OFF, =1:ON)

## OutAllDOSB

The new state for the digital outputs:

Bit 0: New state of DO33 (=0:OFF, =1:ON)

Bit 1: New state of DO34 (=0:OFF, =1:ON)

...

Bit 26: New state of DO59 (=0:OFF, =1:ON)

Bit 27: New state of DO60 (=0:OFF, =1:ON)

GET INITIAL & WATCHDOG STATE FOR DIGITAL OUTPUTS	ASCII READ COMMAND	#GCDOS<CR> Result: #GCDOS:<DOSADec>,<DOSBDec>,<DOSAHex>,<DOSBHex><CR>	ASCII	
	TX	#255,GCDOS<CR>		
	RX	#255,GCDOS:4294967295,4294967295,0xFFFFFFFF,0xFFFFFFFF<CR>		
		Init & watchdog configuration for digital outputs:		
		DO1-DO32:1111.1111.1111.1111.1111.1111.1111.1111		
		DO33-DO60:1111.1111.1111.1111.1111.1111.1111		

Returns the actual initial and watchdog state of the digital outputs as decimal number and as hexadecimal number. This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured

## DOSADec, DOSAHex

The current state of the digital outputs:

Bit 0: State of DO1 (=0:OFF, =1:ON)

Bit 1: State of DO2 (=0:OFF, =1:ON)

...

Bit 30: State of DO31 (=0:OFF, =1:ON)

Bit 31: State of DO32 (=0:OFF, =1:ON)

## DOSBDec, DOSBHex

The current state of the digital outputs:

Bit 0: State of DO33 (=0:OFF, =1:ON)

Bit 1: State of DO34 (=0:OFF, =1:ON)

...

Bit 26: State of DO59 (=0:OFF, =1:ON)

Bit 27: State of DO60 (=0:OFF, =1:ON)

## DIGITAL OUTPUTS: INIT &amp; WATCHDOG ENABLE OPEN WIRE DETECTION WHILE ON

SET CONFIG DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE ON	ASCII WRITE COMMAND	#SCDOEOWDONS:<OpenWireDOSA>,<OpenWireDOSB><CR> Result: #OK<CR>	ASCII	YES
	DO1	1:ENABLE		
	DO2	1:ENABLE		
	DO3	1:ENABLE		
	DO4	1:ENABLE		
	DO5	1:ENABLE		
	DO6	1:ENABLE		
	DO7	1:ENABLE		
	DO8	1:ENABLE		
	DO9	1:ENABLE		
	DO10	1:ENABLE		
	DO11	1:ENABLE		

	DO12	1:ENABLE		
	DO13	1:ENABLE		
	DO14	1:ENABLE		
	DO15	1:ENABLE		
	DO16	1:ENABLE		
	DO17	1:ENABLE		
	DO18	1:ENABLE		
	DO19	1:ENABLE		
	DO20	1:ENABLE		
	DO21	1:ENABLE		
	DO22	1:ENABLE		
	DO23	1:ENABLE		
	DO24	1:ENABLE		
	DO25	1:ENABLE		
	DO26	1:ENABLE		
	DO27	1:ENABLE		
	DO28	1:ENABLE		
	DO29	1:ENABLE		
	DO30	1:ENABLE		
	DO31	1:ENABLE		
	DO32	1:ENABLE		
	OpenWireDOSA	0xFFFFFFFF	4294967295	
	DO33	1:ENABLE		
	DO34	1:ENABLE		
	DO35	1:ENABLE		
	DO36	1:ENABLE		
	DO37	1:ENABLE		
	DO38	1:ENABLE		
	DO39	1:ENABLE		
	DO40	1:ENABLE		
	DO41	1:ENABLE		
	DO42	1:ENABLE		
	DO43	1:ENABLE		
	DO44	1:ENABLE		
	DO45	1:ENABLE		
	DO46	1:ENABLE		
	DO47	1:ENABLE		
	DO48	1:ENABLE		
	DO49	1:ENABLE		
	DO50	1:ENABLE		
	DO51	1:ENABLE		
	DO52	1:ENABLE		
	DO53	1:ENABLE		

	DO54	1:ENABLE		
	DO55	1:ENABLE		
	DO56	1:ENABLE		
	DO57	1:ENABLE		
	DO58	1:ENABLE		
	DO59	1:ENABLE		
	DO60	1:ENABLE		
	OpenWireDOSB	0x0FFFFFFF	268435455	
	TX	#255,SCDOEOWDONS:0xFFFFFFFF,0xFFFFFFFF <CR>		
	RX	#255,OK <CR>		

Sets the open wire mode for all digital outputs to the new mode OpenWireDOS for init & watchdog. This enables the diagnostic of open wire while the digital output is ON.  
This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured  
The new state for all digital outputs

OpenWireDOSA

Bit 0: New mode for DO1 (=0:DISABLED, =1:ENABLED)

Bit 1: New mode for DO2 (=0:DISABLED, =1:ENABLED)

...

Bit 30: New mode for DO31 (=0:DISABLED, =1:ENABLED)

Bit 31: New mode for DO32 (=0:DISABLED, =1:ENABLED)

OpenWireDOSB

Bit 0: New mode for DO33 (=0:DISABLED, =1:ENABLED)

Bit 1: New mode for DO34 (=0:DISABLED, =1:ENABLED)

...

Bit 26: New mode for DO59 (=0:DISABLED, =1:ENABLED)

Bit 27: New mode for DO60 (=0:DISABLED, =1:ENABLED)

GET CONFIG DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE ON	ASCII READ COMMAND	#GCDOEOWDONS <CR> Result: #GCDOEOWDONS:<OpenWireDOSADec>,<OpenWireDOSBDec>, <OpenWireDOSAHex>,<OpenWireDOSBHex> <CR>	ASCII	
	TX	#255,GCDOEOWDONS <CR>		
	RX	#255,GCDOEOWDONS:4294967295,4294967295,0xFFFFFFFF,0xFFFFFFFF <CR>		
		Init & watchdog configuration for open wire diagnostic while ON of digital outputs:		
		DO1-DO32:1111.1111.1111.1111.1111.1111.1111.1111		
		DO33-DO60:1111.1111.1111.1111.1111.1111.1111		

Returns the actual mode for open wire diagnostic while digital output is ON as decimal number and as hexadecimal number.  
This values are used after power on of the module an after a watchdog event.

OpenWireDOSADec, OpenWireDOSAHex

The current mode for open wire diagnostic while ON of the digital outputs:

Bit 0: Open wire diagnostic mode of DO1 (=0:DISABLED, =1:ENABLED)

Bit 1: Open wire diagnostic mode of DO2 (=0:DISABLED, =1:ENABLED)

...

Bit 30: Open wire diagnostic mode of DO31 (=0:DISABLED, =1:ENABLED)

Bit 31: Open wire diagnostic mode of DO32 (=0:DISABLED, =1:ENABLED)

OpenWireDOSBDec, OpenWireDOSBHex

The current mode for open wire diagnostic while ON of the digital outputs:

Bit 0: Open wire diagnostic mode of DO33 (=0:DISABLED, =1:ENABLED)

Bit 1: Open wire diagnostic mode of DO34 (=0:DISABLED, =1:ENABLED)

...

Bit 26: Open wire diagnostic mode of DO59 (=0:DISABLED, =1:ENABLED)

Bit 27: Open wire diagnostic mode of DO60 (=0:DISABLED, =1:ENABLED)

**DIGITAL OUTPUTS: INIT & WATCHDOG ENABLE OPEN WIRE DETECTION WHILE OFF**

SET CONFIG DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE OFF	ASCII WRITE COMMAND	#SCDOEOWDOFFS:<OpenWireDOSA>,<OpenWireDOSB> <CR> Result: #OK<CR>	ASCII	YES
	DO1	1:ENABLE		
	DO2	1:ENABLE		
	DO3	1:ENABLE		
	DO4	1:ENABLE		
	DO5	1:ENABLE		
	DO6	1:ENABLE		
	DO7	1:ENABLE		
	DO8	1:ENABLE		
	DO9	1:ENABLE		
	DO10	1:ENABLE		
	DO11	1:ENABLE		
	DO12	1:ENABLE		
	DO13	1:ENABLE		
	DO14	1:ENABLE		
	DO15	1:ENABLE		
	DO16	1:ENABLE		
	DO17	1:ENABLE		
	DO18	1:ENABLE		
	DO19	1:ENABLE		
	DO20	1:ENABLE		
	DO21	1:ENABLE		
	DO22	1:ENABLE		
	DO23	1:ENABLE		
	DO24	1:ENABLE		
	DO25	1:ENABLE		
	DO26	1:ENABLE		
	DO27	1:ENABLE		
	DO28	1:ENABLE		
	DO29	1:ENABLE		
	DO30	1:ENABLE		
	DO31	1:ENABLE		
	DO32	1:ENABLE		
	OpenWireDOSA	0xFFFFFFFF   4294967295		
	DO33	1:ENABLE		

	DO34	1:ENABLE		
	DO35	1:ENABLE		
	DO36	1:ENABLE		
	DO37	1:ENABLE		
	DO38	1:ENABLE		
	DO39	1:ENABLE		
	DO40	1:ENABLE		
	DO41	1:ENABLE		
	DO42	1:ENABLE		
	DO43	1:ENABLE		
	DO44	1:ENABLE		
	DO45	1:ENABLE		
	DO46	1:ENABLE		
	DO47	1:ENABLE		
	DO48	1:ENABLE		
	DO49	1:ENABLE		
	DO50	1:ENABLE		
	DO51	1:ENABLE		
	DO52	1:ENABLE		
	DO53	1:ENABLE		
	DO54	1:ENABLE		
	DO55	1:ENABLE		
	DO56	1:ENABLE		
	DO57	1:ENABLE		
	DO58	1:ENABLE		
	DO59	1:ENABLE		
	DO60	1:ENABLE		
	OpenWireDOSB	0x0FFFFFFF	268435455	
	TX	#255,SCDOEOWDOFFS:0xFFFFFFFF,0xFFFFFFFF<CR>		
	RX	#255,OK<CR>		

Sets the open wire mode for all digital outputs to the new mode OpenWireDOS for init & watchdog. This enables the diagnostic of open wire while the digital output is OFF.

This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured

The new state for all digital outputs

OpenWireDOSA

Bit 0: New mode for DO1 (=0:DISABLED, =1:ENABLED)

Bit 1: New mode for DO2 (=0:DISABLED, =1:ENABLED)

...

Bit 30: New mode for DO31 (=0:DISABLED, =1:ENABLED)

Bit 31: New mode for DO32 (=0:DISABLED, =1:ENABLED)

OpenWireDOSB

Bit 0: New mode for DO33 (=0:DISABLED, =1:ENABLED)

Bit 1: New mode for DO34 (=0:DISABLED, =1:ENABLED)

...

Bit 26: New mode for DO59 (=0:DISABLED, =1:ENABLED)

Bit 27: New mode for DO60 (=0:DISABLED, =1:ENABLED)

GET CONFIG DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE OFF	ASCII READ COMMAND	#GCDOEOWDOFFS<CR> Result: #GCDOEOWDOFFS:<OpenWireDOSADec>,<OpenWireDOSBDec>, <OpenWireDOSAHex>,<OpenWireDOSBHex><CR>	ASCII	
	TX	#255,GCDOEOWDOFFS<CR>		
	RX	#255,GCDOEOWDOFFS:4294967295,4294967295,0xFFFFFFFF,0xFFFFFFFF<CR>		
		Init & watchdog configuration for open wire diagnostic while OFF of digital outputs:		
		DO1-DO32:1111.1111.1111.1111.1111.1111.1111.1111		
		DO33-DO60:1111.1111.1111.1111.1111.1111.1111		

Returns the actual mode for open wire diagnostic while digital output is OFF as decimal number and as hexadecimal number.  
This values are used after power on of the module an after a watchdog event.

OpenWireDOSADec, OpenWireDOSAHex

The current mode for open wire diagnostic while OFF of the digital outputs:

Bit 0: Open wire diagnostic mode of DO1 (=0:DISABLED, =1:ENABLED)

Bit 1: Open wire diagnostic mode of DO2 (=0:DISABLED, =1:ENABLED)

...

Bit 30: Open wire diagnostic mode of DO31 (=0:DISABLED, =1:ENABLED)

Bit 31: Open wire diagnostic mode of DO32 (=0:DISABLED, =1:ENABLED)

OpenWireDOSBDec, OpenWireDOSBHex

The current mode for open wire diagnostic while OFF of the digital outputs:

Bit 0: Open wire diagnostic mode of DO33 (=0:DISABLED, =1:ENABLED)

Bit 1: Open wire diagnostic mode of DO34 (=0:DISABLED, =1:ENABLED)

...

Bit 26: Open wire diagnostic mode of DO59 (=0:DISABLED, =1:ENABLED)

Bit 27: Open wire diagnostic mode of DO60 (=0:DISABLED, =1:ENABLED)

#### DIGITAL OUTPUTS: INIT & WATCHDOG ENABLE SHORTCUT TO VDD DETECTION WHILE OFF

SET CONFIG DIGITAL OUTPUTS ENABLE SHORTCUT TO VDD DETECTION WHILE OFF	ASCII WRITE COMMAND	#SCDOESVDDS:<ShortCutDOSA>,<ShortCutDOSB><CR> Result: #OK<CR>	ASCII	YES
	DO1	1:ENABLE		
	DO2	1:ENABLE		
	DO3	1:ENABLE		
	DO4	1:ENABLE		
	DO5	1:ENABLE		
	DO6	1:ENABLE		
	DO7	1:ENABLE		
	DO8	1:ENABLE		
	DO9	1:ENABLE		
	DO10	1:ENABLE		
	DO11	1:ENABLE		
	DO12	1:ENABLE		
	DO13	1:ENABLE		
	DO14	1:ENABLE		
	DO15	1:ENABLE		
	DO16	1:ENABLE		
	DO17	1:ENABLE		
	DO18	1:ENABLE		

	DO19	1:ENABLE		
	DO20	1:ENABLE		
	DO21	1:ENABLE		
	DO22	1:ENABLE		
	DO23	1:ENABLE		
	DO24	1:ENABLE		
	DO25	1:ENABLE		
	DO26	1:ENABLE		
	DO27	1:ENABLE		
	DO28	1:ENABLE		
	DO29	1:ENABLE		
	DO30	1:ENABLE		
	DO31	1:ENABLE		
	DO32	1:ENABLE		
	ShortCutDOSA	0xFFFFFFFF	4294967295	
	DO33	1:ENABLE		
	DO34	1:ENABLE		
	DO35	1:ENABLE		
	DO36	1:ENABLE		
	DO37	1:ENABLE		
	DO38	1:ENABLE		
	DO39	1:ENABLE		
	DO40	1:ENABLE		
	DO41	1:ENABLE		
	DO42	1:ENABLE		
	DO43	1:ENABLE		
	DO44	1:ENABLE		
	DO45	1:ENABLE		
	DO46	1:ENABLE		
	DO47	1:ENABLE		
	DO48	1:ENABLE		
	DO49	1:ENABLE		
	DO50	1:ENABLE		
	DO51	1:ENABLE		
	DO52	1:ENABLE		
	DO53	1:ENABLE		
	DO54	1:ENABLE		
	DO55	1:ENABLE		
	DO56	1:ENABLE		
	DO57	1:ENABLE		
	DO58	1:ENABLE		
	DO59	1:ENABLE		
	DO60	1:ENABLE		
	ShortCutDOSB	0x0FFFFFFF	268435455	
	TX	#255,SCDOESVDDS:0xFFFFFFFF,0xFFFFFFFF<CR>		



	RX	#255,OK<CR>		
Sets the shortcut to VDD detection mode for all digital outputs to the new mode ShortCutDOS for init & watchdog. This enables the diagnostic of shortcut to VDD while the digital output is OFF. This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured				
ShortCutDOSA Bit 0: New mode for DO1 (=0:DISABLED, =1:ENABLED) Bit 1: New mode for DO2 (=0:DISABLED, =1:ENABLED) ... Bit 30: New mode for DO31 (=0:DISABLED, =1:ENABLED) Bit 31: New mode for DO32 (=0:DISABLED, =1:ENABLED)				
ShortCutDOSB Bit 0: New mode for DO33 (=0:DISABLED, =1:ENABLED) Bit 1: New mode for DO34 (=0:DISABLED, =1:ENABLED) ... Bit 26: New mode for DO59 (=0:DISABLED, =1:ENABLED) Bit 27: New mode for DO60 (=0:DISABLED, =1:ENABLED)				
GET CONFIG DIGITAL OUTPUTS ENABLE SHORT CUT TO VDD DETECTION WHILE OFF	ASCII READ COMMAND	#GCDOESVDDS<CR> Result: #GCDOESDDS:<ShortCutDOSADec>,<ShortCutDOSBDec>, <ShortCutDOSAHex>,<ShortCutDOSBHex><CR>	ASCII	
	TX	#255,GCDOESVDDS<CR>		
	RX	#255,GCDOESVDDS:4294967295,4294967295,0xFFFFFFFF,0xFFFFFFFF<CR>		
		Init & watchdog configuration for shortcut to VDD diagnostic while OFF of digital outputs:		
		DO1-DO32:1111.1111.1111.1111.1111.1111.1111.1111		
		DO33-DO60:1111.1111.1111.1111.1111.1111.1111.1111		
Returns the actual mode for shortcut to VDD diagnostic while digital output is OFF as decimal number and as hexadecimal number. This values are used after power on of the module an after a watchdog event.				
ShortCutDOSADec, ShortCutDOSAHex The current mode for shortcut to VDD diagnostic while OFF of the digital outputs: Bit 0: Open wire diagnostic mode of DO1 (=0:DISABLED, =1:ENABLED) Bit 1: Open wire diagnostic mode of DO2 (=0:DISABLED, =1:ENABLED) ... Bit 30: Open wire diagnostic mode of DO31 (=0:DISABLED, =1:ENABLED) Bit 31: Open wire diagnostic mode of DO32 (=0:DISABLED, =1:ENABLED)				
ShortCutDOSBDec, ShortCutDOSBHex The current mode for shortcut to VDD diagnostic while OFF of the digital outputs: Bit 0: Open wire diagnostic mode of DO33 (=0:DISABLED, =1:ENABLED) Bit 1: Open wire diagnostic mode of DO34 (=0:DISABLED, =1:ENABLED) ... Bit 26: Open wire diagnostic mode of DO59 (=0:DISABLED, =1:ENABLED) Bit 27: Open wire diagnostic mode of DO60 (=0:DISABLED, =1:ENABLED)				

sessions

Register NAME Command NAME	MODBUS Register ASCII Command	Register VALUE ASCII Command	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE	
<b>STATUS DIGITAL INPUTS</b>							
DI1	1x00001 2x00001 I:0	????			BIT R/O		
		Actual state of DI1:0=OFF					
Current state of the digital input DIx =0:DI is OFF, =1:DI is ON							
DI2	1x00002 2x00002 I:1	????			BIT R/O		
		Actual state of DI2:0=OFF					
DI3	1x00003 2x00003 I:2	????			BIT R/O		
		Actual state of DI3:0=OFF					
DI4	1x00004 2x00004 I:3	????			BIT R/O		
		Actual state of DI4:0=OFF					
DI5	1x00005 2x00005 I:4	????			BIT R/O		
		Actual state of DI5:0=OFF					
DI6	1x00006 2x00006 I:5	????			BIT R/O		
		Actual state of DI6:0=OFF					
DI7	1x00007 2x00007 I:6	????			BIT R/O		
		Actual state of DI7:0=OFF					
DI8	1x00008 2x00008 I:7	????			BIT R/O		
		Actual state of DI8:0=OFF					
DI9	1x00009 2x00009 I:8	????			BIT R/O		
		Actual state of DI9:0=OFF					
DI10	1x00010 2x00010 I:9	????			BIT R/O		

		Actual state of DI10:0=OFF				
DI11	1x00011 2x00011 I:10	????			BIT R/O	
		Actual state of DI11:0=OFF				
DI12	1x00012 2x00012 I:11	????			BIT R/O	
		Actual state of DI12:0=OFF				
DI13	1x00013 2x00013 I:12	????			BIT R/O	
		Actual state of DI13:0=OFF				
DI14	1x00014 2x00014 I:13	????			BIT R/O	
		Actual state of DI14:0=OFF				
DI15	1x00015 2x00015 I:14	????			BIT R/O	
		Actual state of DI15:0=OFF				
DI16	1x00016 2x00016 I:15	????			BIT R/O	
		Actual state of DI16:0=OFF				
DI17	1x00017 2x00017 I:16	????			BIT R/O	
		Actual state of DI17:0=OFF				
DI18	1x00018 2x00018 I:17	????			BIT R/O	
		Actual state of DI18:0=OFF				
DI19	1x00019 2x00019 I:18	????			BIT R/O	
		Actual state of DI19:0=OFF				
DI20	1x00020 2x00020 I:19	????			BIT R/O	
		Actual state of DI20:0=OFF				
DI21	1x00021 2x00021 I:20	????			BIT R/O	
		Actual state of DI21:0=OFF				

DI22	1x00022 2x00022 I:21	????			BIT R/O	
		Actual state of DI22:0=OFF				
DI23	1x00023 2x00023 I:22	????			BIT R/O	
		Actual state of DI23:0=OFF				
DI24	1x00024 2x00024 I:23	????			BIT R/O	
		Actual state of DI24:0=OFF				
DI25	1x00025 2x00025 I:24	????			BIT R/O	
		Actual state of DI25:0=OFF				
DI26	1x00026 2x00026 I:25	????			BIT R/O	
		Actual state of DI26:0=OFF				
DI27	1x00027 2x00027 I:26	????			BIT R/O	
		Actual state of DI27:0=OFF				
DI28	1x00028 2x00028 I:27	????			BIT R/O	
		Actual state of DI28:0=OFF				
DI29	1x00029 2x00029 I:28	????			BIT R/O	
		Actual state of DI29:0=OFF				
DI30	1x00030 2x00030 I:29	????			BIT R/O	
		Actual state of DI30:0=OFF				
DI31	1x00031 2x00031 I:30	????			BIT R/O	
		Actual state of DI31:0=OFF				
DI32	1x00032 2x00032 I:31	????			BIT R/O	
		Actual state of DI32:0=OFF				

DI33	1x00033 2x00033 I:32	????			BIT R/O	
		Actual state of DI33:0=OFF				
DI34	1x00034 2x00034 I:33	????			BIT R/O	
		Actual state of DI34:0=OFF				
DI35	1x00035 2x00035 I:34	????			BIT R/O	
		Actual state of DI35:0=OFF				
DI36	1x00036 2x00036 I:35	????			BIT R/O	
		Actual state of DI36:0=OFF				
DI37	1x00037 2x00037 I:36	????			BIT R/O	
		Actual state of DI37:0=OFF				
DI38	1x00038 2x00038 I:37	????			BIT R/O	
		Actual state of DI38:0=OFF				
DI39	1x00039 2x00039 I:38	????			BIT R/O	
		Actual state of DI39:0=OFF				
DI40	1x00040 2x00040 I:39	????			BIT R/O	
		Actual state of DI40:0=OFF				
DI41	1x00041 2x00041 I:40	????			BIT R/O	
		Actual state of DI41:0=OFF				
DI42	1x00042 2x00042 I:41	????			BIT R/O	
		Actual state of DI42:0=OFF				
DI43	1x00043 2x00043 I:42	????			BIT R/O	
		Actual state of DI43:0=OFF				

DI44	1x00044 2x00044 I:43	????			BIT R/O	
		Actual state of DI44:0=OFF				
DI45	1x00045 2x00045 I:44	????			BIT R/O	
		Actual state of DI45:0=OFF				
DI46	1x00046 2x00046 I:45	????			BIT R/O	
		Actual state of DI46:0=OFF				
DI47	1x00047 2x00047 I:46	????			BIT R/O	
		Actual state of DI47:0=OFF				
DI48	1x00048 2x00048 I:47	????			BIT R/O	
		Actual state of DI48:0=OFF				
DI49	1x00049 2x00049 I:48	????			BIT R/O	
		Actual state of DI49:0=OFF				
DI50	1x00050 2x00050 I:49	????			BIT R/O	
		Actual state of DI50:0=OFF				
DI51	1x00051 2x00051 I:50	????			BIT R/O	
		Actual state of DI51:0=OFF				
DI52	1x00052 2x00052 I:51	????			BIT R/O	
		Actual state of DI52:0=OFF				
DI53	1x00053 2x00053 I:52	????			BIT R/O	
		Actual state of DI53:0=OFF				
DI54	1x00054 2x00054 I:53	????			BIT R/O	
		Actual state of DI54:0=OFF				

DI55	1x00055 2x00055 I:54	????			BIT R/O	
		Actual state of DI55:0=OFF				
DI56	1x00056 2x00056 I:55	????			BIT R/O	
		Actual state of DI56:0=OFF				
DI57	1x00057 2x00057 I:56	????			BIT R/O	
		Actual state of DI57:0=OFF				
DI58	1x00058 2x00058 I:57	????			BIT R/O	
		Actual state of DI58:0=OFF				
DI59	1x00059 2x00059 I:58	????			BIT R/O	
		Actual state of DI59:0=OFF				
DI60	1x00060 2x00060 I:59	????			BIT R/O	
		Actual state of DI60:0=OFF				
DI61	1x00061 2x00061 I:60	????			BIT R/O	
		Actual state of DI61:0=OFF				
DI62	1x00062 2x00062 I:61	????			BIT R/O	
		Actual state of DI62:0=OFF				
DI63	1x00063 2x00063 I:62	????			BIT R/O	
		Actual state of DI63:0=OFF				
DI64	1x00064 2x00064 I:63	????			BIT R/O	
		Actual state of DI64:0=OFF				
<b>STATUS DIGITAL OUTPUTS</b>						
DO1	1x00065 2x00065 I:64	????		1	BIT R/W	NO
		Actual state of DO1:0=OFF		ENTER NEW STATE (0 or 1)		

Current state of the digital output DOx  
 =0:DO is OFF, =1:DO is ON

Writing on this register changes the state of the digital output

DO2	1x00066 2x00066 I:65	????	0	BIT R/W	NO
		Actual state of DO2:0=OFF	ENTER NEW STATE (0 or 1)		
DO3	1x00067 2x00067 I:66	????	0	BIT R/W	NO
		Actual state of DO3:0=OFF	ENTER NEW STATE (0 or 1)		
DO4	1x00068 2x00068 I:67	????	0	BIT R/W	NO
		Actual state of DO4:0=OFF	ENTER NEW STATE (0 or 1)		
DO5	1x00069 2x00069 I:68	????	0	BIT R/W	NO
		Actual state of DO5:0=OFF	ENTER NEW STATE (0 or 1)		
DO6	1x00070 2x00070 I:69	????	0	BIT R/W	NO
		Actual state of DO6:0=OFF	ENTER NEW STATE (0 or 1)		
DO7	1x00071 2x00071 I:70	????	0	BIT R/W	NO
		Actual state of DO7:0=OFF	ENTER NEW STATE (0 or 1)		
DO8	1x00072 2x00072 I:71	????	0	BIT R/W	NO
		Actual state of DO8:0=OFF	ENTER NEW STATE (0 or 1)		
DO9	1x00073 2x00073 I:72	????	0	BIT R/W	NO
		Actual state of DO9:0=OFF	ENTER NEW STATE (0 or 1)		
DO10	1x00074 2x00074 I:73	????	0	BIT R/W	NO
		Actual state of DO10:0=OFF	ENTER NEW STATE (0 or 1)		
DO11	1x00075 2x00075 I:74	????	0	BIT R/W	NO
		Actual state of DO11:0=OFF	ENTER NEW STATE (0 or 1)		
DO12	1x00076 2x00076 I:75	????	0	BIT R/W	NO



		Actual state of DO12:0=OFF		ENTER NEW STATE (0 or 1)		
DO13	1x00077 2x00077 I:76	????		0	BIT R/W	NO
		Actual state of DO13:0=OFF		ENTER NEW STATE (0 or 1)		
DO14	1x00078 2x00078 I:77	????		0	BIT R/W	NO
		Actual state of DO14:0=OFF		ENTER NEW STATE (0 or 1)		
DO15	1x00079 2x00079 I:78	????		0	BIT R/W	NO
		Actual state of DO15:0=OFF		ENTER NEW STATE (0 or 1)		
DO16	1x00080 2x00080 I:79	????		0	BIT R/W	NO
		Actual state of DO16:0=OFF		ENTER NEW STATE (0 or 1)		
DO17	1x00081 2x00081 I:80	????		0	BIT R/W	NO
		Actual state of DO17:0=OFF		ENTER NEW STATE (0 or 1)		
DO18	1x00082 2x00082 I:81	????		0	BIT R/W	NO
		Actual state of DO18:0=OFF		ENTER NEW STATE (0 or 1)		
DO19	1x00083 2x00083 I:82	????		0	BIT R/W	NO
		Actual state of DO19:0=OFF		ENTER NEW STATE (0 or 1)		
DO20	1x00084 2x00084 I:83	????		0	BIT R/W	NO
		Actual state of DO20:0=OFF		ENTER NEW STATE (0 or 1)		
DO21	1x00085 2x00085 I:84	????		0	BIT R/W	NO
		Actual state of DO21:0=OFF		ENTER NEW STATE (0 or 1)		
DO22	1x00086 2x00086 I:85	????		0	BIT R/W	NO
		Actual state of DO22:0=OFF		ENTER NEW STATE (0 or 1)		
DO23	1x00087 2x00087 I:86	????		0	BIT R/W	NO
		Actual state of DO23:0=OFF		ENTER NEW STATE (0 or 1)		

DO24	1x00088 2x00088 I:87	????		0	BIT R/W	NO
		Actual state of DO24:0=OFF		ENTER NEW STATE (0 or 1)		
DO25	1x00089 2x00089 I:88	????		0	BIT R/W	NO
		Actual state of DO25:0=OFF		ENTER NEW STATE (0 or 1)		
DO26	1x00090 2x00090 I:89	????		0	BIT R/W	NO
		Actual state of DO26:0=OFF		ENTER NEW STATE (0 or 1)		
DO27	1x00091 2x00091 I:90	????		0	BIT R/W	NO
		Actual state of DO27:0=OFF		ENTER NEW STATE (0 or 1)		
DO28	1x00092 2x00092 I:91	????		0	BIT R/W	NO
		Actual state of DO28:0=OFF		ENTER NEW STATE (0 or 1)		
DO29	1x00093 2x00093 I:92	????		0	BIT R/W	NO
		Actual state of DO29:0=OFF		ENTER NEW STATE (0 or 1)		
DO30	1x00094 2x00094 I:93	????		0	BIT R/W	NO
		Actual state of DO30:0=OFF		ENTER NEW STATE (0 or 1)		
DO31	1x00095 2x00095 I:94	????		0	BIT R/W	NO
		Actual state of DO31:0=OFF		ENTER NEW STATE (0 or 1)		
DO32	1x00096 2x00096 I:95	????		0	BIT R/W	NO
		Actual state of DO32:0=OFF		ENTER NEW STATE (0 or 1)		
DO33	1x00097 2x00097 I:96	????		0	BIT R/W	NO
		Actual state of DO33:0=OFF		ENTER NEW STATE (0 or 1)		
DO34	1x00098 2x00098 I:97	????		0	BIT R/W	NO
		Actual state of DO34:0=OFF		ENTER NEW STATE (0 or 1)		

DO35	1x00099 2x00099 I:98	????		0	BIT R/W	NO
		Actual state of DO35:0=OFF		ENTER NEW STATE (0 or 1)		
DO36	1x00100 2x00100 I:99	????		0	BIT R/W	NO
		Actual state of DO36:0=OFF		ENTER NEW STATE (0 or 1)		
DO37	1x00101 2x00101 I:100	????		0	BIT R/W	NO
		Actual state of DO37:0=OFF		ENTER NEW STATE (0 or 1)		
DO38	1x00102 2x00102 I:101	????		0	BIT R/W	NO
		Actual state of DO38:0=OFF		ENTER NEW STATE (0 or 1)		
DO39	1x00103 2x00103 I:102	????		0	BIT R/W	NO
		Actual state of DO39:0=OFF		ENTER NEW STATE (0 or 1)		
DO40	1x00104 2x00104 I:103	????		0	BIT R/W	NO
		Actual state of DO40:0=OFF		ENTER NEW STATE (0 or 1)		
DO41	1x00105 2x00105 I:104	????		0	BIT R/W	NO
		Actual state of DO41:0=OFF		ENTER NEW STATE (0 or 1)		
DO42	1x00106 2x00106 I:105	????		0	BIT R/W	NO
		Actual state of DO42:0=OFF		ENTER NEW STATE (0 or 1)		
DO43	1x00107 2x00107 I:106	????		0	BIT R/W	NO
		Actual state of DO43:0=OFF		ENTER NEW STATE (0 or 1)		
DO44	1x00108 2x00108 I:107	????		0	BIT R/W	NO
		Actual state of DO44:0=OFF		ENTER NEW STATE (0 or 1)		
DO45	1x00109 2x00109 I:108	????		0	BIT R/W	NO
		Actual state of DO45:0=OFF		ENTER NEW STATE (0 or 1)		

DO46	1x00110 2x00110 I:109	????		0	BIT R/W	NO
		Actual state of DO46:0=OFF		ENTER NEW STATE (0 or 1)		
DO47	1x00111 2x00111 I:110	????		0	BIT R/W	NO
		Actual state of DO47:0=OFF		ENTER NEW STATE (0 or 1)		
DO48	1x00112 2x00112 I:111	????		0	BIT R/W	NO
		Actual state of DO48:0=OFF		ENTER NEW STATE (0 or 1)		
DO49	1x00113 2x00113 I:112	????		0	BIT R/W	NO
		Actual state of DO49:0=OFF		ENTER NEW STATE (0 or 1)		
DO50	1x00114 2x00114 I:113	????		0	BIT R/W	NO
		Actual state of DO50:0=OFF		ENTER NEW STATE (0 or 1)		
DO51	1x00115 2x00115 I:114	????		0	BIT R/W	NO
		Actual state of DO51:0=OFF		ENTER NEW STATE (0 or 1)		
DO52	1x00116 2x00116 I:115	????		0	BIT R/W	NO
		Actual state of DO52:0=OFF		ENTER NEW STATE (0 or 1)		
DO53	1x00117 2x00117 I:116	????		0	BIT R/W	NO
		Actual state of DO53:0=OFF		ENTER NEW STATE (0 or 1)		
DO54	1x00118 2x00118 I:117	????		0	BIT R/W	NO
		Actual state of DO54:0=OFF		ENTER NEW STATE (0 or 1)		
DO55	1x00119 2x00119 I:118	????		0	BIT R/W	NO
		Actual state of DO55:0=OFF		ENTER NEW STATE (0 or 1)		
DO56	1x00120 2x00120 I:119	????		0	BIT R/W	NO
		Actual state of DO56:0=OFF		ENTER NEW STATE (0 or 1)		

DO57	1x00121 2x00121 I:120	????		0	BIT R/W	NO
		Actual state of DO57:0=OFF		ENTER NEW STATE (0 or 1)		
DO58	1x00122 2x00122 I:121	????		0	BIT R/W	NO
		Actual state of DO58:0=OFF		ENTER NEW STATE (0 or 1)		
DO59	1x00123 2x00123 I:122	????		0	BIT R/W	NO
		Actual state of DO59:0=OFF		ENTER NEW STATE (0 or 1)		
DO60	1x00124 2x00124 I:123	????		0	BIT R/W	NO
		Actual state of DO60:0=OFF		ENTER NEW STATE (0 or 1)		
<b>DIGITAL OUTPUTS: ENABLE OPEN WIRE DETECTION ON</b>						
ENABLE OPEN WIRE DETECTION ON DO1	1x00125 2x00125 I:124	????		1	BIT R/W	NO
		Actual setup of open wire detection for state ON of DO1:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
Enables/disabled detection of an open wire in DO state ON for the digital output DOx =0:Open wire detection is OFF, =1:Open wire detection is ON						
Writing on this register changes the state of the open wire detection for this output						
ENABLE OPEN WIRE DETECTION ON DO2	1x00126 2x00126 I:125	????		1	BIT R/W	NO
		Actual setup of open wire detection for state ON of DO2:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO3	1x00127 2x00127 I:126	????		1	BIT R/W	NO
		Actual setup of open wire detection for state ON of DO3:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO4	1x00128 2x00128 I:127	????		1	BIT R/W	NO
		Actual setup of open wire detection for state ON of DO4:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO5	1x00129 2x00129 I:128	????		1	BIT R/W	NO
		Actual setup of open wire detection for state ON of DO5:0=OFF		ENTER NEW SETUP MODE (0 or 1)		

ENABLE OPEN WIRE DETECTION ON DO6	1x00130 2x00130 I:129	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO6:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO7	1x00131 2x00131 I:130	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO7:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO8	1x00132 2x00132 I:131	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO8:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO9	1x00133 2x00133 I:132	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO9:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO10	1x00134 2x00134 I:133	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO10:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO11	1x00135 2x00135 I:134	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO11:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO12	1x00136 2x00136 I:135	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO12:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO13	1x00137 2x00137 I:136	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO13:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO14	1x00138 2x00138 I:137	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO14:0=OFF		ENTER NEW SETUP MODE (0 or 1)			

ENABLE OPEN WIRE DETECTION ON DO15	1x00139 2x00139 I:138	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO15:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO16	1x00140 2x00140 I:139	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO16:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO17	1x00141 2x00141 I:140	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO17:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO18	1x00142 2x00142 I:141	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO18:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO19	1x00143 2x00143 I:142	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO19:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO20	1x00144 2x00144 I:143	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO20:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO21	1x00145 2x00145 I:144	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO21:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO22	1x00146 2x00146 I:145	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO22:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO23	1x00147 2x00147 I:146	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO23:0=OFF		ENTER NEW SETUP MODE (0 or 1)			

ENABLE OPEN WIRE DETECTION ON DO24	1x00148 2x00148 I:147	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO24:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO25	1x00149 2x00149 I:148	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO25:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO26	1x00150 2x00150 I:149	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO26:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO27	1x00151 2x00151 I:150	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO27:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO28	1x00152 2x00152 I:151	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO28:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO29	1x00153 2x00153 I:152	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO29:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO30	1x00154 2x00154 I:153	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO30:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO31	1x00155 2x00155 I:154	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO31:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO32	1x00156 2x00156 I:155	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO32:0=OFF		ENTER NEW SETUP MODE (0 or 1)			



ENABLE OPEN WIRE DETECTION ON DO33	1x00157 2x00157 I:156	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO33:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO34	1x00158 2x00158 I:157	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO34:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO35	1x00159 2x00159 I:158	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO35:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO36	1x00160 2x00160 I:159	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO36:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO37	1x00161 2x00161 I:160	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO37:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO38	1x00162 2x00162 I:161	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO38:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO39	1x00163 2x00163 I:162	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO39:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO40	1x00164 2x00164 I:163	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO40:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO41	1x00165 2x00165 I:164	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO41:0=OFF		ENTER NEW SETUP MODE (0 or 1)			

ENABLE OPEN WIRE DETECTION ON DO42	1x00166 2x00166 I:165	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO42:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO43	1x00167 2x00167 I:166	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO43:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO44	1x00168 2x00168 I:167	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO44:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO45	1x00169 2x00169 I:168	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO45:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO46	1x00170 2x00170 I:169	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO46:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO47	1x00171 2x00171 I:170	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO47:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO48	1x00172 2x00172 I:171	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO48:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO49	1x00173 2x00173 I:172	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO49:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO50	1x00174 2x00174 I:173	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO50:0=OFF		ENTER NEW SETUP MODE (0 or 1)			

ENABLE OPEN WIRE DETECTION ON DO51	1x00175 2x00175 I:174	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO51:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO52	1x00176 2x00176 I:175	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO52:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO53	1x00177 2x00177 I:176	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO53:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO54	1x00178 2x00178 I:177	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO54:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO55	1x00179 2x00179 I:178	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO55:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO56	1x00180 2x00180 I:179	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO56:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO57	1x00181 2x00181 I:180	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO57:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO58	1x00182 2x00182 I:181	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO58:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO59	1x00183 2x00183 I:182	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO59:0=OFF		ENTER NEW SETUP MODE (0 or 1)			

ENABLE OPEN WIRE DETECTION ON DO60	1x00184 2x00184 I:183	????		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO60:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
<b>DIGITAL OUTPUTS: ENABLE OPEN WIRE DETECTION OFF</b>							
ENABLE OPEN WIRE DETECTION OFF DO1	1x00185 2x00185 I:184	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO1:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
Enables/disabled detection of an open wire in DO state OFF for the digital output DOx =0:Open wire detection is OFF, =1:Open wire detection is ON							
Writing on this register changes the state of the open wire detection for this output							
ENABLE OPEN WIRE DETECTION OFF DO2	1x00186 2x00186 I:185	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO2:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO3	1x00187 2x00187 I:186	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO3:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO4	1x00188 2x00188 I:187	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO4:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO5	1x00189 2x00189 I:188	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO5:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO6	1x00190 2x00190 I:189	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO6:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO7	1x00191 2x00191 I:190	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO7:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO8	1x00192 2x00192 I:191	????		1		BIT R/W	NO

		Actual setup of open wire detection for state OFF of DO8:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO9	1x00193 2x00193 I:192	????	1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO9:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO10	1x00194 2x00194 I:193	????	1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO10:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO11	1x00195 2x00195 I:194	????	1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO11:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO12	1x00196 2x00196 I:195	????	1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO12:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO13	1x00197 2x00197 I:196	????	1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO13:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO14	1x00198 2x00198 I:197	????	1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO14:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO15	1x00199 2x00199 I:198	????	1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO15:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO16	1x00200 2x00200 I:199	????	1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO16:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO17	1x00201 2x00201 I:200	????	1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO17:0=OFF	ENTER NEW SETUP MODE (0 or 1)		

ENABLE OPEN WIRE DETECTION OFF DO18	1x00202 2x00202 I:201	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO18:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO19	1x00203 2x00203 I:202	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO19:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO20	1x00204 2x00204 I:203	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO20:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO21	1x00205 2x00205 I:204	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO21:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO22	1x00206 2x00206 I:205	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO22:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO23	1x00207 2x00207 I:206	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO23:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO24	1x00208 2x00208 I:207	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO24:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO25	1x00209 2x00209 I:208	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO25:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO26	1x00210 2x00210 I:209	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO26:0=OFF		ENTER NEW SETUP MODE (0 or 1)			

ENABLE OPEN WIRE DETECTION OFF DO27	1x00211 2x00211 I:210	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO27:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO28	1x00212 2x00212 I:211	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO28:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO29	1x00213 2x00213 I:212	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO29:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO30	1x00214 2x00214 I:213	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO30:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO31	1x00215 2x00215 I:214	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO31:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO32	1x00216 2x00216 I:215	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO32:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO33	1x00217 2x00217 I:216	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO33:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO34	1x00218 2x00218 I:217	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO34:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO35	1x00219 2x00219 I:218	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO35:0=OFF		ENTER NEW SETUP MODE (0 or 1)			

ENABLE OPEN WIRE DETECTION OFF DO36	1x00220 2x00220 I:219	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO36:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO37	1x00221 2x00221 I:220	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO37:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO38	1x00222 2x00222 I:221	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO38:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO39	1x00223 2x00223 I:222	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO39:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO40	1x00224 2x00224 I:223	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO40:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO41	1x00225 2x00225 I:224	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO41:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO42	1x00226 2x00226 I:225	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO42:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO43	1x00227 2x00227 I:226	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO43:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO44	1x00228 2x00228 I:227	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO44:0=OFF		ENTER NEW SETUP MODE (0 or 1)			



ENABLE OPEN WIRE DETECTION OFF DO45	1x00229 2x00229 I:228	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO45:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO46	1x00230 2x00230 I:229	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO46:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO47	1x00231 2x00231 I:230	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO47:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO48	1x00232 2x00232 I:231	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO48:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO49	1x00233 2x00233 I:232	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO49:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO50	1x00234 2x00234 I:233	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO50:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO51	1x00235 2x00235 I:234	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO51:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO52	1x00236 2x00236 I:235	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO52:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO53	1x00237 2x00237 I:236	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO53:0=OFF		ENTER NEW SETUP MODE (0 or 1)			

ENABLE OPEN WIRE DETECTION OFF DO54	1x00238 2x00238 I:237	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO54:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO55	1x00239 2x00239 I:238	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO55:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO56	1x00240 2x00240 I:239	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO56:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO57	1x00241 2x00241 I:240	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO57:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO58	1x00242 2x00242 I:241	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO58:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO59	1x00243 2x00243 I:242	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO59:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO60	1x00244 2x00244 I:243	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO60:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
<b>DIGITAL OUTPUTS: ENABLE SHORT TO VDD DETECTION</b>							
ENABLE SHORT TO VDD DETECTION DO1	1x00245 2x00245 I:244	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO1:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
Enables/disabled detection of a shortcut to VDD in DO state OFF for the digital output DOx =0:Shortcut to VDD detection is OFF, =1:Shortcut to VDD detection is ON							
Writing on this register changes the state of the shortcut detection for this output							
ENABLE SHORT TO VDD DETECTION DO2	1x00246 2x00246 I:245	????		1		BIT R/W	NO

		Actual setup of open wire detection for state OFF of DO2:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO3	1x00247 2x00247 I:246	????	1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO3:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO4	1x00248 2x00248 I:247	????	1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO4:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO5	1x00249 2x00249 I:248	????	1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO5:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO6	1x00250 2x00250 I:249	????	1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO6:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO7	1x00251 2x00251 I:250	????	1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO7:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO8	1x00252 2x00252 I:251	????	1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO8:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO9	1x00253 2x00253 I:252	????	1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO9:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO10	1x00254 2x00254 I:253	????	1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO10:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO11	1x00255 2x00255 I:254	????	1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO11:0=OFF	ENTER NEW SETUP MODE (0 or 1)		

ENABLE SHORT TO VDD DETECTION DO12	1x00256 2x00256 I:255	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO12:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO13	1x00257 2x00257 I:256	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO13:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO14	1x00258 2x00258 I:257	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO14:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO15	1x00259 2x00259 I:258	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO15:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO16	1x00260 2x00260 I:259	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO16:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO17	1x00261 2x00261 I:260	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO17:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO18	1x00262 2x00262 I:261	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO18:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO19	1x00263 2x00263 I:262	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO19:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO20	1x00264 2x00264 I:263	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO20:0=OFF		ENTER NEW SETUP MODE (0 or 1)			

ENABLE SHORT TO VDD DETECTION DO21	1x00265 2x00265 I:264	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO21:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO22	1x00266 2x00266 I:265	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO22:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO23	1x00267 2x00267 I:266	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO23:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO24	1x00268 2x00268 I:267	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO24:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO25	1x00269 2x00269 I:268	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO25:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO26	1x00270 2x00270 I:269	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO26:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO27	1x00271 2x00271 I:270	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO27:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO28	1x00272 2x00272 I:271	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO28:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO29	1x00273 2x00273 I:272	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO29:0=OFF		ENTER NEW SETUP MODE (0 or 1)			

ENABLE SHORT TO VDD DETECTION DO30	1x00274 2x00274 I:273	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO30:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO31	1x00275 2x00275 I:274	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO31:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO32	1x00276 2x00276 I:275	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO32:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO33	1x00277 2x00277 I:276	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO33:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO34	1x00278 2x00278 I:277	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO34:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO35	1x00279 2x00279 I:278	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO35:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO36	1x00280 2x00280 I:279	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO36:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO37	1x00281 2x00281 I:280	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO37:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO38	1x00282 2x00282 I:281	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO38:0=OFF		ENTER NEW SETUP MODE (0 or 1)			

ENABLE SHORT TO VDD DETECTION DO39	1x00283 2x00283 I:282	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO39:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO40	1x00284 2x00284 I:283	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO40:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO41	1x00285 2x00285 I:284	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO41:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO42	1x00286 2x00286 I:285	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO42:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO43	1x00287 2x00287 I:286	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO43:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO44	1x00288 2x00288 I:287	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO44:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO45	1x00289 2x00289 I:288	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO45:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO45	1x00290 2x00290 I:289	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO45:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO47	1x00291 2x00291 I:290	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO47:0=OFF		ENTER NEW SETUP MODE (0 or 1)			

ENABLE SHORT TO VDD DETECTION DO48	1x00292 2x00292 I:291	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO48:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO49	1x00293 2x00293 I:292	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO49:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO50	1x00294 2x00294 I:293	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO50:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO51	1x00295 2x00295 I:294	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO51:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO52	1x00296 2x00296 I:295	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO52:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO53	1x00297 2x00297 I:296	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO53:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO54	1x00298 2x00298 I:297	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO54:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO55	1x00299 2x00299 I:298	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO55:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO56	1x00300 2x00300 I:299	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO56:0=OFF		ENTER NEW SETUP MODE (0 or 1)			



ENABLE SHORT TO VDD DETECTION DO57	1x00301 2x00301 I:300	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO57:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO58	1x00302 2x00302 I:301	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO58:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO59	1x00303 2x00303 I:302	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO59:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO60	1x00304 2x00304 I:303	????		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO60:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
<b>DIGITAL OUTPUTS: OPEN WIRE DETECTION STATUS WHILE ON</b>							
OPEN WIRE FAULT WHILE ON DO1	1x00305 2x00305 I:304	????				BIT R/O	
		Actual detection state of an open wire fault in state ON for DO1:0=OK					
The current detection state of an open wire in the output state ON for the digital output DOx =0:No fault, =1:Fault-open wire detected							
OPEN WIRE FAULT WHILE ON DO2	1x00306 2x00306 I:305	????				BIT R/O	
		Actual detection state of an open wire fault in state ON for DO2:0=OK					
OPEN WIRE FAULT WHILE ON DO3	1x00307 2x00307 I:306	????				BIT R/O	
		Actual detection state of an open wire fault in state ON for DO3:0=OK					
OPEN WIRE FAULT WHILE ON DO4	1x00308 2x00308 I:307	????				BIT R/O	
		Actual detection state of an open wire fault in state ON for DO4:0=OK					
OPEN WIRE FAULT WHILE ON DO5	1x00309 2x00309 I:308	????				BIT R/O	

		Actual detection state of an open wire fault in state ON for DO5:0=OK			
OPEN WIRE FAULT WHILE ON DO6	1x00310 2x00310 I:309	????			BIT R/O
		Actual detection state of an open wire fault in state ON for DO6:0=OK			
OPEN WIRE FAULT WHILE ON DO7	1x00311 2x00311 I:310	????			BIT R/O
		Actual detection state of an open wire fault in state ON for DO7:0=OK			
OPEN WIRE FAULT WHILE ON DO8	1x00312 2x00312 I:311	????			BIT R/O
		Actual detection state of an open wire fault in state ON for DO8:0=OK			
OPEN WIRE FAULT WHILE ON DO9	1x00313 2x00313 I:312	????			BIT R/O
		Actual detection state of an open wire fault in state ON for DO9:0=OK			
OPEN WIRE FAULT WHILE ON DO10	1x00314 2x00314 I:313	????			BIT R/O
		Actual detection state of an open wire fault in state ON for DO10:0=OK			
OPEN WIRE FAULT WHILE ON DO11	1x00315 2x00315 I:314	????			BIT R/O
		Actual detection state of an open wire fault in state ON for DO11:0=OK			
OPEN WIRE FAULT WHILE ON DO12	1x00316 2x00316 I:315	????			BIT R/O
		Actual detection state of an open wire fault in state ON for DO12:0=OK			
OPEN WIRE FAULT WHILE ON DO13	1x00317 2x00317 I:316	????			BIT R/O
		Actual detection state of an open wire fault in state ON for DO13:0=OK			
OPEN WIRE FAULT WHILE ON DO14	1x00318 2x00318 I:317	????			BIT R/O
		Actual detection state of an open wire fault in state ON for DO14:0=OK			

OPEN WIRE FAULT WHILE ON DO15	1x00319 2x00319 I:318	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO15:0=OK				
OPEN WIRE FAULT WHILE ON DO16	1x00320 2x00320 I:319	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO16:0=OK				
OPEN WIRE FAULT WHILE ON DO17	1x00321 2x00321 I:320	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO17:0=OK				
OPEN WIRE FAULT WHILE ON DO18	1x00322 2x00322 I:321	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO18:0=OK				
OPEN WIRE FAULT WHILE ON DO19	1x00323 2x00323 I:322	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO19:0=OK				
OPEN WIRE FAULT WHILE ON DO20	1x00324 2x00324 I:323	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO20:0=OK				
OPEN WIRE FAULT WHILE ON DO21	1x00325 2x00325 I:324	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO21:0=OK				
OPEN WIRE FAULT WHILE ON DO22	1x00326 2x00326 I:325	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO22:0=OK				
OPEN WIRE FAULT WHILE ON DO23	1x00327 2x00327 I:326	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO23:0=OK				

OPEN WIRE FAULT WHILE ON DO24	1x00328 2x00328 I:327	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO24:0=OK				
OPEN WIRE FAULT WHILE ON DO25	1x00329 2x00329 I:328	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO25:0=OK				
OPEN WIRE FAULT WHILE ON DO26	1x00330 2x00330 I:329	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO26:0=OK				
OPEN WIRE FAULT WHILE ON DO27	1x00331 2x00331 I:330	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO27:0=OK				
OPEN WIRE FAULT WHILE ON DO28	1x00332 2x00332 I:331	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO28:0=OK				
OPEN WIRE FAULT WHILE ON DO29	1x00333 2x00333 I:332	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO29:0=OK				
OPEN WIRE FAULT WHILE ON DO30	1x00334 2x00334 I:333	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO30:0=OK				
OPEN WIRE FAULT WHILE ON DO31	1x00335 2x00335 I:334	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO31:0=OK				
OPEN WIRE FAULT WHILE ON DO32	1x00336 2x00336 I:335	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO32:0=OK				

OPEN WIRE FAULT WHILE ON DO33	1x00337 2x00337 I:336	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO33:0=OK				
OPEN WIRE FAULT WHILE ON DO34	1x00338 2x00338 I:337	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO34:0=OK				
OPEN WIRE FAULT WHILE ON DO35	1x00339 2x00339 I:338	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO35:0=OK				
OPEN WIRE FAULT WHILE ON DO36	1x00340 2x00340 I:339	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO36:0=OK				
OPEN WIRE FAULT WHILE ON DO37	1x00341 2x00341 I:340	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO37:0=OK				
OPEN WIRE FAULT WHILE ON DO38	1x00342 2x00342 I:341	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO38:0=OK				
OPEN WIRE FAULT WHILE ON DO39	1x00343 2x00343 I:342	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO39:0=OK				
OPEN WIRE FAULT WHILE ON DO40	1x00344 2x00344 I:343	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO40:0=OK				
OPEN WIRE FAULT WHILE ON DO41	1x00345 2x00345 I:344	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO41:0=OK				

OPEN WIRE FAULT WHILE ON DO42	1x00346 2x00346 I:345	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO42:0=OK				
OPEN WIRE FAULT WHILE ON DO43	1x00347 2x00347 I:346	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO43:0=OK				
OPEN WIRE FAULT WHILE ON DO44	1x00348 2x00348 I:347	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO44:0=OK				
OPEN WIRE FAULT WHILE ON DO45	1x00349 2x00349 I:348	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO45:0=OK				
OPEN WIRE FAULT WHILE ON DO46	1x00350 2x00350 I:349	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO46:0=OK				
OPEN WIRE FAULT WHILE ON DO47	1x00351 2x00351 I:350	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO47:0=OK				
OPEN WIRE FAULT WHILE ON DO48	1x00352 2x00352 I:351	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO48:0=OK				
OPEN WIRE FAULT WHILE ON DO49	1x00353 2x00353 I:352	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO49:0=OK				
OPEN WIRE FAULT WHILE ON DO50	1x00354 2x00354 I:353	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO50:0=OK				

OPEN WIRE FAULT WHILE ON DO51	1x00355 2x00355 I:354	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO51:0=OK				
OPEN WIRE FAULT WHILE ON DO52	1x00356 2x00356 I:355	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO52:0=OK				
OPEN WIRE FAULT WHILE ON DO53	1x00357 2x00357 I:356	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO53:0=OK				
OPEN WIRE FAULT WHILE ON DO54	1x00358 2x00358 I:357	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO54:0=OK				
OPEN WIRE FAULT WHILE ON DO55	1x00359 2x00359 I:358	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO55:0=OK				
OPEN WIRE FAULT WHILE ON DO56	1x00360 2x00360 I:359	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO56:0=OK				
OPEN WIRE FAULT WHILE ON DO57	1x00361 2x00361 I:360	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO57:0=OK				
OPEN WIRE FAULT WHILE ON DO58	1x00362 2x00362 I:361	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO58:0=OK				
OPEN WIRE FAULT WHILE ON DO59	1x00363 2x00363 I:362	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO59:0=OK				

OPEN WIRE FAULT WHILE ON DO60	1x00364 2x00364 I:363	????			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO60:0=OK				
<b>DIGITAL OUTPUTS: OPEN WIRE DETECTION STATUS WHILE OFF</b>						
OPEN WIRE FAULT WHILE OFF DO1	1x00365 2x00365 I:364	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO1:0=OK				
The current detection state of an open wire in the output state OFF for the digital output DOx =0:No fault, =1:Fault-open wire detected						
OPEN WIRE FAULT WHILE OFF DO2	1x00366 2x00366 I:365	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO2:0=OK				
OPEN WIRE FAULT WHILE OFF DO3	1x00367 2x00367 I:366	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO3:0=OK				
OPEN WIRE FAULT WHILE OFF DO4	1x00368 2x00368 I:367	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO4:0=OK				
OPEN WIRE FAULT WHILE OFF DO5	1x00369 2x00369 I:368	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO5:0=OK				
OPEN WIRE FAULT WHILE OFF DO6	1x00370 2x00370 I:369	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO6:0=OK				
OPEN WIRE FAULT WHILE OFF DO7	1x00371 2x00371 I:370	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO7:0=OK				
OPEN WIRE FAULT WHILE OFF DO8	1x00372 2x00372 I:371	????			BIT R/O	



		Actual detection state of an open wire fault in state OFF for DO8:0=OK			
OPEN WIRE FAULT WHILE OFF DO9	1x00373 2x00373 I:372	????			BIT R/O
		Actual detection state of an open wire fault in state OFF for DO9:0=OK			
OPEN WIRE FAULT WHILE OFF DO10	1x00374 2x00374 I:373	????			BIT R/O
		Actual detection state of an open wire fault in state OFF for DO10:0=OK			
OPEN WIRE FAULT WHILE OFF DO11	1x00375 2x00375 I:374	????			BIT R/O
		Actual detection state of an open wire fault in state OFF for DO11:0=OK			
OPEN WIRE FAULT WHILE OFF DO12	1x00376 2x00376 I:375	????			BIT R/O
		Actual detection state of an open wire fault in state OFF for DO12:0=OK			
OPEN WIRE FAULT WHILE OFF DO13	1x00377 2x00377 I:376	????			BIT R/O
		Actual detection state of an open wire fault in state OFF for DO13:0=OK			
OPEN WIRE FAULT WHILE OFF DO14	1x00378 2x00378 I:377	????			BIT R/O
		Actual detection state of an open wire fault in state OFF for DO14:0=OK			
OPEN WIRE FAULT WHILE OFF DO15	1x00379 2x00379 I:378	????			BIT R/O
		Actual detection state of an open wire fault in state OFF for DO15:0=OK			
OPEN WIRE FAULT WHILE OFF DO16	1x00380 2x00380 I:379	????			BIT R/O
		Actual detection state of an open wire fault in state OFF for DO16:0=OK			
OPEN WIRE FAULT WHILE OFF DO17	1x00381 2x00381 I:380	????			BIT R/O
		Actual detection state of an open wire fault in state OFF for DO17:0=OK			

OPEN WIRE FAULT WHILE OFF DO18	1x00382 2x00382 I:381	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO18:0=OK				
OPEN WIRE FAULT WHILE OFF DO19	1x00383 2x00383 I:382	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO19:0=OK				
OPEN WIRE FAULT WHILE OFF DO20	1x00384 2x00384 I:383	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO20:0=OK				
OPEN WIRE FAULT WHILE OFF DO21	1x00385 2x00385 I:384	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO21:0=OK				
OPEN WIRE FAULT WHILE OFF DO22	1x00386 2x00386 I:385	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO22:0=OK				
OPEN WIRE FAULT WHILE OFF DO23	1x00387 2x00387 I:386	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO23:0=OK				
OPEN WIRE FAULT WHILE OFF DO24	1x00388 2x00388 I:387	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO24:0=OK				
OPEN WIRE FAULT WHILE OFF DO25	1x00389 2x00389 I:388	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO25:0=OK				
OPEN WIRE FAULT WHILE OFF DO26	1x00390 2x00390 I:389	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO26:0=OK				

OPEN WIRE FAULT WHILE OFF DO27	1x00391 2x00391 I:390	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO27:0=OK				
OPEN WIRE FAULT WHILE OFF DO28	1x00392 2x00392 I:391	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO28:0=OK				
OPEN WIRE FAULT WHILE OFF DO29	1x00393 2x00393 I:392	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO29:0=OK				
OPEN WIRE FAULT WHILE OFF DO30	1x00394 2x00394 I:393	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO30:0=OK				
OPEN WIRE FAULT WHILE OFF DO31	1x00395 2x00395 I:394	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO31:0=OK				
OPEN WIRE FAULT WHILE OFF DO32	1x00396 2x00396 I:395	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO32:0=OK				
OPEN WIRE FAULT WHILE OFF DO33	1x00397 2x00397 I:396	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO33:0=OK				
OPEN WIRE FAULT WHILE OFF DO34	1x00398 2x00398 I:397	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO34:0=OK				
OPEN WIRE FAULT WHILE OFF DO35	1x00399 2x00399 I:398	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO35:0=OK				

OPEN WIRE FAULT WHILE OFF DO36	1x00400 2x00400 I:399	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO36:0=OK				
OPEN WIRE FAULT WHILE OFF DO37	1x00401 2x00401 I:400	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO37:0=OK				
OPEN WIRE FAULT WHILE OFF DO38	1x00402 2x00402 I:401	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO38:0=OK				
OPEN WIRE FAULT WHILE OFF DO39	1x00403 2x00403 I:402	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO39:0=OK				
OPEN WIRE FAULT WHILE OFF DO40	1x00404 2x00404 I:403	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO40:0=OK				
OPEN WIRE FAULT WHILE OFF DO41	1x00405 2x00405 I:404	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO41:0=OK				
OPEN WIRE FAULT WHILE OFF DO42	1x00406 2x00406 I:405	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO42:0=OK				
OPEN WIRE FAULT WHILE OFF DO43	1x00407 2x00407 I:406	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO43:0=OK				
OPEN WIRE FAULT WHILE OFF DO44	1x00408 2x00408 I:407	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO44:0=OK				

OPEN WIRE FAULT WHILE OFF DO45	1x00409 2x00409 I:408	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO45:0=OK				
OPEN WIRE FAULT WHILE OFF DO46	1x00410 2x00410 I:409	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO46:0=OK				
OPEN WIRE FAULT WHILE OFF DO47	1x00411 2x00411 I:410	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO47:0=OK				
OPEN WIRE FAULT WHILE OFF DO48	1x00412 2x00412 I:411	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO48:0=OK				
OPEN WIRE FAULT WHILE OFF DO49	1x00413 2x00413 I:412	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO49:0=OK				
OPEN WIRE FAULT WHILE OFF DO50	1x00414 2x00414 I:413	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO50:0=OK				
OPEN WIRE FAULT WHILE OFF DO51	1x00415 2x00415 I:414	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO51:0=OK				
OPEN WIRE FAULT WHILE OFF DO52	1x00416 2x00416 I:415	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO52:0=OK				
OPEN WIRE FAULT WHILE OFF DO53	1x00417 2x00417 I:416	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO53:0=OK				

OPEN WIRE FAULT WHILE OFF DO54	1x00418 2x00418 I:417	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO54:0=OK				
OPEN WIRE FAULT WHILE OFF DO55	1x00419 2x00419 I:418	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO55:0=OK				
OPEN WIRE FAULT WHILE OFF DO56	1x00420 2x00420 I:419	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO56:0=OK				
OPEN WIRE FAULT WHILE OFF DO57	1x00421 2x00421 I:420	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO57:0=OK				
OPEN WIRE FAULT WHILE OFF DO58	1x00422 2x00422 I:421	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO58:0=OK				
OPEN WIRE FAULT WHILE OFF DO59	1x00423 2x00423 I:422	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO59:0=OK				
OPEN WIRE FAULT WHILE OFF DO60	1x00424 2x00424 I:423	????			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO60:0=OK				
<b>DIGITAL OUTPUTS: SHORTCUT DETECTION STATUS TO VDD WHILE OFF</b>						
OPEN WIRE SHORTCUT TO VDD DO1	1x00425 2x00425 I:424	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO1:0=OK				
The current detection state of a shortcut to VDD in the output state OFF for the digital output DOx =0:No fault, =1:Fault-shortcut to VDD detected						
OPEN WIRE SHORTCUT TO VDD DO2	1x00426 2x00426 I:425	????			BIT R/O	

		Actual detection state of a shortcut to VDD in state OFF for VDD DO2:0=OK			
OPEN WIRE SHORTCUT TO VDD DO3	1x00427 2x00427 I:426	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO3:0=OK			
OPEN WIRE SHORTCUT TO VDD DO4	1x00428 2x00428 I:427	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO4:0=OK			
OPEN WIRE SHORTCUT TO VDD DO5	1x00429 2x00429 I:428	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO5:0=OK			
OPEN WIRE SHORTCUT TO VDD DO6	1x00430 2x00430 I:429	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO6:0=OK			
OPEN WIRE SHORTCUT TO VDD DO7	1x00431 2x00431 I:430	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO7:0=OK			
OPEN WIRE SHORTCUT TO VDD DO8	1x00432 2x00432 I:431	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO8:0=OK			
OPEN WIRE SHORTCUT TO VDD DO9	1x00433 2x00433 I:432	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO9:0=OK			

OPEN WIRE SHORTCUT TO VDD DO10	1x00434 2x00434 I:433	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO10:0=OK				
OPEN WIRE SHORTCUT TO VDD DO11	1x00435 2x00435 I:434	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO11:0=OK				
OPEN WIRE SHORTCUT TO VDD DO12	1x00436 2x00436 I:435	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO12:0=OK				
OPEN WIRE SHORTCUT TO VDD DO13	1x00437 2x00437 I:436	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO13:0=OK				
OPEN WIRE SHORTCUT TO VDD DO14	1x00438 2x00438 I:437	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO14:0=OK				
OPEN WIRE SHORTCUT TO VDD DO15	1x00439 2x00439 I:438	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO15:0=OK				
OPEN WIRE SHORTCUT TO VDD DO16	1x00440 2x00440 I:439	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO16:0=OK				
OPEN WIRE SHORTCUT TO VDD DO17	1x00441 2x00441 I:440	????			BIT R/O	



		Actual detection state of a shortcut to VDD in state OFF for VDD DO17:0=OK			
OPEN WIRE SHORTCUT TO VDD DO18	1x00442 2x00442 I:441	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO18:0=OK			
OPEN WIRE SHORTCUT TO VDD DO19	1x00443 2x00443 I:442	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO19:0=OK			
OPEN WIRE SHORTCUT TO VDD DO20	1x00444 2x00444 I:443	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO20:0=OK			
OPEN WIRE SHORTCUT TO VDD DO21	1x00445 2x00445 I:444	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO21:0=OK			
OPEN WIRE SHORTCUT TO VDD DO22	1x00446 2x00446 I:445	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO22:0=OK			
OPEN WIRE SHORTCUT TO VDD DO23	1x00447 2x00447 I:446	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO23:0=OK			
OPEN WIRE SHORTCUT TO VDD DO24	1x00448 2x00448 I:447	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO24:0=OK			

OPEN WIRE SHORTCUT TO VDD DO25	1x00449 2x00449 I:448	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO25:0=OK				
OPEN WIRE SHORTCUT TO VDD DO26	1x00450 2x00450 I:449	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO26:0=OK				
OPEN WIRE SHORTCUT TO VDD DO27	1x00451 2x00451 I:450	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO27:0=OK				
OPEN WIRE SHORTCUT TO VDD DO28	1x00452 2x00452 I:451	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO28:0=OK				
OPEN WIRE SHORTCUT TO VDD DO29	1x00453 2x00453 I:452	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO29:0=OK				
OPEN WIRE SHORTCUT TO VDD DO30	1x00454 2x00454 I:453	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO30:0=OK				
OPEN WIRE SHORTCUT TO VDD DO31	1x00455 2x00455 I:454	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO31:0=OK				
OPEN WIRE SHORTCUT TO VDD DO32	1x00456 2x00456 I:455	????			BIT R/O	

		Actual detection state of a shortcut to VDD in state OFF for VDD DO32:0=OK			
OPEN WIRE SHORTCUT TO VDD DO33	1x00457 2x00457 I:456	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO33:0=OK			
OPEN WIRE SHORTCUT TO VDD DO34	1x00458 2x00458 I:457	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO34:0=OK			
OPEN WIRE SHORTCUT TO VDD DO35	1x00459 2x00459 I:458	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO35:0=OK			
OPEN WIRE SHORTCUT TO VDD DO36	1x00460 2x00460 I:459	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO36:0=OK			
OPEN WIRE SHORTCUT TO VDD DO37	1x00461 2x00461 I:460	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO37:0=OK			
OPEN WIRE SHORTCUT TO VDD DO38	1x00462 2x00462 I:461	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO38:0=OK			
OPEN WIRE SHORTCUT TO VDD DO39	1x00463 2x00463 I:462	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO39:0=OK			

OPEN WIRE SHORTCUT TO VDD DO40	1x00464 2x00464 I:463	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO40:0=OK				
OPEN WIRE SHORTCUT TO VDD DO41	1x00465 2x00465 I:464	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO41:0=OK				
OPEN WIRE SHORTCUT TO VDD DO42	1x00466 2x00466 I:465	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO42:0=OK				
OPEN WIRE SHORTCUT TO VDD DO43	1x00467 2x00467 I:466	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO43:0=OK				
OPEN WIRE SHORTCUT TO VDD DO44	1x00468 2x00468 I:467	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO44:0=OK				
OPEN WIRE SHORTCUT TO VDD DO45	1x00469 2x00469 I:468	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO45:0=OK				
OPEN WIRE SHORTCUT TO VDD DO46	1x00470 2x00470 I:469	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO46:0=OK				
OPEN WIRE SHORTCUT TO VDD DO47	1x00471 2x00471 I:470	????			BIT R/O	

		Actual detection state of a shortcut to VDD in state OFF for VDD DO47:0=OK			
OPEN WIRE SHORTCUT TO VDD DO48	1x00472 2x00472 I:471	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO48:0=OK			
OPEN WIRE SHORTCUT TO VDD DO49	1x00473 2x00473 I:472	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO49:0=OK			
OPEN WIRE SHORTCUT TO VDD DO50	1x00474 2x00474 I:473	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO50:0=OK			
OPEN WIRE SHORTCUT TO VDD DO51	1x00475 2x00475 I:474	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO51:0=OK			
OPEN WIRE SHORTCUT TO VDD DO52	1x00476 2x00476 I:475	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO52:0=OK			
OPEN WIRE SHORTCUT TO VDD DO53	1x00477 2x00477 I:476	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO53:0=OK			
OPEN WIRE SHORTCUT TO VDD DO54	1x00478 2x00478 I:477	????			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO54:0=OK			

OPEN WIRE SHORTCUT TO VDD DO55	1x00479 2x00479 I:478	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO55:0=OK				
OPEN WIRE SHORTCUT TO VDD DO56	1x00480 2x00480 I:479	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO56:0=OK				
OPEN WIRE SHORTCUT TO VDD DO57	1x00481 2x00481 I:480	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO57:0=OK				
OPEN WIRE SHORTCUT TO VDD DO58	1x00482 2x00482 I:481	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO58:0=OK				
OPEN WIRE SHORTCUT TO VDD DO59	1x00483 2x00483 I:482	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO59:0=OK				
OPEN WIRE SHORTCUT TO VDD DO60	1x00484 2x00484 I:483	????			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO60:0=OK				
<b>DIGITAL OUTPUTS: THERMAL OVERLOAD DETECTION STATUS</b>						
THERMAL OVERLOAD DETECTION STATUS DO1	1x00485 2x00485 I:484	????			BIT R/O	
		Actual detection state of a thermal overload for DO1:0=OK				
The current detection state of a thermal overload for the digital output DOx =0:No fault, =1:Fault-thermal overload detected						
THERMAL OVERLOAD DETECTION STATUS DO2	1x00486 2x00486 I:485	????			BIT R/O	

		Actual detection state of a thermal overload for DO2:0=OK			
THERMAL OVERLOAD DETECTION STATUS DO3	1x00487 2x00487 I:486	????			BIT R/O
		Actual detection state of a thermal overload for DO3:0=OK			
THERMAL OVERLOAD DETECTION STATUS DO4	1x00488 2x00488 I:487	????			BIT R/O
		Actual detection state of a thermal overload for DO4:0=OK			
THERMAL OVERLOAD DETECTION STATUS DO5	1x00489 2x00489 I:488	????			BIT R/O
		Actual detection state of a thermal overload for DO5:0=OK			
THERMAL OVERLOAD DETECTION STATUS DO6	1x00490 2x00490 I:489	????			BIT R/O
		Actual detection state of a thermal overload for DO6:0=OK			
THERMAL OVERLOAD DETECTION STATUS DO7	1x00491 2x00491 I:490	????			BIT R/O
		Actual detection state of a thermal overload for DO7:0=OK			
THERMAL OVERLOAD DETECTION STATUS DO8	1x00492 2x00492 I:491	????			BIT R/O
		Actual detection state of a thermal overload for DO8:0=OK			
THERMAL OVERLOAD DETECTION STATUS DO9	1x00493 2x00493 I:492	????			BIT R/O
		Actual detection state of a thermal overload for DO9:0=OK			
THERMAL OVERLOAD DETECTION STATUS DO10	1x00494 2x00494 I:493	????			BIT R/O
		Actual detection state of a thermal overload for DO10:0=OK			
THERMAL OVERLOAD DETECTION STATUS DO11	1x00495 2x00495 I:494	????			BIT R/O
		Actual detection state of a thermal overload for DO11:0=OK			

THERMAL OVERLOAD DETECTION STATUS DO12	1x00496 2x00496 I:495	????			BIT R/O	
		Actual detection state of a thermal overload for DO12:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO13	1x00497 2x00497 I:496	????			BIT R/O	
		Actual detection state of a thermal overload for DO13:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO14	1x00498 2x00498 I:497	????			BIT R/O	
		Actual detection state of a thermal overload for DO14:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO15	1x00499 2x00499 I:498	????			BIT R/O	
		Actual detection state of a thermal overload for DO15:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO16	1x00500 2x00500 I:499	????			BIT R/O	
		Actual detection state of a thermal overload for DO16:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO17	1x00501 2x00501 I:500	????			BIT R/O	
		Actual detection state of a thermal overload for DO17:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO18	1x00502 2x00502 I:501	????			BIT R/O	
		Actual detection state of a thermal overload for DO18:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO19	1x00503 2x00503 I:502	????			BIT R/O	
		Actual detection state of a thermal overload for DO19:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO20	1x00504 2x00504 I:503	????			BIT R/O	
		Actual detection state of a thermal overload for DO20:0=OK				



THERMAL OVERLOAD DETECTION STATUS DO21	1x00505 2x00505 I:504	????			BIT R/O	
		Actual detection state of a thermal overload for DO21:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO22	1x00506 2x00506 I:505	????			BIT R/O	
		Actual detection state of a thermal overload for DO22:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO23	1x00507 2x00507 I:506	????			BIT R/O	
		Actual detection state of a thermal overload for DO23:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO24	1x00508 2x00508 I:507	????			BIT R/O	
		Actual detection state of a thermal overload for DO24:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO25	1x00509 2x00509 I:508	????			BIT R/O	
		Actual detection state of a thermal overload for DO25:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO26	1x00510 2x00510 I:509	????			BIT R/O	
		Actual detection state of a thermal overload for DO26:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO27	1x00511 2x00511 I:510	????			BIT R/O	
		Actual detection state of a thermal overload for DO27:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO28	1x00512 2x00512 I:511	????			BIT R/O	
		Actual detection state of a thermal overload for DO28:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO29	1x00513 2x00513 I:512	????			BIT R/O	
		Actual detection state of a thermal overload for DO29:0=OK				

THERMAL OVERLOAD DETECTION STATUS DO30	1x00514 2x00514 I:513	????			BIT R/O	
		Actual detection state of a thermal overload for DO30:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO31	1x00515 2x00515 I:514	????			BIT R/O	
		Actual detection state of a thermal overload for DO31:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO32	1x00516 2x00516 I:515	????			BIT R/O	
		Actual detection state of a thermal overload for DO32:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO33	1x00517 2x00517 I:516	????			BIT R/O	
		Actual detection state of a thermal overload for DO33:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO34	1x00518 2x00518 I:517	????			BIT R/O	
		Actual detection state of a thermal overload for DO34:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO35	1x00519 2x00519 I:518	????			BIT R/O	
		Actual detection state of a thermal overload for DO35:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO36	1x00520 2x00520 I:519	????			BIT R/O	
		Actual detection state of a thermal overload for DO36:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO37	1x00521 2x00521 I:520	????			BIT R/O	
		Actual detection state of a thermal overload for DO37:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO38	1x00522 2x00522 I:521	????			BIT R/O	
		Actual detection state of a thermal overload for DO38:0=OK				

THERMAL OVERLOAD DETECTION STATUS DO39	1x00523 2x00523 I:522	????			BIT R/O	
		Actual detection state of a thermal overload for DO39:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO40	1x00524 2x00524 I:523	????			BIT R/O	
		Actual detection state of a thermal overload for DO40:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO41	1x00525 2x00525 I:524	????			BIT R/O	
		Actual detection state of a thermal overload for DO41:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO42	1x00526 2x00526 I:525	????			BIT R/O	
		Actual detection state of a thermal overload for DO42:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO43	1x00527 2x00527 I:526	????			BIT R/O	
		Actual detection state of a thermal overload for DO43:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO44	1x00528 2x00528 I:527	????			BIT R/O	
		Actual detection state of a thermal overload for DO44:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO45	1x00529 2x00529 I:528	????			BIT R/O	
		Actual detection state of a thermal overload for DO45:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO46	1x00530 2x00530 I:529	????			BIT R/O	
		Actual detection state of a thermal overload for DO46:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO47	1x00531 2x00531 I:530	????			BIT R/O	
		Actual detection state of a thermal overload for DO47:0=OK				

THERMAL OVERLOAD DETECTION STATUS DO48	1x00532 2x00532 I:531	????			BIT R/O	
		Actual detection state of a thermal overload for DO48:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO49	1x00533 2x00533 I:532	????			BIT R/O	
		Actual detection state of a thermal overload for DO49:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO50	1x00534 2x00534 I:533	????			BIT R/O	
		Actual detection state of a thermal overload for DO50:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO51	1x00535 2x00535 I:534	????			BIT R/O	
		Actual detection state of a thermal overload for DO51:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO52	1x00536 2x00536 I:535	????			BIT R/O	
		Actual detection state of a thermal overload for DO52:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO53	1x00537 2x00537 I:536	????			BIT R/O	
		Actual detection state of a thermal overload for DO53:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO54	1x00538 2x00538 I:537	????			BIT R/O	
		Actual detection state of a thermal overload for DO54:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO55	1x00539 2x00539 I:538	????			BIT R/O	
		Actual detection state of a thermal overload for DO55:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO56	1x00540 2x00540 I:539	????			BIT R/O	
		Actual detection state of a thermal overload for DO56:0=OK				

THERMAL OVERLOAD DETECTION STATUS DO57	1x00541 2x00541 I:540	????			BIT R/O	
		Actual detection state of a thermal overload for DO57:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO58	1x00542 2x00542 I:541	????			BIT R/O	
		Actual detection state of a thermal overload for DO58:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO59	1x00543 2x00543 I:542	????			BIT R/O	
		Actual detection state of a thermal overload for DO59:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO60	1x00544 2x00544 I:543	????			BIT R/O	
		Actual detection state of a thermal overload for DO60:0=OK				
<b>DIGITAL OUTPUTS: CURRENT LIMIT DETECTION STATUS WHILE ON</b>						
CURRENT LIMIT DETECTION STATUS WHILE ON DO1	1x00545 2x00545 I:544	????			BIT R/O	
		Actual detection state of a current limit while ON for DO1:0=OK				
The current detection state of a current limit while output is ON for the digital output DOx =0:No fault, =1:Fault-current limit error						
CURRENT LIMIT DETECTION STATUS WHILE ON DO2	1x00546 2x00546 I:545	????			BIT R/O	
		Actual detection state of a current limit while ON for DO2:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO3	1x00547 2x00547 I:546	????			BIT R/O	
		Actual detection state of a current limit while ON for DO3:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO4	1x00548 2x00548 I:547	????			BIT R/O	
		Actual detection state of a current limit while ON for DO4:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO5	1x00549 2x00549 I:548	????			BIT R/O	

		Actual detection state of a current limit while ON for DO5:0=OK			
CURRENT LIMIT DETECTION STATUS WHILE ON DO6	1x00550 2x00550 I:549	????			BIT R/O
		Actual detection state of a current limit while ON for DO6:0=OK			
CURRENT LIMIT DETECTION STATUS WHILE ON DO7	1x00551 2x00551 I:550	????			BIT R/O
		Actual detection state of a current limit while ON for DO7:0=OK			
CURRENT LIMIT DETECTION STATUS WHILE ON DO8	1x00552 2x00552 I:551	????			BIT R/O
		Actual detection state of a current limit while ON for DO8:0=OK			
CURRENT LIMIT DETECTION STATUS WHILE ON DO9	1x00553 2x00553 I:552	????			BIT R/O
		Actual detection state of a current limit while ON for DO9:0=OK			
CURRENT LIMIT DETECTION STATUS WHILE ON DO10	1x00554 2x00554 I:553	????			BIT R/O
		Actual detection state of a current limit while ON for DO10:0=OK			
CURRENT LIMIT DETECTION STATUS WHILE ON DO11	1x00555 2x00555 I:554	????			BIT R/O
		Actual detection state of a current limit while ON for DO11:0=OK			
CURRENT LIMIT DETECTION STATUS WHILE ON DO12	1x00556 2x00556 I:555	????			BIT R/O
		Actual detection state of a current limit while ON for DO12:0=OK			
CURRENT LIMIT DETECTION STATUS WHILE ON DO13	1x00557 2x00557 I:556	????			BIT R/O
		Actual detection state of a current limit while ON for DO13:0=OK			
CURRENT LIMIT DETECTION STATUS WHILE ON DO14	1x00558 2x00558 I:557	????			BIT R/O
		Actual detection state of a current limit while ON for DO14:0=OK			

CURRENT LIMIT DETECTION STATUS WHILE ON DO15	1x00559 2x00559 I:558	????			BIT R/O	
		Actual detection state of a current limit while ON for DO15:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO16	1x00560 2x00560 I:559	????			BIT R/O	
		Actual detection state of a current limit while ON for DO16:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO17	1x00561 2x00561 I:560	????			BIT R/O	
		Actual detection state of a current limit while ON for DO17:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO18	1x00562 2x00562 I:561	????			BIT R/O	
		Actual detection state of a current limit while ON for DO18:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO19	1x00563 2x00563 I:562	????			BIT R/O	
		Actual detection state of a current limit while ON for DO19:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO20	1x00564 2x00564 I:563	????			BIT R/O	
		Actual detection state of a current limit while ON for DO20:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO21	1x00565 2x00565 I:564	????			BIT R/O	
		Actual detection state of a current limit while ON for DO21:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO22	1x00566 2x00566 I:565	????			BIT R/O	
		Actual detection state of a current limit while ON for DO22:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO23	1x00567 2x00567 I:566	????			BIT R/O	
		Actual detection state of a current limit while ON for DO23:0=OK				

CURRENT LIMIT DETECTION STATUS WHILE ON DO24	1x00568 2x00568 I:567	????			BIT R/O	
		Actual detection state of a current limit while ON for DO24:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO25	1x00569 2x00569 I:568	????			BIT R/O	
		Actual detection state of a current limit while ON for DO25:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO26	1x00570 2x00570 I:569	????			BIT R/O	
		Actual detection state of a current limit while ON for DO26:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO27	1x00571 2x00571 I:570	????			BIT R/O	
		Actual detection state of a current limit while ON for DO27:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO28	1x00572 2x00572 I:571	????			BIT R/O	
		Actual detection state of a current limit while ON for DO28:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO29	1x00573 2x00573 I:572	????			BIT R/O	
		Actual detection state of a current limit while ON for DO29:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO30	1x00574 2x00574 I:573	????			BIT R/O	
		Actual detection state of a current limit while ON for DO30:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO31	1x00575 2x00575 I:574	????			BIT R/O	
		Actual detection state of a current limit while ON for DO31:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO32	1x00576 2x00576 I:575	????			BIT R/O	
		Actual detection state of a current limit while ON for DO32:0=OK				



CURRENT LIMIT DETECTION STATUS WHILE ON DO33	1x00577 2x00577 I:576	????			BIT R/O	
		Actual detection state of a current limit while ON for DO33:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO34	1x00578 2x00578 I:577	????			BIT R/O	
		Actual detection state of a current limit while ON for DO34:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO35	1x00579 2x00579 I:578	????			BIT R/O	
		Actual detection state of a current limit while ON for DO35:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO36	1x00580 2x00580 I:579	????			BIT R/O	
		Actual detection state of a current limit while ON for DO36:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO37	1x00581 2x00581 I:580	????			BIT R/O	
		Actual detection state of a current limit while ON for DO37:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO38	1x00582 2x00582 I:581	????			BIT R/O	
		Actual detection state of a current limit while ON for DO38:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO39	1x00583 2x00583 I:582	????			BIT R/O	
		Actual detection state of a current limit while ON for DO39:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO40	1x00584 2x00584 I:583	????			BIT R/O	
		Actual detection state of a current limit while ON for DO40:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO41	1x00585 2x00585 I:584	????			BIT R/O	
		Actual detection state of a current limit while ON for DO41:0=OK				

CURRENT LIMIT DETECTION STATUS WHILE ON DO42	1x00586 2x00586 I:585	????			BIT R/O	
		Actual detection state of a current limit while ON for DO42:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO43	1x00587 2x00587 I:586	????			BIT R/O	
		Actual detection state of a current limit while ON for DO43:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO44	1x00588 2x00588 I:587	????			BIT R/O	
		Actual detection state of a current limit while ON for DO44:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO45	1x00589 2x00589 I:588	????			BIT R/O	
		Actual detection state of a current limit while ON for DO45:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO46	1x00590 2x00590 I:589	????			BIT R/O	
		Actual detection state of a current limit while ON for DO46:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO47	1x00591 2x00591 I:590	????			BIT R/O	
		Actual detection state of a current limit while ON for DO47:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO48	1x00592 2x00592 I:591	????			BIT R/O	
		Actual detection state of a current limit while ON for DO48:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO49	1x00593 2x00593 I:592	????			BIT R/O	
		Actual detection state of a current limit while ON for DO49:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO50	1x00594 2x00594 I:593	????			BIT R/O	
		Actual detection state of a current limit while ON for DO50:0=OK				

CURRENT LIMIT DETECTION STATUS WHILE ON DO51	1x00595 2x00595 I:594	????			BIT R/O	
		Actual detection state of a current limit while ON for DO51:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO52	1x00596 2x00596 I:595	????			BIT R/O	
		Actual detection state of a current limit while ON for DO52:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO53	1x00597 2x00597 I:596	????			BIT R/O	
		Actual detection state of a current limit while ON for DO53:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO54	1x00598 2x00598 I:597	????			BIT R/O	
		Actual detection state of a current limit while ON for DO54:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO55	1x00599 2x00599 I:598	????			BIT R/O	
		Actual detection state of a current limit while ON for DO55:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO56	1x00600 2x00600 I:599	????			BIT R/O	
		Actual detection state of a current limit while ON for DO56:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO57	1x00601 2x00601 I:600	????			BIT R/O	
		Actual detection state of a current limit while ON for DO57:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO58	1x00602 2x00602 I:601	????			BIT R/O	
		Actual detection state of a current limit while ON for DO58:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO59	1x00603 2x00603 I:602	????			BIT R/O	
		Actual detection state of a current limit while ON for DO59:0=OK				

CURRENT LIMIT DETECTION STATUS WHILE ON DO60	1x00604 2x00604 I:603	????			BIT R/O	
		Actual detection state of a current limit while ON for DO60:0=OK				
<b>DIGITAL OUTPUTS: GLOBAL ERRORS</b>						
<b>CHIP #1:DO1-DO7</b>						
GLOBAL ERRORS BIT 0	1x00605 2x00605 I:604	????			BIT R/O	
		BIT 0:Internal under voltage detected:0=OK				
GLOBAL ERRORS BIT 1	1x00606 2x00606 I:605	????			BIT R/O	
		BIT 1:VA under voltage detected (<2.3V):0=OK				
GLOBAL ERRORS BIT 2	1x00607 2x00607 I:606	????			BIT R/O	
		BIT 2:VDD not good detected (<17V):0=OK				
GLOBAL ERRORS BIT 3	1x00608 2x00608 I:607	????			BIT R/O	
		BIT 3:VDD warning detected (<12V):0=OK				
GLOBAL ERRORS BIT 4	1x00609 2x00609 I:608	????			BIT R/O	
		BIT 4:VDD under voltage detected (<8V):0=OK				
GLOBAL ERRORS BIT 5	1x00610 2x00610 I:609	????			BIT R/O	
		BIT 5:Thermal shutdown:0=OK				
GLOBAL ERRORS BIT 6	1x00611 2x00611 I:610	????			BIT R/O	
		BIT 6:Synchronisation error detected:0=OK				
GLOBAL ERRORS BIT 7	1x00612 2x00612 I:611	????			BIT R/O	
		BIT 7:Watchdog error detected:0=OK				
The global error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
<b>DIGITAL OUTPUTS: GLOBAL ERRORS</b>						
<b>CHIP #2:DO8-DO15</b>						
GLOBAL ERRORS BIT 0	1x00613 2x00613 I:612	????			BIT R/O	

		BIT 0:Internal under voltage detected:0=OK				
GLOBAL ERRORS BIT 1	1x00614 2x00614 I:613	????			BIT R/O	
		BIT 1:VA under voltage detected (<2.3V):0=OK				
GLOBAL ERRORS BIT 2	1x00615 2x00615 I:614	????			BIT R/O	
		BIT 2:VDD not good detected (<17V):0=OK				
GLOBAL ERRORS BIT 3	1x00616 2x00616 I:615	????			BIT R/O	
		BIT 3:VDD warning detected (<12V):0=OK				
GLOBAL ERRORS BIT 4	1x00617 2x00617 I:616	????			BIT R/O	
		BIT 4:VDD under voltage detected (<8V):0=OK				
GLOBAL ERRORS BIT 5	1x00618 2x00618 I:617	????			BIT R/O	
		BIT 5:Thermal shutdown:0=OK				
GLOBAL ERRORS BIT 6	1x00619 2x00619 I:618	????			BIT R/O	
		BIT 6:Synchronisation error detected:0=OK				
GLOBAL ERRORS BIT 7	1x00620 2x00620 I:619	????			BIT R/O	
		BIT 7:Watchdog error detected:0=OK				
The global error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
<b>DIGITAL OUTPUTS: GLOBAL ERRORS</b>						
<b>CHIP #3:DO16-DO22</b>						
GLOBAL ERRORS BIT 0	1x00621 2x00621 I:620	????			BIT R/O	
		BIT 0:Internal under voltage detected:0=OK				
GLOBAL ERRORS BIT 1	1x00622 2x00622 I:621	????			BIT R/O	
		BIT 1:VA under voltage detected (<2.3V):0=OK				
GLOBAL ERRORS BIT 2	1x00623 2x00623 I:622	????			BIT R/O	
		BIT 2:VDD not good detected (<17V):0=OK				

GLOBAL ERRORS BIT 3	1x00624 2x00624 I:623	????			BIT R/O	
		BIT 3:VDD warning detected (<12V):0=OK				
GLOBAL ERRORS BIT 4	1x00625 2x00625 I:624	????			BIT R/O	
		BIT 4:VDD under voltage detected (<8V):0=OK				
GLOBAL ERRORS BIT 5	1x00626 2x00626 I:625	????			BIT R/O	
		BIT 5:Thermal shutdown:0=OK				
GLOBAL ERRORS BIT 6	1x00627 2x00627 I:626	????			BIT R/O	
		BIT 6:Synchronisation error detected:0=OK				
GLOBAL ERRORS BIT 7	1x00628 2x00628 I:627	????			BIT R/O	
		BIT 7:Watchdog error detected:0=OK				
The global error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
<b>DIGITAL OUTPUTS: GLOBAL ERRORS</b>						
<b>CHIP #4:DO23-DO30</b>						
GLOBAL ERRORS BIT 0	1x00629 2x00629 I:628	????			BIT R/O	
		BIT 0:Internal under voltage detected:0=OK				
GLOBAL ERRORS BIT 1	1x00630 2x00630 I:629	????			BIT R/O	
		BIT 1:VA under voltage detected (<2.3V):0=OK				
GLOBAL ERRORS BIT 2	1x00631 2x00631 I:630	????			BIT R/O	
		BIT 2:VDD not good detected (<17V):0=OK				
GLOBAL ERRORS BIT 3	1x00632 2x00632 I:631	????			BIT R/O	
		BIT 3:VDD warning detected (<12V):0=OK				
GLOBAL ERRORS BIT 4	1x00633 2x00633 I:632	????			BIT R/O	
		BIT 4:VDD under voltage detected (<8V):0=OK				
GLOBAL ERRORS BIT 5	1x00634 2x00634 I:633	????			BIT R/O	

		BIT 5:Thermal shutdown:0=OK				
GLOBAL ERRORS BIT 6	1x00635 2x00635 I:634	????			BIT R/O	
		BIT 6:Synchronisation error detected:0=OK				
GLOBAL ERRORS BIT 7	1x00636 2x00636 I:635	????			BIT R/O	
		BIT 7:Watchdog error detected:0=OK				
The global error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
<b>DIGITAL OUTPUTS: GLOBAL ERRORS</b>						
<b>CHIP #5:DO31-DO38</b>						
GLOBAL ERRORS BIT 0	1x00637 2x00637 I:636	????			BIT R/O	
		BIT 0:Internal under voltage detected:0=OK				
GLOBAL ERRORS BIT 1	1x00638 2x00638 I:637	????			BIT R/O	
		BIT 1:VA under voltage detected (<2.3V):0=OK				
GLOBAL ERRORS BIT 2	1x00639 2x00639 I:638	????			BIT R/O	
		BIT 2:VDD not good detected (<17V):0=OK				
GLOBAL ERRORS BIT 3	1x00640 2x00640 I:639	????			BIT R/O	
		BIT 3:VDD warning detected (<12V):0=OK				
GLOBAL ERRORS BIT 4	1x00641 2x00641 I:640	????			BIT R/O	
		BIT 4:VDD under voltage detected (<8V):0=OK				
GLOBAL ERRORS BIT 5	1x00642 2x00642 I:641	????			BIT R/O	
		BIT 5:Thermal shutdown:0=OK				
GLOBAL ERRORS BIT 6	1x00643 2x00643 I:642	????			BIT R/O	
		BIT 6:Synchronisation error detected:0=OK				
GLOBAL ERRORS BIT 7	1x00644 2x00644 I:643	????			BIT R/O	
		BIT 7:Watchdog error detected:0=OK				
The global error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						

DIGITAL OUTPUTS: GLOBAL ERRORS						
CHIP #6:DO39-DO45						
GLOBAL ERRORS BIT 0	1x00645 2x00645 I:644	????				BIT R/O
BIT 0:Internal under voltage detected:0=OK						
GLOBAL ERRORS BIT 1	1x00646 2x00646 I:645	????				BIT R/O
BIT 1:VA under voltage detected (<2.3V):0=OK						
GLOBAL ERRORS BIT 2	1x00647 2x00647 I:646	????				BIT R/O
BIT 2:VDD not good detected (<17V):0=OK						
GLOBAL ERRORS BIT 3	1x00648 2x00648 I:647	????				BIT R/O
BIT 3:VDD warning detected (<12V):0=OK						
GLOBAL ERRORS BIT 4	1x00649 2x00649 I:648	????				BIT R/O
BIT 4:VDD under voltage detected (<8V):0=OK						
GLOBAL ERRORS BIT 5	1x00650 2x00650 I:649	????				BIT R/O
BIT 5:Thermal shutdown:0=OK						
GLOBAL ERRORS BIT 6	1x00651 2x00651 I:650	????				BIT R/O
BIT 6:Synchronisation error detected:0=OK						
GLOBAL ERRORS BIT 7	1x00652 2x00652 I:651	????				BIT R/O
BIT 7:Watchdog error detected:0=OK						
The global error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
DIGITAL OUTPUTS: GLOBAL ERRORS						
CHIP #7:DO46-DO53						
GLOBAL ERRORS BIT 0	1x00653 2x00653 I:652	????				BIT R/O
BIT 0:Internal under voltage detected:0=OK						
GLOBAL ERRORS BIT 1	1x00654 2x00654 I:653	????				BIT R/O
BIT 1:VA under voltage detected (<2.3V):0=OK						



GLOBAL ERRORS BIT 2	1x00655 2x00655 I:654	????			BIT R/O	
BIT 2:VDD not good detected (<17V):0=OK						
GLOBAL ERRORS BIT 3	1x00656 2x00656 I:655	????			BIT R/O	
BIT 3:VDD warning detected (<12V):0=OK						
GLOBAL ERRORS BIT 4	1x00657 2x00657 I:656	????			BIT R/O	
BIT 4:VDD under voltage detected (<8V):0=OK						
GLOBAL ERRORS BIT 5	1x00658 2x00658 I:657	????			BIT R/O	
BIT 5:Thermal shutdown:0=OK						
GLOBAL ERRORS BIT 6	1x00659 2x00659 I:658	????			BIT R/O	
BIT 6:Synchronisation error detected:0=OK						
GLOBAL ERRORS BIT 7	1x00660 2x00660 I:659	????			BIT R/O	
BIT 7:Watchdog error detected:0=OK						

The global error state for the output group. Each bit stands for a different error  
=0:No fault, =1:Fault

**DIGITAL OUTPUTS: GLOBAL ERRORS****CHIP #8:DO54-DO60**

GLOBAL ERRORS BIT 0	1x00661 2x00661 I:660	????			BIT R/O	
BIT 0:Internal under voltage detected:0=OK						
GLOBAL ERRORS BIT 1	1x00662 2x00662 I:661	????			BIT R/O	
BIT 1:VA under voltage detected (<2.3V):0=OK						
GLOBAL ERRORS BIT 2	1x00663 2x00663 I:662	????			BIT R/O	
BIT 2:VDD not good detected (<17V):0=OK						
GLOBAL ERRORS BIT 3	1x00664 2x00664 I:663	????			BIT R/O	
BIT 3:VDD warning detected (<12V):0=OK						
GLOBAL ERRORS BIT 4	1x00665 2x00665 I:664	????			BIT R/O	

		BIT 4:VDD under voltage detected (<8V):0=OK				
GLOBAL ERRORS BIT 5	1x00666 2x00666 I:665	????			BIT R/O	
		BIT 5:Thermal shutdown:0=OK				
GLOBAL ERRORS BIT 6	1x00667 2x00667 I:666	????			BIT R/O	
		BIT 6:Synchronisation error detected:0=OK				
GLOBAL ERRORS BIT 7	1x00668 2x00668 I:667	????			BIT R/O	
		BIT 7:Watchdog error detected:0=OK				
The global error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
<b>DIGITAL OUTPUTS: INTERRUPT STATUS</b>						
<b>CHIP #1:DO1-DO7</b>						
INTERRUPT STATUS BIT 0	1x00669 2x00669 I:668	????			BIT R/O	
		BIT 0:Overload detected:0=OK				
INTERRUPT STATUS BIT 1	1x00670 2x00670 I:669	????			BIT R/O	
		BIT 1:Current limit detected:0=OK				
INTERRUPT STATUS BIT 2	1x00671 2x00671 I:670	????			BIT R/O	
		BIT 2:Open wire while OFF detected:0=OK				
INTERRUPT STATUS BIT 3	1x00672 2x00672 I:671	????			BIT R/O	
		BIT 3:Open wire while ON detected:0=OK				
INTERRUPT STATUS BIT 4	1x00673 2x00673 I:672	????			BIT R/O	
		BIT 4:Short to VDD while ON detected:0=OK				
INTERRUPT STATUS BIT 5	1x00674 2x00674 I:673	????			BIT R/O	
		BIT 5:Thermal error detected-shutdown:0=OK				
INTERRUPT STATUS BIT 6	1x00675 2x00675 I:674	????			BIT R/O	
		BIT 6:Supply error detected:0=OK				

INTERRUPT STATUS BIT 7	1x00676 2x00676 I:675	????			BIT R/O	
BIT 7:Communication error detected:0=OK						
The global interrupt error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
<b>DIGITAL OUTPUTS: INTERRUPT STATUS</b>						
<b>CHIP #2:DO8-DO15</b>						
INTERRUPT STATUS BIT 0	1x00677 2x00677 I:676	????			BIT R/O	
BIT 0:Overload detected:0=OK						
INTERRUPT STATUS BIT 1	1x00678 2x00678 I:677	????			BIT R/O	
BIT 1:Current limit detected:0=OK						
INTERRUPT STATUS BIT 2	1x00679 2x00679 I:678	????			BIT R/O	
BIT 2:Open wire while OFF detected:0=OK						
INTERRUPT STATUS BIT 3	1x00680 2x00680 I:679	????			BIT R/O	
BIT 3:Open wire while ON detected:0=OK						
INTERRUPT STATUS BIT 4	1x00681 2x00681 I:680	????			BIT R/O	
BIT 4:Short to VDD while ON detected:0=OK						
INTERRUPT STATUS BIT 5	1x00682 2x00682 I:681	????			BIT R/O	
BIT 5:Thermal error detected-shutdown:0=OK						
INTERRUPT STATUS BIT 6	1x00683 2x00683 I:682	????			BIT R/O	
BIT 6:Supply error detected:0=OK						
INTERRUPT STATUS BIT 7	1x00684 2x00684 I:683	????			BIT R/O	
BIT 7:Communication error detected:0=OK						
The global interrupt error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
<b>DIGITAL OUTPUTS: INTERRUPT STATUS</b>						
<b>CHIP #3:DO16-DO22</b>						
INTERRUPT STATUS BIT 0	1x00685 2x00685 I:684	????			BIT R/O	

		BIT 0:Overload detected:0=OK				
INTERRUPT STATUS BIT 1	1x00686 2x00686 I:685	????			BIT R/O	
		BIT 1:Current limit detected:0=OK				
INTERRUPT STATUS BIT 2	1x00687 2x00687 I:686	????			BIT R/O	
		BIT 2:Open wire while OFF detected:0=OK				
INTERRUPT STATUS BIT 3	1x00688 2x00688 I:687	????			BIT R/O	
		BIT 3:Open wire while ON detected:0=OK				
INTERRUPT STATUS BIT 4	1x00689 2x00689 I:688	????			BIT R/O	
		BIT 4:Short to VDD while ON detected:0=OK				
INTERRUPT STATUS BIT 5	1x00690 2x00690 I:689	????			BIT R/O	
		BIT 5:Thermal error detected-shutdown:0=OK				
INTERRUPT STATUS BIT 6	1x00691 2x00691 I:690	????			BIT R/O	
		BIT 6:Supply error detected:0=OK				
INTERRUPT STATUS BIT 7	1x00692 2x00692 I:691	????			BIT R/O	
		BIT 7:Communication error detected:0=OK				
The global interrupt error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
<b>DIGITAL OUTPUTS: INTERRUPT STATUS</b>						
<b>CHIP #4:DO23-DO30</b>						
INTERRUPT STATUS BIT 0	1x00693 2x00693 I:692	????			BIT R/O	
		BIT 0:Overload detected:0=OK				
INTERRUPT STATUS BIT 1	1x00694 2x00694 I:693	????			BIT R/O	
		BIT 1:Current limit detected:0=OK				
INTERRUPT STATUS BIT 2	1x00695 2x00695 I:694	????			BIT R/O	
		BIT 2:Open wire while OFF detected:0=OK				

INTERRUPT STATUS BIT 3	1x00696 2x00696 I:695	????			BIT R/O	
BIT 3:Open wire while ON detected:0=OK						
INTERRUPT STATUS BIT 4	1x00697 2x00697 I:696	????			BIT R/O	
BIT 4:Short to VDD while ON detected:0=OK						
INTERRUPT STATUS BIT 5	1x00698 2x00698 I:697	????			BIT R/O	
BIT 5:Thermal error detected-shutdown:0=OK						
INTERRUPT STATUS BIT 6	1x00699 2x00699 I:698	????			BIT R/O	
BIT 6:Supply error detected:0=OK						
INTERRUPT STATUS BIT 7	1x00700 2x00700 I:699	????			BIT R/O	
BIT 7:Communication error detected:0=OK						

The global interrupt error state for the output group. Each bit stands for a different error  
=0:No fault, =1:Fault

**DIGITAL OUTPUTS: INTERRUPT STATUS****CHIP #5:DO31-DO38**

INTERRUPT STATUS BIT 0	1x00701 2x00701 I:700	????			BIT R/O	
BIT 0:Overload detected:0=OK						
INTERRUPT STATUS BIT 1	1x00702 2x00702 I:701	????			BIT R/O	
BIT 1:Current limit detected:0=OK						
INTERRUPT STATUS BIT 2	1x00703 2x00703 I:702	????			BIT R/O	
BIT 2:Open wire while OFF detected:0=OK						
INTERRUPT STATUS BIT 3	1x00704 2x00704 I:703	????			BIT R/O	
BIT 3:Open wire while ON detected:0=OK						
INTERRUPT STATUS BIT 4	1x00705 2x00705 I:704	????			BIT R/O	
BIT 4:Short to VDD while ON detected:0=OK						
INTERRUPT STATUS BIT 5	1x00706 2x00706 I:705	????			BIT R/O	

		BIT 5:Thermal error detected-shutdown:0=OK				
INTERRUPT STATUS BIT 6	1x00707 2x00707 I:706	????			BIT R/O	
		BIT 6:Supply error detected:0=OK				
INTERRUPT STATUS BIT 7	1x00708 2x00708 I:707	????			BIT R/O	
		BIT 7:Communication error detected:0=OK				
The global interrupt error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
<b>DIGITAL OUTPUTS: INTERRUPT STATUS</b>						
<b>CHIP #6:DO39-DO45</b>						
INTERRUPT STATUS BIT 0	1x00709 2x00709 I:708	????			BIT R/O	
		BIT 0:Overload detected:0=OK				
INTERRUPT STATUS BIT 1	1x00710 2x00710 I:709	????			BIT R/O	
		BIT 1:Current limit detected:0=OK				
INTERRUPT STATUS BIT 2	1x00711 2x00711 I:710	????			BIT R/O	
		BIT 2:Open wire while OFF detected:0=OK				
INTERRUPT STATUS BIT 3	1x00712 2x00712 I:711	????			BIT R/O	
		BIT 3:Open wire while ON detected:0=OK				
INTERRUPT STATUS BIT 4	1x00713 2x00713 I:712	????			BIT R/O	
		BIT 4:Short to VDD while ON detected:0=OK				
INTERRUPT STATUS BIT 5	1x00714 2x00714 I:713	????			BIT R/O	
		BIT 5:Thermal error detected-shutdown:0=OK				
INTERRUPT STATUS BIT 6	1x00715 2x00715 I:714	????			BIT R/O	
		BIT 6:Supply error detected:0=OK				
INTERRUPT STATUS BIT 7	1x00716 2x00716 I:715	????			BIT R/O	
		BIT 7:Communication error detected:0=OK				
The global interrupt error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						

**DIGITAL OUTPUTS: INTERRUPT STATUS**

CHIP #7:DO46-DO53

INTERRUPT STATUS BIT 0	1x00717 2x00717 I:716	????			BIT R/O	
BIT 0:Overload detected:0=OK						
INTERRUPT STATUS BIT 1	1x00718 2x00718 I:717	????			BIT R/O	
BIT 1:Current limit detected:0=OK						
INTERRUPT STATUS BIT 2	1x00719 2x00719 I:718	????			BIT R/O	
BIT 2:Open wire while OFF detected:0=OK						
INTERRUPT STATUS BIT 3	1x00720 2x00720 I:719	????			BIT R/O	
BIT 3:Open wire while ON detected:0=OK						
INTERRUPT STATUS BIT 4	1x00721 2x00721 I:720	????			BIT R/O	
BIT 4:Short to VDD while ON detected:0=OK						
INTERRUPT STATUS BIT 5	1x00722 2x00722 I:721	????			BIT R/O	
BIT 5:Thermal error detected-shutdown:0=OK						
INTERRUPT STATUS BIT 6	1x00723 2x00723 I:722	????			BIT R/O	
BIT 6:Supply error detected:0=OK						
INTERRUPT STATUS BIT 7	1x00724 2x00724 I:723	????			BIT R/O	
BIT 7:Communication error detected:0=OK						

The global interrupt error state for the output group. Each bit stands for a different error

=0:No fault, =1:Fault

**DIGITAL OUTPUTS: INTERRUPT STATUS**

CHIP #8:DO54-DO60

INTERRUPT STATUS BIT 0	1x00725 2x00725 I:724	????			BIT R/O	
BIT 0:Overload detected:0=OK						
INTERRUPT STATUS BIT 1	1x00726 2x00726 I:725	????			BIT R/O	
BIT 1:Current limit detected:0=OK						

INTERRUPT STATUS BIT 2	1x00727 2x00727 I:726	????			BIT R/O	
BIT 2:Open wire while OFF detected:0=OK						
INTERRUPT STATUS BIT 3	1x00728 2x00728 I:727	????			BIT R/O	
BIT 3:Open wire while ON detected:0=OK						
INTERRUPT STATUS BIT 4	1x00729 2x00729 I:728	????			BIT R/O	
BIT 4:Short to VDD while ON detected:0=OK						
INTERRUPT STATUS BIT 5	1x00730 2x00730 I:729	????			BIT R/O	
BIT 5:Thermal error detected-shutdown:0=OK						
INTERRUPT STATUS BIT 6	1x00731 2x00731 I:730	????			BIT R/O	
BIT 6:Supply error detected:0=OK						
INTERRUPT STATUS BIT 7	1x00732 2x00732 I:731	????			BIT R/O	
BIT 7:Communication error detected:0=OK						
The global interrupt error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
<b>SPI COMMUNICATION DIGITAL OUTPUTS</b>						
SPI COMMUNICATION CHIP #1: DO1-DO7	1x00733 2x00733 I:732	????			BIT R/O	
Actual SPI communication state:0=NO FAULT						
The current monitoring state of the SPI communication for the digital output group =0:No fault, =1:Fault						
SPI COMMUNICATION CHIP #2: DO8-DO15	1x00734 2x00734 I:733	????			BIT R/O	
Actual SPI communication state:0=NO FAULT						
The current monitoring state of the SPI communication for the digital output group =0:No fault, =1:Fault						
SPI COMMUNICATION CHIP #3: DO16-DO22	1x00735 2x00735 I:734	????			BIT R/O	
Actual SPI communication state:0=NO FAULT						
The current monitoring state of the SPI communication for the digital output group =0:No fault, =1:Fault						
SPI COMMUNICATION CHIP #4: DO23-DO30	1x00736 2x00736 I:735	????			BIT R/O	



		Actual SPI communication state:0=NO FAULT				
SPI COMMUNICATION CHIP #5: DO31-DO38	1x00737 2x00737 l:736	????			BIT R/O	
		Actual SPI communication state:0=NO FAULT				
The current monitoring state of the SPI communication for the digital output group =0:No fault, =1:Fault						
SPI COMMUNICATION CHIP #6: DO39-DO45	1x00738 2x00738 l:737	????			BIT R/O	
		Actual SPI communication state:0=NO FAULT				
The current monitoring state of the SPI communication for the digital output group =0:No fault, =1:Fault						
SPI COMMUNICATION CHIP #7: DO46-DO53	1x00739 2x00739 l:738	????			BIT R/O	
		Actual SPI communication state:0=NO FAULT				
The current monitoring state of the SPI communication for the digital output group =0:No fault, =1:Fault						
SPI COMMUNICATION CHIP #8: DO54-DO60	1x00740 2x00740 l:739	????			BIT R/O	
		Actual SPI communication state:0=NO FAULT				
<b>DIGITAL INPUTS: RESET</b>						
RESET COUNTERS	1x10000 2x10000 l:9999	????		1:PERFORM RESET	BIT R/W	NO
If this register is written to 1, all internal edge counters and event counters are set to 0. 0 is always returned when reading.						
<b>STATUS REAL DIGITAL INPUTS</b>						
DI1	1x15001 2x14001 l:15000	????			BIT R/O	
		Actual state of DI1:0=OFF				
Current state of the digital input DIx with the internal software filter to suppress glitches or spike on thie line =0:DI is OFF, =1:DI is ON						
DI2	1x15002 2x15002 l:15001	????			BIT R/O	
		Actual state of DI2:0=OFF				
DI3	1x15003 2x15003 l:15002	????			BIT R/O	
		Actual state of DI3:0=OFF				
DI4	1x15004 2x15004 l:15003	????			BIT R/O	
		Actual state of DI4:0=OFF				

DI5	1x15005 2x15005 I:15004	????			BIT R/O	
		Actual state of DI5:0=OFF				
DI6	1x15006 2x15006 I:15005	????			BIT R/O	
		Actual state of DI6:0=OFF				
DI7	1x15007 2x15007 I:15006	????			BIT R/O	
		Actual state of DI7:0=OFF				
DI8	1x15008 2x15008 I:15007	????			BIT R/O	
		Actual state of DI8:0=OFF				
DI9	1x15009 2x15009 I:15008	????			BIT R/O	
		Actual state of DI9:0=OFF				
DI10	1x15010 2x15010 I:15009	????			BIT R/O	
		Actual state of DI10:0=OFF				
DI11	1x15011 2x15011 I:15010	????			BIT R/O	
		Actual state of DI11:0=OFF				
DI12	1x15012 2x15012 I:15011	????			BIT R/O	
		Actual state of DI12:0=OFF				
DI13	1x15013 2x15013 I:15012	????			BIT R/O	
		Actual state of DI13:0=OFF				
DI14	1x15014 2x15014 I:15013	????			BIT R/O	
		Actual state of DI14:0=OFF				
DI15	1x15015 2x15015 I:15014	????			BIT R/O	
		Actual state of DI15:0=OFF				
DI16	1x15016 2x15016 I:15015	????			BIT R/O	

		Actual state of DI16:0=OFF				
DI17	1x15017 2x15017 I:15016	????			BIT R/O	
		Actual state of DI17:0=OFF				
DI18	1x15018 2x15018 I:15017	????			BIT R/O	
		Actual state of DI18:0=OFF				
DI19	1x15019 2x15019 I:15018	????			BIT R/O	
		Actual state of DI19:0=OFF				
DI20	1x15020 2x15020 I:15019	????			BIT R/O	
		Actual state of DI20:0=OFF				
DI21	1x15021 2x15021 I:15020	????			BIT R/O	
		Actual state of DI21:0=OFF				
DI22	1x15022 2x15022 I:15021	????			BIT R/O	
		Actual state of DI22:0=OFF				
D23	1x15023 2x15023 I:15022	????			BIT R/O	
		Actual state of D23:0=OFF				
DI24	1x15024 2x15024 I:15023	????			BIT R/O	
		Actual state of DI24:0=OFF				
DI25	1x15025 2x15025 I:15024	????			BIT R/O	
		Actual state of DI25:0=OFF				
DI26	1x15026 2x15026 I:15025	????			BIT R/O	
		Actual state of DI26:0=OFF				
DI27	1x15027 2x15027 I:15026	????			BIT R/O	
		Actual state of DI27:0=OFF				

DI28	1x15028 2x15028 I:15027	????			BIT R/O	
		Actual state of DI28:0=OFF				
DI29	1x15029 2x15029 I:15028	????			BIT R/O	
		Actual state of DI29:0=OFF				
DI30	1x15030 2x15030 I:15029	????			BIT R/O	
		Actual state of DI30:0=OFF				
DI31	1x15031 2x15031 I:15030	????			BIT R/O	
		Actual state of DI31:0=OFF				
DI32	1x15032 2x15032 I:15031	????			BIT R/O	
		Actual state of DI32:0=OFF				
DI33	1x15033 2x15033 I:15032	????			BIT R/O	
		Actual state of DI33:0=OFF				
DI34	1x15034 2x15034 I:15033	????			BIT R/O	
		Actual state of DI34:0=OFF				
DI35	1x15035 2x15035 I:15034	????			BIT R/O	
		Actual state of DI35:0=OFF				
DI36	1x15036 2x15036 I:15035	????			BIT R/O	
		Actual state of DI36:0=OFF				
DI37	1x15037 2x15037 I:15036	????			BIT R/O	
		Actual state of DI37:0=OFF				
DI38	1x15038 2x15038 I:15037	????			BIT R/O	
		Actual state of DI38:0=OFF				
DI39	1x15039 2x15039 I:15038	????			BIT R/O	

		Actual state of DI39:0=OFF				
DI40	1x15040 2x15040 I:15039	????			BIT R/O	
		Actual state of DI40:0=OFF				
DI41	1x15041 2x15041 I:15040	????			BIT R/O	
		Actual state of DI41:0=OFF				
DI42	1x15042 2x15042 I:15041	????			BIT R/O	
		Actual state of DI42:0=OFF				
DI43	1x15043 2x15043 I:15042	????			BIT R/O	
		Actual state of DI43:0=OFF				
DI44	1x15044 2x15044 I:15043	????			BIT R/O	
		Actual state of DI44:0=OFF				
DI45	1x15045 2x15045 I:15044	????			BIT R/O	
		Actual state of DI45:0=OFF				
DI46	1x15046 2x15046 I:15045	????			BIT R/O	
		Actual state of DI46:0=OFF				
DI47	1x15047 2x15047 I:15046	????			BIT R/O	
		Actual state of DI47:0=OFF				
DI48	1x15048 2x15048 I:15047	????			BIT R/O	
		Actual state of DI48:0=OFF				
DI49	1x15049 2x15049 I:15048	????			BIT R/O	
		Actual state of DI49:0=OFF				
DI50	1x15050 2x15050 I:15049	????			BIT R/O	
		Actual state of DI50:0=OFF				

DI51	1x15051 2x15051 I:15050	????			BIT R/O	
		Actual state of DI51:0=OFF				
DI52	1x15052 2x15052 I:15051	????			BIT R/O	
		Actual state of DI52:0=OFF				
DI53	1x15053 2x15053 I:15052	????			BIT R/O	
		Actual state of DI53:0=OFF				
DI54	1x15054 2x15054 I:15053	????			BIT R/O	
		Actual state of DI54:0=OFF				
DI55	1x15055 2x15055 I:15054	????			BIT R/O	
		Actual state of DI55:0=OFF				
DI56	1x15056 2x15056 I:15055	????			BIT R/O	
		Actual state of DI56:0=OFF				
DI57	1x15057 2x15057 I:15056	????			BIT R/O	
		Actual state of DI57:0=OFF				
DI58	1x15058 2x15058 I:15057	????			BIT R/O	
		Actual state of DI58:0=OFF				
DI59	1x15059 2x15059 I:15058	????			BIT R/O	
		Actual state of DI59:0=OFF				
DI60	1x15060 2x15060 I:15059	????			BIT R/O	
		Actual state of DI60:0=OFF				
DI61	1x15061 2x15061 I:15060	????			BIT R/O	
		Actual state of DI61:0=OFF				
DI62	1x15062 2x15062 I:15061	????			BIT R/O	

		Actual state of DI62:0=OFF				
DI63	1x15063 2x15063 I:15062	????			BIT R/O	
		Actual state of DI63:0=OFF				
DI64	1x15064 2x15064 I:15063	????			BIT R/O	
		Actual state of DI64:0=OFF				
<b>STATUS DIGITAL INPUTS</b>						
UNFILTERED DI1	1x15065 2x15065 I:15064	????			BIT R/O	
		Actual state of UNFILTERED DI1:0=OFF				
Current state of the real digital input DIx without the internal software filter to suppress glitches or spike on this line =0:DI is OFF, =1:DI is ON						
UNFILTERED DI2	1x15066 2x15066 I:15065	????			BIT R/O	
		Actual state of UNFILTERED DI2:0=OFF				
UNFILTERED DI3	1x15067 2x15067 I:15066	????			BIT R/O	
		Actual state of UNFILTERED DI3:0=OFF				
UNFILTERED DI4	1x15068 2x15068 I:15067	????			BIT R/O	
		Actual state of UNFILTERED DI4:0=OFF				
UNFILTERED DI5	1x15069 2x15069 I:15068	????			BIT R/O	
		Actual state of UNFILTERED DI5:0=OFF				
UNFILTERED DI6	1x15070 2x15070 I:15069	????			BIT R/O	
		Actual state of UNFILTERED DI6:0=OFF				
UNFILTERED DI7	1x15071 2x15071 I:15070	????			BIT R/O	
		Actual state of UNFILTERED DI7:0=OFF				
UNFILTERED DI8	1x15072 2x15072 I:15071	????			BIT R/O	
		Actual state of UNFILTERED DI8:0=OFF				
UNFILTERED DI9	1x15073 2x15073 I:15072	????			BIT R/O	

		Actual state of UNFILTERED DI9:0=OFF				
UNFILTERED DI10	1x15074 2x15074 I:15073	????			BIT R/O	
		Actual state of UNFILTERED DI10:0=OFF				
UNFILTERED DI11	1x15075 2x15075 I:15074	????			BIT R/O	
		Actual state of UNFILTERED DI11:0=OFF				
UNFILTERED DI12	1x15076 2x15076 I:15075	????			BIT R/O	
		Actual state of UNFILTERED DI12:0=OFF				
UNFILTERED DI13	1x15077 2x15077 I:15076	????			BIT R/O	
		Actual state of UNFILTERED DI13:0=OFF				
UNFILTERED DI14	1x15078 2x15078 I:15077	????			BIT R/O	
		Actual state of UNFILTERED DI14:0=OFF				
UNFILTERED DI15	1x15079 2x15079 I:15078	????			BIT R/O	
		Actual state of UNFILTERED DI15:0=OFF				
UNFILTERED DI16	1x15080 2x15080 I:15079	????			BIT R/O	
		Actual state of UNFILTERED DI16:0=OFF				
UNFILTERED DI17	1x15081 2x15081 I:15080	????			BIT R/O	
		Actual state of UNFILTERED DI17:0=OFF				
UNFILTERED DI18	1x15082 2x15082 I:15081	????			BIT R/O	
		Actual state of UNFILTERED DI18:0=OFF				
UNFILTERED DI19	1x15083 2x15083 I:15082	????			BIT R/O	
		Actual state of UNFILTERED DI19:0=OFF				
UNFILTERED DI20	1x15084 2x15084 I:15083	????			BIT R/O	
		Actual state of UNFILTERED DI20:0=OFF				



UNFILTERED DI21	1x15085 2x15085 I:15084	????			BIT R/O	
		Actual state of UNFILTERED DI21:0=OFF				
UNFILTERED DI22	1x15086 2x15086 I:15085	????			BIT R/O	
		Actual state of UNFILTERED DI22:0=OFF				
UNFILTERED DI23	1x15087 2x15087 I:15086	????			BIT R/O	
		Actual state of UNFILTERED DI23:0=OFF				
UNFILTERED DI24	1x15088 2x15088 I:15087	????			BIT R/O	
		Actual state of UNFILTERED DI24:0=OFF				
UNFILTERED DI25	1x15089 2x15089 I:15088	????			BIT R/O	
		Actual state of UNFILTERED DI25:0=OFF				
UNFILTERED DI26	1x15090 2x15090 I:15089	????			BIT R/O	
		Actual state of UNFILTERED DI26:0=OFF				
UNFILTERED DI27	1x15091 2x15091 I:15090	????			BIT R/O	
		Actual state of UNFILTERED DI27:0=OFF				
UNFILTERED DI28	1x15092 2x15092 I:15091	????			BIT R/O	
		Actual state of UNFILTERED DI28:0=OFF				
UNFILTERED DI29	1x15093 2x15093 I:15092	????			BIT R/O	
		Actual state of UNFILTERED DI29:0=OFF				
UNFILTERED DI30	1x15094 2x15094 I:15093	????			BIT R/O	
		Actual state of UNFILTERED DI30:0=OFF				
UNFILTERED DI31	1x15095 2x15095 I:15094	????			BIT R/O	
		Actual state of UNFILTERED DI31:0=OFF				
UNFILTERED DI32	1x15096 2x15096 I:15095	????			BIT R/O	

		Actual state of UNFILTERED DI32:0=OFF				
UNFILTERED DI33	1x15097 2x15097 I:15096	????			BIT R/O	
		Actual state of UNFILTERED DI33:0=OFF				
UNFILTERED DI34	1x15098 2x15098 I:15097	????			BIT R/O	
		Actual state of UNFILTERED DI34:0=OFF				
UNFILTERED DI35	1x15099 2x15099 I:15098	????			BIT R/O	
		Actual state of UNFILTERED DI35:0=OFF				
UNFILTERED DI36	1x15100 2x15100 I:15099	????			BIT R/O	
		Actual state of UNFILTERED DI36:0=OFF				
UNFILTERED DI37	1x15101 2x15101 I:15100	????			BIT R/O	
		Actual state of UNFILTERED DI37:0=OFF				
UNFILTERED DI38	1x15102 2x15102 I:15101	????			BIT R/O	
		Actual state of UNFILTERED DI38:0=OFF				
UNFILTERED DI39	1x15103 2x15103 I:15102	????			BIT R/O	
		Actual state of UNFILTERED DI39:0=OFF				
UNFILTERED DI40	1x15104 2x15104 I:15103	????			BIT R/O	
		Actual state of UNFILTERED DI40:0=OFF				
UNFILTERED DI41	1x15105 2x15105 I:15104	????			BIT R/O	
		Actual state of UNFILTERED DI41:0=OFF				
UNFILTERED DI42	1x15106 2x15106 I:15105	????			BIT R/O	
		Actual state of UNFILTERED DI42:0=OFF				
UNFILTERED DI43	1x15107 2x15107 I:15106	????			BIT R/O	
		Actual state of UNFILTERED DI43:0=OFF				

UNFILTERED DI44	1x15108 2x15108 I:15107	????			BIT R/O	
		Actual state of UNFILTERED DI44:0=OFF				
UNFILTERED DI45	1x15109 2x15109 I:15108	????			BIT R/O	
		Actual state of UNFILTERED DI45:0=OFF				
UNFILTERED DI46	1x15110 2x15110 I:15109	????			BIT R/O	
		Actual state of UNFILTERED DI46:0=OFF				
UNFILTERED DI47	1x15111 2x15111 I:15110	????			BIT R/O	
		Actual state of UNFILTERED DI47:0=OFF				
UNFILTERED DI48	1x15112 2x15112 I:15111	????			BIT R/O	
		Actual state of UNFILTERED DI48:0=OFF				
UNFILTERED DI49	1x15113 2x15113 I:15112	????			BIT R/O	
		Actual state of UNFILTERED DI49:0=OFF				
UNFILTERED DI50	1x15114 2x15114 I:15113	????			BIT R/O	
		Actual state of UNFILTERED DI50:0=OFF				
UNFILTERED DI51	1x15115 2x15115 I:15114	????			BIT R/O	
		Actual state of UNFILTERED DI51:0=OFF				
UNFILTERED DI52	1x15116 2x15116 I:15115	????			BIT R/O	
		Actual state of UNFILTERED DI52:0=OFF				
UNFILTERED DI53	1x15117 2x15117 I:15116	????			BIT R/O	
		Actual state of UNFILTERED DI53:0=OFF				
UNFILTERED DI54	1x15118 2x15118 I:15117	????			BIT R/O	
		Actual state of UNFILTERED DI54:0=OFF				
UNFILTERED DI55	1x15119 2x15119 I:15118	????			BIT R/O	

		Actual state of UNFILTERED DI55:0=OFF				
UNFILTERED DI56	1x15120 2x15120 I:15119	????			BIT R/O	
		Actual state of UNFILTERED DI56:0=OFF				
UNFILTERED DI57	1x15121 2x15121 I:15120	????			BIT R/O	
		Actual state of UNFILTERED DI57:0=OFF				
UNFILTERED DI58	1x15122 2x15122 I:15121	????			BIT R/O	
		Actual state of UNFILTERED DI58:0=OFF				
UNFILTERED DI59	1x15123 2x15123 I:15122	????			BIT R/O	
		Actual state of UNFILTERED DI59:0=OFF				
UNFILTERED DI60	1x15124 2x15124 I:15123	????			BIT R/O	
		Actual state of UNFILTERED DI60:0=OFF				
UNFILTERED DI61	1x15125 2x15125 I:15124	????			BIT R/O	
		Actual state of UNFILTERED DI61:0=OFF				
UNFILTERED DI62	1x15126 2x15126 I:15125	????			BIT R/O	
		Actual state of UNFILTERED DI62:0=OFF				
UNFILTERED DI63	1x15127 2x15127 I:15126	????			BIT R/O	
		Actual state of UNFILTERED DI63:0=OFF				
UNFILTERED DI64	1x15128 2x15128 I:15127	????			BIT R/O	
		Actual state of UNFILTERED DI64:0=OFF				
<b>STATUS DIGITAL OUTPUTS</b>						
DO1	1x16001 2x16001 I:16000	????		1	BIT R/W	NO
		Actual state of DO1:0=OFF			ENTER NEW STATE (0 or 1)	
Current state of the digital output DOx =0:DO is OFF, =1:DO is ON						
Writing on this register changes the state of the digital output						

DO2	1x16002 2x16002 I:16001	????		0	BIT R/W	NO
		Actual state of DO2:0=OFF		ENTER NEW STATE (0 or 1)		
DO3	1x16003 2x16003 I:16002	????		0	BIT R/W	NO
		Actual state of DO3:0=OFF		ENTER NEW STATE (0 or 1)		
DO4	1x16004 2x16004 I:16003	????		0	BIT R/W	NO
		Actual state of DO4:0=OFF		ENTER NEW STATE (0 or 1)		
DO5	1x16005 2x16005 I:16004	????		0	BIT R/W	NO
		Actual state of DO5:0=OFF		ENTER NEW STATE (0 or 1)		
DO6	1x16006 2x16006 I:16005	????		0	BIT R/W	NO
		Actual state of DO6:0=OFF		ENTER NEW STATE (0 or 1)		
DO7	1x16007 2x16007 I:16006	????		0	BIT R/W	NO
		Actual state of DO7:0=OFF		ENTER NEW STATE (0 or 1)		
DO8	1x16008 2x16008 I:16007	????		0	BIT R/W	NO
		Actual state of DO8:0=OFF		ENTER NEW STATE (0 or 1)		
DO9	1x16009 2x16009 I:16008	????		0	BIT R/W	NO
		Actual state of DO9:0=OFF		ENTER NEW STATE (0 or 1)		
DO10	1x16010 2x16010 I:16009	????		0	BIT R/W	NO
		Actual state of DO10:0=OFF		ENTER NEW STATE (0 or 1)		
DO11	1x16011 2x16011 I:16010	????		0	BIT R/W	NO
		Actual state of DO11:0=OFF		ENTER NEW STATE (0 or 1)		
DO12	1x16012 2x16012 I:16011	????		0	BIT R/W	NO
		Actual state of DO12:0=OFF		ENTER NEW STATE (0 or 1)		

DO13	1x16013 2x16013 I:16012	????		0	BIT R/W	NO
		Actual state of DO13:0=OFF		ENTER NEW STATE (0 or 1)		
DO14	1x16014 2x16014 I:16013	????		0	BIT R/W	NO
		Actual state of DO14:0=OFF		ENTER NEW STATE (0 or 1)		
DO15	1x16015 2x16015 I:16014	????		0	BIT R/W	NO
		Actual state of DO15:0=OFF		ENTER NEW STATE (0 or 1)		
DO16	1x16016 2x16016 I:16015	????		0	BIT R/W	NO
		Actual state of DO16:0=OFF		ENTER NEW STATE (0 or 1)		
DO17	1x16017 2x16017 I:16016	????		0	BIT R/W	NO
		Actual state of DO17:0=OFF		ENTER NEW STATE (0 or 1)		
DO18	1x16018 2x16018 I:16017	????		0	BIT R/W	NO
		Actual state of DO18:0=OFF		ENTER NEW STATE (0 or 1)		
DO19	1x16019 2x16019 I:16018	????		0	BIT R/W	NO
		Actual state of DO19:0=OFF		ENTER NEW STATE (0 or 1)		
DO20	1x16020 2x16020 I:16019	????		0	BIT R/W	NO
		Actual state of DO20:0=OFF		ENTER NEW STATE (0 or 1)		
DO21	1x16021 2x16021 I:16020	????		0	BIT R/W	NO
		Actual state of DO21:0=OFF		ENTER NEW STATE (0 or 1)		
DO22	1x16022 2x16022 I:16021	????		0	BIT R/W	NO
		Actual state of DO22:0=OFF		ENTER NEW STATE (0 or 1)		
DO23	1x16023 2x16023 I:16022	????		0	BIT R/W	NO
		Actual state of DO23:0=OFF		ENTER NEW STATE (0 or 1)		

DO24	1x16024 2x16024 I:16023	????		0	BIT R/W	NO
		Actual state of DO24:0=OFF		ENTER NEW STATE (0 or 1)		
DO25	1x16025 2x16025 I:16024	????		0	BIT R/W	NO
		Actual state of DO25:0=OFF		ENTER NEW STATE (0 or 1)		
DO26	1x16026 2x16026 I:16025	????		0	BIT R/W	NO
		Actual state of DO26:0=OFF		ENTER NEW STATE (0 or 1)		
DO27	1x16027 2x16027 I:16026	????		0	BIT R/W	NO
		Actual state of DO27:0=OFF		ENTER NEW STATE (0 or 1)		
DO28	1x16028 2x16028 I:16027	????		0	BIT R/W	NO
		Actual state of DO28:0=OFF		ENTER NEW STATE (0 or 1)		
DO29	1x16029 2x16029 I:16028	????		0	BIT R/W	NO
		Actual state of DO29:0=OFF		ENTER NEW STATE (0 or 1)		
DO30	1x16030 2x16030 I:16029	????		0	BIT R/W	NO
		Actual state of DO30:0=OFF		ENTER NEW STATE (0 or 1)		
DO31	1x16031 2x16031 I:16030	????		0	BIT R/W	NO
		Actual state of DO31:0=OFF		ENTER NEW STATE (0 or 1)		
DO32	1x16032 2x16032 I:16031	????		0	BIT R/W	NO
		Actual state of DO32:0=OFF		ENTER NEW STATE (0 or 1)		
DO33	1x16033 2x16033 I:16032	????		0	BIT R/W	NO
		Actual state of DO33:0=OFF		ENTER NEW STATE (0 or 1)		
DO34	1x16034 2x16034 I:16033	????		0	BIT R/W	NO
		Actual state of DO34:0=OFF		ENTER NEW STATE (0 or 1)		

DO35	1x16035 2x16035 I:16034	????		0	BIT R/W	NO
		Actual state of DO35:0=OFF		ENTER NEW STATE (0 or 1)		
DO36	1x16036 2x16036 I:16035	????		0	BIT R/W	NO
		Actual state of DO36:0=OFF		ENTER NEW STATE (0 or 1)		
DO37	1x16037 2x16037 I:16036	????		0	BIT R/W	NO
		Actual state of DO37:0=OFF		ENTER NEW STATE (0 or 1)		
DO38	1x16038 2x16038 I:16037	????		0	BIT R/W	NO
		Actual state of DO38:0=OFF		ENTER NEW STATE (0 or 1)		
DO39	1x16039 2x16039 I:16038	????		0	BIT R/W	NO
		Actual state of DO39:0=OFF		ENTER NEW STATE (0 or 1)		
DO40	1x16040 2x16040 I:16039	????		0	BIT R/W	NO
		Actual state of DO40:0=OFF		ENTER NEW STATE (0 or 1)		
DO41	1x16041 2x16041 I:16040	????		0	BIT R/W	NO
		Actual state of DO41:0=OFF		ENTER NEW STATE (0 or 1)		
DO42	1x16042 2x16042 I:16041	????		0	BIT R/W	NO
		Actual state of DO42:0=OFF		ENTER NEW STATE (0 or 1)		
DO43	1x16043 2x16043 I:16042	????		0	BIT R/W	NO
		Actual state of DO43:0=OFF		ENTER NEW STATE (0 or 1)		
DO44	1x16044 2x16044 I:16043	????		0	BIT R/W	NO
		Actual state of DO44:0=OFF		ENTER NEW STATE (0 or 1)		
DO45	1x16045 2x16045 I:16044	????		0	BIT R/W	NO
		Actual state of DO45:0=OFF		ENTER NEW STATE (0 or 1)		



DO46	1x16046 2x16046 I:16045	????		0	BIT R/W	NO
		Actual state of DO46:0=OFF		ENTER NEW STATE (0 or 1)		
DO47	1x16047 2x16047 I:16046	????		0	BIT R/W	NO
		Actual state of DO47:0=OFF		ENTER NEW STATE (0 or 1)		
DO48	1x16048 2x16048 I:16047	????		0	BIT R/W	NO
		Actual state of DO48:0=OFF		ENTER NEW STATE (0 or 1)		
DO49	1x16049 2x16049 I:16048	????		0	BIT R/W	NO
		Actual state of DO49:0=OFF		ENTER NEW STATE (0 or 1)		
DO50	1x16050 2x16050 I:16049	????		0	BIT R/W	NO
		Actual state of DO50:0=OFF		ENTER NEW STATE (0 or 1)		
DO51	1x16051 2x16051 I:16050	????		0	BIT R/W	NO
		Actual state of DO51:0=OFF		ENTER NEW STATE (0 or 1)		
DO52	1x16052 2x16052 I:16051	????		0	BIT R/W	NO
		Actual state of DO52:0=OFF		ENTER NEW STATE (0 or 1)		
DO53	1x16053 2x16053 I:16052	????		0	BIT R/W	NO
		Actual state of DO53:0=OFF		ENTER NEW STATE (0 or 1)		
DO54	1x16054 2x16054 I:16053	????		0	BIT R/W	NO
		Actual state of DO54:0=OFF		ENTER NEW STATE (0 or 1)		
DO55	1x16055 2x16055 I:16054	????		0	BIT R/W	NO
		Actual state of DO55:0=OFF		ENTER NEW STATE (0 or 1)		
DO56	1x16056 2x16056 I:16055	????		0	BIT R/W	NO
		Actual state of DO56:0=OFF		ENTER NEW STATE (0 or 1)		

DO57	1x16057 2x16057 I:16056	????		0	BIT R/W	NO
		Actual state of DO57:0=OFF		ENTER NEW STATE (0 or 1)		
DO58	1x16058 2x16058 I:16057	????		0	BIT R/W	NO
		Actual state of DO58:0=OFF		ENTER NEW STATE (0 or 1)		
DO59	1x16059 2x16059 I:16058	????		0	BIT R/W	NO
		Actual state of DO59:0=OFF		ENTER NEW STATE (0 or 1)		
DO60	1x16060 2x16060 I:16059	????		0	BIT R/W	NO
		Actual state of DO60:0=OFF		ENTER NEW STATE (0 or 1)		

Register NAME Command NAME	MODBUS Register ASCII Command	Register VALUE ASCII Command	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
<b>DIGITAL INPUTS: DIGITAL INPUT HAS CHANGED IT'S STATE</b>						
DI HAS CHANGED DI1	1x20001 2x20001 I:20000	1,0x01 B:01			BIT R/O	
If the digital input has changed this bit inverts its last state						
DI HAS CHANGED DI2	1x20002 2x20002 I:20001	1,0x01 B:01			BIT R/O	
DI HAS CHANGED DI3	1x20003 2x20003 I:20002	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI4	1x20004 2x20004 I:20003	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI5	1x20005 2x20005 I:20004	1,0x01 B:01			BIT R/O	
DI HAS CHANGED DI6	1x20006 2x20006 I:20005	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI7	1x20007 2x20007 I:20006	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI8	1x20008 2x20008 I:20007	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI9	1x20009 2x20009 I:20008	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI10	1x20010 2x20010 I:20009	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI11	1x20011 2x20011 I:20010	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI12	1x20012 2x20012 I:20011	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI13	1x20013 2x20013 I:20012	0,0x00 B:00			BIT R/O	

DI HAS CHANGED DI14	1x20014 2x20014 I:20013	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI15	1x20015 2x20015 I:20014	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI16	1x20016 2x20016 I:20015	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI17	1x20017 2x20017 I:20016	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI18	1x20018 2x20018 I:20017	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI19	1x20019 2x20019 I:20018	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI20	1x20020 2x20020 I:20019	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI21	1x20021 2x20021 I:20020	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI22	1x20022 2x20022 I:20021	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI23	1x20023 2x20023 I:20022	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI24	1x20024 2x20024 I:20023	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI25	1x20025 2x20025 I:20024	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI26	1x20026 2x20026 I:20025	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI27	1x20027 2x20027 I:20026	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI28	1x20028 2x20028 I:20027	0,0x00 B:00			BIT R/O	

DI HAS CHANGED DI29	1x20029 2x20029 I:20028	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI30	1x20030 2x20030 I:20029	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI31	1x20031 2x20031 I:20030	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI32	1x20032 2x20032 I:20031	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI33	1x20033 2x20033 I:20032	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI34	1x20034 2x20034 I:20033	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI35	1x20035 2x20035 I:20034	1,0x01 B:01			BIT R/O	
DI HAS CHANGED DI36	1x20036 2x20036 I:20035	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI37	1x20037 2x20037 I:20036	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI38	1x20038 2x20038 I:20037	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI39	1x20039 2x20039 I:20038	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI40	1x20040 2x20040 I:20039	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI41	1x20041 2x20041 I:20040	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI42	1x20042 2x20042 I:20041	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI43	1x20043 2x20043 I:20042	0,0x00 B:00			BIT R/O	

DI HAS CHANGED DI44	1x20044 2x20044 I:20043	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI45	1x20045 2x20045 I:20044	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI46	1x20046 2x20046 I:20045	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI47	1x20047 2x20047 I:20046	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI48	1x20048 2x20048 I:20047	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI49	1x20049 2x20049 I:20048	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI50	1x20050 2x20050 I:20049	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI51	1x20051 2x20051 I:20050	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI52	1x20052 2x20052 I:20051	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI53	1x20053 2x20053 I:20052	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI54	1x20054 2x20054 I:20053	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI55	1x20055 2x20055 I:20054	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI56	1x20056 2x20056 I:20055	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI57	1x20057 2x20057 I:20056	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI58	1x20058 2x20058 I:20057	0,0x00 B:00			BIT R/O	

DI HAS CHANGED DI59	1x20059 2x20059 I:20058	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI60	1x20060 2x20060 I:20059	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI61	1x20061 2x20061 I:20060	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI62	1x20062 2x20062 I:20061	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI63	1x20063 2x20063 I:20062	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI64	1x20064 2x20064 I:20063	0,0x00 B:00			BIT R/O	
<b>DIGITAL INPUTS: SHORT KEYPRESS EVENT ON DIGITAL INPUT DETECTED</b>						
SHORT KEYPRESS ON DI1	1x20065 2x20065 I:20064	1,0x01 B:01			BIT R/O	
If a short keypress event was detected on the digital input this bit inverts its last state						
SHORT KEYPRESS ON DI2	1x20066 2x20066 I:20065	1,0x01 B:01			BIT R/O	
SHORT KEYPRESS ON DI3	1x20067 2x20067 I:20066	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI4	1x20068 2x20068 I:20067	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI5	1x20069 2x20069 I:20068	1,0x01 B:01			BIT R/O	
SHORT KEYPRESS ON DI6	1x20070 2x20070 I:20069	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI7	1x20071 2x20071 I:20070	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI8	1x20072 2x20072 I:20071	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI9	1x20073 2x20073 I:20072	0,0x00 B:00			BIT R/O	

SHORT KEYPRESS ON DI10	1x20074 2x20074 I:20073	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI11	1x20075 2x20075 I:20074	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI12	1x20076 2x20076 I:20075	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI13	1x20077 2x20077 I:20076	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI14	1x20078 2x20078 I:20077	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI15	1x20079 2x20079 I:20078	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI16	1x20080 2x20080 I:20079	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI17	1x20081 2x20081 I:20080	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI18	1x20082 2x20082 I:20081	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI19	1x20083 2x20083 I:20082	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI20	1x20084 2x20084 I:20083	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI21	1x20085 2x20085 I:20084	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI22	1x20086 2x20086 I:20085	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI23	1x20087 2x20087 I:20086	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI24	1x20088 2x20088 I:20087	0,0x00 B:00			BIT R/O	



SHORT KEYPRESS ON DI25	1x20089 2x20089 I:20088	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI26	1x20090 2x20090 I:20089	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI27	1x20091 2x20091 I:20090	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI28	1x20092 2x20092 I:20091	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI29	1x20093 2x20093 I:20092	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI30	1x20094 2x20094 I:20093	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI31	1x20095 2x20095 I:20094	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI32	1x20096 2x20096 I:20095	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI33	1x20097 2x20097 I:20096	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI34	1x20098 2x20098 I:20097	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI35	1x20099 2x20099 I:20098	1,0x01 B:01			BIT R/O	
SHORT KEYPRESS ON DI36	1x20100 2x20100 I:20099	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI37	1x20101 2x20101 I:20100	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI38	1x20102 2x20102 I:20101	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI39	1x20103 2x20103 I:20102	0,0x00 B:00			BIT R/O	

SHORT KEYPRESS ON DI40	1x20104 2x20104 I:20103	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI41	1x20105 2x20105 I:20104	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI42	1x20106 2x20106 I:20105	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI43	1x20107 2x20107 I:20106	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI44	1x20108 2x20108 I:20107	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI45	1x20109 2x20109 I:20108	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI46	1x20110 2x20110 I:20109	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI47	1x20111 2x20111 I:20110	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI48	1x20112 2x20112 I:20111	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI49	1x20113 2x20113 I:20112	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI50	1x20114 2x20114 I:20113	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI51	1x20115 2x20115 I:20114	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI52	1x20116 2x20116 I:20115	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI53	1x20117 2x20117 I:20116	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI54	1x20118 2x20118 I:20117	0,0x00 B:00			BIT R/O	

SHORT KEYPRESS ON DI55	1x20119 2x20119 I:20118	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI56	1x20120 2x20120 I:20119	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI57	1x20121 2x20121 I:20120	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI58	1x20122 2x20122 I:20121	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI59	1x20123 2x20123 I:20122	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI60	1x20124 2x20124 I:20123	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI61	1x20125 2x20125 I:20124	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI62	1x20126 2x20126 I:20125	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI63	1x20127 2x20127 I:20126	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI64	1x20128 2x20128 I:20127	0,0x00 B:00			BIT R/O	
<b>DIGITAL INPUTS: LONG KEYPRESS START EVENT ON DIGITAL INPUT DETECTED</b>						
LONG KEYPRESS START ON DI1	1x20129 2x20129 I:20128	1,0x01 B:01			BIT R/O	
If a long keypress start event was detected on the digital input this bit inverts its last state						
LONG KEYPRESS START ON DI2	1x20130 2x20130 I:20129	1,0x01 B:01			BIT R/O	
LONG KEYPRESS START ON DI3	1x20131 2x20131 I:20130	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI4	1x20132 2x20132 I:20131	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI5	1x20133 2x20133 I:20132	1,0x01 B:01			BIT R/O	

LONG KEYPRESS START ON DI6	1x20134 2x20134 I:20133	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI7	1x20135 2x20135 I:20134	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI8	1x20136 2x20136 I:20135	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI9	1x20137 2x20137 I:20136	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI10	1x20138 2x20138 I:20137	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI11	1x20139 2x20139 I:20138	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI12	1x20140 2x20140 I:20139	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI13	1x20141 2x20141 I:20140	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI14	1x20142 2x20142 I:20141	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI15	1x20143 2x20143 I:20142	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI16	1x20144 2x20144 I:20143	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI17	1x20145 2x20145 I:20144	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI18	1x20146 2x20146 I:20145	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI19	1x20147 2x20147 I:20146	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI20	1x20148 2x20148 I:20147	0,0x00 B:00			BIT R/O	

LONG KEYPRESS START ON DI21	1x20149 2x20149 I:20148	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI22	1x20150 2x20150 I:20149	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI23	1x20151 2x20151 I:20150	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI24	1x20152 2x20152 I:20151	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI25	1x20153 2x20153 I:20152	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI26	1x20154 2x20154 I:20153	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI27	1x20155 2x20155 I:20154	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI28	1x20156 2x20156 I:20155	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI29	1x20157 2x20157 I:20156	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI30	1x20158 2x20158 I:20157	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI31	1x20159 2x20159 I:20158	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI32	1x20160 2x20160 I:20159	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI33	1x20161 2x20161 I:20160	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI34	1x20162 2x20162 I:20161	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI35	1x20163 2x20163 I:20162	1,0x01 B:01			BIT R/O	

LONG KEYPRESS START ON DI36	1x20164 2x20164 I:20163	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI37	1x20165 2x20165 I:20164	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI38	1x20166 2x20166 I:20165	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI39	1x20167 2x20167 I:20166	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI40	1x20168 2x20168 I:20167	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI41	1x20169 2x20169 I:20168	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI42	1x20170 2x20170 I:20169	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI43	1x20171 2x20171 I:20170	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI44	1x20172 2x20172 I:20171	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI45	1x20173 2x20173 I:20172	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI46	1x20174 2x20174 I:20173	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI47	1x20175 2x20175 I:20174	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI48	1x20176 2x20176 I:20175	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI49	1x20177 2x20177 I:20176	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI50	1x20178 2x20178 I:20177	0,0x00 B:00			BIT R/O	

LONG KEYPRESS START ON DI51	1x20179 2x20179 I:20178	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI52	1x20180 2x20180 I:20179	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI53	1x20181 2x20181 I:20180	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI54	1x20182 2x20182 I:20181	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI55	1x20183 2x20183 I:20182	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI56	1x20184 2x20184 I:20183	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI57	1x20185 2x20185 I:20184	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI58	1x20186 2x20186 I:20185	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI59	1x20187 2x20187 I:20186	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI60	1x20188 2x20188 I:20187	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI61	1x20189 2x20189 I:20188	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI62	1x20190 2x20190 I:20189	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI63	1x20191 2x20191 I:20190	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI64	1x20192 2x20192 I:20191	0,0x00 B:00			BIT R/O	
<b>DIGITAL INPUTS: LONG KEYPRESS END EVENT ON DIGITAL INPUT DETECTED</b>						
LONG KEYPRESS END ON DI1	1x20193 2x20193 I:20192	1,0x01 B:01			BIT R/O	

If a long keypress end event was detected on the digital input this bit inverts its last state

LONG KEYPRESS END ON DI2	1x20194 2x20194 I:20193	1,0x01 B:01			BIT R/O	
LONG KEYPRESS END ON DI3	1x20195 2x20195 I:20194	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI4	1x20196 2x20196 I:20195	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI5	1x20197 2x20197 I:20196	1,0x01 B:01			BIT R/O	
LONG KEYPRESS END ON DI6	1x20198 2x20198 I:20197	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI7	1x20199 2x20199 I:20198	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI8	1x20200 2x20200 I:20199	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI9	1x20201 2x20201 I:20200	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI10	1x20202 2x20202 I:20201	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI11	1x20203 2x20203 I:20202	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI12	1x20204 2x20204 I:20203	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI13	1x20205 2x20205 I:20204	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI14	1x20206 2x20206 I:20205	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI15	1x20207 2x20207 I:20206	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI16	1x20208 2x20208 I:20207	0,0x00 B:00			BIT R/O	



LONG KEYPRESS END ON DI17	1x20209 2x20209 I:20208	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI18	1x20210 2x20210 I:20209	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI19	1x20211 2x20211 I:20210	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI20	1x20212 2x20212 I:20211	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI21	1x20213 2x20213 I:20212	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI22	1x20214 2x20214 I:20213	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI23	1x20215 2x20215 I:20214	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI24	1x20216 2x20216 I:20215	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI25	1x20217 2x20217 I:20216	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI26	1x20218 2x20218 I:20217	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI27	1x20219 2x20219 I:20218	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI28	1x20220 2x20220 I:20219	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI29	1x20221 2x20221 I:20220	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI30	1x20222 2x20222 I:20221	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI31	1x20223 2x20223 I:20222	0,0x00 B:00			BIT R/O	

LONG KEYPRESS END ON DI32	1x20224 2x20224 I:20223	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI33	1x20225 2x20225 I:20224	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI34	1x20226 2x20226 I:20225	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI35	1x20227 2x20227 I:20226	1,0x01 B:01			BIT R/O	
LONG KEYPRESS END ON DI36	1x20228 2x20228 I:20227	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI37	1x20229 2x20229 I:20228	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI38	1x20230 2x20230 I:20229	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI39	1x20231 2x20231 I:20230	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI40	1x20232 2x20232 I:20231	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI41	1x20233 2x20233 I:20232	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI42	1x20234 2x20234 I:20233	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI43	1x20235 2x20235 I:20234	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI44	1x20236 2x20236 I:20235	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI45	1x20237 2x20237 I:20236	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI46	1x20238 2x20238 I:20237	0,0x00 B:00			BIT R/O	

LONG KEYPRESS END ON DI47	1x20239 2x20239 I:20238	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI48	1x20240 2x20240 I:20239	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI49	1x20241 2x20241 I:20240	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI50	1x20242 2x20242 I:20241	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI51	1x20243 2x20243 I:20242	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI52	1x20244 2x20244 I:20243	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI53	1x20245 2x20245 I:20244	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI54	1x20246 2x20246 I:20245	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI55	1x20247 2x20247 I:20246	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI56	1x20248 2x20248 I:20247	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI57	1x20249 2x20249 I:20248	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI58	1x20250 2x20250 I:20249	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI59	1x20251 2x20251 I:20250	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI60	1x20252 2x20252 I:20251	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI61	1x20253 2x20253 I:20252	0,0x00 B:00			BIT R/O	

LONG KEYPRESS END ON DI62	1x20254 2x20254 I:20253	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI63	1x20255 2x20255 I:20254	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI64	1x20256 2x20256 I:20255	0,0x00 B:00			BIT R/O	
<b>DIGITAL INPUTS: RISING EDGE ON DIGITAL INPUT DETECTED</b>						
RISING EDGE ON DI1	1x20257 2x20257 I:20256	1,0x01 B:01			BIT R/O	
If a rising edge was detected on the digital input this bit inverts its last state						
RISING EDGE ON DI2	1x20258 2x20258 I:20257	1,0x01 B:01			BIT R/O	
RISING EDGE ON DI3	1x20259 2x20259 I:20258	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI4	1x20260 2x20260 I:20259	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI5	1x20261 2x20261 I:20260	1,0x01 B:01			BIT R/O	
RISING EDGE ON DI6	1x20262 2x20262 I:20261	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI7	1x20263 2x20263 I:20262	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI8	1x20264 2x20264 I:20263	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI9	1x20265 2x20265 I:20264	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI10	1x20266 2x20266 I:20265	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI11	1x20267 2x20267 I:20266	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI12	1x20268 2x20268 I:20267	0,0x00 B:00			BIT R/O	

RISING EDGE ON DI13	1x20269 2x20269 I:20268	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI14	1x20270 2x20270 I:20269	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI15	1x20271 2x20271 I:20270	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI16	1x20272 2x20272 I:20271	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI17	1x20273 2x20273 I:20272	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI18	1x20274 2x20274 I:20273	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI19	1x20275 2x20275 I:20274	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI20	1x20276 2x20276 I:20275	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI21	1x20277 2x20277 I:20276	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI22	1x20278 2x20278 I:20277	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI23	1x20279 2x20279 I:20278	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI24	1x20280 2x20280 I:20279	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI25	1x20281 2x20281 I:20280	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI26	1x20282 2x20282 I:20281	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI27	1x20283 2x20283 I:20282	0,0x00 B:00			BIT R/O	

RISING EDGE ON DI28	1x20284 2x20284 I:20283	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI29	1x20285 2x20285 I:20284	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI30	1x20286 2x20286 I:20285	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI31	1x20287 2x20287 I:20286	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI32	1x20288 2x20288 I:20287	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI33	1x20289 2x20289 I:20288	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI34	1x20290 2x20290 I:20289	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI35	1x20291 2x20291 I:20290	1,0x01 B:01			BIT R/O	
RISING EDGE ON DI36	1x20292 2x20292 I:20291	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI37	1x20293 2x20293 I:20292	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI38	1x20294 2x20294 I:20293	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI39	1x20295 2x20295 I:20294	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI40	1x20296 2x20296 I:20295	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI41	1x20297 2x20297 I:20296	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI42	1x20298 2x20298 I:20297	0,0x00 B:00			BIT R/O	

RISING EDGE ON DI43	1x20299 2x20299 I:20298	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI44	1x20300 2x20300 I:20299	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI45	1x20301 2x20301 I:20300	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI46	1x20302 2x20302 I:20301	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI47	1x20303 2x20303 I:20302	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI48	1x20304 2x20304 I:20303	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI49	1x20305 2x20305 I:20304	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI50	1x20306 2x20306 I:20305	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI51	1x20307 2x20307 I:20306	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI52	1x20308 2x20308 I:20307	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI53	1x20309 2x20309 I:20308	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI54	1x20310 2x20310 I:20309	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI55	1x20311 2x20311 I:20310	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI56	1x20312 2x20312 I:20311	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI57	1x20313 2x20313 I:20312	0,0x00 B:00			BIT R/O	

RISING EDGE ON DI58	1x20314 2x20314 I:20313	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI59	1x20315 2x20315 I:20314	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI60	1x20316 2x20316 I:20315	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI61	1x20317 2x20317 I:20316	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI62	1x20318 2x20318 I:20317	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI63	1x20319 2x20319 I:20318	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI64	1x20320 2x20320 I:20319	0,0x00 B:00			BIT R/O	
<b>DIGITAL INPUTS: FALLING EDGE ON DIGITAL INPUT DETECTED</b>						
FALLING EDGE ON DI1	1x20321 2x20321 I:20320	1,0x01 B:01			BIT R/O	
If a falling edge was detected on the digital input this bit inverts its last state						
FALLING EDGE ON DI2	1x20322 2x20322 I:20321	1,0x01 B:01			BIT R/O	
FALLING EDGE ON DI3	1x20323 2x20323 I:20322	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI4	1x20324 2x20324 I:20323	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI5	1x20325 2x20325 I:20324	1,0x01 B:01			BIT R/O	
FALLING EDGE ON DI6	1x20326 2x20326 I:20325	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI7	1x20327 2x20327 I:20326	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI8	1x20328 2x20328 I:20327	0,0x00 B:00			BIT R/O	



FALLING EDGE ON DI9	1x20329 2x20329 I:20328	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI10	1x20330 2x20330 I:20329	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI11	1x20331 2x20331 I:20330	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI12	1x20332 2x20332 I:20331	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI13	1x20333 2x20333 I:20332	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI14	1x20334 2x20334 I:20333	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI15	1x20335 2x20335 I:20334	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI16	1x20336 2x20336 I:20335	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI17	1x20337 2x20337 I:20336	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI18	1x20338 2x20338 I:20337	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI19	1x20339 2x20339 I:20338	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI20	1x20340 2x20340 I:20339	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI21	1x20341 2x20341 I:20340	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI22	1x20342 2x20342 I:20341	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI23	1x20343 2x20343 I:20342	0,0x00 B:00			BIT R/O	

FALLING EDGE ON DI24	1x20344 2x20344 I:20343	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI25	1x20345 2x20345 I:20344	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI26	1x20346 2x20346 I:20345	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI27	1x20347 2x20347 I:20346	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI28	1x20348 2x20348 I:20347	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI29	1x20349 2x20349 I:20348	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI30	1x20350 2x20350 I:20349	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI31	1x20351 2x20351 I:20350	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI32	1x20352 2x20352 I:20351	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI33	1x20353 2x20353 I:20352	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI34	1x20354 2x20354 I:20353	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI35	1x20355 2x20355 I:20354	1,0x01 B:01			BIT R/O	
FALLING EDGE ON DI36	1x20356 2x20356 I:20355	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI37	1x20357 2x20357 I:20356	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI38	1x20358 2x20358 I:20357	0,0x00 B:00			BIT R/O	

FALLING EDGE ON DI39	1x20359 2x20359 I:20358	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI40	1x20360 2x20360 I:20359	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI41	1x20361 2x20361 I:20360	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI42	1x20362 2x20362 I:20361	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI43	1x20363 2x20363 I:20362	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI44	1x20364 2x20364 I:20363	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI45	1x20365 2x20365 I:20364	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI46	1x20366 2x20366 I:20365	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI47	1x20367 2x20367 I:20366	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI48	1x20368 2x20368 I:20367	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI49	1x20369 2x20369 I:20368	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI50	1x20370 2x20370 I:20369	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI51	1x20371 2x20371 I:20370	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI52	1x20372 2x20372 I:20371	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI53	1x20373 2x20373 I:20372	0,0x00 B:00			BIT R/O	

FALLING EDGE ON DI54	1x20374 2x20374 I:20373	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI55	1x20375 2x20375 I:20374	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI56	1x20376 2x20376 I:20375	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI57	1x20377 2x20377 I:20376	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI58	1x20378 2x20378 I:20377	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI59	1x20379 2x20379 I:20378	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI60	1x20380 2x20380 I:20379	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI61	1x20381 2x20381 I:20380	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI62	1x20382 2x20382 I:20381	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI63	1x20383 2x20383 I:20382	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI64	1x20384 2x20384 I:20383	0,0x00 B:00			BIT R/O	

Register NAME Command NAME	MODBUS Register ASCII Command	Register VALUE ASCII Command	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE	
<b>STATUS DIGITAL INPUTS</b>							
DI1	3x00001 4x00001 I:0	0,0x0000 B:00 00			UINT16 R/O		
		Actual state of DI1:0=OFF					
Current state of the digital input DIx =0:DI is OFF, =1:DI is ON							
DI2	3x00002 4x00002 I:1	0,0x0000 B:00 00			UINT16 R/O		
		Actual state of DI2:0=OFF					
DI3	3x00003 4x00003 I:2	0,0x0000 B:00 00			UINT16 R/O		
		Actual state of DI3:0=OFF					
DI4	3x00004 4x00004 I:3	0,0x0000 B:00 00			UINT16 R/O		
		Actual state of DI4:0=OFF					
DI5	3x00005 4x00005 I:4	0,0x0000 B:00 00			UINT16 R/O		
		Actual state of DI5:0=OFF					
DI6	3x00006 4x00006 I:5	0,0x0000 B:00 00			UINT16 R/O		
		Actual state of DI6:0=OFF					
DI7	3x00007 4x00007 I:6	0,0x0000 B:00 00			UINT16 R/O		
		Actual state of DI7:0=OFF					
DI8	3x00008 4x00008 I:7	0,0x0000 B:00 00			UINT16 R/O		
		Actual state of DI8:0=OFF					
DI9	3x00009 4x00009 I:8	0,0x0000 B:00 00			UINT16 R/O		
		Actual state of DI9:0=OFF					
DI10	3x00010 4x00010 I:9	0,0x0000 B:00 00			UINT16 R/O		

		Actual state of DI10:0=OFF				
DI11	3x00011 4x00011 I:10	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI11:0=OFF				
DI12	3x00012 4x00012 I:11	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI12:0=OFF				
DI13	3x00013 4x00013 I:12	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI13:0=OFF				
DI14	3x00014 4x00014 I:13	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI14:0=OFF				
DI15	3x00015 4x00015 I:14	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI15:0=OFF				
DI16	3x00016 4x00016 I:15	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI16:0=OFF				
DI17	3x00017 4x00017 I:16	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI17:0=OFF				
DI18	3x00018 4x00018 I:17	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI18:0=OFF				
DI19	3x00019 4x00019 I:18	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI19:0=OFF				
DI20	3x00020 4x00020 I:19	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI20:0=OFF				
DI21	3x00021 4x00021 I:20	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI21:0=OFF				

DI22	3x00022 4x00022 I:21	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI22:0=OFF				
DI23	3x00023 4x00023 I:22	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI23:0=OFF				
DI24	3x00024 4x00024 I:23	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI24:0=OFF				
DI25	3x00025 4x00025 I:24	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI25:0=OFF				
DI26	3x00026 4x00026 I:25	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI26:0=OFF				
DI27	3x00027 4x00027 I:26	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI27:0=OFF				
DI28	3x00028 4x00028 I:27	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI28:0=OFF				
DI29	3x00029 4x00029 I:28	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI29:0=OFF				
DI30	3x00030 4x00030 I:29	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI30:0=OFF				
DI31	3x00031 4x00031 I:30	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI31:0=OFF				
DI32	3x00032 4x00032 I:31	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI32:0=OFF				

DI33	3x00033 4x00033 I:32	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI33:0=OFF				
DI34	3x00034 4x00034 I:33	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI34:0=OFF				
DI35	3x00035 4x00035 I:34	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI35:0=OFF				
DI36	3x00036 4x00036 I:35	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI36:0=OFF				
DI37	3x00037 4x00037 I:36	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI37:0=OFF				
DI38	3x00038 4x00038 I:37	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI38:0=OFF				
DI39	3x00039 4x00039 I:38	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI39:0=OFF				
DI40	3x00040 4x00040 I:39	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI40:0=OFF				
DI41	3x00041 4x00041 I:40	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI41:0=OFF				
DI42	3x00042 4x00042 I:41	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI42:0=OFF				
DI43	3x00043 4x00043 I:42	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI43:0=OFF				



DI44	3x00044 4x00044 I:43	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI44:0=OFF				
DI45	3x00045 4x00045 I:44	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI45:0=OFF				
DI46	3x00046 4x00046 I:45	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI46:0=OFF				
DI47	3x00047 4x00047 I:46	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI47:0=OFF				
DI48	3x00048 4x00048 I:47	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI48:0=OFF				
DI49	3x00049 4x00049 I:48	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI49:0=OFF				
DI50	3x00050 4x00050 I:49	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI50:0=OFF				
DI51	3x00051 4x00051 I:50	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI51:0=OFF				
DI52	3x00052 4x00052 I:51	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI52:0=OFF				
DI53	3x00053 4x00053 I:52	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI53:0=OFF				
DI54	3x00054 4x00054 I:53	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI54:0=OFF				

DI55	3x00055 4x00055 I:54	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI55:0=OFF				
DI56	3x00056 4x00056 I:55	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI56:0=OFF				
DI57	3x00057 4x00057 I:56	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI57:0=OFF				
DI58	3x00058 4x00058 I:57	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI58:0=OFF				
DI59	3x00059 4x00059 I:58	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI59:0=OFF				
DI60	3x00060 4x00060 I:59	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI60:0=OFF				
DI61	3x00061 4x00061 I:60	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI61:0=OFF				
DI62	3x00062 4x00062 I:61	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI62:0=OFF				
DI63	3x00063 4x00063 I:62	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI63:0=OFF				
DI64	3x00064 4x00064 I:63	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI64:0=OFF				
<b>STATUS DIGITAL OUTPUTS</b>						
DO1	3x00065 4x00065 I:64	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual state of DO1:0=OFF			ENTER NEW STATE (0 or 1)	

Current state of the digital output DOx  
=0:DO is OFF, =1:DO is ON

Writing on this register changes the state of the digital output

DO2	3x00066 4x00066 I:65	0,0x0000 B:00 00	0	UINT16 R/W	NO
		Actual state of DO2:0=OFF	ENTER NEW STATE (0 or 1)		
DO3	3x00067 4x00067 I:66	0,0x0000 B:00 00	0	UINT16 R/W	NO
		Actual state of DO3:0=OFF	ENTER NEW STATE (0 or 1)		
DO4	3x00068 4x00068 I:67	0,0x0000 B:00 00	0	UINT16 R/W	NO
		Actual state of DO4:0=OFF	ENTER NEW STATE (0 or 1)		
DO5	3x00069 4x00069 I:68	0,0x0000 B:00 00	0	UINT16 R/W	NO
		Actual state of DO5:0=OFF	ENTER NEW STATE (0 or 1)		
DO6	3x00070 4x00070 I:69	0,0x0000 B:00 00	0	UINT16 R/W	NO
		Actual state of DO6:0=OFF	ENTER NEW STATE (0 or 1)		
DO7	3x00071 4x00071 I:70	0,0x0000 B:00 00	0	UINT16 R/W	NO
		Actual state of DO7:0=OFF	ENTER NEW STATE (0 or 1)		
DO8	3x00072 4x00072 I:71	0,0x0000 B:00 00	0	UINT16 R/W	NO
		Actual state of DO8:0=OFF	ENTER NEW STATE (0 or 1)		
DO9	3x00073 4x00073 I:72	0,0x0000 B:00 00	0	UINT16 R/W	NO
		Actual state of DO9:0=OFF	ENTER NEW STATE (0 or 1)		
DO10	3x00074 4x00074 I:73	0,0x0000 B:00 00	0	UINT16 R/W	NO
		Actual state of DO10:0=OFF	ENTER NEW STATE (0 or 1)		
DO11	3x00075 4x00075 I:74	0,0x0000 B:00 00	0	UINT16 R/W	NO
		Actual state of DO11:0=OFF	ENTER NEW STATE (0 or 1)		
DO12	3x00076 4x00076 I:75	0,0x0000 B:00 00	0	UINT16 R/W	NO

		Actual state of DO12:0=OFF	ENTER NEW STATE (0 or 1)		
DO13	3x00077 4x00077 I:76	0,0x0000 B:00 00	0	UINT16 R/W	NO
		Actual state of DO13:0=OFF	ENTER NEW STATE (0 or 1)		
DO14	3x00078 4x00078 I:77	0,0x0000 B:00 00	0	UINT16 R/W	NO
		Actual state of DO14:0=OFF	ENTER NEW STATE (0 or 1)		
DO15	3x00079 4x00079 I:78	0,0x0000 B:00 00	0	UINT16 R/W	NO
		Actual state of DO15:0=OFF	ENTER NEW STATE (0 or 1)		
DO16	3x00080 4x00080 I:79	0,0x0000 B:00 00	0	UINT16 R/W	NO
		Actual state of DO16:0=OFF	ENTER NEW STATE (0 or 1)		
DO17	3x00081 4x00081 I:80	0,0x0000 B:00 00	0	UINT16 R/W	NO
		Actual state of DO17:0=OFF	ENTER NEW STATE (0 or 1)		
DO18	3x00082 4x00082 I:81	0,0x0000 B:00 00	0	UINT16 R/W	NO
		Actual state of DO18:0=OFF	ENTER NEW STATE (0 or 1)		
DO19	3x00083 4x00083 I:82	0,0x0000 B:00 00	0	UINT16 R/W	NO
		Actual state of DO19:0=OFF	ENTER NEW STATE (0 or 1)		
DO20	3x00084 4x00084 I:83	0,0x0000 B:00 00	0	UINT16 R/W	NO
		Actual state of DO20:0=OFF	ENTER NEW STATE (0 or 1)		
DO21	3x00085 4x00085 I:84	0,0x0000 B:00 00	0	UINT16 R/W	NO
		Actual state of DO21:0=OFF	ENTER NEW STATE (0 or 1)		
DO22	3x00086 4x00086 I:85	0,0x0000 B:00 00	0	UINT16 R/W	NO
		Actual state of DO22:0=OFF	ENTER NEW STATE (0 or 1)		
DO23	3x00087 4x00087 I:86	0,0x0000 B:00 00	0	UINT16 R/W	NO
		Actual state of DO23:0=OFF	ENTER NEW STATE (0 or 1)		

DO24	3x00088 4x00088 I:87	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO24:0=OFF		ENTER NEW STATE (0 or 1)		
DO25	3x00089 4x00089 I:88	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO25:0=OFF		ENTER NEW STATE (0 or 1)		
DO26	3x00090 4x00090 I:89	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO26:0=OFF		ENTER NEW STATE (0 or 1)		
DO27	3x00091 4x00091 I:90	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO27:0=OFF		ENTER NEW STATE (0 or 1)		
DO28	3x00092 4x00092 I:91	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO28:0=OFF		ENTER NEW STATE (0 or 1)		
DO29	3x00093 4x00093 I:92	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO29:0=OFF		ENTER NEW STATE (0 or 1)		
DO30	3x00094 4x00094 I:93	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO30:0=OFF		ENTER NEW STATE (0 or 1)		
DO31	3x00095 4x00095 I:94	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO31:0=OFF		ENTER NEW STATE (0 or 1)		
DO32	3x00096 4x00096 I:95	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO32:0=OFF		ENTER NEW STATE (0 or 1)		
DO33	3x00097 4x00097 I:96	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO33:0=OFF		ENTER NEW STATE (0 or 1)		
DO34	3x00098 4x00098 I:97	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO34:0=OFF		ENTER NEW STATE (0 or 1)		

DO35	3x00099 4x00099 I:98	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO35:0=OFF		ENTER NEW STATE (0 or 1)		
DO36	3x00100 4x00100 I:99	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO36:0=OFF		ENTER NEW STATE (0 or 1)		
DO37	3x00101 4x00101 I:100	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO37:0=OFF		ENTER NEW STATE (0 or 1)		
DO38	3x00102 4x00102 I:101	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO38:0=OFF		ENTER NEW STATE (0 or 1)		
DO39	3x00103 4x00103 I:102	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO39:0=OFF		ENTER NEW STATE (0 or 1)		
DO40	3x00104 4x00104 I:103	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO40:0=OFF		ENTER NEW STATE (0 or 1)		
DO41	3x00105 4x00105 I:104	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO41:0=OFF		ENTER NEW STATE (0 or 1)		
DO42	3x00106 4x00106 I:105	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO42:0=OFF		ENTER NEW STATE (0 or 1)		
DO43	3x00107 4x00107 I:106	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO43:0=OFF		ENTER NEW STATE (0 or 1)		
DO44	3x00108 4x00108 I:107	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO44:0=OFF		ENTER NEW STATE (0 or 1)		
DO45	3x00109 4x00109 I:108	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO45:0=OFF		ENTER NEW STATE (0 or 1)		

DO46	3x00110 4x00110 I:109	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO46:0=OFF		ENTER NEW STATE (0 or 1)		
DO47	3x00111 4x00111 I:110	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO47:0=OFF		ENTER NEW STATE (0 or 1)		
DO48	3x00112 4x00112 I:111	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO48:0=OFF		ENTER NEW STATE (0 or 1)		
DO49	3x00113 4x00113 I:112	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO49:0=OFF		ENTER NEW STATE (0 or 1)		
DO50	3x00114 4x00114 I:113	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO50:0=OFF		ENTER NEW STATE (0 or 1)		
DO51	3x00115 4x00115 I:114	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO51:0=OFF		ENTER NEW STATE (0 or 1)		
DO52	3x00116 4x00116 I:115	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO52:0=OFF		ENTER NEW STATE (0 or 1)		
DO53	3x00117 4x00117 I:116	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO53:0=OFF		ENTER NEW STATE (0 or 1)		
DO54	3x00118 4x00118 I:117	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO54:0=OFF		ENTER NEW STATE (0 or 1)		
DO55	3x00119 4x00119 I:118	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO55:0=OFF		ENTER NEW STATE (0 or 1)		
DO56	3x00120 4x00120 I:119	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO56:0=OFF		ENTER NEW STATE (0 or 1)		

DO57	3x00121 4x00121 I:120	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO57:0=OFF		ENTER NEW STATE (0 or 1)		
DO58	3x00122 4x00122 I:121	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO58:0=OFF		ENTER NEW STATE (0 or 1)		
DO59	3x00123 4x00123 I:122	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO59:0=OFF		ENTER NEW STATE (0 or 1)		
DO60	3x00124 4x00124 I:123	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO60:0=OFF		ENTER NEW STATE (0 or 1)		
<b>DIGITAL OUTPUTS: ENABLE OPEN WIRE DETECTION ON</b>						
ENABLE OPEN WIRE DETECTION ON DO1	3x00125 4x00125 I:124	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO1:1=ON		ENTER NEW SETUP MODE (0 or 1)		
Enables/disabled detection of an open wire in DO state ON for the digital output DOx =0:Open wire detection is OFF, =1:Open wire detection is ON						
Writing on this register changes the state of the open wire detection for this output						
ENABLE OPEN WIRE DETECTION ON DO2	3x00126 4x00126 I:125	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO2:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO3	3x00127 4x00127 I:126	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO3:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO4	3x00128 4x00128 I:127	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO4:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO5	3x00129 4x00129 I:128	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO5:1=ON		ENTER NEW SETUP MODE (0 or 1)		



ENABLE OPEN WIRE DETECTION ON DO6	3x00130 4x00130 I:129	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO6:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO7	3x00131 4x00131 I:130	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO7:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO8	3x00132 4x00132 I:131	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO8:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO9	3x00133 4x00133 I:132	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO9:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO10	3x00134 4x00134 I:133	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO10:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO11	3x00135 4x00135 I:134	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO11:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO12	3x00136 4x00136 I:135	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO12:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO13	3x00137 4x00137 I:136	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO13:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO14	3x00138 4x00138 I:137	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO14:1=ON		ENTER NEW SETUP MODE (0 or 1)		

ENABLE OPEN WIRE DETECTION ON DO15	3x00139 4x00139 I:138	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO15:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO16	3x00140 4x00140 I:139	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO16:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO17	3x00141 4x00141 I:140	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO17:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO18	3x00142 4x00142 I:141	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO18:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO19	3x00143 4x00143 I:142	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO19:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO20	3x00144 4x00144 I:143	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO20:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO21	3x00145 4x00145 I:144	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO21:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO22	3x00146 4x00146 I:145	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO22:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO23	3x00147 4x00147 I:146	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO23:1=ON		ENTER NEW SETUP MODE (0 or 1)			

ENABLE OPEN WIRE DETECTION ON DO24	3x00148 4x00148 I:147	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO24:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO25	3x00149 4x00149 I:148	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO25:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO26	3x00150 4x00150 I:149	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO26:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO27	3x00151 4x00151 I:150	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO27:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO28	3x00152 4x00152 I:151	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO28:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO29	3x00153 4x00153 I:152	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO29:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO30	3x00154 4x00154 I:153	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO30:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO31	3x00155 4x00155 I:154	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO31:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO32	3x00156 4x00156 I:155	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO32:1=ON		ENTER NEW SETUP MODE (0 or 1)		

ENABLE OPEN WIRE DETECTION ON DO33	3x00157 4x00157 I:156	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO33:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO34	3x00158 4x00158 I:157	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO34:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO35	3x00159 4x00159 I:158	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO35:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO36	3x00160 4x00160 I:159	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO36:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO37	3x00161 4x00161 I:160	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO37:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO38	3x00162 4x00162 I:161	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO38:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO39	3x00163 4x00163 I:162	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO39:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO40	3x00164 4x00164 I:163	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO40:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO41	3x00165 4x00165 I:164	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO41:1=ON		ENTER NEW SETUP MODE (0 or 1)			

ENABLE OPEN WIRE DETECTION ON DO42	3x00166 4x00166 I:165	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO42:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO43	3x00167 4x00167 I:166	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO43:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO44	3x00168 4x00168 I:167	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO44:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO45	3x00169 4x00169 I:168	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO45:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO46	3x00170 4x00170 I:169	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO46:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO47	3x00171 4x00171 I:170	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO47:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO48	3x00172 4x00172 I:171	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO48:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO49	3x00173 4x00173 I:172	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO49:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO50	3x00174 4x00174 I:173	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO50:1=ON		ENTER NEW SETUP MODE (0 or 1)		

ENABLE OPEN WIRE DETECTION ON DO51	3x00175 4x00175 I:174	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO51:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO52	3x00176 4x00176 I:175	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO52:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO53	3x00177 4x00177 I:176	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO53:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO54	3x00178 4x00178 I:177	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO54:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO55	3x00179 4x00179 I:178	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO55:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO56	3x00180 4x00180 I:179	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO56:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO57	3x00181 4x00181 I:180	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO57:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO58	3x00182 4x00182 I:181	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO58:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO59	3x00183 4x00183 I:182	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO59:1=ON		ENTER NEW SETUP MODE (0 or 1)			

ENABLE OPEN WIRE DETECTION ON DO60	3x00184 4x00184 I:183	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO60:1=ON		ENTER NEW SETUP MODE (0 or 1)		
<b>DIGITAL OUTPUTS: ENABLE OPEN WIRE DETECTION OFF</b>						
ENABLE OPEN WIRE DETECTION OFF DO1	3x00185 4x00185 I:184	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO1:1=ON		ENTER NEW SETUP MODE (0 or 1)		
Enables/disabled detection of an open wire in DO state OFF for the digital output DOx =0:Open wire detection is OFF, =1:Open wire detection is ON						
Writing on this register changes the state of the open wire detection for this output						
ENABLE OPEN WIRE DETECTION OFF DO2	3x00186 4x00186 I:185	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO2:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO3	3x00187 4x00187 I:186	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO3:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO4	3x00188 4x00188 I:187	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO4:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO5	3x00189 4x00189 I:188	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO5:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO6	3x00190 4x00190 I:189	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO6:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO7	3x00191 4x00191 I:190	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO7:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO8	3x00192 4x00192 I:191	1,0x0001 B:00 01		1	UINT16 R/W	NO

		Actual setup of open wire detection for state OFF of DO8:1=ON	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO9	3x00193 4x00193 I:192	1,0x0001 B:00 01	1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO9:1=ON	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO10	3x00194 4x00194 I:193	1,0x0001 B:00 01	1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO10:1=ON	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO11	3x00195 4x00195 I:194	1,0x0001 B:00 01	1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO11:1=ON	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO12	3x00196 4x00196 I:195	1,0x0001 B:00 01	1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO12:1=ON	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO13	3x00197 4x00197 I:196	1,0x0001 B:00 01	1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO13:1=ON	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO14	3x00198 4x00198 I:197	1,0x0001 B:00 01	1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO14:1=ON	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO15	3x00199 4x00199 I:198	1,0x0001 B:00 01	1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO15:1=ON	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO16	3x00200 4x00200 I:199	1,0x0001 B:00 01	1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO16:1=ON	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO17	3x00201 4x00201 I:200	1,0x0001 B:00 01	1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO17:1=ON	ENTER NEW SETUP MODE (0 or 1)		



ENABLE OPEN WIRE DETECTION OFF DO18	3x00202 4x00202 I:201	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO18:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO19	3x00203 4x00203 I:202	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO19:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO20	3x00204 4x00204 I:203	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO20:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO21	3x00205 4x00205 I:204	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO21:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO22	3x00206 4x00206 I:205	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO22:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO23	3x00207 4x00207 I:206	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO23:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO24	3x00208 4x00208 I:207	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO24:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO25	3x00209 4x00209 I:208	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO25:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO26	3x00210 4x00210 I:209	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO26:1=ON		ENTER NEW SETUP MODE (0 or 1)			

ENABLE OPEN WIRE DETECTION OFF DO27	3x00211 4x00211 I:210	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO27:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO28	3x00212 4x00212 I:211	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO28:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO29	3x00213 4x00213 I:212	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO29:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO30	3x00214 4x00214 I:213	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO30:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO31	3x00215 4x00215 I:214	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO31:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO32	3x00216 4x00216 I:215	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO32:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO33	3x00217 4x00217 I:216	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO33:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO34	3x00218 4x00218 I:217	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO34:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO35	3x00219 4x00219 I:218	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO35:1=ON		ENTER NEW SETUP MODE (0 or 1)			

ENABLE OPEN WIRE DETECTION OFF DO36	3x00220 4x00220 I:219	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO36:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO37	3x00221 4x00221 I:220	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO37:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO38	3x00222 4x00222 I:221	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO38:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO39	3x00223 4x00223 I:222	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO39:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO40	3x00224 4x00224 I:223	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO40:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO41	3x00225 4x00225 I:224	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO41:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO42	3x00226 4x00226 I:225	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO42:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO43	3x00227 4x00227 I:226	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO43:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO44	3x00228 4x00228 I:227	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO44:1=ON		ENTER NEW SETUP MODE (0 or 1)			

ENABLE OPEN WIRE DETECTION OFF DO45	3x00229 4x00229 I:228	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO45:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO46	3x00230 4x00230 I:229	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO46:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO47	3x00231 4x00231 I:230	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO47:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO48	3x00232 4x00232 I:231	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO48:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO49	3x00233 4x00233 I:232	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO49:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO50	3x00234 4x00234 I:233	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO50:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO51	3x00235 4x00235 I:234	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO51:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO52	3x00236 4x00236 I:235	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO52:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO53	3x00237 4x00237 I:236	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO53:1=ON		ENTER NEW SETUP MODE (0 or 1)			

ENABLE OPEN WIRE DETECTION OFF DO54	3x00238 4x00238 I:237	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO54:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO55	3x00239 4x00239 I:238	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO55:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO56	3x00240 4x00240 I:239	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO56:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO57	3x00241 4x00241 I:240	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO57:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO58	3x00242 4x00242 I:241	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO58:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO59	3x00243 4x00243 I:242	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO59:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO60	3x00244 4x00244 I:243	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO60:1=ON		ENTER NEW SETUP MODE (0 or 1)			
<b>DIGITAL OUTPUTS: ENABLE SHORT TO VDD DETECTION</b>							
ENABLE SHORT TO VDD DETECTION DO1	3x00245 4x00245 I:244	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO1:1=ON		ENTER NEW SETUP MODE (0 or 1)			
Enables/disabled detection of a shortcut to VDD in DO state OFF for the digital output DOx =0:Shortcut to VDD detection is OFF, =1:Shortcut to VDD detection is ON							
Writing on this register changes the state of the shortcut detection for this output							
ENABLE SHORT TO VDD DETECTION DO2	3x00246 4x00246 I:245	1,0x0001 B:00 01		1		UINT16 R/W	NO

		Actual setup of open wire detection for state OFF of DO2:1=ON	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO3	3x00247 4x00247 I:246	1,0x0001 B:00 01	1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO3:1=ON	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO4	3x00248 4x00248 I:247	1,0x0001 B:00 01	1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO4:1=ON	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO5	3x00249 4x00249 I:248	1,0x0001 B:00 01	1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO5:1=ON	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO6	3x00250 4x00250 I:249	1,0x0001 B:00 01	1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO6:1=ON	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO7	3x00251 4x00251 I:250	1,0x0001 B:00 01	1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO7:1=ON	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO8	3x00252 4x00252 I:251	1,0x0001 B:00 01	1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO8:1=ON	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO9	3x00253 4x00253 I:252	1,0x0001 B:00 01	1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO9:1=ON	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO10	3x00254 4x00254 I:253	1,0x0001 B:00 01	1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO10:1=ON	ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO11	3x00255 4x00255 I:254	1,0x0001 B:00 01	1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO11:1=ON	ENTER NEW SETUP MODE (0 or 1)		

ENABLE SHORT TO VDD DETECTION DO12	3x00256 4x00256 I:255	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO12:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO13	3x00257 4x00257 I:256	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO13:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO14	3x00258 4x00258 I:257	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO14:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO15	3x00259 4x00259 I:258	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO15:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO16	3x00260 4x00260 I:259	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO16:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO17	3x00261 4x00261 I:260	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO17:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO18	3x00262 4x00262 I:261	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO18:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO19	3x00263 4x00263 I:262	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO19:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO20	3x00264 4x00264 I:263	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO20:1=ON		ENTER NEW SETUP MODE (0 or 1)			

ENABLE SHORT TO VDD DETECTION DO21	3x00265 4x00265 I:264	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO21:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO22	3x00266 4x00266 I:265	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO22:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO23	3x00267 4x00267 I:266	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO23:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO24	3x00268 4x00268 I:267	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO24:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO25	3x00269 4x00269 I:268	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO25:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO26	3x00270 4x00270 I:269	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO26:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO27	3x00271 4x00271 I:270	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO27:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO28	3x00272 4x00272 I:271	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO28:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO29	3x00273 4x00273 I:272	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO29:1=ON		ENTER NEW SETUP MODE (0 or 1)		



ENABLE SHORT TO VDD DETECTION DO30	3x00274 4x00274 I:273	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO30:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO31	3x00275 4x00275 I:274	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO31:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO32	3x00276 4x00276 I:275	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO32:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO33	3x00277 4x00277 I:276	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO33:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO34	3x00278 4x00278 I:277	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO34:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO35	3x00279 4x00279 I:278	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO35:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO36	3x00280 4x00280 I:279	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO36:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO37	3x00281 4x00281 I:280	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO37:1=ON		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO38	3x00282 4x00282 I:281	1,0x0001 B:00 01		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO38:1=ON		ENTER NEW SETUP MODE (0 or 1)		

ENABLE SHORT TO VDD DETECTION DO39	3x00283 4x00283 I:282	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO39:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO40	3x00284 4x00284 I:283	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO40:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO41	3x00285 4x00285 I:284	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO41:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO42	3x00286 4x00286 I:285	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO42:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO43	3x00287 4x00287 I:286	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO43:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO44	3x00288 4x00288 I:287	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO44:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO45	3x00289 4x00289 I:288	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO45:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO46	3x00290 4x00290 I:289	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO46:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO47	3x00291 4x00291 I:290	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO47:1=ON		ENTER NEW SETUP MODE (0 or 1)			

ENABLE SHORT TO VDD DETECTION DO48	3x00292 4x00292 I:291	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO48:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO49	3x00293 4x00293 I:292	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO49:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO50	3x00294 4x00294 I:293	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO50:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO51	3x00295 4x00295 I:294	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO51:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO52	3x00296 4x00296 I:295	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO52:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO53	3x00297 4x00297 I:296	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO53:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO54	3x00298 4x00298 I:297	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO54:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO55	3x00299 4x00299 I:298	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO55:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO56	3x00300 4x00300 I:299	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO56:1=ON		ENTER NEW SETUP MODE (0 or 1)			

ENABLE SHORT TO VDD DETECTION DO57	3x00301 4x00301 I:300	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO57:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO58	3x00302 4x00302 I:301	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO58:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO59	3x00303 4x00303 I:302	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO59:1=ON		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO60	3x00304 4x00304 I:303	1,0x0001 B:00 01		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO60:1=ON		ENTER NEW SETUP MODE (0 or 1)			
<b>DIGITAL OUTPUTS: OPEN WIRE DETECTION STATUS WHILE ON</b>							
OPEN WIRE FAULT WHILE ON DO1	3x00305 4x00305 I:304	0,0x0000 B:00 00				UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO1:0=OK					
The current detection state of an open wire in the output state ON for the digital output DOx =0:No fault, =1:Fault-open wire detected							
OPEN WIRE FAULT WHILE ON DO2	3x00306 4x00306 I:305	0,0x0000 B:00 00				UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO2:0=OK					
OPEN WIRE FAULT WHILE ON DO3	3x00307 4x00307 I:306	0,0x0000 B:00 00				UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO3:0=OK					
OPEN WIRE FAULT WHILE ON DO4	3x00308 4x00308 I:307	0,0x0000 B:00 00				UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO4:0=OK					
OPEN WIRE FAULT WHILE ON DO5	3x00309 4x00309 I:308	0,0x0000 B:00 00				UINT16 R/O	

		Actual detection state of an open wire fault in state ON for DO5:0=OK			
OPEN WIRE FAULT WHILE ON DO6	3x00310 4x00310 I:309	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of an open wire fault in state ON for DO6:0=OK			
OPEN WIRE FAULT WHILE ON DO7	3x00311 4x00311 I:310	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of an open wire fault in state ON for DO7:0=OK			
OPEN WIRE FAULT WHILE ON DO8	3x00312 4x00312 I:311	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of an open wire fault in state ON for DO8:0=OK			
OPEN WIRE FAULT WHILE ON DO9	3x00313 4x00313 I:312	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of an open wire fault in state ON for DO9:0=OK			
OPEN WIRE FAULT WHILE ON DO10	3x00314 4x00314 I:313	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of an open wire fault in state ON for DO10:0=OK			
OPEN WIRE FAULT WHILE ON DO11	3x00315 4x00315 I:314	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of an open wire fault in state ON for DO11:0=OK			
OPEN WIRE FAULT WHILE ON DO12	3x00316 4x00316 I:315	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of an open wire fault in state ON for DO12:0=OK			
OPEN WIRE FAULT WHILE ON DO13	3x00317 4x00317 I:316	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of an open wire fault in state ON for DO13:0=OK			
OPEN WIRE FAULT WHILE ON DO14	3x00318 4x00318 I:317	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of an open wire fault in state ON for DO14:0=OK			

OPEN WIRE FAULT WHILE ON DO15	3x00319 4x00319 I:318	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO15:0=OK				
OPEN WIRE FAULT WHILE ON DO16	3x00320 4x00320 I:319	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO16:0=OK				
OPEN WIRE FAULT WHILE ON DO17	3x00321 4x00321 I:320	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO17:0=OK				
OPEN WIRE FAULT WHILE ON DO18	3x00322 4x00322 I:321	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO18:0=OK				
OPEN WIRE FAULT WHILE ON DO19	3x00323 4x00323 I:322	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO19:0=OK				
OPEN WIRE FAULT WHILE ON DO20	3x00324 4x00324 I:323	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO20:0=OK				
OPEN WIRE FAULT WHILE ON DO21	3x00325 4x00325 I:324	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO21:0=OK				
OPEN WIRE FAULT WHILE ON DO22	3x00326 4x00326 I:325	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO22:0=OK				
OPEN WIRE FAULT WHILE ON DO23	3x00327 4x00327 I:326	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO23:0=OK				

OPEN WIRE FAULT WHILE ON DO24	3x00328 4x00328 I:327	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO24:0=OK				
OPEN WIRE FAULT WHILE ON DO25	3x00329 4x00329 I:328	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO25:0=OK				
OPEN WIRE FAULT WHILE ON DO26	3x00330 4x00330 I:329	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO26:0=OK				
OPEN WIRE FAULT WHILE ON DO27	3x00331 4x00331 I:330	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO27:0=OK				
OPEN WIRE FAULT WHILE ON DO28	3x00332 4x00332 I:331	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO28:0=OK				
OPEN WIRE FAULT WHILE ON DO29	3x00333 4x00333 I:332	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO29:0=OK				
OPEN WIRE FAULT WHILE ON DO30	3x00334 4x00334 I:333	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO30:0=OK				
OPEN WIRE FAULT WHILE ON DO31	3x00335 4x00335 I:334	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO31:0=OK				
OPEN WIRE FAULT WHILE ON DO32	3x00336 4x00336 I:335	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO32:0=OK				

OPEN WIRE FAULT WHILE ON DO33	3x00337 4x00337 I:336	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO33:0=OK				
OPEN WIRE FAULT WHILE ON DO34	3x00338 4x00338 I:337	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO34:0=OK				
OPEN WIRE FAULT WHILE ON DO35	3x00339 4x00339 I:338	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO35:0=OK				
OPEN WIRE FAULT WHILE ON DO36	3x00340 4x00340 I:339	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO36:0=OK				
OPEN WIRE FAULT WHILE ON DO37	3x00341 4x00341 I:340	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO37:0=OK				
OPEN WIRE FAULT WHILE ON DO38	3x00342 4x00342 I:341	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO38:0=OK				
OPEN WIRE FAULT WHILE ON DO39	3x00343 4x00343 I:342	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO39:0=OK				
OPEN WIRE FAULT WHILE ON DO40	3x00344 4x00344 I:343	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO40:0=OK				
OPEN WIRE FAULT WHILE ON DO41	3x00345 4x00345 I:344	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO41:0=OK				



OPEN WIRE FAULT WHILE ON DO42	3x00346 4x00346 I:345	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO42:0=OK				
OPEN WIRE FAULT WHILE ON DO43	3x00347 4x00347 I:346	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO43:0=OK				
OPEN WIRE FAULT WHILE ON DO44	3x00348 4x00348 I:347	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO44:0=OK				
OPEN WIRE FAULT WHILE ON DO45	3x00349 4x00349 I:348	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO45:0=OK				
OPEN WIRE FAULT WHILE ON DO46	3x00350 4x00350 I:349	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO46:0=OK				
OPEN WIRE FAULT WHILE ON DO47	3x00351 4x00351 I:350	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO47:0=OK				
OPEN WIRE FAULT WHILE ON DO48	3x00352 4x00352 I:351	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO48:0=OK				
OPEN WIRE FAULT WHILE ON DO49	3x00353 4x00353 I:352	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO49:0=OK				
OPEN WIRE FAULT WHILE ON DO50	3x00354 4x00354 I:353	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO50:0=OK				

OPEN WIRE FAULT WHILE ON DO51	3x00355 4x00355 I:354	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO51:0=OK				
OPEN WIRE FAULT WHILE ON DO52	3x00356 4x00356 I:355	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO52:0=OK				
OPEN WIRE FAULT WHILE ON DO53	3x00357 4x00357 I:356	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO53:0=OK				
OPEN WIRE FAULT WHILE ON DO54	3x00358 4x00358 I:357	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO54:0=OK				
OPEN WIRE FAULT WHILE ON DO55	3x00359 4x00359 I:358	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO55:0=OK				
OPEN WIRE FAULT WHILE ON DO56	3x00360 4x00360 I:359	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO56:0=OK				
OPEN WIRE FAULT WHILE ON DO57	3x00361 4x00361 I:360	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO57:0=OK				
OPEN WIRE FAULT WHILE ON DO58	3x00362 4x00362 I:361	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO58:0=OK				
OPEN WIRE FAULT WHILE ON DO59	3x00363 4x00363 I:362	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO59:0=OK				

OPEN WIRE FAULT WHILE ON DO60	3x00364 4x00364 I:363	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO60:0=OK				
<b>DIGITAL OUTPUTS: OPEN WIRE DETECTION STATUS WHILE OFF</b>						
OPEN WIRE FAULT WHILE OFF DO1	3x00365 4x00365 I:364	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO1:0=OK				
The current detection state of an open wire in the output state OFF for the digital output DOx =0:No fault, =1:Fault-open wire detected						
OPEN WIRE FAULT WHILE OFF DO2	3x00366 4x00366 I:365	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO2:0=OK				
OPEN WIRE FAULT WHILE OFF DO3	3x00367 4x00367 I:366	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO3:0=OK				
OPEN WIRE FAULT WHILE OFF DO4	3x00368 4x00368 I:367	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO4:0=OK				
OPEN WIRE FAULT WHILE OFF DO5	3x00369 4x00369 I:368	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO5:0=OK				
OPEN WIRE FAULT WHILE OFF DO6	3x00370 4x00370 I:369	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO6:0=OK				
OPEN WIRE FAULT WHILE OFF DO7	3x00371 4x00371 I:370	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO7:0=OK				
OPEN WIRE FAULT WHILE OFF DO8	3x00372 4x00372 I:371	0,0x0000 B:00 00			UINT16 R/O	

		Actual detection state of an open wire fault in state OFF for DO8:0=OK			
OPEN WIRE FAULT WHILE OFF DO9	3x00373 4x00373 I:372	1,0x0001 B:00 01			UINT16 R/O
		Actual detection state of an open wire fault in state OFF for DO9:1=FAULT			
OPEN WIRE FAULT WHILE OFF DO10	3x00374 4x00374 I:373	1,0x0001 B:00 01			UINT16 R/O
		Actual detection state of an open wire fault in state OFF for DO10:1=FAULT			
OPEN WIRE FAULT WHILE OFF DO11	3x00375 4x00375 I:374	1,0x0001 B:00 01			UINT16 R/O
		Actual detection state of an open wire fault in state OFF for DO11:1=FAULT			
OPEN WIRE FAULT WHILE OFF DO12	3x00376 4x00376 I:375	1,0x0001 B:00 01			UINT16 R/O
		Actual detection state of an open wire fault in state OFF for DO12:1=FAULT			
OPEN WIRE FAULT WHILE OFF DO13	3x00377 4x00377 I:376	1,0x0001 B:00 01			UINT16 R/O
		Actual detection state of an open wire fault in state OFF for DO13:1=FAULT			
OPEN WIRE FAULT WHILE OFF DO14	3x00378 4x00378 I:377	1,0x0001 B:00 01			UINT16 R/O
		Actual detection state of an open wire fault in state OFF for DO14:1=FAULT			
OPEN WIRE FAULT WHILE OFF DO15	3x00379 4x00379 I:378	1,0x0001 B:00 01			UINT16 R/O
		Actual detection state of an open wire fault in state OFF for DO15:1=FAULT			
OPEN WIRE FAULT WHILE OFF DO16	3x00380 4x00380 I:379	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of an open wire fault in state OFF for DO16:0=OK			
OPEN WIRE FAULT WHILE OFF DO17	3x00381 4x00381 I:380	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of an open wire fault in state OFF for DO17:0=OK			

OPEN WIRE FAULT WHILE OFF DO18	3x00382 4x00382 I:381	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO18:0=OK				
OPEN WIRE FAULT WHILE OFF DO19	3x00383 4x00383 I:382	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO19:0=OK				
OPEN WIRE FAULT WHILE OFF DO20	3x00384 4x00384 I:383	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO20:0=OK				
OPEN WIRE FAULT WHILE OFF DO21	3x00385 4x00385 I:384	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO21:0=OK				
OPEN WIRE FAULT WHILE OFF DO22	3x00386 4x00386 I:385	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO22:0=OK				
OPEN WIRE FAULT WHILE OFF DO23	3x00387 4x00387 I:386	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO23:0=OK				
OPEN WIRE FAULT WHILE OFF DO24	3x00388 4x00388 I:387	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO24:0=OK				
OPEN WIRE FAULT WHILE OFF DO25	3x00389 4x00389 I:388	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO25:0=OK				
OPEN WIRE FAULT WHILE OFF DO26	3x00390 4x00390 I:389	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO26:0=OK				

OPEN WIRE FAULT WHILE OFF DO27	3x00391 4x00391 I:390	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO27:0=OK				
OPEN WIRE FAULT WHILE OFF DO28	3x00392 4x00392 I:391	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO28:0=OK				
OPEN WIRE FAULT WHILE OFF DO29	3x00393 4x00393 I:392	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO29:0=OK				
OPEN WIRE FAULT WHILE OFF DO30	3x00394 4x00394 I:393	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO30:0=OK				
OPEN WIRE FAULT WHILE OFF DO31	3x00395 4x00395 I:394	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO31:0=OK				
OPEN WIRE FAULT WHILE OFF DO32	3x00396 4x00396 I:395	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO32:0=OK				
OPEN WIRE FAULT WHILE OFF DO33	3x00397 4x00397 I:396	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO33:0=OK				
OPEN WIRE FAULT WHILE OFF DO34	3x00398 4x00398 I:397	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO34:0=OK				
OPEN WIRE FAULT WHILE OFF DO35	3x00399 4x00399 I:398	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO35:0=OK				

OPEN WIRE FAULT WHILE OFF DO36	3x00400 4x00400 I:399	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO36:0=OK				
OPEN WIRE FAULT WHILE OFF DO37	3x00401 4x00401 I:400	1,0x0001 B:00 01			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO37:1=FAULT				
OPEN WIRE FAULT WHILE OFF DO38	3x00402 4x00402 I:401	1,0x0001 B:00 01			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO38:1=FAULT				
OPEN WIRE FAULT WHILE OFF DO39	3x00403 4x00403 I:402	1,0x0001 B:00 01			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO39:1=FAULT				
OPEN WIRE FAULT WHILE OFF DO40	3x00404 4x00404 I:403	1,0x0001 B:00 01			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO40:1=FAULT				
OPEN WIRE FAULT WHILE OFF DO41	3x00405 4x00405 I:404	1,0x0001 B:00 01			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO41:1=FAULT				
OPEN WIRE FAULT WHILE OFF DO42	3x00406 4x00406 I:405	1,0x0001 B:00 01			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO42:1=FAULT				
OPEN WIRE FAULT WHILE OFF DO43	3x00407 4x00407 I:406	1,0x0001 B:00 01			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO43:1=FAULT				
OPEN WIRE FAULT WHILE OFF DO44	3x00408 4x00408 I:407	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO44:0=OK				

OPEN WIRE FAULT WHILE OFF DO45	3x00409 4x00409 I:408	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO45:0=OK				
OPEN WIRE FAULT WHILE OFF DO46	3x00410 4x00410 I:409	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO46:0=OK				
OPEN WIRE FAULT WHILE OFF DO47	3x00411 4x00411 I:410	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO47:0=OK				
OPEN WIRE FAULT WHILE OFF DO48	3x00412 4x00412 I:411	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO48:0=OK				
OPEN WIRE FAULT WHILE OFF DO49	3x00413 4x00413 I:412	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO49:0=OK				
OPEN WIRE FAULT WHILE OFF DO50	3x00414 4x00414 I:413	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO50:0=OK				
OPEN WIRE FAULT WHILE OFF DO51	3x00415 4x00415 I:414	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO51:0=OK				
OPEN WIRE FAULT WHILE OFF DO52	3x00416 4x00416 I:415	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO52:0=OK				
OPEN WIRE FAULT WHILE OFF DO53	3x00417 4x00417 I:416	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO53:0=OK				



OPEN WIRE FAULT WHILE OFF DO54	3x00418 4x00418 I:417	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO54:0=OK				
OPEN WIRE FAULT WHILE OFF DO55	3x00419 4x00419 I:418	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO55:0=OK				
OPEN WIRE FAULT WHILE OFF DO56	3x00420 4x00420 I:419	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO56:0=OK				
OPEN WIRE FAULT WHILE OFF DO57	3x00421 4x00421 I:420	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO57:0=OK				
OPEN WIRE FAULT WHILE OFF DO58	3x00422 4x00422 I:421	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO58:0=OK				
OPEN WIRE FAULT WHILE OFF DO59	3x00423 4x00423 I:422	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO59:0=OK				
OPEN WIRE FAULT WHILE OFF DO60	3x00424 4x00424 I:423	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO60:0=OK				
<b>DIGITAL OUTPUTS: SHORTCUT DETECTION STATUS TO VDD WHILE OFF</b>						
OPEN WIRE SHORTCUT TO VDD DO1	3x00425 4x00425 I:424	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO1:0=OK				
The current detection state of a shortcut to VDD in the output state OFF for the digital output DOx =0:No fault, =1:Fault-shortcut to VDD detected						
OPEN WIRE SHORTCUT TO VDD DO2	3x00426 4x00426 I:425	0,0x0000 B:00 00			UINT16 R/O	

		Actual detection state of a shortcut to VDD in state OFF for VDD DO2:0=OK			
OPEN WIRE SHORTCUT TO VDD DO3	3x00427 4x00427 I:426	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO3:0=OK			
OPEN WIRE SHORTCUT TO VDD DO4	3x00428 4x00428 I:427	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO4:0=OK			
OPEN WIRE SHORTCUT TO VDD DO5	3x00429 4x00429 I:428	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO5:0=OK			
OPEN WIRE SHORTCUT TO VDD DO6	3x00430 4x00430 I:429	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO6:0=OK			
OPEN WIRE SHORTCUT TO VDD DO7	3x00431 4x00431 I:430	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO7:0=OK			
OPEN WIRE SHORTCUT TO VDD DO8	3x00432 4x00432 I:431	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO8:0=OK			
OPEN WIRE SHORTCUT TO VDD DO9	3x00433 4x00433 I:432	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO9:0=OK			

OPEN WIRE SHORTCUT TO VDD DO10	3x00434 4x00434 I:433	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO10:0=OK				
OPEN WIRE SHORTCUT TO VDD DO11	3x00435 4x00435 I:434	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO11:0=OK				
OPEN WIRE SHORTCUT TO VDD DO12	3x00436 4x00436 I:435	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO12:0=OK				
OPEN WIRE SHORTCUT TO VDD DO13	3x00437 4x00437 I:436	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO13:0=OK				
OPEN WIRE SHORTCUT TO VDD DO14	3x00438 4x00438 I:437	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO14:0=OK				
OPEN WIRE SHORTCUT TO VDD DO15	3x00439 4x00439 I:438	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO15:0=OK				
OPEN WIRE SHORTCUT TO VDD DO16	3x00440 4x00440 I:439	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO16:0=OK				
OPEN WIRE SHORTCUT TO VDD DO17	3x00441 4x00441 I:440	0,0x0000 B:00 00			UINT16 R/O	

		Actual detection state of a shortcut to VDD in state OFF for VDD DO17:0=OK			
OPEN WIRE SHORTCUT TO VDD DO18	3x00442 4x00442 I:441	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO18:0=OK			
OPEN WIRE SHORTCUT TO VDD DO19	3x00443 4x00443 I:442	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO19:0=OK			
OPEN WIRE SHORTCUT TO VDD DO20	3x00444 4x00444 I:443	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO20:0=OK			
OPEN WIRE SHORTCUT TO VDD DO21	3x00445 4x00445 I:444	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO21:0=OK			
OPEN WIRE SHORTCUT TO VDD DO22	3x00446 4x00446 I:445	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO22:0=OK			
OPEN WIRE SHORTCUT TO VDD DO23	3x00447 4x00447 I:446	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO23:0=OK			
OPEN WIRE SHORTCUT TO VDD DO24	3x00448 4x00448 I:447	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO24:0=OK			

OPEN WIRE SHORTCUT TO VDD DO25	3x00449 4x00449 I:448	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO25:0=OK				
OPEN WIRE SHORTCUT TO VDD DO26	3x00450 4x00450 I:449	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO26:0=OK				
OPEN WIRE SHORTCUT TO VDD DO27	3x00451 4x00451 I:450	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO27:0=OK				
OPEN WIRE SHORTCUT TO VDD DO28	3x00452 4x00452 I:451	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO28:0=OK				
OPEN WIRE SHORTCUT TO VDD DO29	3x00453 4x00453 I:452	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO29:0=OK				
OPEN WIRE SHORTCUT TO VDD DO30	3x00454 4x00454 I:453	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO30:0=OK				
OPEN WIRE SHORTCUT TO VDD DO31	3x00455 4x00455 I:454	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO31:0=OK				
OPEN WIRE SHORTCUT TO VDD DO32	3x00456 4x00456 I:455	0,0x0000 B:00 00			UINT16 R/O	

		Actual detection state of a shortcut to VDD in state OFF for VDD DO32:0=OK			
OPEN WIRE SHORTCUT TO VDD DO33	3x00457 4x00457 I:456	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO33:0=OK			
OPEN WIRE SHORTCUT TO VDD DO34	3x00458 4x00458 I:457	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO34:0=OK			
OPEN WIRE SHORTCUT TO VDD DO35	3x00459 4x00459 I:458	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO35:0=OK			
OPEN WIRE SHORTCUT TO VDD DO36	3x00460 4x00460 I:459	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO36:0=OK			
OPEN WIRE SHORTCUT TO VDD DO37	3x00461 4x00461 I:460	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO37:0=OK			
OPEN WIRE SHORTCUT TO VDD DO38	3x00462 4x00462 I:461	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO38:0=OK			
OPEN WIRE SHORTCUT TO VDD DO39	3x00463 4x00463 I:462	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO39:0=OK			

OPEN WIRE SHORTCUT TO VDD DO40	3x00464 4x00464 I:463	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO40:0=OK				
OPEN WIRE SHORTCUT TO VDD DO41	3x00465 4x00465 I:464	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO41:0=OK				
OPEN WIRE SHORTCUT TO VDD DO42	3x00466 4x00466 I:465	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO42:0=OK				
OPEN WIRE SHORTCUT TO VDD DO43	3x00467 4x00467 I:466	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO43:0=OK				
OPEN WIRE SHORTCUT TO VDD DO44	3x00468 4x00468 I:467	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO44:0=OK				
OPEN WIRE SHORTCUT TO VDD DO45	3x00469 4x00469 I:468	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO45:0=OK				
OPEN WIRE SHORTCUT TO VDD DO46	3x00470 4x00470 I:469	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO46:0=OK				
OPEN WIRE SHORTCUT TO VDD DO47	3x00471 4x00471 I:470	0,0x0000 B:00 00			UINT16 R/O	

		Actual detection state of a shortcut to VDD in state OFF for VDD DO47:0=OK			
OPEN WIRE SHORTCUT TO VDD DO48	3x00472 4x00472 I:471	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO48:0=OK			
OPEN WIRE SHORTCUT TO VDD DO48	3x00473 4x00473 I:472	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO48:0=OK			
OPEN WIRE SHORTCUT TO VDD DO50	3x00474 4x00474 I:473	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO50:0=OK			
OPEN WIRE SHORTCUT TO VDD DO51	3x00475 4x00475 I:474	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO51:0=OK			
OPEN WIRE SHORTCUT TO VDD DO52	3x00476 4x00476 I:475	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO52:0=OK			
OPEN WIRE SHORTCUT TO VDD DO53	3x00477 4x00477 I:476	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO53:0=OK			
OPEN WIRE SHORTCUT TO VDD DO54	3x00478 4x00478 I:477	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO54:0=OK			



OPEN WIRE SHORTCUT TO VDD DO55	3x00479 4x00479 I:478	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO55:0=OK				
OPEN WIRE SHORTCUT TO VDD DO56	3x00480 4x00480 I:479	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO56:0=OK				
OPEN WIRE SHORTCUT TO VDD DO57	3x00481 4x00481 I:480	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO57:0=OK				
OPEN WIRE SHORTCUT TO VDD DO58	3x00482 4x00482 I:481	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO58:0=OK				
OPEN WIRE SHORTCUT TO VDD DO59	3x00483 4x00483 I:482	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO59:0=OK				
OPEN WIRE SHORTCUT TO VDD DO60	3x00484 4x00484 I:483	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO60:0=OK				
<b>DIGITAL OUTPUTS: THERMAL OVERLOAD DETECTION STATUS</b>						
THERMAL OVERLOAD DETECTION STATUS DO1	3x00485 4x00485 I:484	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO1:0=OK				
The current detection state of a thermal overload for the digital output DOx =0:No fault, =1:Fault-thermal overload detected						
THERMAL OVERLOAD DETECTION STATUS DO2	3x00486 4x00486 I:485	0,0x0000 B:00 00			UINT16 R/O	

		Actual detection state of a thermal overload for DO2:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO3	3x00487 4x00487 I:486	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO3:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO4	3x00488 4x00488 I:487	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO4:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO5	3x00489 4x00489 I:488	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO5:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO6	3x00490 4x00490 I:489	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO6:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO7	3x00491 4x00491 I:490	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO7:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO8	3x00492 4x00492 I:491	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO8:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO9	3x00493 4x00493 I:492	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO9:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO10	3x00494 4x00494 I:493	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO10:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO11	3x00495 4x00495 I:494	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO11:0=OK				

THERMAL OVERLOAD DETECTION STATUS DO12	3x00496 4x00496 I:495	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO12:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO13	3x00497 4x00497 I:496	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO13:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO14	3x00498 4x00498 I:497	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO14:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO15	3x00499 4x00499 I:498	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO15:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO16	3x00500 4x00500 I:499	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO16:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO17	3x00501 4x00501 I:500	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO17:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO18	3x00502 4x00502 I:501	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO18:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO19	3x00503 4x00503 I:502	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO19:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO20	3x00504 4x00504 I:503	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO20:0=OK				

THERMAL OVERLOAD DETECTION STATUS DO21	3x00505 4x00505 I:504	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO21:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO22	3x00506 4x00506 I:505	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO22:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO23	3x00507 4x00507 I:506	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO23:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO24	3x00508 4x00508 I:507	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO24:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO25	3x00509 4x00509 I:508	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO25:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO26	3x00510 4x00510 I:509	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO26:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO27	3x00511 4x00511 I:510	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO27:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO28	3x00512 4x00512 I:511	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO28:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO29	3x00513 4x00513 I:512	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO29:0=OK				

THERMAL OVERLOAD DETECTION STATUS DO30	3x00514 4x00514 I:513	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO30:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO31	3x00515 4x00515 I:514	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO31:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO32	3x00516 4x00516 I:515	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO32:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO33	3x00517 4x00517 I:516	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO33:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO34	3x00518 4x00518 I:517	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO34:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO35	3x00519 4x00519 I:518	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO35:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO36	3x00520 4x00520 I:519	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO36:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO37	3x00521 4x00521 I:520	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO37:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO38	3x00522 4x00522 I:521	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO38:0=OK				

THERMAL OVERLOAD DETECTION STATUS DO39	3x00523 4x00523 I:522	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO39:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO40	3x00524 4x00524 I:523	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO40:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO41	3x00525 4x00525 I:524	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO41:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO42	3x00526 4x00526 I:525	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO42:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO43	3x00527 4x00527 I:526	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO43:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO44	3x00528 4x00528 I:527	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO44:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO45	3x00529 4x00529 I:528	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO45:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO46	3x00530 4x00530 I:529	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO46:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO47	3x00531 4x00531 I:530	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO47:0=OK				

THERMAL OVERLOAD DETECTION STATUS DO48	3x00532 4x00532 I:531	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO48:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO49	3x00533 4x00533 I:532	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO49:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO50	3x00534 4x00534 I:533	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO50:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO51	3x00535 4x00535 I:534	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO51:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO52	3x00536 4x00536 I:535	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO52:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO53	3x00537 4x00537 I:536	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO53:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO54	3x00538 4x00538 I:537	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO54:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO55	3x00539 4x00539 I:538	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO55:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO56	3x00540 4x00540 I:539	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO56:0=OK				

THERMAL OVERLOAD DETECTION STATUS DO57	3x00541 4x00541 I:540	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO57:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO58	3x00542 4x00542 I:541	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO58:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO59	3x00543 4x00543 I:542	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO59:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO60	3x00544 4x00544 I:543	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO60:0=OK				
<b>DIGITAL OUTPUTS: CURRENT LIMIT DETECTION STATUS WHILE ON</b>						
CURRENT LIMIT DETECTION STATUS WHILE ON DO1	3x00545 4x00545 I:544	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO1:0=OK				
The current detection state of a current limit while output is ON for the digital output DOx =0:No fault, =1:Fault-current limit error						
CURRENT LIMIT DETECTION STATUS WHILE ON DO2	3x00546 4x00546 I:545	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO2:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO3	3x00547 4x00547 I:546	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO3:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO4	3x00548 4x00548 I:547	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO4:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO5	3x00549 4x00549 I:548	0,0x0000 B:00 00			UINT16 R/O	



		Actual detection state of a current limit while ON for DO5:0=OK			
CURRENT LIMIT DETECTION STATUS WHILE ON DO6	3x00550 4x00550 I:549	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a current limit while ON for DO6:0=OK			
CURRENT LIMIT DETECTION STATUS WHILE ON DO7	3x00551 4x00551 I:550	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a current limit while ON for DO7:0=OK			
CURRENT LIMIT DETECTION STATUS WHILE ON DO8	3x00552 4x00552 I:551	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a current limit while ON for DO8:0=OK			
CURRENT LIMIT DETECTION STATUS WHILE ON DO9	3x00553 4x00553 I:552	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a current limit while ON for DO9:0=OK			
CURRENT LIMIT DETECTION STATUS WHILE ON DO10	3x00554 4x00554 I:553	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a current limit while ON for DO10:0=OK			
CURRENT LIMIT DETECTION STATUS WHILE ON DO11	3x00555 4x00555 I:554	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a current limit while ON for DO11:0=OK			
CURRENT LIMIT DETECTION STATUS WHILE ON DO12	3x00556 4x00556 I:555	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a current limit while ON for DO12:0=OK			
CURRENT LIMIT DETECTION STATUS WHILE ON DO13	3x00557 4x00557 I:556	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a current limit while ON for DO13:0=OK			
CURRENT LIMIT DETECTION STATUS WHILE ON DO14	3x00558 4x00558 I:557	0,0x0000 B:00 00			UINT16 R/O
		Actual detection state of a current limit while ON for DO14:0=OK			

CURRENT LIMIT DETECTION STATUS WHILE ON DO15	3x00559 4x00559 I:558	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO15:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO16	3x00560 4x00560 I:559	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO16:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO17	3x00561 4x00561 I:560	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO17:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO18	3x00562 4x00562 I:561	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO18:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO19	3x00563 4x00563 I:562	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO19:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO20	3x00564 4x00564 I:563	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO20:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO21	3x00565 4x00565 I:564	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO21:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO22	3x00566 4x00566 I:565	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO22:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO23	3x00567 4x00567 I:566	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO23:0=OK				

CURRENT LIMIT DETECTION STATUS WHILE ON DO24	3x00568 4x00568 I:567	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO24:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO25	3x00569 4x00569 I:568	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO25:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO26	3x00570 4x00570 I:569	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO26:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO27	3x00571 4x00571 I:570	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO27:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO28	3x00572 4x00572 I:571	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO28:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO29	3x00573 4x00573 I:572	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO29:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO30	3x00574 4x00574 I:573	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO30:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO31	3x00575 4x00575 I:574	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO31:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO32	3x00576 4x00576 I:575	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO32:0=OK				

CURRENT LIMIT DETECTION STATUS WHILE ON DO33	3x00577 4x00577 I:576	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO33:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO34	3x00578 4x00578 I:577	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO34:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO35	3x00579 4x00579 I:578	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO35:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO36	3x00580 4x00580 I:579	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO36:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO37	3x00581 4x00581 I:580	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO37:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO38	3x00582 4x00582 I:581	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO38:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO39	3x00583 4x00583 I:582	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO39:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO40	3x00584 4x00584 I:583	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO40:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO41	3x00585 4x00585 I:584	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO41:0=OK				

CURRENT LIMIT DETECTION STATUS WHILE ON DO42	3x00586 4x00586 I:585	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO42:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO43	3x00587 4x00587 I:586	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO43:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO44	3x00588 4x00588 I:587	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO44:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO45	3x00589 4x00589 I:588	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO45:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO46	3x00590 4x00590 I:589	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO46:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO47	3x00591 4x00591 I:590	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO47:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO48	3x00592 4x00592 I:591	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO48:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO49	3x00593 4x00593 I:592	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO49:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO50	3x00594 4x00594 I:593	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO50:0=OK				

CURRENT LIMIT DETECTION STATUS WHILE ON DO51	3x00595 4x00595 I:594	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO51:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO52	3x00596 4x00596 I:595	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO52:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO53	3x00597 4x00597 I:596	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO53:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO54	3x00598 4x00598 I:597	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO54:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO55	3x00599 4x00599 I:598	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO55:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO56	3x00600 4x00600 I:599	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO56:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO57	3x00601 4x00601 I:600	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO57:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO58	3x00602 4x00602 I:601	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO58:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO59	3x00603 4x00603 I:602	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO59:0=OK				

CURRENT LIMIT DETECTION STATUS WHILE ON DO60	3x00604 4x00604 I:603	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO60:0=OK				

**DIGITAL OUTPUTS: GLOBAL ERRORS****CHIP #1:DO1-DO7**

GLOBAL ERRORS BIT 0	3x00605 4x00605 I:604	0,0x0000 B:00 00			UINT16 R/O	
		BIT 0:Internal under voltage detected:0=OK				
GLOBAL ERRORS BIT 1	3x00606 4x00606 I:605	0,0x0000 B:00 00			UINT16 R/O	
		BIT 1:VA under voltage detected (<2.3V):0=OK				
GLOBAL ERRORS BIT 2	3x00607 4x00607 I:606	0,0x0000 B:00 00			UINT16 R/O	
		BIT 2:VDD not good detected (<17V):0=OK				
GLOBAL ERRORS BIT 3	3x00608 4x00608 I:607	0,0x0000 B:00 00			UINT16 R/O	
		BIT 3:VDD warning detected (<12V):0=OK				
GLOBAL ERRORS BIT 4	3x00609 4x00609 I:608	0,0x0000 B:00 00			UINT16 R/O	
		BIT 4:VDD under voltage detected (<8V):0=OK				
GLOBAL ERRORS BIT 5	3x00610 4x00610 I:609	0,0x0000 B:00 00			UINT16 R/O	
		BIT 5:Thermal shutdown:0=OK				
GLOBAL ERRORS BIT 6	3x00611 4x00611 I:610	0,0x0000 B:00 00			UINT16 R/O	
		BIT 6:Synchronisation error detected:0=OK				
GLOBAL ERRORS BIT 7	3x00612 4x00612 I:611	0,0x0000 B:00 00			UINT16 R/O	
		BIT 7:Watchdog error detected:0=OK				

The global error state for the output group. Each bit stands for a different error  
=0:No fault, =1:Fault

**DIGITAL OUTPUTS: GLOBAL ERRORS****CHIP #2:DO8-DO15**

GLOBAL ERRORS BIT 0	3x00613 4x00613 I:612	0,0x0000 B:00 00			UINT16 R/O	
		BIT 0:Internal under voltage detected:0=OK				

GLOBAL ERRORS BIT 1	3x00614 4x00614 I:613	0,0x0000 B:00 00			UINT16 R/O	
BIT 1:VA under voltage detected (<2.3V):0=OK						
GLOBAL ERRORS BIT 2	3x00615 4x00615 I:614	0,0x0000 B:00 00			UINT16 R/O	
BIT 2:VDD not good detected (<17V):0=OK						
GLOBAL ERRORS BIT 3	3x00616 4x00616 I:615	0,0x0000 B:00 00			UINT16 R/O	
BIT 3:VDD warning detected (<12V):0=OK						
GLOBAL ERRORS BIT 4	3x00617 4x00617 I:616	0,0x0000 B:00 00			UINT16 R/O	
BIT 4:VDD under voltage detected (<8V):0=OK						
GLOBAL ERRORS BIT 5	3x00618 4x00618 I:617	0,0x0000 B:00 00			UINT16 R/O	
BIT 5:Thermal shutdown:0=OK						
GLOBAL ERRORS BIT 6	3x00619 4x00619 I:618	0,0x0000 B:00 00			UINT16 R/O	
BIT 6:Synchronisation error detected:0=OK						
GLOBAL ERRORS BIT 7	3x00620 4x00620 I:619	0,0x0000 B:00 00			UINT16 R/O	
BIT 7:Watchdog error detected:0=OK						
The global error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
<b>DIGITAL OUTPUTS: GLOBAL ERRORS</b>						
<b>CHIP #3:DO16-DO22</b>						
GLOBAL ERRORS BIT 0	3x00621 4x00621 I:620	0,0x0000 B:00 00			UINT16 R/O	
BIT 0:Internal under voltage detected:0=OK						
GLOBAL ERRORS BIT 1	3x00622 4x00622 I:621	0,0x0000 B:00 00			UINT16 R/O	
BIT 1:VA under voltage detected (<2.3V):0=OK						
GLOBAL ERRORS BIT 2	3x00623 4x00623 I:622	1,0x0001 B:00 01			UINT16 R/O	
BIT 2:VDD not good detected (<17V):1=FAULT						
GLOBAL ERRORS BIT 3	3x00624 4x00624 I:623	1,0x0001 B:00 01			UINT16 R/O	



		BIT 3:VDD warning detected (<12V):1=FAULT				
GLOBAL ERRORS BIT 4	3x00625 4x00625 I:624	1,0x0001 B:00 01			UINT16 R/O	
		BIT 4:VDD under voltage detected (<8V):1=FAULT				
GLOBAL ERRORS BIT 5	3x00626 4x00626 I:625	0,0x0000 B:00 00			UINT16 R/O	
		BIT 5:Thermal shutdown:0=OK				
GLOBAL ERRORS BIT 6	3x00627 4x00627 I:626	0,0x0000 B:00 00			UINT16 R/O	
		BIT 6:Synchronisation error detected:0=OK				
GLOBAL ERRORS BIT 7	3x00628 4x00628 I:627	0,0x0000 B:00 00			UINT16 R/O	
		BIT 7:Watchdog error detected:0=OK				

The global error state for the output group. Each bit stands for a different error  
=0:No fault, =1:Fault

**DIGITAL OUTPUTS: GLOBAL ERRORS****CHIP #4:DO23-DO30**

GLOBAL ERRORS BIT 0	3x00629 4x00629 I:628	0,0x0000 B:00 00			UINT16 R/O	
		BIT 0:Internal under voltage detected:0=OK				
GLOBAL ERRORS BIT 1	3x00630 4x00630 I:629	0,0x0000 B:00 00			UINT16 R/O	
		BIT 1:VA under voltage detected (<2.3V):0=OK				
GLOBAL ERRORS BIT 2	3x00631 4x00631 I:630	1,0x0001 B:00 01			UINT16 R/O	
		BIT 2:VDD not good detected (<17V):1=FAULT				
GLOBAL ERRORS BIT 3	3x00632 4x00632 I:631	1,0x0001 B:00 01			UINT16 R/O	
		BIT 3:VDD warning detected (<12V):1=FAULT				
GLOBAL ERRORS BIT 4	3x00633 4x00633 I:632	1,0x0001 B:00 01			UINT16 R/O	
		BIT 4:VDD under voltage detected (<8V):1=FAULT				
GLOBAL ERRORS BIT 5	3x00634 4x00634 I:633	0,0x0000 B:00 00			UINT16 R/O	
		BIT 5:Thermal shutdown:0=OK				

GLOBAL ERRORS BIT 6	3x00635 4x00635 I:634	0,0x0000 B:00 00			UINT16 R/O	
BIT 6:Synchronisation error detected:0=OK						
GLOBAL ERRORS BIT 7	3x00636 4x00636 I:635	0,0x0000 B:00 00			UINT16 R/O	
BIT 7:Watchdog error detected:0=OK						

The global error state for the output group. Each bit stands for a different error  
=0:No fault, =1:Fault

**DIGITAL OUTPUTS: GLOBAL ERRORS****CHIP #5:DO31-DO38**

GLOBAL ERRORS BIT 0	3x00637 4x00637 I:636	0,0x0000 B:00 00			UINT16 R/O	
BIT 0:Internal under voltage detected:0=OK						
GLOBAL ERRORS BIT 1	3x00638 4x00638 I:637	0,0x0000 B:00 00			UINT16 R/O	
BIT 1:VA under voltage detected (<2.3V):0=OK						
GLOBAL ERRORS BIT 2	3x00639 4x00639 I:638	1,0x0001 B:00 01			UINT16 R/O	
BIT 2:VDD not good detected (<17V):1=FAULT						
GLOBAL ERRORS BIT 3	3x00640 4x00640 I:639	1,0x0001 B:00 01			UINT16 R/O	
BIT 3:VDD warning detected (<12V):1=FAULT						
GLOBAL ERRORS BIT 4	3x00641 4x00641 I:640	1,0x0001 B:00 01			UINT16 R/O	
BIT 4:VDD under voltage detected (<8V):1=FAULT						
GLOBAL ERRORS BIT 5	3x00642 4x00642 I:641	0,0x0000 B:00 00			UINT16 R/O	
BIT 5:Thermal shutdown:0=OK						
GLOBAL ERRORS BIT 6	3x00643 4x00643 I:642	0,0x0000 B:00 00			UINT16 R/O	
BIT 6:Synchronisation error detected:0=OK						
GLOBAL ERRORS BIT 7	3x00644 4x00644 I:643	0,0x0000 B:00 00			UINT16 R/O	
BIT 7:Watchdog error detected:0=OK						

The global error state for the output group. Each bit stands for a different error  
=0:No fault, =1:Fault

**DIGITAL OUTPUTS: GLOBAL ERRORS**

CHIP #6:DO39-DO45						
GLOBAL ERRORS BIT 0	3x00645 4x00645 I:644	0,0x0000 B:00 00			UINT16 R/O	
BIT 0:Internal under voltage detected:0=OK						
GLOBAL ERRORS BIT 1	3x00646 4x00646 I:645	0,0x0000 B:00 00			UINT16 R/O	
BIT 1:VA under voltage detected (<2.3V):0=OK						
GLOBAL ERRORS BIT 2	3x00647 4x00647 I:646	1,0x0001 B:00 01			UINT16 R/O	
BIT 2:VDD not good detected (<17V):1=FAULT						
GLOBAL ERRORS BIT 3	3x00648 4x00648 I:647	1,0x0001 B:00 01			UINT16 R/O	
BIT 3:VDD warning detected (<12V):1=FAULT						
GLOBAL ERRORS BIT 4	3x00649 4x00649 I:648	1,0x0001 B:00 01			UINT16 R/O	
BIT 4:VDD under voltage detected (<8V):1=FAULT						
GLOBAL ERRORS BIT 5	3x00650 4x00650 I:649	0,0x0000 B:00 00			UINT16 R/O	
BIT 5:Thermal shutdown:0=OK						
GLOBAL ERRORS BIT 6	3x00651 4x00651 I:650	0,0x0000 B:00 00			UINT16 R/O	
BIT 6:Synchronisation error detected:0=OK						
GLOBAL ERRORS BIT 7	3x00652 4x00652 I:651	0,0x0000 B:00 00			UINT16 R/O	
BIT 7:Watchdog error detected:0=OK						
The global error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
DIGITAL OUTPUTS: GLOBAL ERRORS						
CHIP #7:DO46-DO53						
GLOBAL ERRORS BIT 0	3x00653 4x00653 I:652	0,0x0000 B:00 00			UINT16 R/O	
BIT 0:Internal under voltage detected:0=OK						
GLOBAL ERRORS BIT 1	3x00654 4x00654 I:653	0,0x0000 B:00 00			UINT16 R/O	
BIT 1:VA under voltage detected (<2.3V):0=OK						

GLOBAL ERRORS BIT 2	3x00655 4x00655 I:654	1,0x0001 B:00 01			UINT16 R/O	
BIT 2:VDD not good detected (<17V):1=FAULT						
GLOBAL ERRORS BIT 3	3x00656 4x00656 I:655	1,0x0001 B:00 01			UINT16 R/O	
BIT 3:VDD warning detected (<12V):1=FAULT						
GLOBAL ERRORS BIT 4	3x00657 4x00657 I:656	1,0x0001 B:00 01			UINT16 R/O	
BIT 4:VDD under voltage detected (<8V):1=FAULT						
GLOBAL ERRORS BIT 5	3x00658 4x00658 I:657	0,0x0000 B:00 00			UINT16 R/O	
BIT 5:Thermal shutdown:0=OK						
GLOBAL ERRORS BIT 6	3x00659 4x00659 I:658	0,0x0000 B:00 00			UINT16 R/O	
BIT 6:Synchronisation error detected:0=OK						
GLOBAL ERRORS BIT 7	3x00660 4x00660 I:659	0,0x0000 B:00 00			UINT16 R/O	
BIT 7:Watchdog error detected:0=OK						

The global error state for the output group. Each bit stands for a different error  
=0:No fault, =1:Fault

**DIGITAL OUTPUTS: GLOBAL ERRORS****CHIP #8:DO54-DO60**

GLOBAL ERRORS BIT 0	3x00661 4x00661 I:660	0,0x0000 B:00 00			UINT16 R/O	
BIT 0:Internal under voltage detected:0=OK						
GLOBAL ERRORS BIT 1	3x00662 4x00662 I:661	0,0x0000 B:00 00			UINT16 R/O	
BIT 1:VA under voltage detected (<2.3V):0=OK						
GLOBAL ERRORS BIT 2	3x00663 4x00663 I:662	1,0x0001 B:00 01			UINT16 R/O	
BIT 2:VDD not good detected (<17V):1=FAULT						
GLOBAL ERRORS BIT 3	3x00664 4x00664 I:663	1,0x0001 B:00 01			UINT16 R/O	
BIT 3:VDD warning detected (<12V):1=FAULT						
GLOBAL ERRORS BIT 4	3x00665 4x00665 I:664	1,0x0001 B:00 01			UINT16 R/O	

		BIT 4:VDD under voltage detected (<8V):1=FAULT				
GLOBAL ERRORS BIT 5	3x00666 4x00666 I:665	0,0x0000 B:00 00			UINT16 R/O	
		BIT 5:Thermal shutdown:0=OK				
GLOBAL ERRORS BIT 6	3x00667 4x00667 I:666	0,0x0000 B:00 00			UINT16 R/O	
		BIT 6:Synchronisation error detected:0=OK				
GLOBAL ERRORS BIT 7	3x00668 4x00668 I:667	0,0x0000 B:00 00			UINT16 R/O	
		BIT 7:Watchdog error detected:0=OK				
The global error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
<b>DIGITAL OUTPUTS: INTERRUPT STATUS</b>						
<b>CHIP #1:DO1-DO7</b>						
INTERRUPT STATUS BIT 0	3x00669 4x00669 I:668	0,0x0000 B:00 00			UINT16 R/O	
		BIT 0:Overload detected:0=OK				
INTERRUPT STATUS BIT 1	3x00670 4x00670 I:669	0,0x0000 B:00 00			UINT16 R/O	
		BIT 1:Current limit detected:0=OK				
INTERRUPT STATUS BIT 2	3x00671 4x00671 I:670	0,0x0000 B:00 00			UINT16 R/O	
		BIT 2:Open wire while OFF detected:0=OK				
INTERRUPT STATUS BIT 3	3x00672 4x00672 I:671	0,0x0000 B:00 00			UINT16 R/O	
		BIT 3:Open wire while ON detected:0=OK				
INTERRUPT STATUS BIT 4	3x00673 4x00673 I:672	0,0x0000 B:00 00			UINT16 R/O	
		BIT 4:Short to VDD while ON detected:0=OK				
INTERRUPT STATUS BIT 5	3x00674 4x00674 I:673	0,0x0000 B:00 00			UINT16 R/O	
		BIT 5:Thermal error detected-shutdown:0=OK				
INTERRUPT STATUS BIT 6	3x00675 4x00675 I:674	0,0x0000 B:00 00			UINT16 R/O	
		BIT 6:Supply error detected:0=OK				

INTERRUPT STATUS BIT 7	3x00676 4x00676 I:675	0,0x0000 B:00 00			UINT16 R/O	
BIT 7:Communication error detected:0=OK						
The global interrupt error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
<b>DIGITAL OUTPUTS: INTERRUPT STATUS</b>						
<b>CHIP #2:DO8-DO15</b>						
INTERRUPT STATUS BIT 0	3x00677 4x00677 I:676	0,0x0000 B:00 00			UINT16 R/O	
BIT 0:Overload detected:0=OK						
INTERRUPT STATUS BIT 1	3x00678 4x00678 I:677	0,0x0000 B:00 00			UINT16 R/O	
BIT 1:Current limit detected:0=OK						
INTERRUPT STATUS BIT 2	3x00679 4x00679 I:678	1,0x0001 B:00 01			UINT16 R/O	
BIT 2:Open wire while OFF detected:1=FAULT						
INTERRUPT STATUS BIT 3	3x00680 4x00680 I:679	0,0x0000 B:00 00			UINT16 R/O	
BIT 3:Open wire while ON detected:0=OK						
INTERRUPT STATUS BIT 4	3x00681 4x00681 I:680	0,0x0000 B:00 00			UINT16 R/O	
BIT 4:Short to VDD while ON detected:0=OK						
INTERRUPT STATUS BIT 5	3x00682 4x00682 I:681	0,0x0000 B:00 00			UINT16 R/O	
BIT 5:Thermal error detected-shutdown:0=OK						
INTERRUPT STATUS BIT 6	3x00683 4x00683 I:682	0,0x0000 B:00 00			UINT16 R/O	
BIT 6:Supply error detected:0=OK						
INTERRUPT STATUS BIT 7	3x00684 4x00684 I:683	0,0x0000 B:00 00			UINT16 R/O	
BIT 7:Communication error detected:0=OK						
The global interrupt error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
<b>DIGITAL OUTPUTS: INTERRUPT STATUS</b>						
<b>CHIP #3:DO16-DO22</b>						
INTERRUPT STATUS BIT 0	3x00685 4x00685 I:684	0,0x0000 B:00 00			UINT16 R/O	

		BIT 0:Overload detected:0=OK				
INTERRUPT STATUS BIT 1	3x00686 4x00686 I:685	0,0x0000 B:00 00			UINT16 R/O	
		BIT 1:Current limit detected:0=OK				
INTERRUPT STATUS BIT 2	3x00687 4x00687 I:686	0,0x0000 B:00 00			UINT16 R/O	
		BIT 2:Open wire while OFF detected:0=OK				
INTERRUPT STATUS BIT 3	3x00688 4x00688 I:687	0,0x0000 B:00 00			UINT16 R/O	
		BIT 3:Open wire while ON detected:0=OK				
INTERRUPT STATUS BIT 4	3x00689 4x00689 I:688	0,0x0000 B:00 00			UINT16 R/O	
		BIT 4:Short to VDD while ON detected:0=OK				
INTERRUPT STATUS BIT 5	3x00690 4x00690 I:689	0,0x0000 B:00 00			UINT16 R/O	
		BIT 5:Thermal error detected-shutdown:0=OK				
INTERRUPT STATUS BIT 6	3x00691 4x00691 I:690	1,0x0001 B:00 01			UINT16 R/O	
		BIT 6:Supply error detected:1=FAULT				
INTERRUPT STATUS BIT 7	3x00692 4x00692 I:691	0,0x0000 B:00 00			UINT16 R/O	
		BIT 7:Communication error detected:0=OK				
The global interrupt error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
<b>DIGITAL OUTPUTS: INTERRUPT STATUS</b>						
<b>CHIP #4:DO23-DO30</b>						
INTERRUPT STATUS BIT 0	3x00693 4x00693 I:692	0,0x0000 B:00 00			UINT16 R/O	
		BIT 0:Overload detected:0=OK				
INTERRUPT STATUS BIT 1	3x00694 4x00694 I:693	0,0x0000 B:00 00			UINT16 R/O	
		BIT 1:Current limit detected:0=OK				
INTERRUPT STATUS BIT 2	3x00695 4x00695 I:694	0,0x0000 B:00 00			UINT16 R/O	
		BIT 2:Open wire while OFF detected:0=OK				

INTERRUPT STATUS BIT 3	3x00696 4x00696 I:695	0,0x0000 B:00 00			UINT16 R/O	
BIT 3:Open wire while ON detected:0=OK						
INTERRUPT STATUS BIT 4	3x00697 4x00697 I:696	0,0x0000 B:00 00			UINT16 R/O	
BIT 4:Short to VDD while ON detected:0=OK						
INTERRUPT STATUS BIT 5	3x00698 4x00698 I:697	0,0x0000 B:00 00			UINT16 R/O	
BIT 5:Thermal error detected-shutdown:0=OK						
INTERRUPT STATUS BIT 6	3x00699 4x00699 I:698	1,0x0001 B:00 01			UINT16 R/O	
BIT 6:Supply error detected:1=FAULT						
INTERRUPT STATUS BIT 7	3x00700 4x00700 I:699	0,0x0000 B:00 00			UINT16 R/O	
BIT 7:Communication error detected:0=OK						

The global interrupt error state for the output group. Each bit stands for a different error  
=0:No fault, =1:Fault

**DIGITAL OUTPUTS: INTERRUPT STATUS****CHIP #5:DO31-DO38**

INTERRUPT STATUS BIT 0	3x00701 4x00701 I:700	0,0x0000 B:00 00			UINT16 R/O	
BIT 0:Overload detected:0=OK						
INTERRUPT STATUS BIT 1	3x00702 4x00702 I:701	0,0x0000 B:00 00			UINT16 R/O	
BIT 1:Current limit detected:0=OK						
INTERRUPT STATUS BIT 2	3x00703 4x00703 I:702	0,0x0000 B:00 00			UINT16 R/O	
BIT 2:Open wire while OFF detected:0=OK						
INTERRUPT STATUS BIT 3	3x00704 4x00704 I:703	0,0x0000 B:00 00			UINT16 R/O	
BIT 3:Open wire while ON detected:0=OK						
INTERRUPT STATUS BIT 4	3x00705 4x00705 I:704	0,0x0000 B:00 00			UINT16 R/O	
BIT 4:Short to VDD while ON detected:0=OK						
INTERRUPT STATUS BIT 5	3x00706 4x00706 I:705	0,0x0000 B:00 00			UINT16 R/O	



		BIT 5:Thermal error detected-shutdown:0=OK				
INTERRUPT STATUS BIT 6	3x00707 4x00707 I:706	1,0x0001 B:00 01			UINT16 R/O	
		BIT 6:Supply error detected:1=FAULT				
INTERRUPT STATUS BIT 7	3x00708 4x00708 I:707	0,0x0000 B:00 00			UINT16 R/O	
		BIT 7:Communication error detected:0=OK				
The global interrupt error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
<b>DIGITAL OUTPUTS: INTERRUPT STATUS</b>						
<b>CHIP #6:DO39-DO45</b>						
INTERRUPT STATUS BIT 0	3x00709 4x00709 I:708	0,0x0000 B:00 00			UINT16 R/O	
		BIT 0:Overload detected:0=OK				
INTERRUPT STATUS BIT 1	3x00710 4x00710 I:709	0,0x0000 B:00 00			UINT16 R/O	
		BIT 1:Current limit detected:0=OK				
INTERRUPT STATUS BIT 2	3x00711 4x00711 I:710	0,0x0000 B:00 00			UINT16 R/O	
		BIT 2:Open wire while OFF detected:0=OK				
INTERRUPT STATUS BIT 3	3x00712 4x00712 I:711	0,0x0000 B:00 00			UINT16 R/O	
		BIT 3:Open wire while ON detected:0=OK				
INTERRUPT STATUS BIT 4	3x00713 4x00713 I:712	0,0x0000 B:00 00			UINT16 R/O	
		BIT 4:Short to VDD while ON detected:0=OK				
INTERRUPT STATUS BIT 5	3x00714 4x00714 I:713	0,0x0000 B:00 00			UINT16 R/O	
		BIT 5:Thermal error detected-shutdown:0=OK				
INTERRUPT STATUS BIT 6	3x00715 4x00715 I:714	1,0x0001 B:00 01			UINT16 R/O	
		BIT 6:Supply error detected:1=FAULT				
INTERRUPT STATUS BIT 7	3x00716 4x00716 I:715	0,0x0000 B:00 00			UINT16 R/O	
		BIT 7:Communication error detected:0=OK				
The global interrupt error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						

**DIGITAL OUTPUTS: INTERRUPT STATUS**

CHIP #7:DO46-DO53

INTERRUPT STATUS BIT 0	3x00717 4x00717 I:716	0,0x0000 B:00 00			UINT16 R/O	
BIT 0:Overload detected:0=OK						
INTERRUPT STATUS BIT 1	3x00718 4x00718 I:717	0,0x0000 B:00 00			UINT16 R/O	
BIT 1:Current limit detected:0=OK						
INTERRUPT STATUS BIT 2	3x00719 4x00719 I:718	0,0x0000 B:00 00			UINT16 R/O	
BIT 2:Open wire while OFF detected:0=OK						
INTERRUPT STATUS BIT 3	3x00720 4x00720 I:719	0,0x0000 B:00 00			UINT16 R/O	
BIT 3:Open wire while ON detected:0=OK						
INTERRUPT STATUS BIT 4	3x00721 4x00721 I:720	0,0x0000 B:00 00			UINT16 R/O	
BIT 4:Short to VDD while ON detected:0=OK						
INTERRUPT STATUS BIT 5	3x00722 4x00722 I:721	0,0x0000 B:00 00			UINT16 R/O	
BIT 5:Thermal error detected-shutdown:0=OK						
INTERRUPT STATUS BIT 6	3x00723 4x00723 I:722	1,0x0001 B:00 01			UINT16 R/O	
BIT 6:Supply error detected:1=FAULT						
INTERRUPT STATUS BIT 7	3x00724 4x00724 I:723	0,0x0000 B:00 00			UINT16 R/O	
BIT 7:Communication error detected:0=OK						

The global interrupt error state for the output group. Each bit stands for a different error

=0:No fault, =1:Fault

**DIGITAL OUTPUTS: INTERRUPT STATUS**

CHIP #8:DO54-DO60

INTERRUPT STATUS BIT 0	3x00725 4x00725 I:724	0,0x0000 B:00 00			UINT16 R/O	
BIT 0:Overload detected:0=OK						
INTERRUPT STATUS BIT 1	3x00726 4x00726 I:725	0,0x0000 B:00 00			UINT16 R/O	
BIT 1:Current limit detected:0=OK						

INTERRUPT STATUS BIT 2	3x00727 4x00727 I:726	0,0x0000 B:00 00			UINT16 R/O	
BIT 2:Open wire while OFF detected:0=OK						
INTERRUPT STATUS BIT 3	3x00728 4x00728 I:727	0,0x0000 B:00 00			UINT16 R/O	
BIT 3:Open wire while ON detected:0=OK						
INTERRUPT STATUS BIT 4	3x00729 4x00729 I:728	0,0x0000 B:00 00			UINT16 R/O	
BIT 4:Short to VDD while ON detected:0=OK						
INTERRUPT STATUS BIT 5	3x00730 4x00730 I:729	0,0x0000 B:00 00			UINT16 R/O	
BIT 5:Thermal error detected-shutdown:0=OK						
INTERRUPT STATUS BIT 6	3x00731 4x00731 I:730	1,0x0001 B:00 01			UINT16 R/O	
BIT 6:Supply error detected:1=FAULT						
INTERRUPT STATUS BIT 7	3x00732 4x00732 I:731	0,0x0000 B:00 00			UINT16 R/O	
BIT 7:Communication error detected:0=OK						
The global interrupt error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
<b>SPI COMMUNICATION DIGITAL OUTPUTS</b>						
SPI COMMUNICATION CHIP #1: DO1-DO7	3x00733 4x00733 I:732	0,0x0000 B:00 00			UINT16 R/O	
Actual SPI communication state:0=NO FAULT						
The current monitoring state of the SPI communication for the digital output group =0:No fault, =1:Fault						
SPI COMMUNICATION CHIP #2: DO8-DO15	3x00734 4x00734 I:733	0,0x0000 B:00 00			UINT16 R/O	
Actual SPI communication state:0=NO FAULT						
The current monitoring state of the SPI communication for the digital output group =0:No fault, =1:Fault						
SPI COMMUNICATION CHIP #3: DO16-DO22	3x00735 4x00735 I:734	0,0x0000 B:00 00			UINT16 R/O	
Actual SPI communication state:0=NO FAULT						
The current monitoring state of the SPI communication for the digital output group =0:No fault, =1:Fault						
SPI COMMUNICATION CHIP #4: DO23-DO30	3x00736 4x00736 I:735	0,0x0000 B:00 00			UINT16 R/O	

		Actual SPI communication state:0=NO FAULT				
The current monitoring state of the SPI communication for the digital output group =0:No fault, =1:Fault						
SPI COMMUNICATION CHIP #5: DO31-DO38	3x00737 4x00737 I:736	0,0x0000 B:00 00			UINT16 R/O	
		Actual SPI communication state:0=NO FAULT				
The current monitoring state of the SPI communication for the digital output group =0:No fault, =1:Fault						
SPI COMMUNICATION CHIP #6: DO39-DO45	3x00738 4x00738 I:737	0,0x0000 B:00 00			UINT16 R/O	
		Actual SPI communication state:0=NO FAULT				
The current monitoring state of the SPI communication for the digital output group =0:No fault, =1:Fault						
SPI COMMUNICATION CHIP #7: DO46-DO53	3x00739 4x00739 I:738	0,0x0000 B:00 00			UINT16 R/O	
		Actual SPI communication state:0=NO FAULT				
The current monitoring state of the SPI communication for the digital output group =0:No fault, =1:Fault						
SPI COMMUNICATION CHIP #8: DO54-DO60	3x00740 4x00740 I:739	0,0x0000 B:00 00			UINT16 R/O	
		Actual SPI communication state:0=NO FAULT				
The current monitoring state of the SPI communication for the digital output group =0:No fault, =1:Fault						
<b>DIGITAL INPUTS: RESET</b>						
RESET COUNTERS	3x10000 4x10000 I:9999	0,0x0000 B:00 00		1:PERFORM RESET	UINT16 R/W	NO
If this register is written to 1, all internal edge counters and event counters are set to 0. 0 is always returned when reading.						
HAS DIS CHANGED	3x10001 4x10001 I:10000	32,0x0020 B:00 20			UINT16 R/O	
		32 event(s)				
As soon as the module registers an event on one of the available digital inputs, this global event counter is incremented by 1. Possible events are: Detection of a short keypress Detection of the start of a long keypress Detection of the end of a long keypress						
STATUS OF ALL DIS DI1..DI16	3x10002 4x10002 I:10001	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI1:0=OFF				
		Actual state of DI2:0=OFF				
		Actual state of DI3:0=OFF				
		Actual state of DI4:0=OFF				
		Actual state of DI5:0=OFF				

		Actual state of DI6:0=OFF			
		Actual state of DI7:0=OFF			
		Actual state of DI8:0=OFF			
		Actual state of DI9:0=OFF			
		Actual state of DI10:0=OFF			
		Actual state of DI11:0=OFF			
		Actual state of DI12:0=OFF			
		Actual state of DI13:0=OFF			
		Actual state of DI14:0=OFF			
		Actual state of DI15:0=OFF			
		Actual state of DI16:0=OFF			
Actual state of all digital inputs DI1..DI12 Bit 0: =0:DI1 is OFF, =1:DI1 is ON Bit 1: =0:DI2 is OFF, =1:DI2 is ON ... Bit 14: =0:DI15 is OFF, =1:DI15 is ON Bit 15: =0:DI16 is OFF, =1:DI16 is ON					
STATUS OF ALL DIS DI17..DI32	3x10003 4x10003 I:10002	0,0x0000 B:00 00		UINT16 R/O	
		Actual state of DI17:0=OFF			
		Actual state of DI18:0=OFF			
		Actual state of DI19:0=OFF			
		Actual state of DI20:0=OFF			
		Actual state of DI21:0=OFF			
		Actual state of DI22:0=OFF			
		Actual state of DI23:0=OFF			
		Actual state of DI24:0=OFF			
		Actual state of DI25:0=OFF			
		Actual state of DI26:0=OFF			
		Actual state of DI27:0=OFF			
		Actual state of DI28:0=OFF			
		Actual state of DI29:0=OFF			
		Actual state of DI30:0=OFF			
		Actual state of DI31:0=OFF			
		Actual state of DI32:0=OFF			
Actual state of all digital inputs DI1..DI12 Bit 0: =0:DI17 is OFF, =1:DI17 is ON Bit 1: =0:DI18 is OFF, =1:DI18 is ON ... Bit 14: =0:DI31 is OFF, =1:DI31 is ON Bit 15: =0:DI32 is OFF, =1:DI32 is ON					
STATUS OF ALL DIS DI33..DI48	3x10004 4x10004 I:10003	0,0x0000 B:00 00		UINT16 R/O	
		Actual state of DI33:0=OFF			
		Actual state of DI34:0=OFF			
		Actual state of DI35:0=OFF			

		Actual state of DI36:0=OFF			
		Actual state of DI37:0=OFF			
		Actual state of DI38:0=OFF			
		Actual state of DI39:0=OFF			
		Actual state of DI40:0=OFF			
		Actual state of DI41:0=OFF			
		Actual state of DI42:0=OFF			
		Actual state of DI43:0=OFF			
		Actual state of DI44:0=OFF			
		Actual state of DI45:0=OFF			
		Actual state of DI46:0=OFF			
		Actual state of DI47:0=OFF			
		Actual state of DI48:0=OFF			
Actual state of all digital inputs DI1..DI12 Bit 0: =0:DI33 is OFF, =1:DI33 is ON Bit 1: =0:DI34 is OFF, =1:DI34 is ON ... Bit 14: =0:DI47 is OFF, =1:DI47 is ON Bit 15: =0:DI48 is OFF, =1:DI48 is ON					
STATUS OF ALL DIS DI49..DI64	3x10005 4x10005 I:10004	0,0x0000 B:00 00		UINT16 R/O	
		Actual state of DI49:0=OFF			
		Actual state of DI50:0=OFF			
		Actual state of DI51:0=OFF			
		Actual state of DI52:0=OFF			
		Actual state of DI53:0=OFF			
		Actual state of DI54:0=OFF			
		Actual state of DI55:0=OFF			
		Actual state of DI56:0=OFF			
		Actual state of DI57:0=OFF			
		Actual state of DI58:0=OFF			
		Actual state of DI59:0=OFF			
		Actual state of DI60:0=OFF			
		Actual state of DI61:0=OFF			
		Actual state of DI62:0=OFF			
		Actual state of DI63:0=OFF			
		Actual state of DI64:0=OFF			
Actual state of all digital inputs DI1..DI12 Bit 0: =0:DI49 is OFF, =1:DI49 is ON Bit 1: =0:DI50 is OFF, =1:DI50 is ON ... Bit 14: =0:DI63 is OFF, =1:DI63 is ON Bit 15: =0:DI64 is OFF, =1:DI64 is ON					
<b>STATUS OF DIGITAL OUTPUTS</b>					
STATUS OF ALL DOS DO1-DO16	3x10006 4x10006 I:10005	0,0x0000 B:00 00		0xFFFF UINT16 R/W	NO

		Actual state of DO1:0=OFF	1		
		Actual state of DO2:0=OFF	1		
		Actual state of DO3:0=OFF	1		
		Actual state of DO4:0=OFF	1		
		Actual state of DO5:0=OFF	1		
		Actual state of DO6:0=OFF	1		
		Actual state of DO7:0=OFF	1		
		Actual state of DO8:0=OFF	1		
		Actual state of DO9:0=OFF	1		
		Actual state of DO10:0=OFF	1		
		Actual state of DO11:0=OFF	1		
		Actual state of DO12:0=OFF	1		
		Actual state of DO13:0=OFF	1		
		Actual state of DO14:0=OFF	1		
		Actual state of DO15:0=OFF	1		
		Actual state of DO16:0=OFF	1		

Actual state of all digital outputs  
 Bit 0: =0:DO1 is OFF, =1:DO1 is ON  
 Bit 1: =0:DO2 is OFF, =1:DO2 is ON  
 ...  
 Bit 14: =0:DO15 is OFF, =1:DO15 is ON  
 Bit 15: =0:DO16 is OFF, =1:DO16 is ON

Write on this register sets all digital outputs to a new state

STATUS OF ALL DOS DO17-DO32	3x10007 4x10007 1:10006	0,0x0000 B:00 00		0xFFFF	UINT16 R/W	NO
		Actual state of DO17:0=OFF	1			
		Actual state of DO18:0=OFF	1			
		Actual state of DO19:0=OFF	1			
		Actual state of DO20:0=OFF	1			
		Actual state of DO21:0=OFF	1			
		Actual state of DO22:0=OFF	1			
		Actual state of DO23:0=OFF	1			
		Actual state of DO24:0=OFF	1			
		Actual state of DO25:0=OFF	1			
		Actual state of DO26:0=OFF	1			
		Actual state of DO27:0=OFF	1			
		Actual state of DO28:0=OFF	1			
		Actual state of DO29:0=OFF	1			
		Actual state of DO30:0=OFF	1			
		Actual state of DO31:0=OFF	1			
		Actual state of DO32:0=OFF	1			

Actual state of all digital outputs  
 Bit 0: =0:DO17 is OFF, =1:DO17 is ON  
 Bit 1: =0:DO18 is OFF, =1:DO18 is ON  
 ...  
 Bit 14: =0:DO31 is OFF, =1:DO31 is ON  
 Bit 15: =0:DO32 is OFF, =1:DO32 is ON

Write on this register sets all digital outputs to a new state

STATUS OF ALL DOS DO33-DO48	3x10008 4x10008 l:10007	0,0x0000 B:00 00		0xFFFF	UINT16 R/W	NO
		Actual state of DO33:0=OFF	1			
		Actual state of DO34:0=OFF	1			
		Actual state of DO35:0=OFF	1			
		Actual state of DO36:0=OFF	1			
		Actual state of DO37:0=OFF	1			
		Actual state of DO38:0=OFF	1			
		Actual state of DO39:0=OFF	1			
		Actual state of DO40:0=OFF	1			
		Actual state of DO41:0=OFF	1			
		Actual state of DO42:0=OFF	1			
		Actual state of DO43:0=OFF	1			
		Actual state of DO44:0=OFF	1			
		Actual state of DO45:0=OFF	1			
		Actual state of DO46:0=OFF	1			
		Actual state of DO47:0=OFF	1			
		Actual state of DO48:0=OFF	1			

Actual state of all digital outputs  
 Bit 0: =0:DO33 is OFF, =1:DO33 is ON  
 Bit 1: =0:DO34 is OFF, =1:DO34 is ON  
 ...  
 Bit 14: =0:DO47 is OFF, =1:DO47 is ON  
 Bit 15: =0:DO48 is OFF, =1:DO48 is ON

Write on this register sets all digital outputs to a new state

STATUS OF ALL DOS DO49-DO60	3x10009 4x10009 l:10008	0,0x0000 B:00 00		#BEZUG!	UINT16 R/W	NO
		Actual state of DO49:0=OFF	1			
		Actual state of DO50:0=OFF	1			
		Actual state of DO51:0=OFF	1			
		Actual state of DO52:0=OFF	1			
		Actual state of DO53:0=OFF	1			
		Actual state of DO54:0=OFF	1			
		Actual state of DO55:0=OFF	1			
		Actual state of DO56:0=OFF	1			
		Actual state of DO57:0=OFF	1			
		Actual state of DO58:0=OFF	1			
		Actual state of DO59:0=OFF	1			



		Actual state of DO60:0=OFF	1		
<p>Actual state of all digital outputs          Bit 0: =0:DO49 is OFF, =1:DO49 is ON          Bit 1: =0:DO50 is OFF, =1:DO50 is ON          ...          Bit 10: =0:DO59 is OFF, =1:DO59 is ON          Bit 11: =0:DO60 is OFF, =1:DO60 is ON</p> <p>Write on this register sets all digital outputs to a new state</p>					
<b>DIGITAL OUTPUTS:ENABLE OPEN WIRE DETECTION WHILE ON</b>					
ENABLE OPEN WIRE DETECTION WHILE ON DO1-DO16	3x10010 4x10010 1:10009	65535,0xFFFF B:FF FF		0xFFFF	UINT16 R/W
		Actual setup of open wire detection while ON for DO1:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO2:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO3:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO4:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO5:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO6:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO7:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO8:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO9:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO10:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO11:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO12:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO13:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO14:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO15:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO16:1=ENABLED	1		

Actual setup state for open wire detection while ON for digital output DOx

Bit 0: =0:Open wire detection for DO1 is DISABLED, =1:Open wire detection for DO1 is ENABLED

Bit 1: =0:Open wire detection for DO2 is DISABLED, =1:Open wire detection for DO2 is ENABLED

...

Bit 14: =0:Open wire detection for DO15 is DISABLED, =1:Open wire detection for DO15 is ENABLED

Bit 15: =0:Open wire detection for DO16 is DISABLED, =1:Open wire detection for DO16 is ENABLED

Write on this register sets for all digital outputs a new setup state

ENABLE OPEN WIRE DETECTION WHILE ON DO17-DO32	3x10011 4x10011 1:10010	65535,0xFFFF B:FF FF		0xFFFF	UINT16 R/W	NO
		Actual setup of open wire detection while ON for DO17:1=ENABLED	1			
		Actual setup of open wire detection while ON for DO18:1=ENABLED	1			
		Actual setup of open wire detection while ON for DO19:1=ENABLED	1			
		Actual setup of open wire detection while ON for DO20:1=ENABLED	1			
		Actual setup of open wire detection while ON for DO21:1=ENABLED	1			
		Actual setup of open wire detection while ON for DO22:1=ENABLED	1			
		Actual setup of open wire detection while ON for DO23:1=ENABLED	1			
		Actual setup of open wire detection while ON for DO24:1=ENABLED	1			
		Actual setup of open wire detection while ON for DO25:1=ENABLED	1			
		Actual setup of open wire detection while ON for DO26:1=ENABLED	1			
		Actual setup of open wire detection while ON for DO27:1=ENABLED	1			
		Actual setup of open wire detection while ON for DO28:1=ENABLED	1			
		Actual setup of open wire detection while ON for DO29:1=ENABLED	1			
		Actual setup of open wire detection while ON for DO30:1=ENABLED	1			
		Actual setup of open wire detection while ON for DO31:1=ENABLED	1			
		Actual setup of open wire detection while ON for DO32:1=ENABLED	1			

Actual setup state for open wire detection while ON for digital output DOx  
 Bit 0: =0:Open wire detection for DO17 is DISABLED, =1:Open wire detection for DO17 is ENABLED  
 Bit 1: =0:Open wire detection for DO18 is DISABLED, =1:Open wire detection for DO18 is ENABLED  
 ...  
 Bit 14: =0:Open wire detection for DO31 is DISABLED, =1:Open wire detection for DO31 is ENABLED  
 Bit 15: =0:Open wire detection for DO32 is DISABLED, =1:Open wire detection for DO32 is ENABLED

Write on this register sets for all digital outputs a new setup state

ENABLE OPEN WIRE DETECTION WHILE ON DO33-DO48	3x10012 4x10012 l:10011	65535,0xFFFF B:FF FF	0xFFFF	UINT16 R/W	NO
		Actual setup of open wire detection while ON for DO33:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO34:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO35:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO36:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO37:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO38:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO39:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO40:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO41:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO42:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO43:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO44:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO45:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO46:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO47:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO48:1=ENABLED	1		

Actual setup state for open wire detection while ON for digital output DOx  
 Bit 0: =0:Open wire detection for DO33 is DISABLED, =1:Open wire detection for DO33 is ENABLED  
 Bit 1: =0:Open wire detection for DO34 is DISABLED, =1:Open wire detection for DO34 is ENABLED  
 ...  
 Bit 14: =0:Open wire detection for DO47 is DISABLED, =1:Open wire detection for DO47 is ENABLED  
 Bit 15: =0:Open wire detection for DO48 is DISABLED, =1:Open wire detection for DO48 is ENABLED

Write on this register sets for all digital outputs a new setup state

ENABLE OPEN WIRE DETECTION WHILE ON DO49-DO60	3x10013 4x10013 l:10012	16383,0x3FFF B:3F FF	#BEZUG!	UINT16 R/W	NO
		Actual setup of open wire detection while ON for DO49:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO50:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO51:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO52:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO53:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO54:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO55:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO56:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO57:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO58:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO59:1=ENABLED	1		
		Actual setup of open wire detection while ON for DO60:1=ENABLED	1		

Actual setup state for open wire detection while ON for digital output DOx  
 Bit 0: =0:Open wire detection for DO49 is DISABLED, =1:Open wire detection for DO49 is ENABLED  
 Bit 1: =0:Open wire detection for DO50 is DISABLED, =1:Open wire detection for DO50 is ENABLED  
 ...  
 Bit 10: =0:Open wire detection for DO59 is DISABLED, =1:Open wire detection for DO59 is ENABLED  
 Bit 11: =0:Open wire detection for DO60 is DISABLED, =1:Open wire detection for DO60 is ENABLED

Write on this register sets for all digital outputs a new setup state

#### DIGITAL OUTPUTS:ENABLE OPEN WIRE DETECTION WHILE OFF

ENABLE OPEN WIRE DETECTION WHILE OFF DO1-DO16	3x10014 4x10014 l:10013	65535,0xFFFF B:FF FF	0xFFFF	UINT16 R/W	NO
		Actual setup of open wire detection while OFF for DO1:1=ENABLED	1		

		Actual setup of open wire detection while OFF for DO2:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO3:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO4:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO5:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO6:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO7:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO8:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO9:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO10:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO11:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO12:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO13:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO14:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO15:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO16:1=ENABLED	1		

Actual setup state for open wire detection while OFF for digital output DOx

Bit 0: =0:Open wire detection for DO1 is DISABLED, =1:Open wire detection for DO1 is ENABLED

Bit 1: =0:Open wire detection for DO2 is DISABLED, =1:Open wire detection for DO2 is ENABLED

...

Bit 14: =0:Open wire detection for DO15 is DISABLED, =1:Open wire detection for DO15 is ENABLED

Bit 15: =0:Open wire detection for DO16 is DISABLED, =1:Open wire detection for DO16 is ENABLED

Write on this register sets for all digital outputs a new setup state

ENABLE OPEN WIRE DETECTION WHILE OFF DO17-DO32	3x10015 4x10015 l:10014	65535,0xFFFF B:FF FF		0xFFFF	UINT16 R/W	NO
		Actual setup of open wire detection while OFF for DO17:1=ENABLED	1			
		Actual setup of open wire detection while OFF for DO18:1=ENABLED	1			
		Actual setup of open wire detection while OFF for DO19:1=ENABLED	1			

		Actual setup of open wire detection while OFF for DO20:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO21:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO22:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO23:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO24:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO25:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO26:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO27:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO28:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO29:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO30:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO31:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO32:1=ENABLED	1		

Actual setup state for open wire detection while OFF for digital output DOx

Bit 0: =0:Open wire detection for DO17 is DISABLED, =1:Open wire detection for DO17 is ENABLED

Bit 1: =0:Open wire detection for DO18 is DISABLED, =1:Open wire detection for DO18 is ENABLED

...

Bit 14: =0:Open wire detection for DO31 is DISABLED, =1:Open wire detection for DO31 is ENABLED

Bit 15: =0:Open wire detection for DO32 is DISABLED, =1:Open wire detection for DO32 is ENABLED

Write on this register sets for all digital outputs a new setup state

ENABLE OPEN WIRE DETECTION WHILE OFF DO33-DO48	3x10016 4x10016 l:10015	65535,0xFFFF B:FF FF		0xFFFF	UINT16 R/W	NO
		Actual setup of open wire detection while OFF for DO33:1=ENABLED	1			
		Actual setup of open wire detection while OFF for DO34:1=ENABLED	1			
		Actual setup of open wire detection while OFF for DO35:1=ENABLED	1			
		Actual setup of open wire detection while OFF for DO36:1=ENABLED	1			
		Actual setup of open wire detection while OFF for DO37:1=ENABLED	1			

		Actual setup of open wire detection while OFF for DO38:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO39:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO40:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO41:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO42:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO43:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO44:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO45:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO46:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO47:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO48:1=ENABLED	1		

Actual setup state for open wire detection while OFF for digital output DOx

Bit 0: =0:Open wire detection for DO33 is DISABLED, =1:Open wire detection for DO33 is ENABLED

Bit 1: =0:Open wire detection for DO34 is DISABLED, =1:Open wire detection for DO34 is ENABLED

...

Bit 14: =0:Open wire detection for DO47 is DISABLED, =1:Open wire detection for DO47 is ENABLED

Bit 15: =0:Open wire detection for DO48 is DISABLED, =1:Open wire detection for DO48 is ENABLED

Write on this register sets for all digital outputs a new setup state

ENABLE OPEN WIRE DETECTION WHILE OFF DO49-DO60	3x10017 4x10017 l:10016	16383,0x3FFF B:3F FF		#BEZUG!	UINT16 R/W	NO
		Actual setup of open wire detection while OFF for DO49:1=ENABLED	1			
		Actual setup of open wire detection while OFF for DO50:1=ENABLED	1			
		Actual setup of open wire detection while OFF for DO51:1=ENABLED	1			
		Actual setup of open wire detection while OFF for DO52:1=ENABLED	1			
		Actual setup of open wire detection while OFF for DO53:1=ENABLED	1			
		Actual setup of open wire detection while OFF for DO54:1=ENABLED	1			
		Actual setup of open wire detection while OFF for DO55:1=ENABLED	1			

		Actual setup of open wire detection while OFF for DO56:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO57:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO58:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO59:1=ENABLED	1		
		Actual setup of open wire detection while OFF for DO60:1=ENABLED	1		

Actual setup state for open wire detection while OFF for digital output DOx  
 Bit 0: =0:Open wire detection for DO49 is DISABLED, =1:Open wire detection for DO49 is ENABLED  
 Bit 1: =0:Open wire detection for DO50 is DISABLED, =1:Open wire detection for DO50 is ENABLED  
 ...  
 Bit 10: =0:Open wire detection for DO59 is DISABLED, =1:Open wire detection for DO59 is ENABLED  
 Bit 11: =0:Open wire detection for DO60 is DISABLED, =1:Open wire detection for DO60 is ENABLED

Write on this register sets for all digital outputs a new setup state

#### DIGITAL OUTPUTS:ENABLE SHORTCUT TO VDD DETECTION WHILE OFF

ENABLE SHORTCUT TO VDD DETECTION WHILE OFF DO1-DO16	3x10018 4x10018 1:10017	65535,0xFFFF B:FF FF		0xFFFF	UINT16 R/W	NO
		Actual setup of shortcut detection to VDD while OFF for DO1:1=ENABLED	1			
		Actual setup of shortcut detection to VDD while OFF for DO2:1=ENABLED	1			
		Actual setup of shortcut detection to VDD while OFF for DO3:1=ENABLED	1			
		Actual setup of shortcut detection to VDD while OFF for DO4:1=ENABLED	1			
		Actual setup of shortcut detection to VDD while OFF for DO5:1=ENABLED	1			
		Actual setup of shortcut detection to VDD while OFF for DO6:1=ENABLED	1			
		Actual setup of shortcut detection to VDD while OFF for DO7:1=ENABLED	1			
		Actual setup of shortcut detection to VDD while OFF for DO8:1=ENABLED	1			
		Actual setup of shortcut detection to VDD while OFF for DO9:1=ENABLED	1			
		Actual setup of shortcut detection to VDD while OFF for DO10:1=ENABLED	1			
		Actual setup of shortcut detection to VDD while OFF for DO11:1=ENABLED	1			
		Actual setup of shortcut detection to VDD while OFF for DO12:1=ENABLED	1			
		Actual setup of shortcut detection to VDD while OFF for DO13:1=ENABLED	1			



		Actual setup of shortcut detection to VDD while OFF for DO14:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO15:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO16:1=ENABLED	1		
Actual setup state for shortcut to VDD detection while OFF for digital output DOx Bit 0: =0:Shortcut to VDD detection for DO1 is DISABLED, =1:Shortcut to VDD detection for DO1 is ENABLED Bit 1: =0:Shortcut to VDD detection for DO2 is DISABLED, =1:Shortcut to VDD detection for DO2 is ENABLED ... Bit 14: =0:Shortcut to VDD detection for DO15 is DISABLED, =1:Shortcut to VDD detection for DO15 is ENABLED Bit 15: =0:Shortcut to VDD detection for DO16 is DISABLED, =1:Shortcut to VDD detection for DO16 is ENABLED					
Write on this register sets for all digital outputs a new setup state					
ENABLE SHORTCUT TO VDD DETECTION WHILE OFF DO17-DO32	3x10019 4x10019 l:10018	65535,0xFFFF B:FF FF		0xFFFF	UINT16 R/W
		Actual setup of shortcut detection to VDD while OFF for DO17:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO18:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO19:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO20:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO21:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO22:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO23:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO24:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO25:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO26:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO27:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO28:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO29:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO30:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO31:1=ENABLED	1		

		Actual setup of shortcut detection to VDD while OFF for DO32:1=ENABLED	1		
Actual setup state for shortcut to VDD detection while OFF for digital output DOx Bit 0: =0:Shortcut to VDD detection for DO17 is DISABLED, =1:Shortcut to VDD detection for DO17 is ENABLED Bit 1: =0:Shortcut to VDD detection for DO18 is DISABLED, =1:Shortcut to VDD detection for DO18 is ENABLED ... Bit 14: =0:Shortcut to VDD detection for DO31 is DISABLED, =1:Shortcut to VDD detection for DO31 is ENABLED Bit 15: =0:Shortcut to VDD detection for DO32 is DISABLED, =1:Shortcut to VDD detection for DO32 is ENABLED					
Write on this register sets for all digital outputs a new setup state					
ENABLE SHORTCUT TO VDD DETECTION WHILE OFF DO33-DO48	3x10020 4x10020 1:10019	65535,0xFFFF B:FF FF		0xFFFF	UINT16 R/W
		Actual setup of shortcut detection to VDD while OFF for DO33:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO34:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO35:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO36:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO37:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO38:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO39:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO40:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO41:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO42:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO43:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO44:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO45:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO46:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO47:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO48:1=ENABLED	1		

Actual setup state for shortcut to VDD detection while OFF for digital output DOx  
 Bit 0: =0:Shortcut to VDD detection for DO33 is DISABLED, =1:Shortcut to VDD detection for DO33 is ENABLED  
 Bit 1: =0:Shortcut to VDD detection for DO34 is DISABLED, =1:Shortcut to VDD detection for DO34 is ENABLED  
 ...  
 Bit 14: =0:Shortcut to VDD detection for DO47 is DISABLED, =1:Shortcut to VDD detection for DO47 is ENABLED  
 Bit 15: =0:Shortcut to VDD detection for DO48 is DISABLED, =1:Shortcut to VDD detection for DO48 is ENABLED

Write on this register sets for all digital outputs a new setup state

ENABLE SHORTCUT TO VDD DETECTION WHILE OFF DO49-DO60	3x10021 4x10021 l:10020	16383,0x3FFF B:3F FF	#BEZUG!	UINT16 R/W	NO
		Actual setup of shortcut detection to VDD while OFF for DO49:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO50:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO51:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO52:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO53:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO54:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO55:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO56:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO57:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO58:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO59:1=ENABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO60:1=ENABLED	1		

Actual setup state for shortcut to VDD detection while OFF for digital output DOx  
 Bit 17: =0:Shortcut to VDD detection for DO49 is DISABLED, =1:Shortcut to VDD detection for DO49 is ENABLED  
 Bit 18: =0:Shortcut to VDD detection for DO50 is DISABLED, =1:Shortcut to VDD detection for DO50 is ENABLED  
 ...  
 Bit 10: =0:Shortcut to VDD detection for DO59 is DISABLED, =1:Shortcut to VDD detection for DO59 is ENABLED  
 Bit 11: =0:Shortcut to VDD detection for DO60 is DISABLED, =1:Shortcut to VDD detection for DO60 is ENABLED

Write on this register sets for all digital outputs a new setup state

#### DIGITAL OUTPUTS:OPEN WIRE WHILE ON DETECTION STATE

OPEN WIRE DETECTION STATE WHILE ON DO1-DO16	3x10022 4x10022 l:10021	0,0x0000 B:00 00		UINT16 R/O	
		Actual state of open wire detection while ON for DO1:0=OFF			

		Actual state of open wire detection while ON for DO2:0=OFF			
		Actual state of open wire detection while ON for DO3:0=OFF			
		Actual state of open wire detection while ON for DO4:0=OFF			
		Actual state of open wire detection while ON for DO5:0=OFF			
		Actual state of open wire detection while ON for DO6:0=OFF			
		Actual state of open wire detection while ON for DO7:0=OFF			
		Actual state of open wire detection while ON for DO8:0=OFF			
		Actual state of open wire detection while ON for DO9:0=OFF			
		Actual state of open wire detection while ON for DO10:0=OFF			
		Actual state of open wire detection while ON for DO11:0=OFF			
		Actual state of open wire detection while ON for DO12:0=OFF			
		Actual state of open wire detection while ON for DO13:0=OFF			
		Actual state of open wire detection while ON for DO14:0=OFF			
		Actual state of open wire detection while ON for DO15:0=OFF			
		Actual state of open wire detection while ON for DO16:0=OFF			
Actual diagnostic state for open wire detection while ON for digital output DOx Bit 0: =0:Output DO1 is OK, =1:Fault-Open wire detected on DO1 Bit 1: =0:Output DO2 is OK, =1:Fault-Open wire detected on DO2 ... Bit 14: =0:Output DO15 is OK, =1:Fault-Open wire detected on DO15 Bit 15: =0:Output DO16 is OK, =1:Fault-Open wire detected on DO16					
OPEN WIRE DETECTION STATE WHILE ON DO17-DO32	3x10023 4x10023 l:10022	0,0x0000 B:00 00			UINT16 R/O
		Actual state of open wire detection while ON for DO17:0=OFF			
		Actual state of open wire detection while ON for DO18:0=OFF			
		Actual state of open wire detection while ON for DO19:0=OFF			
		Actual state of open wire detection while ON for DO20:0=OFF			

		Actual state of open wire detection while ON for DO21:0=OFF			
		Actual state of open wire detection while ON for DO22:0=OFF			
		Actual state of open wire detection while ON for DO23:0=OFF			
		Actual state of open wire detection while ON for DO24:0=OFF			
		Actual state of open wire detection while ON for DO25:0=OFF			
		Actual state of open wire detection while ON for DO26:0=OFF			
		Actual state of open wire detection while ON for DO27:0=OFF			
		Actual state of open wire detection while ON for DO28:0=OFF			
		Actual state of open wire detection while ON for DO29:0=OFF			
		Actual state of open wire detection while ON for DO30:0=OFF			
		Actual state of open wire detection while ON for DO31:0=OFF			
		Actual state of open wire detection while ON for DO32:0=OFF			
Actual diagnostic state for open wire detection while ON for digital output DOx Bit 0: =0:Output DO17 is OK, =1:Fault-Open wire detected on DO17 Bit 1: =0:Output DO18 is OK, =1:Fault-Open wire detected on DO18 ... Bit 14: =0:Output DO31 is OK, =1:Fault-Open wire detected on DO31 Bit 15: =0:Output DO32 is OK, =1:Fault-Open wire detected on DO32					
OPEN WIRE DETECTION STATE WHILE ON DO33-DO48	3x10024 4x10024 l:10023	0,0x0000 B:00 00		UINT16 R/O	
		Actual state of open wire detection while ON for DO33:0=OFF			
		Actual state of open wire detection while ON for DO34:0=OFF			
		Actual state of open wire detection while ON for DO35:0=OFF			
		Actual state of open wire detection while ON for DO36:0=OFF			
		Actual state of open wire detection while ON for DO37:0=OFF			
		Actual state of open wire detection while ON for DO38:0=OFF			
		Actual state of open wire detection while ON for DO39:0=OFF			

		Actual state of open wire detection while ON for DO40:0=OFF			
		Actual state of open wire detection while ON for DO41:0=OFF			
		Actual state of open wire detection while ON for DO42:0=OFF			
		Actual state of open wire detection while ON for DO43:0=OFF			
		Actual state of open wire detection while ON for DO44:0=OFF			
		Actual state of open wire detection while ON for DO45:0=OFF			
		Actual state of open wire detection while ON for DO46:0=OFF			
		Actual state of open wire detection while ON for DO47:0=OFF			
		Actual state of open wire detection while ON for DO48:0=OFF			
Actual diagnostic state for open wire detection while ON for digital output DOx Bit 0: =0:Output DO33 is OK, =1:Fault-Open wire detected on DO33 Bit 1: =0:Output DO34 is OK, =1:Fault-Open wire detected on DO34 ... Bit 14: =0:Output DO47 is OK, =1:Fault-Open wire detected on DO47 Bit 15: =0:Output DO48 is OK, =1:Fault-Open wire detected on DO48					
OPEN WIRE DETECTION STATE WHILE ON DO49-DO60	3x10025 4x10025 I:10024	0,0x0000 B:00 00		UINT16 R/O	
		Actual state of open wire detection while ON for DO49:0=OFF			
		Actual state of open wire detection while ON for DO50:0=OFF			
		Actual state of open wire detection while ON for DO51:0=OFF			
		Actual state of open wire detection while ON for DO52:0=OFF			
		Actual state of open wire detection while ON for DO53:0=OFF			
		Actual state of open wire detection while ON for DO54:0=OFF			
		Actual state of open wire detection while ON for DO55:0=OFF			
		Actual state of open wire detection while ON for DO56:0=OFF			
		Actual state of open wire detection while ON for DO57:0=OFF			
		Actual state of open wire detection while ON for DO58:0=OFF			

		Actual state of open wire detection while ON for DO59:0=OFF			
		Actual state of open wire detection while ON for DO60:0=OFF			
Actual diagnostic state for open wire detection while ON for digital output DOx Bit 0: =0:Output DO49 is OK, =1:Fault-Open wire detected on DO49 Bit 1: =0:Output DO50 is OK, =1:Fault-Open wire detected on DO50 ... Bit 10: =0:Output DO59 is OK, =1:Fault-Open wire detected on DO59 Bit 11: =0:Output DO60 is OK, =1:Fault-Open wire detected on DO60					
<b>DIGITAL OUTPUTS:OPEN WIRE WHILE OFF DETECTION STATE</b>					
OPEN WIRE DETECTION STATE WHILE OFF DO1-DO16	3x10026 4x10026 1:10025	32512,0x7F00 B:7F 00			UINT16 R/O
		Actual state of open wire detection while OFF for DO1:0=OFF			
		Actual state of open wire detection while OFF for DO2:0=OFF			
		Actual state of open wire detection while OFF for DO3:0=OFF			
		Actual state of open wire detection while OFF for DO4:0=OFF			
		Actual state of open wire detection while OFF for DO5:0=OFF			
		Actual state of open wire detection while OFF for DO6:0=OFF			
		Actual state of open wire detection while OFF for DO7:0=OFF			
		Actual state of open wire detection while OFF for DO8:0=OFF			
		Actual state of open wire detection while OFF for DO9:1=ON			
		Actual state of open wire detection while OFF for DO10:1=ON			
		Actual state of open wire detection while OFF for DO11:1=ON			
		Actual state of open wire detection while OFF for DO12:1=ON			
		Actual state of open wire detection while OFF for DO13:1=ON			
		Actual state of open wire detection while OFF for DO14:1=ON			
		Actual state of open wire detection while OFF for DO15:1=ON			
		Actual state of open wire detection while OFF for DO16:0=OFF			

Actual diagnostic state for open wire detection while OFF for digital output DOx

Bit 0: =0:Output DO1 is OK, =1:Fault-Open wire detected on DO1

Bit 1: =0:Output DO2 is OK, =1:Fault-Open wire detected on DO2

...

Bit 14: =0:Output DO15 is OK, =1:Fault-Open wire detected on DO15

Bit 15: =0:Output DO16 is OK, =1:Fault-Open wire detected on DO16

OPEN WIRE DETECTION STATE WHILE OFF DO17-DO32	3x10027 4x10027 1:10026	32512,0x7F00 B:7F 00		UINT16 R/O	
		Actual state of open wire detection while OFF for DO17:0=OFF			
		Actual state of open wire detection while OFF for DO18:0=OFF			
		Actual state of open wire detection while OFF for DO19:0=OFF			
		Actual state of open wire detection while OFF for DO20:0=OFF			
		Actual state of open wire detection while OFF for DO21:0=OFF			
		Actual state of open wire detection while OFF for DO22:0=OFF			
		Actual state of open wire detection while OFF for DO23:0=OFF			
		Actual state of open wire detection while OFF for DO24:0=OFF			
		Actual state of open wire detection while OFF for DO25:1=ON			
		Actual state of open wire detection while OFF for DO26:1=ON			
		Actual state of open wire detection while OFF for DO27:1=ON			
		Actual state of open wire detection while OFF for DO28:1=ON			
		Actual state of open wire detection while OFF for DO29:1=ON			
		Actual state of open wire detection while OFF for DO30:1=ON			
		Actual state of open wire detection while OFF for DO31:1=ON			
		Actual state of open wire detection while OFF for DO32:0=OFF			

Actual diagnostic state for open wire detection while OFF for digital output DOx

Bit 0: =0:Output DO17 is OK, =1:Fault-Open wire detected on DO17

Bit 1: =0:Output DO18 is OK, =1:Fault-Open wire detected on DO18

...

Bit 14: =0:Output DO31 is OK, =1:Fault-Open wire detected on DO31

Bit 15: =0:Output DO32 is OK, =1:Fault-Open wire detected on DO32



OPEN WIRE DETECTION STATE WHILE OFF DO33-DO48	3x10028 4x10028 I:10027	32512,0x7F00 B:7F 00		UINT16 R/O	
		Actual state of open wire detection while OFF for DO33:0=OFF			
		Actual state of open wire detection while OFF for DO34:0=OFF			
		Actual state of open wire detection while OFF for DO35:0=OFF			
		Actual state of open wire detection while OFF for DO36:0=OFF			
		Actual state of open wire detection while OFF for DO37:0=OFF			
		Actual state of open wire detection while OFF for DO38:0=OFF			
		Actual state of open wire detection while OFF for DO39:0=OFF			
		Actual state of open wire detection while OFF for DO40:0=OFF			
		Actual state of open wire detection while OFF for DO41:1=ON			
		Actual state of open wire detection while OFF for DO42:1=ON			
		Actual state of open wire detection while OFF for DO43:1=ON			
		Actual state of open wire detection while OFF for DO44:1=ON			
		Actual state of open wire detection while OFF for DO45:1=ON			
		Actual state of open wire detection while OFF for DO46:1=ON			
		Actual state of open wire detection while OFF for DO47:1=ON			
		Actual state of open wire detection while OFF for DO48:0=OFF			
Actual diagnostic state for open wire detection while OFF for digital output DOx Bit 0: =0:Output DO33 is OK, =1:Fault-Open wire detected on DO33 Bit 1: =0:Output DO34 is OK, =1:Fault-Open wire detected on DO34 ... Bit 14: =0:Output DO47 is OK, =1:Fault-Open wire detected on DO47 Bit 15: =0:Output DO48 is OK, =1:Fault-Open wire detected on DO48					
OPEN WIRE DETECTION STATE WHILE OFF DO49-DO60	3x10029 4x10029 I:10028	0,0x0000 B:00 00		UINT16 R/O	
		Actual state of open wire detection while OFF for DO49:0=OFF			

		Actual state of open wire detection while OFF for DO50:0=OFF			
		Actual state of open wire detection while OFF for DO51:0=OFF			
		Actual state of open wire detection while OFF for DO52:0=OFF			
		Actual state of open wire detection while OFF for DO53:0=OFF			
		Actual state of open wire detection while OFF for DO54:0=OFF			
		Actual state of open wire detection while OFF for DO55:0=OFF			
		Actual state of open wire detection while OFF for DO56:0=OFF			
		Actual state of open wire detection while OFF for DO57:0=OFF			
		Actual state of open wire detection while OFF for DO58:0=OFF			
		Actual state of open wire detection while OFF for DO59:0=OFF			
		Actual state of open wire detection while OFF for DO60:0=OFF			

Actual diagnostic state for open wire detection while OFF for digital output DOx

Bit 0: =0:Output DO49 is OK, =1:Fault-Open wire detected on DO49

Bit 1: =0:Output DO50 is OK, =1:Fault-Open wire detected on DO50

...

Bit 10: =0:Output DO59 is OK, =1:Fault-Open wire detected on DO59

Bit 11: =0:Output DO60 is OK, =1:Fault-Open wire detected on DO60

#### DIGITAL OUTPUTS:SHORTCUT TO VDD WHILE OFF DETECTION STATE

SHORTCUT TO VDD WHILE OFF DETECTION STATE DO1-DO16	3x10030 4x10030 1:10029	0,0x0000 B:00 00			UINT16 R/O
		Actual state of shortcut to VDD detection while OFF for DO1:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO2:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO3:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO4:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO5:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO6:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO7:0=OFF			

		Actual state of shortcut to VDD detection while OFF for DO8:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO9:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO10:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO11:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO12:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO13:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO14:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO15:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO16:0=OFF			
Actual diagnostic state for shortcut to VDD detection while OFF for digital output DOx Bit 0: =0:Output DO1 is OK, =1:Fault-shortcut detected on DO1 Bit 1: =0:Output DO2 is OK, =1:Fault-shortcut detected on DO2 ... Bit 14: =0:Output DO15 is OK, =1:Fault-shortcut detected on DO15 Bit 15: =0:Output DO16 is OK, =1:Fault-shortcut detected on DO16					
SHORTCUT TO VDD WHILE OFF DETECTION STATE DO17-DO32	3x10031 4x10031 l:10030	0,0x0000 B:00 00		UINT16 R/O	
		Actual state of shortcut to VDD detection while OFF for DO17:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO18:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO19:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO20:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO21:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO22:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO23:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO24:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO25:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO26:0=OFF			

		Actual state of shortcut to VDD detection while OFF for DO27:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO28:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO29:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO30:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO31:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO32:0=OFF			
Actual diagnostic state for shortcut to VDD detection while OFF for digital output DOx Bit 0: =0:Output DO17 is OK, =1:Fault-shortcut detected on DO17 Bit 1: =0:Output DO18 is OK, =1:Fault-shortcut detected on DO18 ... Bit 14: =0:Output DO31 is OK, =1:Fault-shortcut detected on DO31 Bit 15: =0:Output DO32 is OK, =1:Fault-shortcut detected on DO32					
SHORTCUT TO VDD WHILE OFF DETECTION STATE DO33-DO60	3x10032 4x10032 1:10031	0,0x0000 B:00 00		UINT16 R/O	
		Actual state of shortcut to VDD detection while OFF for DO33:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO34:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO35:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO36:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO37:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO38:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO39:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO40:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO41:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO42:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO43:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO44:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO45:0=OFF			

		Actual state of shortcut to VDD detection while OFF for DO46:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO47:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO48:0=OFF			
Actual diagnostic state for shortcut to VDD detection while OFF for digital output DOx Bit 0: =0:Output DO33 is OK, =1:Fault-shortcut detected on DO33 Bit 1: =0:Output DO34 is OK, =1:Fault-shortcut detected on DO34 ... Bit 14: =0:Output DO47 is OK, =1:Fault-shortcut detected on DO47 Bit 15: =0:Output DO48 is OK, =1:Fault-shortcut detected on DO48					
SHORTCUT TO VDD WHILE OFF DETECTION STATE DO49-DO60	3x10033 4x10033 1:10032	0,0x0000 B:00 00		UINT16 R/O	
		Actual state of shortcut to VDD detection while OFF for DO49:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO50:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO51:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO52:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO53:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO54:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO55:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO56:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO57:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO58:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO59:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO60:0=OFF			
Actual diagnostic state for shortcut to VDD detection while OFF for digital output DOx Bit 0: =0:Output DO49 is OK, =1:Fault-shortcut detected on DO49 Bit 1: =0:Output DO50 is OK, =1:Fault-shortcut detected on DO50 ... Bit 10: =0:Output DO59 is OK, =1:Fault-shortcut detected on DO59 Bit 11: =0:Output DO60 is OK, =1:Fault-shortcut detected on DO60					
<b>DIGITAL OUTPUTS:THERMAL OVERLOAD DETECTION STATE</b>					

THERMAL OVERLOAD DETECTION STATE DO1-DO16	3x10034 4x10034 I:10033	0,0x0000 B:00 00			UINT16 R/O	
		Actual thermal overload detection state for DO1:0=OFF				
		Actual thermal overload detection state for DO2:0=OFF				
		Actual thermal overload detection state for DO3:0=OFF				
		Actual thermal overload detection state for DO4:0=OFF				
		Actual thermal overload detection state for DO5:0=OFF				
		Actual thermal overload detection state for DO6:0=OFF				
		Actual thermal overload detection state for DO7:0=OFF				
		Actual thermal overload detection state for DO8:0=OFF				
		Actual thermal overload detection state for DO9:0=OFF				
		Actual thermal overload detection state for DO10:0=OFF				
		Actual thermal overload detection state for DO11:0=OFF				
		Actual thermal overload detection state for DO12:0=OFF				
		Actual thermal overload detection state for DO13:0=OFF				
		Actual thermal overload detection state for DO14:0=OFF				
		Actual thermal overload detection state for DO15:0=OFF				
		Actual thermal overload detection state for DO16:0=OFF				
Actual thermal overload detection state for digital output DOx Bit 0: =0:Output DO1 is OK, =1:Fault-Thermal overload on DO1 Bit 1: =0:Output DO2 is OK, =1:Fault-Thermal overload on DO2 ... Bit 14: =0:Output DO15 is OK, =1:Fault-Thermal overload on DO15 Bit 15: =0:Output DO16 is OK, =1:Fault-Thermal overload on DO16						
THERMAL OVERLOAD DETECTION STATE DO17-DO32	3x10035 4x10035 I:10034	0,0x0000 B:00 00			UINT16 R/O	
		Actual thermal overload detection state for DO17:0=OFF				

		Actual thermal overload detection state for DO18:0=OFF			
		Actual thermal overload detection state for DO19:0=OFF			
		Actual thermal overload detection state for DO20:0=OFF			
		Actual thermal overload detection state for DO21:0=OFF			
		Actual thermal overload detection state for DO22:0=OFF			
		Actual thermal overload detection state for DO23:0=OFF			
		Actual thermal overload detection state for DO24:0=OFF			
		Actual thermal overload detection state for DO25:0=OFF			
		Actual thermal overload detection state for DO26:0=OFF			
		Actual thermal overload detection state for DO27:0=OFF			
		Actual thermal overload detection state for DO28:0=OFF			
		Actual thermal overload detection state for DO29:0=OFF			
		Actual thermal overload detection state for DO30:0=OFF			
		Actual thermal overload detection state for DO31:0=OFF			
		Actual thermal overload detection state for DO32:0=OFF			
<p>Actual thermal overload detection state for digital output DOx            Bit 0: =0:Output DO17 is OK, =1:Fault-Thermal overload on DO17            Bit 1: =0:Output DO18 is OK, =1:Fault-Thermal overload on DO18            ...            Bit 14: =0:Output DO31 is OK, =1:Fault-Thermal overload on DO31            Bit 15: =0:Output DO32 is OK, =1:Fault-Thermal overload on DO32</p>					
THERMAL OVERLOAD DETECTION STATE DO33-DO48	3x10036 4x10036 1:10035	0,0x0000 B:00 00			UINT16 R/O
		Actual thermal overload detection state for DO33:0=OFF			
		Actual thermal overload detection state for DO34:0=OFF			
		Actual thermal overload detection state for DO35:0=OFF			
		Actual thermal overload detection state for DO36:0=OFF			

		Actual thermal overload detection state for DO37:0=OFF			
		Actual thermal overload detection state for DO38:0=OFF			
		Actual thermal overload detection state for DO39:0=OFF			
		Actual thermal overload detection state for DO40:0=OFF			
		Actual thermal overload detection state for DO41:0=OFF			
		Actual thermal overload detection state for DO42:0=OFF			
		Actual thermal overload detection state for DO43:0=OFF			
		Actual thermal overload detection state for DO44:0=OFF			
		Actual thermal overload detection state for DO45:0=OFF			
		Actual thermal overload detection state for DO46:0=OFF			
		Actual thermal overload detection state for DO47:0=OFF			
		Actual thermal overload detection state for DO48:0=OFF			
Actual thermal overload detection state for digital output DOx Bit 0: =0:Output DO33 is OK, =1:Fault-Thermal overload on DO33 Bit 1: =0:Output DO34 is OK, =1:Fault-Thermal overload on DO34 ... Bit 14: =0:Output DO47 is OK, =1:Fault-Thermal overload on DO47 Bit 15: =0:Output DO48 is OK, =1:Fault-Thermal overload on DO48					
THERMAL OVERLOAD DETECTION STATE DO49-DO60	3x10037 4x10037 l:10036	0,0x0000 B:00 00		UINT16 R/O	
		Actual thermal overload detection state for DO49:0=OFF			
		Actual thermal overload detection state for DO50:0=OFF			
		Actual thermal overload detection state for DO51:0=OFF			
		Actual thermal overload detection state for DO52:0=OFF			
		Actual thermal overload detection state for DO53:0=OFF			
		Actual thermal overload detection state for DO54:0=OFF			
		Actual thermal overload detection state for DO55:0=OFF			



		Actual thermal overload detection state for DO56:0=OFF			
		Actual thermal overload detection state for DO57:0=OFF			
		Actual thermal overload detection state for DO58:0=OFF			
		Actual thermal overload detection state for DO59:0=OFF			
		Actual thermal overload detection state for DO60:0=OFF			

Actual thermal overload detection state for digital output DOx  
 Bit 0: =0:Output DO49 is OK, =1:Fault-Thermal overload on DO49  
 Bit 1: =0:Output DO50 is OK, =1:Fault-Thermal overload on DO50  
 ...  
 Bit 10: =0:Output DO59 is OK, =1:Fault-Thermal overload on DO59  
 Bit 11: =0:Output DO60 is OK, =1:Fault-Thermal overload on DO60

#### DIGITAL OUTPUTS:CURRENT LIMIT DETECTION STATE

CURRENT LIMIT	3x10038	0,0x0000			UINT16
DETECTION STATE	4x10038	B:00 00			R/O
DO1-DO16	1:10037				
		Actual current limit detection state for DO1:0=OFF			
		Actual current limit detection state for DO2:0=OFF			
		Actual current limit detection state for DO3:0=OFF			
		Actual current limit detection state for DO4:0=OFF			
		Actual current limit detection state for DO5:0=OFF			
		Actual current limit detection state for DO6:0=OFF			
		Actual current limit detection state for DO7:0=OFF			
		Actual current limit detection state for DO8:0=OFF			
		Actual current limit detection state for DO9:0=OFF			
		Actual current limit detection state for DO10:0=OFF			
		Actual current limit detection state for DO11:0=OFF			
		Actual current limit detection state for DO12:0=OFF			
		Actual current limit detection state for DO13:0=OFF			

		Actual current limit detection state for DO14:0=OFF			
		Actual current limit detection state for DO15:0=OFF			
		Actual current limit detection state for DO16:0=OFF			
Actual current limit detection state for digital output DOx Bit 0: =0:Output DO1 is OK, =1:Fault-Current limit on DO1 Bit 1: =0:Output DO2 is OK, =1:Fault-Current limit on DO2 ... Bit 14: =0:Output DO15 is OK, =1:Fault-Current limit on DO15 Bit 15: =0:Output DO16 is OK, =1:Fault-Current limit on DO16					
CURRENT LIMIT DETECTION STATE DO17-DO32	3x10039 4x10039 1:10038	0,0x0000 B:00 00			UINT16 R/O
		Actual current limit detection state for DO17:0=OFF			
		Actual current limit detection state for DO18:0=OFF			
		Actual current limit detection state for DO19:0=OFF			
		Actual current limit detection state for DO20:0=OFF			
		Actual current limit detection state for DO21:0=OFF			
		Actual current limit detection state for DO22:0=OFF			
		Actual current limit detection state for DO23:0=OFF			
		Actual current limit detection state for DO24:0=OFF			
		Actual current limit detection state for DO25:0=OFF			
		Actual current limit detection state for DO26:0=OFF			
		Actual current limit detection state for DO27:0=OFF			
		Actual current limit detection state for DO28:0=OFF			
		Actual current limit detection state for DO29:0=OFF			
		Actual current limit detection state for DO30:0=OFF			
		Actual current limit detection state for DO31:0=OFF			
		Actual current limit detection state for DO32:0=OFF			

Actual current limit detection state for digital output DOx  
 Bit 0: =0:Output DO17 is OK, =1:Fault-Current limit on DO17  
 Bit 1: =0:Output DO18 is OK, =1:Fault-Current limit on DO18  
 ...  
 Bit 14: =0:Output DO31 is OK, =1:Fault-Current limit on DO31  
 Bit 15: =0:Output DO32 is OK, =1:Fault-Current limit on DO32

CURRENT LIMIT DETECTION STATE DO33-DO48	3x10040 4x10040 1:10039	0,0x0000 B:00 00		UINT16 R/O	
		Actual current limit detection state for DO33:0=OFF			
		Actual current limit detection state for DO34:0=OFF			
		Actual current limit detection state for DO35:0=OFF			
		Actual current limit detection state for DO36:0=OFF			
		Actual current limit detection state for DO37:0=OFF			
		Actual current limit detection state for DO38:0=OFF			
		Actual current limit detection state for DO39:0=OFF			
		Actual current limit detection state for DO40:0=OFF			
		Actual current limit detection state for DO41:0=OFF			
		Actual current limit detection state for DO42:0=OFF			
		Actual current limit detection state for DO43:0=OFF			
		Actual current limit detection state for DO44:0=OFF			
		Actual current limit detection state for DO45:0=OFF			
		Actual current limit detection state for DO46:0=OFF			
		Actual current limit detection state for DO47:0=OFF			
		Actual current limit detection state for DO48:0=OFF			

Actual current limit detection state for digital output DOx  
 Bit 0: =0:Output DO33 is OK, =1:Fault-Current limit on DO33  
 Bit 1: =0:Output DO34 is OK, =1:Fault-Current limit on DO34  
 ...  
 Bit 14: =0:Output DO47 is OK, =1:Fault-Current limit on DO47  
 Bit 15: =0:Output DO48 is OK, =1:Fault-Current limit on DO48

CURRENT LIMIT DETECTION STATE DO49-DO60	3x10041 4x10041 I:10040	0,0x0000 B:00 00			UINT16 R/O	
		Actual current limit detection state for DO49:0=OFF				
		Actual current limit detection state for DO50:0=OFF				
		Actual current limit detection state for DO51:0=OFF				
		Actual current limit detection state for DO52:0=OFF				
		Actual current limit detection state for DO53:0=OFF				
		Actual current limit detection state for DO54:0=OFF				
		Actual current limit detection state for DO55:0=OFF				
		Actual current limit detection state for DO56:0=OFF				
		Actual current limit detection state for DO57:0=OFF				
		Actual current limit detection state for DO58:0=OFF				
		Actual current limit detection state for DO59:0=OFF				
		Actual current limit detection state for DO60:0=OFF				
Actual current limit detection state for digital output DOx Bit 0: =0:Output DO49 is OK, =1:Fault-Current limit on DO49 Bit 1: =0:Output DO50 is OK, =1:Fault-Current limit on DO50 ... Bit 10: =0:Output DO59 is OK, =1:Fault-Current limit on DO59 Bit 11: =0:Output DO60 is OK, =1:Fault-Current limit on DO60						
<b>DIGITAL OUTPUTS: GLOBAL ERRORS</b>						
<b>CHIP #1:DO1-DO7</b>						
<b>CHIP #2:DO8-DO15</b>						
GLOBAL ERRORS FOR CHIP #1+#2	3x10042 4x10042 I:10041	0,0x0000 B:00 00			UINT16 R/O	
		BIT 0:CHIP#1:Internal under voltage detected:0=OK				
		BIT 1:CHIP#1:VA under voltage detected (<2.3V):0=OK				
		BIT 2:CHIP#1:VDD not good detected (<17V):0=OK				
		BIT 3:CHIP#1:VDD warning detected (<12V):0=OK				
		BIT 4:CHIP#1:VDD under voltage detected (<8V):0=OK				
		BIT 5:CHIP#1:Thermal shutdown:0=OK				
		BIT 6:CHIP#1:Synchronisation error detected:0=OK				
		BIT 7:CHIP#1:Watchdog error detected:0=OK				

		BIT 8:CHIP#2:Internal under voltage detected:0=OK		
		BIT 9:CHIP#2:VA under voltage detected (<2.3V):0=OK		
		BIT 10:CHIP#2:VDD not good detected (<17V):0=OK		
		BIT 11:CHIP#2:VDD warning detected (<12V):0=OK		
		BIT 12:CHIP#2:VDD under voltage detected (<8V):0=OK		
		BIT 13:CHIP#2:Thermal shutdown:0=OK		
		BIT 14:CHIP#2:Synchronisation error detected:0=OK		
		BIT 15:CHIP#2:Watchdog error detected:0=OK		

The global error state for the output group. Each bit stands for a different error  
=0:No fault, =1:Fault

**CHIP #3:DO16-DO22****CHIP #4:DO23-DO30**

GLOBAL ERRORS FOR CHIP #3+#4	3x10043 4x10043 l:10042	7196,0x1C1C B:1C 1C		UINT16 R/O	
		BIT 0:CHIP#3:Internal under voltage detected:0=OK			
		BIT 1:CHIP#3:VA under voltage detected (<2.3V):0=OK			
		BIT 2:CHIP#3:VDD not good detected (<17V):1=FAULT			
		BIT 3:CHIP#3:VDD warning detected (<12V):1=FAULT			
		BIT 4:CHIP#3:VDD under voltage detected (<8V):1=FAULT			
		BIT 5:CHIP#3:Thermal shutdown:0=OK			
		BIT 6:CHIP#3:Synchronisation error detected:0=OK			
		BIT 7:CHIP#3:Watchdog error detected:0=OK			
		BIT 8:CHIP#4:Internal under voltage detected:0=OK			
		BIT 9:CHIP#4:VA under voltage detected (<2.3V):0=OK			
		BIT 10:CHIP#4:VDD not good detected (<17V):1=FAULT			
		BIT 11:CHIP#4:VDD warning detected (<12V):1=FAULT			
		BIT 12:CHIP#4:VDD under voltage detected (<8V):1=FAULT			
		BIT 13:CHIP#4:Thermal shutdown:0=OK			
		BIT 14:CHIP#4:Synchronisation error detected:0=OK			
		BIT 15:CHIP#4:Watchdog error detected:0=OK			

The global error state for the output group. Each bit stands for a different error  
=0:No fault, =1:Fault

**CHIP #5:DO31-DO38****CHIP #6:DO39-DO45**

GLOBAL ERRORS FOR CHIP #5+#6	3x10044 4x10044 l:10043	7196,0x1C1C B:1C 1C		UINT16 R/O	
		BIT 0:CHIP#3:Internal under voltage detected:0=OK			
		BIT 1:CHIP#3:VA under voltage detected (<2.3V):0=OK			
		BIT 2:CHIP#3:VDD not good detected (<17V):1=FAULT			
		BIT 3:CHIP#3:VDD warning detected (<12V):1=FAULT			
		BIT 4:CHIP#3:VDD under voltage detected (<8V):1=FAULT			
		BIT 5:CHIP#3:Thermal shutdown:0=OK			
		BIT 6:CHIP#3:Synchronisation error detected:0=OK			
		BIT 7:CHIP#3:Watchdog error detected:0=OK			
		BIT 8:CHIP#4:Internal under voltage detected:0=OK			

		BIT 9:CHIP#4:VA under voltage detected (<2.3V):0=OK		
		BIT 10:CHIP#4:VDD not good detected (<17V):1=FAULT		
		BIT 11:CHIP#4:VDD warning detected (<12V):1=FAULT		
		BIT 12:CHIP#4:VDD under voltage detected (<8V):1=FAULT		
		BIT 13:CHIP#4:Thermal shutdown:0=OK		
		BIT 14:CHIP#4:Synchronisation error detected:0=OK		
		BIT 15:CHIP#4:Watchdog error detected:0=OK		

The global error state for the output group. Each bit stands for a different error  
=0:No fault, =1:Fault

**CHIP #7:DO46-DO53****CHIP #8:DO54-DO60**

GLOBAL ERRORS FOR CHIP #7+#8	3x10045 4x10045 l:10044	7196,0x1C1C B:1C 1C		UINT16 R/O	
		BIT 0:CHIP#3:Internal under voltage detected:0=OK			
		BIT 1:CHIP#3:VA under voltage detected (<2.3V):0=OK			
		BIT 2:CHIP#3:VDD not good detected (<17V):1=FAULT			
		BIT 3:CHIP#3:VDD warning detected (<12V):1=FAULT			
		BIT 4:CHIP#3:VDD under voltage detected (<8V):1=FAULT			
		BIT 5:CHIP#3:Thermal shutdown:0=OK			
		BIT 6:CHIP#3:Synchronisation error detected:0=OK			
		BIT 7:CHIP#3:Watchdog error detected:0=OK			
		BIT 8:CHIP#4:Internal under voltage detected:0=OK			
		BIT 9:CHIP#4:VA under voltage detected (<2.3V):0=OK			
		BIT 10:CHIP#4:VDD not good detected (<17V):1=FAULT			
		BIT 11:CHIP#4:VDD warning detected (<12V):1=FAULT			
		BIT 12:CHIP#4:VDD under voltage detected (<8V):1=FAULT			
		BIT 13:CHIP#4:Thermal shutdown:0=OK			
		BIT 14:CHIP#4:Synchronisation error detected:0=OK			
		BIT 15:CHIP#4:Watchdog error detected:0=OK			

The global error state for the output group. Each bit stands for a different error  
=0:No fault, =1:Fault

**DIGITAL OUTPUTS: INTERRUPT STATUS****CHIP #1:DO1-DO7****CHIP #2:DO8-DO15**

INTERRUPT STATUS FOR CHIP#1+#2	3x10046 4x10046 l:10045	1024,0xFFFF B:04 00		UINT16 R/O	
		BIT 0:CHIP#1:Overload detected:1=FAULT			
		BIT 1:CHIP#1:Current limit detected:1=FAULT			
		BIT 2:CHIP#1:Open wire while OFF detected:1=FAULT			
		BIT 3:CHIP#1:Open wire while ON detected:1=FAULT			
		BIT 4:CHIP#1:Shortcut to VDD detected:1=FAULT			
		BIT 5:CHIP#1:Thermal shutdown:1=FAULT			
		BIT 6:CHIP#1:Supply error detected:1=FAULT			
		BIT 7:CHIP#1:Communication error detected:1=FAULT			
		BIT 8:CHIP#2:Overload detected:1=FAULT			

		BIT 9:CHIP#2:Current limit detected:1=FAULT		
		BIT 10:CHIP#2:Open wire while OFF detected:1=FAULT		
		BIT 11:CHIP#2:Open wire while ON detected:1=FAULT		
		BIT 12:CHIP#2:Shortcut to VDD detected:1=FAULT		
		BIT 13:CHIP#2:Thermal shutdown:1=FAULT		
		BIT 14:CHIP#2:Supply error detected:1=FAULT		
		BIT 15:CHIP#2:Communication error detected:1=FAULT		

The interrupt state for the output group. Each bit stands for a different error  
=0:No fault, =1:Fault

**CHIP #3:DO16-DO22****CHIP #4:DO23-DO30**

INTERRUPT STATUS FOR CHIP#3+#4	3x10047 4x10047 l:10046	16448,0x4040 B:40 40			UINT16 R/O
		BIT 0:CHIP#3:Overload detected:0=OK			
		BIT 1:CHIP#3:Current limit detected:0=OK			
		BIT 2:CHIP#3:Open wire while OFF detected:0=OK			
		BIT 3:CHIP#3:Open wire while ON detected:0=OK			
		BIT 4:CHIP#3:Shortcut to VDD detected:0=OK			
		BIT 5:CHIP#3:Thermal shutdown:0=OK			
		BIT 6:CHIP#3:Supply error detected:1=FAULT			
		BIT 7:CHIP#3:Communication error detected:0=OK			
		BIT 8:CHIP#4:Overload detected:0=OK			
		BIT 9:CHIP#4:Current limit detected:0=OK			
		BIT 10:CHIP#4:Open wire while OFF detected:0=OK			
		BIT 11:CHIP#4:Open wire while ON detected:0=OK			
		BIT 12:CHIP#4:Shortcut to VDD detected:0=OK			
		BIT 13:CHIP#4:Thermal shutdown:0=OK			
		BIT 14:CHIP#4:Supply error detected:1=FAULT			
		BIT 15:CHIP#4:Communication error detected:0=OK			

The interrupt state for the output group. Each bit stands for a different error  
=0:No fault, =1:Fault

**CHIP #5:DO31-DO38****CHIP #6:DO39-DO45**

INTERRUPT STATUS FOR CHIP#5+#6	3x10048 4x10048 l:10047	16448,0x4040 B:40 40			UINT16 R/O
		BIT 0:CHIP#3:Overload detected:0=OK			
		BIT 1:CHIP#3:Current limit detected:0=OK			
		BIT 2:CHIP#3:Open wire while OFF detected:0=OK			
		BIT 3:CHIP#3:Open wire while ON detected:0=OK			
		BIT 4:CHIP#3:Shortcut to VDD detected:0=OK			
		BIT 5:CHIP#3:Thermal shutdown:0=OK			
		BIT 6:CHIP#3:Supply error detected:1=FAULT			
		BIT 7:CHIP#3:Communication error detected:0=OK			
		BIT 8:CHIP#4:Overload detected:0=OK			
		BIT 9:CHIP#4:Current limit detected:0=OK			

		BIT 10:CHIP#4:Open wire while OFF detected:0=OK		
		BIT 11:CHIP#4:Open wire while ON detected:0=OK		
		BIT 12:CHIP#4:Shortcut to VDD detected:0=OK		
		BIT 13:CHIP#4:Thermal shutdown:0=OK		
		BIT 14:CHIP#4:Supply error detected:1=FAULT		
		BIT 15:CHIP#4:Communication error detected:0=OK		

The interrupt state for the output group. Each bit stands for a different error  
=0:No fault, =1:Fault

**CHIP #7:DO46-DO53****CHIP #8:DO54-DO60**

INTERRUPT STATUS FOR CHIP#7+#8	3x10049 4x10049 l:10048	16448,0x4040 B:40 40		UINT16 R/O
		BIT 0:CHIP#3:Overload detected:0=OK		
		BIT 1:CHIP#3:Current limit detected:0=OK		
		BIT 2:CHIP#3:Open wire while OFF detected:0=OK		
		BIT 3:CHIP#3:Open wire while ON detected:0=OK		
		BIT 4:CHIP#3:Shortcut to VDD detected:0=OK		
		BIT 5:CHIP#3:Thermal shutdown:0=OK		
		BIT 6:CHIP#3:Supply error detected:1=FAULT		
		BIT 7:CHIP#3:Communication error detected:0=OK		
		BIT 8:CHIP#4:Overload detected:0=OK		
		BIT 9:CHIP#4:Current limit detected:0=OK		
		BIT 10:CHIP#4:Open wire while OFF detected:0=OK		
		BIT 11:CHIP#4:Open wire while ON detected:0=OK		
		BIT 12:CHIP#4:Shortcut to VDD detected:0=OK		
		BIT 13:CHIP#4:Thermal shutdown:0=OK		
		BIT 14:CHIP#4:Supply error detected:1=FAULT		
		BIT 15:CHIP#4:Communication error detected:0=OK		

The interrupt state for the output group. Each bit stands for a different error  
=0:No fault, =1:Fault

**SPI COMMUNICATION DIGITAL OUTPUTS****CHIP #1:DO1-DO7****CHIP #2:DO8-DO15****CHIP #3:DO16-DO22****CHIP #4:DO23-DO30****CHIP #5:DO31-DO38****CHIP #6:DO39-DO45****CHIP #7:DO46-DO53****CHIP #8:DO54-DO60**

SPI COMMUNICATION DIGITAL OUTPUTS	3x10050 4x10050 l:10049	0,0x0000 B:00 00		UINT16 R/O
		Actual SPI communcation state of CHIP#1:0=OK		
		Actual SPI communcation state of CHIP#2:0=OK		
		Actual SPI communcation state of CHIP#3:0=OK		
		Actual SPI communcation state of CHIP#4:0=OK		



		Actual SPI communcation state of CHIP#5:0=OK		
		Actual SPI communcation state of CHIP#6:0=OK		
		Actual SPI communcation state of CHIP#7:0=OK		
		Actual SPI communcation state of CHIP#8:0=OK		

The current monitoring state of the SPI communication for the digital output group  
 =0:No fault, =1:Fault

Current SPI communication state of all digital output groups  
 Bit x: =0:CHIP x has no fault, =1:CHIP x SPI Fault

**DIGITAL OUTPUTS: NUMBER OF CHIPSET**

NUMBER OF DIGITAL OUTPUT CHIPS	3x10099 4x10099 l:10098	4,0x0004 B:00 04			UINT16 R/O
		Actual number of installed DO CHIPS:4			

The actual number of used output chips

**DIGITAL OUTPUTS: CHIPSET TYPE**

DIGITAL OUTPUTS CHIPSET TYPE	3x10100 4x10100 l:10099	1,0x0001 B:00 01			UINT16 R/O
		Actual chipset for DOs:1=MAX14915			

The current chipset for the digital outputs:  
 =0: NCV7608  
 =1: MAX14915

**STATUS OF DIGITAL OUTPUTS**

REAL STATUS OF ALL DOS DO1-DO16	3x10501 4x10501 l:10500	0,0x0000 B:00 00			UINT16 R/O
		Real state of DO1:0=OFF			
		Real state of DO2:0=OFF			
		Real state of DO3:0=OFF			
		Real state of DO4:0=OFF			
		Real state of DO5:0=OFF			
		Real state of DO6:0=OFF			
		Real state of DO7:0=OFF			
		Real state of DO8:0=OFF			
		Real state of DO9:0=OFF			
		Real state of DO10:0=OFF			
		Real state of DO11:0=OFF			
		Real state of DO12:0=OFF			
		Real state of DO13:0=OFF			
		Real state of DO14:0=OFF			
		Real state of DO15:0=OFF			
		Real state of DO16:0=OFF			

Actual state of all digital outputs in the DO chips

Bit 0: =0:DO1 is OFF, =1:DO1 is ON

Bit 1: =0:DO2 is OFF, =1:DO2 is ON

...

Bit 14: =0:DO15 is OFF, =1:DO15 is ON

Bit 15: =0:DO16 is OFF, =1:DO16 is ON

Write on this register sets all digital outputs to a new state

REAL STATUS OF ALL DOS DO17-DO32	3x10502 4x10502 I:10501	0,0x0000 B:00 00			UINT16 R/O
		Real state of DO17:0=OFF			
		Real state of DO18:0=OFF			
		Real state of DO19:0=OFF			
		Real state of DO20:0=OFF			
		Real state of DO21:0=OFF			
		Real state of DO22:0=OFF			
		Real state of DO23:0=OFF			
		Real state of DO24:0=OFF			
		Real state of DO25:0=OFF			
		Real state of DO26:0=OFF			
		Real state of DO27:0=OFF			
		Real state of DO28:0=OFF			
		Real state of DO29:0=OFF			
		Real state of DO30:0=OFF			
		Real state of DO31:0=OFF			
		Real state of DO32:0=OFF			

Actual state of all digital outputs in the DO chips

Bit 0: =0:DO17 is OFF, =1:DO17 is ON

Bit 1: =0:DO18 is OFF, =1:DO18 is ON

...

Bit 14: =0:DO31 is OFF, =1:DO31 is ON

Bit 15: =0:DO32 is OFF, =1:DO32 is ON

Write on this register sets all digital outputs to a new state

REAL STATUS OF ALL DOS DO33-DO48	3x10503 4x10503 I:10502	0,0x0000 B:00 00			UINT16 R/O
		Real state of DO33:0=OFF			
		Real state of DO34:0=OFF			
		Real state of DO35:0=OFF			
		Real state of DO36:0=OFF			
		Real state of DO37:0=OFF			
		Real state of DO38:0=OFF			
		Real state of DO39:0=OFF			
		Real state of DO40:0=OFF			
		Real state of DO41:0=OFF			
		Real state of DO42:0=OFF			
		Real state of DO43:0=OFF			

		Real state of DO44:0=OFF			
		Real state of DO45:0=OFF			
		Real state of DO46:0=OFF			
		Real state of DO47:0=OFF			
		Real state of DO48:0=OFF			

Actual state of all digital outputs in the DO chips

Bit 0: =0:DO33 is OFF, =1:DO33 is ON

Bit 1: =0:DO34 is OFF, =1:DO34 is ON

...

Bit 14: =0:DO47 is OFF, =1:DO47 is ON

Bit 15: =0:DO48 is OFF, =1:DO48 is ON

Write on this register sets all digital outputs to a new state

REAL STATUS OF ALL DOS DO49-DO60	3x10502 4x10502 I:10501	0,0x0000 B:00 00			UINT16 R/O
		Real state of DO49:0=OFF			
		Real state of DO50:0=OFF			
		Real state of DO51:0=OFF			
		Real state of DO52:0=OFF			
		Real state of DO53:0=OFF			
		Real state of DO54:0=OFF			
		Real state of DO55:0=OFF			
		Real state of DO56:0=OFF			
		Real state of DO57:0=OFF			
		Real state of DO58:0=OFF			
		Real state of DO59:0=OFF			
		Real state of DO60:0=OFF			

Actual state of all digital outputs in the DO chips

Bit 0: =0:DO49 is OFF, =1:DO49 is ON

Bit 1: =0:DO50 is OFF, =1:DO50 is ON

...

Bit 12: =0:DO59 is OFF, =1:DO59 is ON

Bit 13: =0:DO60 is OFF, =1:DO60 is ON

Write on this register sets all digital outputs to a new state

<b>STATUS REAL DIGITAL INPUTS</b>					
DI1	1x15001 2x15001 I:15000	0,0x0000 B:00			UINT16 R/O
		Actual state of DI1:0=OFF			
Current state of the digital input DIx with the internal software filter to suppress glitches or spike on this line =0:DI is OFF, =1:DI is ON					
DI2	1x15002 2x15002 I:15001	1,0x0001 B:00			UINT16 R/O
		Actual state of DI2:1=ON			

DI3	1x15003 2x15003 I:15002	2,0x0002 B:00			UINT16 R/O	
		Actual state of DI3:2=ON				
DI4	1x15004 2x15004 I:15003	3,0x0003 B:00			UINT16 R/O	
		Actual state of DI4:3=ON				
DI5	1x15005 2x15005 I:15004	4,0x0004 B:00			UINT16 R/O	
		Actual state of DI5:4=ON				
DI6	1x15006 2x15006 I:15005	5,0x0005 B:00			UINT16 R/O	
		Actual state of DI6:5=ON				
DI7	1x15007 2x15007 I:15006	6,0x0006 B:00			UINT16 R/O	
		Actual state of DI7:6=ON				
DI8	1x15008 2x15008 I:15007	7,0x0007 B:00			UINT16 R/O	
		Actual state of DI8:7=ON				
DI9	1x15009 2x15009 I:15008	0,0x0000 B:00			UINT16 R/O	
		Actual state of DI9:0=OFF				
DI10	1x15010 2x15010 I:15009	1,0x0001 B:00			UINT16 R/O	
		Actual state of DI10:1=ON				
DI11	1x15011 2x15011 I:15010	2,0x0002 B:00			UINT16 R/O	
		Actual state of DI11:2=ON				
DI12	1x15012 2x15012 I:15011	3,0x0003 B:00			UINT16 R/O	
		Actual state of DI12:3=ON				
DI13	1x15013 2x15013 I:15012	4,0x0004 B:00			UINT16 R/O	
		Actual state of DI13:4=ON				
DI14	1x15014 2x15014 I:15013	5,0x0005 B:00			UINT16 R/O	

		Actual state of DI14:5=ON				
DI15	1x15015 2x15015 I:15014	6,0x0006 B:00			UINT16 R/O	
		Actual state of DI15:6=ON				
DI16	1x15016 2x15016 I:15015	7,0x0007 B:00			UINT16 R/O	
		Actual state of DI16:7=ON				
DI17	1x15017 2x15017 I:15016	0,0x0000 B:00			UINT16 R/O	
		Actual state of DI17:0=OFF				
DI18	1x15018 2x15018 I:15017	1,0x0001 B:00			UINT16 R/O	
		Actual state of DI18:1=ON				
DI19	1x15019 2x15019 I:15018	2,0x0002 B:00			UINT16 R/O	
		Actual state of DI19:2=ON				
DI20	1x15020 2x15020 I:15019	0,0x0000 B:00			UINT16 R/O	
		Actual state of DI20:0=OFF				
DI21	1x15021 2x15021 I:15020	1,0x0001 B:00			UINT16 R/O	
		Actual state of DI21:1=ON				
DI22	1x15022 2x15022 I:15021	2,0x0002 B:00			UINT16 R/O	
		Actual state of DI22:2=ON				
D23	1x15023 2x15023 I:15022	3,0x0003 B:00			UINT16 R/O	
		Actual state of D23:3=ON				
DI24	1x15024 2x15024 I:15023	4,0x0004 B:00			UINT16 R/O	
		Actual state of DI24:4=ON				
DI25	1x15025 2x15025 I:15024	5,0x0005 B:00			UINT16 R/O	
		Actual state of DI25:5=ON				

DI26	1x15026 2x15026 I:15025	6,0x0006 B:00			UINT16 R/O	
		Actual state of DI26:6=ON				
DI27	1x15027 2x15027 I:15026	7,0x0007 B:00			UINT16 R/O	
		Actual state of DI27:7=ON				
DI28	1x15028 2x15028 I:15027	0,0x0000 B:00			UINT16 R/O	
		Actual state of DI28:0=OFF				
DI29	1x15029 2x15029 I:15028	1,0x0001 B:00			UINT16 R/O	
		Actual state of DI29:1=ON				
DI30	1x15030 2x15030 I:15029	2,0x0002 B:00			UINT16 R/O	
		Actual state of DI30:2=ON				
DI31	1x15031 2x15031 I:15030	3,0x0003 B:00			UINT16 R/O	
		Actual state of DI31:3=ON				
DI32	1x15032 2x15032 I:15031	4,0x0004 B:00			UINT16 R/O	
		Actual state of DI32:4=ON				
DI33	1x15033 2x15033 I:15032	2,0x0002 B:00			UINT16 R/O	
		Actual state of DI33:2=ON				
DI34	1x15034 2x15034 I:15033	3,0x0003 B:00			UINT16 R/O	
		Actual state of DI34:3=ON				
DI35	1x15035 2x15035 I:15034	4,0x0004 B:00			UINT16 R/O	
		Actual state of DI35:4=ON				
DI36	1x15036 2x15036 I:15035	5,0x0005 B:00			UINT16 R/O	
		Actual state of DI36:5=ON				
DI37	1x15037 2x15037 I:15036	6,0x0006 B:00			UINT16 R/O	

		Actual state of DI37:6=ON				
DI38	1x15038 2x15038 I:15037	7,0x0007 B:00			UINT16 R/O	
		Actual state of DI38:7=ON				
DI39	1x15039 2x15039 I:15038	0,0x0000 B:00			UINT16 R/O	
		Actual state of DI39:0=OFF				
DI40	1x15040 2x15040 I:15039	1,0x0001 B:00			UINT16 R/O	
		Actual state of DI40:1=ON				
DI41	1x15041 2x15041 I:15040	2,0x0002 B:00			UINT16 R/O	
		Actual state of DI41:2=ON				
DI42	1x15042 2x15042 I:15041	3,0x0003 B:00			UINT16 R/O	
		Actual state of DI42:3=ON				
DI43	1x15043 2x15043 I:15042	4,0x0004 B:00			UINT16 R/O	
		Actual state of DI43:4=ON				
DI44	1x15044 2x15044 I:15043	5,0x0005 B:00			UINT16 R/O	
		Actual state of DI44:5=ON				
DI45	1x15045 2x15045 I:15044	6,0x0006 B:00			UINT16 R/O	
		Actual state of DI45:6=ON				
DI46	1x15046 2x15046 I:15045	7,0x0007 B:00			UINT16 R/O	
		Actual state of DI46:7=ON				
DI47	1x15047 2x15047 I:15046	0,0x0000 B:00			UINT16 R/O	
		Actual state of DI47:0=OFF				
DI48	1x15048 2x15048 I:15047	1,0x0001 B:00			UINT16 R/O	
		Actual state of DI48:1=ON				

DI49	1x15049 2x15049 I:15048	2,0x0002 B:00			UINT16 R/O	
		Actual state of DI49:2=ON				
DI50	1x15050 2x15050 I:15049	0,0x0000 B:00			UINT16 R/O	
		Actual state of DI50:0=OFF				
DI51	1x15051 2x15051 I:15050	1,0x0001 B:00			UINT16 R/O	
		Actual state of DI51:1=ON				
DI52	1x15052 2x15052 I:15051	2,0x0002 B:00			UINT16 R/O	
		Actual state of DI52:2=ON				
D53	1x15053 2x15053 I:15052	3,0x0003 B:00			UINT16 R/O	
		Actual state of D53:3=ON				
DI54	1x15054 2x15054 I:15053	4,0x0004 B:00			UINT16 R/O	
		Actual state of DI54:4=ON				
DI55	1x15055 2x15055 I:15054	5,0x0005 B:00			UINT16 R/O	
		Actual state of DI55:5=ON				
DI56	1x15056 2x15056 I:15055	6,0x0006 B:00			UINT16 R/O	
		Actual state of DI56:6=ON				
DI57	1x15057 2x15057 I:15056	7,0x0007 B:00			UINT16 R/O	
		Actual state of DI57:7=ON				
DI58	1x15058 2x15058 I:15057	0,0x0000 B:00			UINT16 R/O	
		Actual state of DI58:0=OFF				
DI59	1x15059 2x15059 I:15058	1,0x0001 B:00			UINT16 R/O	
		Actual state of DI59:1=ON				
DI60	1x15060 2x15060 I:15059	2,0x0002 B:00			UINT16 R/O	



		Actual state of DI60:2=ON				
DI61	1x15061 2x15061 I:15060	3,0x0003 B:00			UINT16 R/O	
		Actual state of DI61:3=ON				
DI62	1x15062 2x15062 I:15061	4,0x0004 B:00			UINT16 R/O	
		Actual state of DI62:4=ON				
DI63	1x15063 2x15063 I:15062	3,0x0003 B:00			UINT16 R/O	
		Actual state of DI63:3=ON				
DI64	1x15064 2x15064 I:15063	4,0x0004 B:00			UINT16 R/O	
		Actual state of DI64:4=ON				
<b>STATUS DIGITAL INPUTS</b>						
UNFILTERED DI1	1x15065 2x15065 I:15064	5,0x0005 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI1:5=ON				
Current state of the real digital input DIx without the internal software filter to suppress glitches or spike on this line =0:DI is OFF, =1:DI is ON						
UNFILTERED DI2	1x15066 2x15066 I:15065	6,0x0006 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI2:6=ON				
UNFILTERED DI3	1x15067 2x15067 I:15066	7,0x0007 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI3:7=ON				
UNFILTERED DI4	1x15068 2x15068 I:15067	0,0x0000 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI4:0=OFF				
UNFILTERED DI5	1x15069 2x15069 I:15068	1,0x0001 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI5:1=ON				
UNFILTERED DI6	1x15070 2x15070 I:15069	2,0x0002 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI6:2=ON				
UNFILTERED DI7	1x15071 2x15071 I:15070	0,0x0000 B:00			UINT16 R/O	

		Actual state of UNFILTERED DI7:0=OFF				
UNFILTERED DI8	1x15072 2x15072 I:15071	1,0x0001 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI8:1=ON				
UNFILTERED DI9	1x15073 2x15073 I:15072	2,0x0002 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI9:2=ON				
UNFILTERED DI10	1x15074 2x15074 I:15073	3,0x0003 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI10:3=ON				
UNFILTERED DI11	1x15075 2x15075 I:15074	4,0x0004 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI11:4=ON				
UNFILTERED DI12	1x15076 2x15076 I:15075	5,0x0005 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI12:5=ON				
UNFILTERED DI13	1x15077 2x15077 I:15076	6,0x0006 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI13:6=ON				
UNFILTERED DI14	1x15078 2x15078 I:15077	7,0x0007 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI14:7=ON				
UNFILTERED DI15	1x15079 2x15079 I:15078	0,0x0000 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI15:0=OFF				
UNFILTERED DI16	1x15080 2x15080 I:15079	1,0x0001 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI16:1=ON				
UNFILTERED DI17	1x15081 2x15081 I:15080	2,0x0002 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI17:2=ON				
UNFILTERED DI18	1x15082 2x15082 I:15081	3,0x0003 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI18:3=ON				

UNFILTERED DI19	1x15083 2x15083 I:15082	4,0x0004 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI19:4=ON				
UNFILTERED DI20	1x15084 2x15084 I:15083	5,0x0005 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI20:5=ON				
UNFILTERED DI21	1x15085 2x15085 I:15084	6,0x0006 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI21:6=ON				
UNFILTERED DI22	1x15086 2x15086 I:15085	7,0x0007 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI22:7=ON				
UNFILTERED DI23	1x15087 2x15087 I:15086	0,0x0000 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI23:0=OFF				
UNFILTERED DI24	1x15088 2x15088 I:15087	1,0x0001 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI24:1=ON				
UNFILTERED DI25	1x15089 2x15089 I:15088	2,0x0002 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI25:2=ON				
UNFILTERED DI26	1x15090 2x15090 I:15089	0,0x0000 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI26:0=OFF				
UNFILTERED DI27	1x15091 2x15091 I:15090	1,0x0001 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI27:1=ON				
UNFILTERED DI28	1x15092 2x15092 I:15091	2,0x0002 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI28:2=ON				
UNFILTERED DI29	1x15093 2x15093 I:15092	3,0x0003 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI29:3=ON				
UNFILTERED DI30	1x15094 2x15094 I:15093	4,0x0004 B:00			UINT16 R/O	

		Actual state of UNFILTERED DI30:4=ON				
UNFILTERED DI31	1x15095 2x15095 I:15094	5,0x0005 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI31:5=ON				
UNFILTERED DI32	1x15096 2x15096 I:15095	6,0x0006 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI32:6=ON				
UNFILTERED DI33	1x15097 2x15097 I:15096	7,0x0007 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI33:7=ON				
UNFILTERED DI34	1x15098 2x15098 I:15097	0,0x0000 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI34:0=OFF				
UNFILTERED DI35	1x15099 2x15099 I:15098	1,0x0001 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI35:1=ON				
UNFILTERED DI36	1x15100 2x15100 I:15099	2,0x0002 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI36:2=ON				
UNFILTERED DI37	1x15101 2x15101 I:15100	0,0x0000 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI37:0=OFF				
UNFILTERED DI38	1x15102 2x15102 I:15101	1,0x0001 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI38:1=ON				
UNFILTERED DI39	1x15103 2x15103 I:15102	2,0x0002 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI39:2=ON				
UNFILTERED DI40	1x15104 2x15104 I:15103	3,0x0003 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI40:3=ON				
UNFILTERED DI41	1x15105 2x15105 I:15104	4,0x0004 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI41:4=ON				

UNFILTERED DI42	1x15106 2x15106 I:15105	5,0x0005 B:00			UINT16 R/O		
		Actual state of UNFILTERED DI42:5=ON					
UNFILTERED DI43	1x15107 2x15107 I:15106	6,0x0006 B:00			UINT16 R/O		
		Actual state of UNFILTERED DI43:6=ON					
UNFILTERED DI44	1x15108 2x15108 I:15107	7,0x0007 B:00			UINT16 R/O		
		Actual state of UNFILTERED DI44:7=ON					
UNFILTERED DI45	1x15109 2x15109 I:15108	0,0x0000 B:00			UINT16 R/O		
		Actual state of UNFILTERED DI45:0=OFF					
UNFILTERED DI46	1x15110 2x15110 I:15109	1,0x0001 B:00			UINT16 R/O		
		Actual state of UNFILTERED DI46:1=ON					
UNFILTERED DI47	1x15111 2x15111 I:15110	2,0x0002 B:00			UINT16 R/O		
		Actual state of UNFILTERED DI47:2=ON					
UNFILTERED DI48	1x15112 2x15112 I:15111	3,0x0003 B:00			UINT16 R/O		
		Actual state of UNFILTERED DI48:3=ON					
UNFILTERED DI49	1x15113 2x15113 I:15112	4,0x0004 B:00			UINT16 R/O		
		Actual state of UNFILTERED DI49:4=ON					
UNFILTERED DI50	1x15114 2x15114 I:15113	5,0x0005 B:00			UINT16 R/O		
		Actual state of UNFILTERED DI50:5=ON					
UNFILTERED DI51	1x15115 2x15115 I:15114	6,0x0006 B:00			UINT16 R/O		
		Actual state of UNFILTERED DI51:6=ON					
UNFILTERED DI52	1x15116 2x15116 I:15115	7,0x0007 B:00			UINT16 R/O		
		Actual state of UNFILTERED DI52:7=ON					
UNFILTERED DI53	1x15117 2x15117 I:15116	0,0x0000 B:00			UINT16 R/O		

		Actual state of UNFILTERED DI53:0=OFF				
UNFILTERED DI54	1x15118 2x15118 I:15117	1,0x0001 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI54:1=ON				
UNFILTERED DI55	1x15119 2x15119 I:15118	2,0x0002 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI55:2=ON				
UNFILTERED DI56	1x15120 2x15120 I:15119	0,0x0000 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI56:0=OFF				
UNFILTERED DI57	1x15121 2x15121 I:15120	1,0x0001 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI57:1=ON				
UNFILTERED DI58	1x15122 2x15122 I:15121	2,0x0002 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI58:2=ON				
UNFILTERED DI59	1x15123 2x15123 I:15122	3,0x0003 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI59:3=ON				
UNFILTERED DI60	1x15124 2x15124 I:15123	4,0x0004 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI60:4=ON				
UNFILTERED DI61	1x15125 2x15125 I:15124	5,0x0005 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI61:5=ON				
UNFILTERED DI62	1x15126 2x15126 I:15125	6,0x0006 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI62:6=ON				
UNFILTERED DI63	1x15127 2x15127 I:15126	5,0x0005 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI63:5=ON				
UNFILTERED DI64	1x15128 2x15128 I:15127	6,0x0006 B:00			UINT16 R/O	
		Actual state of UNFILTERED DI64:6=ON				
<b>STATUS DIGITAL OUTPUTS</b>						

DO1	3x16001 4x16001 I:16000	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual state of DO1:0=OFF		ENTER NEW STATE (0 or 1)		
Current state of the digital output DOx =0:DO is OFF, =1:DO is ON						
Writing on this register changes the state of the digital output						
DO2	3x16002 4x16002 I:16001	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO2:0=OFF		ENTER NEW STATE (0 or 1)		
DO3	3x16003 4x16003 I:16002	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO3:0=OFF		ENTER NEW STATE (0 or 1)		
DO4	3x16004 4x16004 I:16003	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO4:0=OFF		ENTER NEW STATE (0 or 1)		
DO5	3x16005 4x16005 I:16004	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO5:0=OFF		ENTER NEW STATE (0 or 1)		
DO6	3x16006 4x16006 I:16005	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO6:0=OFF		ENTER NEW STATE (0 or 1)		
DO7	3x16007 4x16007 I:16006	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO7:0=OFF		ENTER NEW STATE (0 or 1)		
DO8	3x16008 4x16008 I:16007	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO8:0=OFF		ENTER NEW STATE (0 or 1)		
DO9	3x16009 4x16009 I:16008	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO9:0=OFF		ENTER NEW STATE (0 or 1)		
DO10	3x16010 4x16010 I:16009	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO10:0=OFF		ENTER NEW STATE (0 or 1)		
DO11	3x16011 4x16011 I:16010	0,0x0000 B:00 00		0	UINT16 R/W	NO

		Actual state of DO11:0=OFF		ENTER NEW STATE (0 or 1)		
DO12	3x16012 4x16012 I:16011	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO12:0=OFF		ENTER NEW STATE (0 or 1)		
DO13	3x16013 4x16013 I:16012	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO13:0=OFF		ENTER NEW STATE (0 or 1)		
DO14	3x16014 4x16014 I:16013	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO14:0=OFF		ENTER NEW STATE (0 or 1)		
DO15	3x16015 4x16015 I:16014	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO15:0=OFF		ENTER NEW STATE (0 or 1)		
DO16	3x16016 4x16016 I:16015	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO16:0=OFF		ENTER NEW STATE (0 or 1)		
DO17	3x16017 4x16017 I:16016	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO17:0=OFF		ENTER NEW STATE (0 or 1)		
DO18	3x16018 4x16018 I:16017	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO18:0=OFF		ENTER NEW STATE (0 or 1)		
DO19	3x16019 4x16019 I:16018	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO19:0=OFF		ENTER NEW STATE (0 or 1)		
DO20	3x16020 4x16020 I:16019	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO20:0=OFF		ENTER NEW STATE (0 or 1)		
DO21	3x16021 4x16021 I:16020	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO21:0=OFF		ENTER NEW STATE (0 or 1)		
DO22	3x16022 4x16022 I:16021	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO22:0=OFF		ENTER NEW STATE (0 or 1)		



DO23	3x16023 4x16023 I:16022	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO23:0=OFF		ENTER NEW STATE (0 or 1)		
DO24	3x16024 4x16024 I:16023	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO24:0=OFF		ENTER NEW STATE (0 or 1)		
DO25	3x16025 4x16025 I:16024	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO25:0=OFF		ENTER NEW STATE (0 or 1)		
DO26	3x16026 4x16026 I:16025	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO26:0=OFF		ENTER NEW STATE (0 or 1)		
DO27	3x16027 4x16027 I:16026	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO27:0=OFF		ENTER NEW STATE (0 or 1)		
DO28	3x16028 4x16028 I:16027	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO28:0=OFF		ENTER NEW STATE (0 or 1)		
DO29	3x16029 4x16029 I:16028	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO29:0=OFF		ENTER NEW STATE (0 or 1)		
DO30	3x16030 4x16030 I:16029	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO30:0=OFF		ENTER NEW STATE (0 or 1)		
DO31	3x16031 4x16031 I:16030	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO31:0=OFF		ENTER NEW STATE (0 or 1)		
DO32	3x16032 4x16032 I:16031	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO32:0=OFF		ENTER NEW STATE (0 or 1)		
DO33	3x16033 4x16033 I:16032	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO33:0=OFF		ENTER NEW STATE (0 or 1)		

DO34	3x16034 4x16034 I:16033	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO34:0=OFF		ENTER NEW STATE (0 or 1)		
DO35	3x16035 4x16035 I:16034	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO35:0=OFF		ENTER NEW STATE (0 or 1)		
DO36	3x16036 4x16036 I:16035	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO36:0=OFF		ENTER NEW STATE (0 or 1)		
DO37	3x16037 4x16037 I:16036	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO37:0=OFF		ENTER NEW STATE (0 or 1)		
DO38	3x16038 4x16038 I:16037	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO38:0=OFF		ENTER NEW STATE (0 or 1)		
DO39	3x16039 4x16039 I:16038	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO39:0=OFF		ENTER NEW STATE (0 or 1)		
DO40	3x16040 4x16040 I:16039	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO40:0=OFF		ENTER NEW STATE (0 or 1)		
DO41	3x16041 4x16041 I:16040	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO41:0=OFF		ENTER NEW STATE (0 or 1)		
DO42	3x16042 4x16042 I:16041	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO42:0=OFF		ENTER NEW STATE (0 or 1)		
DO43	3x16043 4x16043 I:16042	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO43:0=OFF		ENTER NEW STATE (0 or 1)		
DO44	3x16044 4x16044 I:16043	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO44:0=OFF		ENTER NEW STATE (0 or 1)		

DO45	3x16045 4x16045 I:16044	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO45:0=OFF		ENTER NEW STATE (0 or 1)		
DO46	3x16046 4x16046 I:16045	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO46:0=OFF		ENTER NEW STATE (0 or 1)		
DO47	3x16047 4x16047 I:16046	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO47:0=OFF		ENTER NEW STATE (0 or 1)		
DO48	3x16048 4x16048 I:16047	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO48:0=OFF		ENTER NEW STATE (0 or 1)		
DO49	3x16049 4x16049 I:16048	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO49:0=OFF		ENTER NEW STATE (0 or 1)		
DO50	3x16050 4x16050 I:16049	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO50:0=OFF		ENTER NEW STATE (0 or 1)		
DO51	3x16051 4x16051 I:16050	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO51:0=OFF		ENTER NEW STATE (0 or 1)		
DO52	3x16052 4x16052 I:16051	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO52:0=OFF		ENTER NEW STATE (0 or 1)		
DO53	3x16053 4x16053 I:16052	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO53:0=OFF		ENTER NEW STATE (0 or 1)		
DO54	3x16054 4x16054 I:16053	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO54:0=OFF		ENTER NEW STATE (0 or 1)		
DO55	3x16055 4x16055 I:16054	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO55:0=OFF		ENTER NEW STATE (0 or 1)		

DO56	3x16056 4x16056 I:16055	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO56:0=OFF		ENTER NEW STATE (0 or 1)		
DO57	3x16057 4x16057 I:16056	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO57:0=OFF		ENTER NEW STATE (0 or 1)		
DO58	3x16058 4x16058 I:16057	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO58:0=OFF		ENTER NEW STATE (0 or 1)		
DO59	3x16059 4x16059 I:16058	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO59:0=OFF		ENTER NEW STATE (0 or 1)		
DO60	3x16060 4x16060 I:16059	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO60:0=OFF		ENTER NEW STATE (0 or 1)		

Register NAME Command NAME	MODBUS Register ASCII Command	Register VALUE ASCII Command	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
<b>PULSE TIME FOR DIGITAL OUTPUTS</b>						
PULSE TIME DO1	3x20001 4x20001 I:20000	0,0x0000 B:00 00	200	20,0	UINT16 R/W	YES
Generate a pulse on digital output x in 100ms units (0,1 to 6553,5 Seconds selectable) If you write onto this register, the digital output will be switched on for the desired time in 100ms units.						
PULSE TIME DO2	3x20002 4x20002 I:20001	0,0x0000 B:00 00	300	30,0	UINT16 R/W	NO
PULSE TIME DO3	3x20003 4x20003 I:20002	0,0x0000 B:00 00	400	40,0	UINT16 R/W	NO
PULSE TIME DO4	3x20004 4x20004 I:20003	0,0x0000 B:00 00	500	50,0	UINT16 R/W	NO
PULSE TIME DO5	3x20005 4x20005 I:20004	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO6	3x20006 4x20006 I:20005	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO7	3x20007 4x20007 I:20006	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO8	3x20008 4x20008 I:20007	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO9	3x20009 4x20009 I:20008	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO10	3x20010 4x20010 I:20009	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO11	3x20011 4x20011 I:20010	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO12	3x20012 4x20012 I:20011	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO13	3x20013 4x20013 I:20012	0,0x0000 B:00 00	400	40,0	UINT16 R/W	NO

PULSE TIME DO14	3x20014 4x20014 I:20013	0,0x0000 B:00 00	500	50,0	UINT16 R/W	NO
PULSE TIME DO15	3x20015 4x20015 I:20014	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO16	3x20016 4x20016 I:20015	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO17	3x20017 4x20017 I:20016	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO18	3x20018 4x20018 I:20017	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO19	3x20019 4x20019 I:20018	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO20	3x20020 4x20020 I:20019	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO21	3x20021 4x20021 I:20020	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO22	3x20022 4x20022 I:20021	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO23	3x20023 4x20023 I:20022	0,0x0000 B:00 00	400	40,0	UINT16 R/W	NO
PULSE TIME DO24	3x20024 4x20024 I:20023	0,0x0000 B:00 00	500	50,0	UINT16 R/W	NO
PULSE TIME DO25	3x20025 4x20025 I:20024	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO26	3x20026 4x20026 I:20025	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO27	3x20027 4x20027 I:20026	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO28	3x20028 4x20028 I:20027	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO

PULSE TIME DO29	3x20029 4x20029 I:20028	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO30	3x20030 4x20030 I:20029	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO31	3x20031 4x20031 I:20030	0,0x0000 B:00 00	400	40,0	UINT16 R/W	NO
PULSE TIME DO32	3x20032 4x20032 I:20031	0,0x0000 B:00 00	500	50,0	UINT16 R/W	NO
PULSE TIME DO33	3x20033 4x20033 I:20032	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO34	3x20034 4x20034 I:20033	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO35	3x20035 4x20035 I:20034	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO36	3x20036 4x20036 I:20035	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO37	3x20037 4x20037 I:20036	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO38	3x20038 4x20038 I:20037	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO39	3x20039 4x20039 I:20038	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO40	3x20040 4x20040 I:20039	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO41	3x20041 4x20041 I:20040	0,0x0000 B:00 00	400	40,0	UINT16 R/W	NO
PULSE TIME DO42	3x20042 4x20042 I:20041	0,0x0000 B:00 00	500	50,0	UINT16 R/W	NO
PULSE TIME DO43	3x20043 4x20043 I:20042	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO

PULSE TIME DO44	3x20044 4x20044 I:20043	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO45	3x20045 4x20045 I:20044	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO46	3x20046 4x20046 I:20045	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO47	3x20047 4x20047 I:20046	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO48	3x20048 4x20048 I:20047	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO49	3x20049 4x20049 I:20048	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO50	3x20050 4x20050 I:20049	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO51	3x20051 4x20051 I:20050	0,0x0000 B:00 00	400	40,0	UINT16 R/W	NO
PULSE TIME DO52	3x20052 4x20052 I:20051	0,0x0000 B:00 00	500	50,0	UINT16 R/W	NO
PULSE TIME DO53	3x20053 4x20053 I:20052	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO54	3x20054 4x20054 I:20053	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO55	3x20055 4x20055 I:20054	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO56	3x20056 4x20056 I:20055	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO57	3x20057 4x20057 I:20056	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO58	3x20058 4x20058 I:20057	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO



PULSE TIME DO59	3x20059 4x20059 I:20058	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO60	3x20060 4x20060 I:20059	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
<b>PULSE STATUS FOR DIGITAL OUTPUTS</b>						
PULSE TIMER DO1	3x21001 4x21001 I:21000	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
Remaining time of the pulse on digital output x in Milliseconds.						
PULSE TIMER DO2	3x21003 4x21003 I:21002	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO3	3x21005 4x21005 I:21004	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO4	3x21007 4x21007 I:21006	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO5	3x21009 4x21009 I:21008	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO6	3x21011 4x21011 I:21010	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO7	3x21013 4x21013 I:21012	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO8	3x21015 4x21015 I:21014	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO9	3x21017 4x21017 I:21016	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO10	3x21019 4x21019 I:21018	0,0x00000000 B:00 00 00 00			UINT32 R/O	

		0,0 seconds			
PULSE TIMER DO11	3x21021 4x21021 I:21020	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO12	3x21023 4x21023 I:21022	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO13	3x21025 4x21025 I:21024	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO14	3x21027 4x21027 I:21026	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO15	3x21029 4x21029 I:21028	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO16	3x21031 4x21031 I:21030	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO17	3x21033 4x21033 I:21032	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO18	3x21035 4x21035 I:21034	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO19	3x21037 4x21037 I:21036	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO20	3x21039 4x21039 I:21038	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO21	3x21041 4x21041 I:21040	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			

PULSE TIMER DO22	3x21043 4x21043 I:21042	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO23	3x21045 4x21045 I:21044	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO24	3x21047 4x21047 I:21046	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO25	3x21049 4x21049 I:21048	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO26	3x21051 4x21051 I:21050	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO27	3x21053 4x21053 I:21052	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO28	3x21055 4x21055 I:21054	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO29	3x21057 4x21057 I:21056	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO30	3x21059 4x21059 I:21058	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO31	3x21061 4x21061 I:21060	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO32	3x21063 4x21063 I:21062	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				

PULSE TIMER DO33	3x21065 4x21065 I:21064	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO34	3x21067 4x21067 I:21066	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO35	3x21069 4x21069 I:21068	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO36	3x21071 4x21071 I:21070	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO37	3x21073 4x21073 I:21072	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO38	3x21075 4x21075 I:21074	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO39	3x21077 4x21077 I:21076	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO40	3x21079 4x21079 I:21078	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO41	3x21081 4x21081 I:21080	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO42	3x21083 4x21083 I:21082	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO43	3x21085 4x21085 I:21084	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				

PULSE TIMER DO44	3x21087 4x21087 I:21086	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO45	3x21089 4x21089 I:21088	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO46	3x21091 4x21091 I:21090	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO47	3x21093 4x21093 I:21092	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO48	3x21095 4x21095 I:21094	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO49	3x21097 4x21097 I:21096	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO50	3x21099 4x21099 I:21098	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO51	3x21101 4x21101 I:21100	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO52	3x21103 4x21103 I:21102	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO53	3x21105 4x21105 I:21104	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO54	3x21107 4x21107 I:21106	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				

PULSE TIMER DO55	3x21109 4x21109 I:21108	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO56	3x21111 4x21111 I:21110	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO57	3x21113 4x21113 I:21112	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO58	3x21115 4x21115 I:21114	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO59	3x21117 4x21117 I:21116	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO60	3x21119 4x21119 I:21118	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
<b>PULSE STATUS FOR DIGITAL OUTPUTS</b>						
PULSE TIMER DO1	3x21121 4x21121 I:21120	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
Remaining time of the pulse on digital output x in Milliseconds.						
PULSE TIMER DO2	3x21123 4x21123 I:21122	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO3	3x21125 4x21125 I:21124	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO4	3x21127 4x21127 I:21126	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO5	3x21129 4x21129 I:21128	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				

PULSE TIMER DO6	3x21131 4x21131 I:21130	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO7	3x21133 4x21133 I:21132	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO8	3x21135 4x21135 I:21134	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO9	3x21137 4x21137 I:21136	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO10	3x21139 4x21139 I:21138	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO11	3x21141 4x21141 I:21140	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO12	3x21143 4x21143 I:21142	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO13	3x21145 4x21145 I:21144	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO14	3x21147 4x21147 I:21146	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO15	3x21149 4x21149 I:21148	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO16	3x21151 4x21151 I:21150	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO17	3x21153 4x21153 I:21152	0,0x00000000 B:00 00 00 00			UINT32R R/O	

		0,0 seconds			
PULSE TIMER DO18	3x21155 4x21155 I:21154	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO19	3x21157 4x21157 I:21156	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO20	3x21159 4x21159 I:21158	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO21	3x21161 4x21161 I:21160	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO22	3x21163 4x21163 I:21162	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO23	3x21165 4x21165 I:21164	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO24	3x21167 4x21167 I:21166	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO25	3x21169 4x21169 I:21168	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO26	3x21171 4x21171 I:21170	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO27	3x21173 4x21173 I:21172	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO28	3x21175 4x21175 I:21174	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			



PULSE TIMER DO29	3x21177 4x21177 I:21176	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO30	3x21179 4x21179 I:21178	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO31	3x21181 4x21181 I:21180	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO32	3x21183 4x21183 I:21182	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO33	3x21185 4x21185 I:21184	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO34	3x21187 4x21187 I:21186	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO35	3x21189 4x21189 I:21188	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO36	3x21191 4x21191 I:21190	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO37	3x21193 4x21193 I:21192	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO38	3x21195 4x21195 I:21194	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO39	3x21197 4x21197 I:21196	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO40	3x21199 4x21199 I:21198	0,0x00000000 B:00 00 00 00			UINT32R R/O	

		0,0 seconds			
PULSE TIMER DO41	3x21201 4x21201 I:21200	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO42	3x21203 4x21203 I:21202	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO43	3x21205 4x21205 I:21204	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO44	3x21207 4x21207 I:21206	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO45	3x21209 4x21209 I:21208	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO46	3x21211 4x21211 I:21210	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO47	3x21213 4x21213 I:21212	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO48	3x21215 4x21215 I:21214	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO49	3x21217 4x21217 I:21216	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO50	3x21219 4x21219 I:21218	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO51	3x21221 4x21221 I:21220	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			

PULSE TIMER DO52	3x21223 4x21223 I:21222	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO53	3x21225 4x21225 I:21224	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO54	3x21227 4x21227 I:21226	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO55	3x21229 4x21229 I:21228	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO56	3x21231 4x21231 I:21230	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO57	3x21233 4x21233 I:21232	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO58	3x21235 4x21235 I:21234	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO59	3x21237 4x21237 I:21236	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO60	3x21239 4x21239 I:21238	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				

Register NAME Command NAME	MODBUS Register ASCII Command	Register VALUE ASCII Command	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
<b>DIGITAL INPUTS</b>						
STATUS DI1 A	3x05001 4x05001 I:5000	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
Status for the digital input DIx Bit 0-4: Lower 5 bits of CHANGE COUNTER Bit 5-9: Lower 5 bits of RISING EDGE COUNTER Bit 10-14: Lower 5 bits of FALLING EDGE COUNTER Bit 15: Current Status of DIx =0: DIx si OFF, =1: DIx is ON						
STATUS DI1 B	3x05002 4x05002 I:5001	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
Status for the digital input DIx Bit 0-4: Lower 5 bits of SHORT KEYPRESS EVENTS Bit 5-9: Lower 5 bits of LONG KEYPRESS START EVENTS Bit 10-14: Lower 5 bits of LONG KEYPRESS END EVENTS Bit 15: Current Status of DIx =0: DIx si OFF, =1: DIx is ON						
STATUS DI2 A	3x05003 4x05003 I:5002	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI2 B	3x05004 4x05004 I:5003	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI3 A	3x05005 4x05005 I:5004	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI3 B	3x05006 4x05006 I:5005	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI4 A	3x05007 4x05007 I:5006	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI4 B	3x05008 4x05008 I:5007	0,0x0000 B:00 00			UINT16 R/O	

		DI:0,SKE:0,LKSE:0,LKEE:0			
STATUS DI5 A	3x05009 4x05009 I:5008	0,0x0000 B:00 00			UINT16 R/O
		DI:0,CC:0,REC:0,FEC:0			
STATUS DI5 B	3x05010 4x05010 I:5009	0,0x0000 B:00 00			UINT16 R/O
		DI:0,SKE:0,LKSE:0,LKEE:0			
STATUS DI6 A	3x05011 4x05011 I:5010	0,0x0000 B:00 00			UINT16 R/O
		DI:0,CC:0,REC:0,FEC:0			
STATUS DI6 B	3x05012 4x05012 I:5011	0,0x0000 B:00 00			UINT16 R/O
		DI:0,SKE:0,LKSE:0,LKEE:0			
STATUS DI7 A	3x05013 4x05013 I:5012	0,0x0000 B:00 00			UINT16 R/O
		DI:0,CC:0,REC:0,FEC:0			
STATUS DI7 B	3x05014 4x05014 I:5013	0,0x0000 B:00 00			UINT16 R/O
		DI:0,SKE:0,LKSE:0,LKEE:0			
STATUS DI8 A	3x05015 4x05015 I:5014	0,0x0000 B:00 00			UINT16 R/O
		DI:0,CC:0,REC:0,FEC:0			
STATUS DI8 B	3x05016 4x05016 I:5015	0,0x0000 B:00 00			UINT16 R/O
		DI:0,SKE:0,LKSE:0,LKEE:0			
STATUS DI9 A	3x05017 4x05017 I:5016	0,0x0000 B:00 00			UINT16 R/O
		DI:0,CC:0,REC:0,FEC:0			
STATUS DI9 B	3x05018 4x05018 I:5017	0,0x0000 B:00 00			UINT16 R/O
		DI:0,SKE:0,LKSE:0,LKEE:0			
STATUS DI10 A	3x05019 4x05019 I:5018	0,0x0000 B:00 00			UINT16 R/O
		DI:0,CC:0,REC:0,FEC:0			

STATUS DI10 B	3x05020 4x05020 I:5019	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI11 A	3x05021 4x05021 I:5020	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI11 B	3x05022 4x05022 I:5021	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI12 A	3x05023 4x05023 I:5022	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI12 B	3x05024 4x05024 I:5023	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI13 A	3x05025 4x05025 I:5024	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI13 B	3x05026 4x05026 I:5025	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI14 A	3x05027 4x05027 I:5026	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI14 B	3x05028 4x05028 I:5027	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI15 A	3x05029 4x05029 I:5028	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI15 B	3x05030 4x05030 I:5029	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				

STATUS DI16 A	3x05031 4x05031 I:5030	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI16 B	3x05032 4x05032 I:5031	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI17 A	3x05033 4x05033 I:5032	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI17 B	3x05034 4x05034 I:5033	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI18 A	3x05035 4x05035 I:5034	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI18 B	3x05036 4x05036 I:5035	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI19 A	3x05037 4x05037 I:5036	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI19 B	3x05038 4x05038 I:5037	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI20 A	3x05039 4x05039 I:5038	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI20 B	3x05040 4x05040 I:5039	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI21 A	3x05041 4x05041 I:5040	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				

STATUS DI21 B	3x05042 4x05042 I:5041	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI22 A	3x05043 4x05043 I:5042	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI22 B	3x05044 4x05044 I:5043	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI23 A	3x05045 4x05045 I:5044	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI23 B	3x05046 4x05046 I:5045	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI24 A	3x05047 4x05047 I:5046	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI24 B	3x05048 4x05048 I:5047	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI25 A	3x05049 4x05049 I:5048	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI25 B	3x05050 4x05050 I:5049	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI26 A	3x05051 4x05051 I:5050	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI26 B	3x05052 4x05052 I:5051	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				



STATUS DI27 A	3x05053 4x05053 I:5052	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI27 B	3x05054 4x05054 I:5053	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI28 A	3x05055 4x05055 I:5054	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI28 B	3x05056 4x05056 I:5055	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI29 A	3x05057 4x05057 I:5056	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI29 B	3x05058 4x05058 I:5057	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI30 A	3x05059 4x05059 I:5058	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI30 B	3x05060 4x05060 I:5059	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI31 A	3x05061 4x05061 I:5060	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI31 B	3x05062 4x05062 I:5061	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI32 A	3x05063 4x05063 I:5062	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				

STATUS DI32 B	3x05064 4x05064 I:5063	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI33 A	3x05065 4x05065 I:5064	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI33 B	3x05066 4x05066 I:5065	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI34 A	3x05067 4x05067 I:5066	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI34 B	3x05068 4x05068 I:5067	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI35 A	3x05069 4x05069 I:5068	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI35 B	3x05070 4x05070 I:5069	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI36 A	3x05071 4x05071 I:5070	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI36 B	3x05072 4x05072 I:5071	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI37 A	3x05073 4x05073 I:5072	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI37 B	3x05074 4x05074 I:5073	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				

STATUS DI38 A	3x05075 4x05075 I:5074	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI38 B	3x05076 4x05076 I:5075	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI39 A	3x05077 4x05077 I:5076	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI39 B	3x05078 4x05078 I:5077	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI40 A	3x05079 4x05079 I:5078	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI40 B	3x05080 4x05080 I:5079	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI41 A	3x05081 4x05081 I:5080	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI41 B	3x05082 4x05082 I:5081	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI42 A	3x05083 4x05083 I:5082	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI42 B	3x05084 4x05084 I:5083	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI43 A	3x05085 4x05085 I:5084	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				

STATUS DI43 B	3x05086 4x05086 I:5085	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI44 A	3x05087 4x05087 I:5086	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI44 B	3x05088 4x05088 I:5087	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI45 A	3x05089 4x05089 I:5088	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI45 B	3x05090 4x05090 I:5089	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI46 A	3x05091 4x05091 I:5090	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI46 B	3x05092 4x05092 I:5091	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI47 A	3x05093 4x05093 I:5092	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI47 B	3x05094 4x05094 I:5093	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI48 A	3x05095 4x05095 I:5094	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI48 B	3x05096 4x05096 I:5095	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				

STATUS DI49 A	3x05097 4x05097 I:5096	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI49 B	3x05098 4x05098 I:5097	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI50 A	3x05099 4x05099 I:5098	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI50 B	3x05100 4x05100 I:5099	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI51 A	3x05101 4x05101 I:5100	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI51 B	3x05102 4x05102 I:5101	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI52 A	3x05103 4x05103 I:5102	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI52 B	3x05104 4x05104 I:5103	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI53 A	3x05105 4x05105 I:5104	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI53 B	3x05106 4x05106 I:5105	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI54 A	3x05107 4x05107 I:5106	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				

STATUS DI54 B	3x05108 4x05108 I:5107	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI55 A	3x05109 4x05109 I:5108	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI55 B	3x05110 4x05110 I:5109	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI56 A	3x05111 4x05111 I:5110	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI56 B	3x05112 4x05112 I:5111	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI57 A	3x05113 4x05113 I:5112	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI57 B	3x05114 4x05114 I:5113	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI58 A	3x05115 4x05115 I:5114	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI58 B	3x05116 4x05116 I:5115	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI59 A	3x05117 4x05117 I:5116	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI59 B	3x05118 4x05118 I:5117	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				

STATUS DI60 A	3x05119 4x05119 I:5118	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI60 B	3x05120 4x05120 I:5119	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI61 A	3x05121 4x05121 I:5120	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI61 B	3x05122 4x05122 I:5121	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI62 A	3x05123 4x05123 I:5122	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI62 B	3x05124 4x05124 I:5123	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI63 A	3x05125 4x05125 I:5124	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI63 B	3x05126 4x05126 I:5125	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI64 A	3x05127 4x05127 I:5126	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI64 B	3x05128 4x05128 I:5127	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
<b>STATUS</b>						
FILTER PATTERN DI1	3x05129 4x05129 I:5128	0,0x00000000 B:00 00 00 00			UINT32 R/O	

The internal pattern for corresponding digital input for AC/DC filtering. The internal used state is created out of this internal pattern via oversampling.

FILTER PATTERN DI2	3x05131 4x05131 I:5130	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI3	3x05133 4x05133 I:5132	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI4	3x05135 4x05135 I:5134	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI5	3x05137 4x05137 I:5136	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI6	3x05139 4x05139 I:5138	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI7	3x05141 4x05141 I:5140	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI8	3x05143 4x05143 I:5142	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI9	3x05145 4x05145 I:5144	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI10	3x05147 4x05147 I:5146	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI11	3x05149 4x05149 I:5148	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI12	3x05151 4x05151 I:5150	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI13	3x05153 4x05153 I:5152	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI14	3x05155 4x05155 I:5154	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI15	3x05157 4x05157 I:5156	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI16	3x05159 4x05159 I:5158	0,0x00000000 B:00 00 00 00			UINT32 R/O	



FILTER PATTERN DI17	3x05161 4x05161 I:5160	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI18	3x05163 4x05163 I:5162	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI19	3x05165 4x05165 I:5164	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI20	3x05167 4x05167 I:5166	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI21	3x05169 4x05169 I:5168	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI22	3x05171 4x05171 I:5170	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI23	3x05173 4x05173 I:5172	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI24	3x05175 4x05175 I:5174	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI25	3x05177 4x05177 I:5176	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI26	3x05179 4x05179 I:5178	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI27	3x05181 4x05181 I:5180	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI28	3x05183 4x05183 I:5182	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI29	3x05185 4x05185 I:5184	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI30	3x05187 4x05187 I:5186	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI31	3x05189 4x05189 I:5188	0,0x00000000 B:00 00 00 00			UINT32 R/O	

FILTER PATTERN DI32	3x05191 4x05191 I:5190	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI33	3x05193 4x05193 I:5192	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI34	3x05195 4x05195 I:5194	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI35	3x05197 4x05197 I:5196	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI36	3x05199 4x05199 I:5198	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI37	3x05201 4x05201 I:5200	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI38	3x05203 4x05203 I:5202	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI39	3x05205 4x05205 I:5204	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI40	3x05207 4x05207 I:5206	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI41	3x05209 4x05209 I:5208	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI42	3x05211 4x05211 I:5210	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI43	3x05213 4x05213 I:5212	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI44	3x05215 4x05215 I:5214	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI45	3x05217 4x05217 I:5216	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI46	3x05219 4x05219 I:5218	0,0x00000000 B:00 00 00 00			UINT32 R/O	

FILTER PATTERN DI47	3x05221 4x05221 I:5220	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI48	3x05223 4x05223 I:5222	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI49	3x05225 4x05225 I:5224	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI50	3x05227 4x05227 I:5226	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI51	3x05229 4x05229 I:5228	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI52	3x05231 4x05231 I:5230	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI53	3x05233 4x05233 I:5232	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI54	3x05235 4x05235 I:5234	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI55	3x05237 4x05237 I:5236	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI56	3x05239 4x05239 I:5238	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI57	3x05241 4x05241 I:5240	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI58	3x05243 4x05243 I:5242	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI59	3x05245 4x05245 I:5244	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI60	3x05247 4x05247 I:5246	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI61	3x05249 4x05249 I:5248	0,0x00000000 B:00 00 00 00			UINT32 R/O	

FILTER PATTERN DI62	3x05251 4x05251 I:5250	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI63	3x05253 4x05253 I:5252	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI64	3x05255 4x05255 I:5254	0,0x00000000 B:00 00 00 00			UINT32 R/O	
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI1</b>						
RISE DI1	3x07001 4x07001 I:7000	3,0x0003 B:00 03			UINT16 R/O	
	3 event(s)					
Counter for rising edges on the digital input DIx. If the module detects a rising edge on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.						
FALL DI1	3x07002 4x07002 I:7001	3,0x0003 B:00 03			UINT16 R/O	
	3 event(s)					
Counter for falling edges on the digital input DIx. If the module detects a falling edge on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.						
CHANGE DI1	3x07003 4x07003 I:7002	4,0x0004 B:00 04			UINT16 R/O	
	4 event(s)					
Counter for events on the digital input DIx. If the module detects an event on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0. The following events are available: Detection of a short keypress Detection of the start of a long keypress Detection of the end of a long keypress						
SHORT KEYPRESS DI1	3x07004 4x07004 I:7003	2,0x0002 B:00 02			UINT16 R/O	
	2 event(s)					
Counter for short keypress events on the digital input DIx. If the module detects a short keypress on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.						
LONG KEYPRESS START DI1	3x07005 4x07005 I:7004	1,0x0001 B:00 01			UINT16 R/O	
	1 event(s)					
Counter for start events of long keypress actions on the digital input DIx. If the module detects the start of a long keypress action on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.						

LONG KEYPRESS END DI1	3x07006 4x07006 I:7005	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
Counter for end events of long keypress actions on the digital input DIx. If the module detects the end of a long keypress action on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.						
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI2</b>						
RISE DI2	3x07011 4x07011 I:7010	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI2	3x07012 4x07012 I:7011	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI2	3x07013 4x07013 I:7012	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI2	3x07014 4x07014 I:7013	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI2	3x07015 4x07015 I:7014	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI2	3x07016 4x07016 I:7015	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI3</b>						
RISE DI3	3x07021 4x07021 I:7020	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI3	3x07022 4x07022 I:7021	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI3	3x07023 4x07023 I:7022	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				

SHORT KEYPRESS DI3	3x07024 4x07024 I:7023	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI3	3x07025 4x07025 I:7024	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI3	3x07026 4x07026 I:7025	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI4</b>						
RISE DI4	3x07031 4x07031 I:7030	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI4	3x07032 4x07032 I:7031	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI4	3x07033 4x07033 I:7032	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI4	3x07034 4x07034 I:7033	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI4	3x07035 4x07035 I:7034	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI4	3x07036 4x07036 I:7035	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI5</b>						
RISE DI5	3x07041 4x07041 I:7040	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI5	3x07042 4x07042 I:7041	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				

CHANGE DI5	3x07043 4x07043 I:7042	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI5	3x07044 4x07044 I:7043	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI5	3x07045 4x07045 I:7044	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI5	3x07046 4x07046 I:7045	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI6</b>						
RISE DI6	3x07051 4x07051 I:7050	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI6	3x07052 4x07052 I:7051	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI6	3x07053 4x07053 I:7052	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI6	3x07054 4x07054 I:7053	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI6	3x07055 4x07055 I:7054	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI6	3x07056 4x07056 I:7055	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI7</b>						
RISE DI7	3x07061 4x07061 I:7060	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				

FALL DI7	3x07062 4x07062 I:7061	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI7	3x07063 4x07063 I:7062	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI7	3x07064 4x07064 I:7063	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI7	3x07065 4x07065 I:7064	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI7	3x07066 4x07066 I:7065	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI8</b>						
RISE DI8	3x07071 4x07071 I:7070	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI8	3x07072 4x07072 I:7071	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI8	3x07073 4x07073 I:7072	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI8	3x07074 4x07074 I:7073	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI8	3x07075 4x07075 I:7074	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI8	3x07076 4x07076 I:7075	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI9</b>						



RISE DI9	3x07081 4x07081 I:7080	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI9	3x07082 4x07082 I:7081	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI9	3x07083 4x07083 I:7082	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI9	3x07084 4x07084 I:7083	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI9	3x07085 4x07085 I:7084	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI9	3x07086 4x07086 I:7085	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI10</b>						
RISE DI10	3x07091 4x07091 I:7090	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI10	3x07092 4x07092 I:7091	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI10	3x07093 4x07093 I:7092	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI10	3x07094 4x07094 I:7093	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI10	3x07095 4x07095 I:7094	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				

LONG KEYPRESS END DI10	3x07096 4x07096 I:7095	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI11</b>						
RISE DI11	3x07101 4x07101 I:7100	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI11	3x07102 4x07102 I:7101	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI11	3x07103 4x07103 I:7102	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI11	3x07104 4x07104 I:7103	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI11	3x07105 4x07105 I:7104	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI11	3x07106 4x07106 I:7105	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI12</b>						
RISE DI12	3x07111 4x07111 I:7110	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI12	3x07112 4x07112 I:7111	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI12	3x07113 4x07113 I:7112	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI12	3x07114 4x07114 I:7113	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				

LONG KEYPRESS START DI12	3x07115 4x07115 I:7114	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI12	3x07116 4x07116 I:7115	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI13</b>						
RISE DI13	3x07121 4x07121 I:7120	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI13	3x07122 4x07122 I:7121	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI13	3x07123 4x07123 I:7122	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI13	3x07124 4x07124 I:7123	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI13	3x07125 4x07125 I:7124	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI13	3x07126 4x07126 I:7125	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI14</b>						
RISE DI14	3x07131 4x07131 I:7130	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI14	3x07132 4x07132 I:7131	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI14	3x07133 4x07133 I:7132	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				

SHORT KEYPRESS DI14	3x07134 4x07134 I:7133	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI14	3x07135 4x07135 I:7134	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI14	3x07136 4x07136 I:7135	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI15</b>						
RISE DI15	3x07141 4x07141 I:7140	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI15	3x07142 4x07142 I:7141	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI15	3x07143 4x07143 I:7142	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI15	3x07144 4x07144 I:7143	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI15	3x07145 4x07145 I:7144	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI15	3x07146 4x07146 I:7145	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI16</b>						
RISE DI16	3x07151 4x07151 I:7150	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI16	3x07152 4x07152 I:7151	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				

CHANGE DI16	3x07153 4x07153 I:7152	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI16	3x07154 4x07154 I:7153	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI16	3x07155 4x07155 I:7154	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI16	3x07156 4x07156 I:7155	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI17</b>						
RISE DI17	3x07161 4x07161 I:7160	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI17	3x07162 4x07162 I:7161	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI17	3x07163 4x07163 I:7162	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI17	3x07164 4x07164 I:7163	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI17	3x07165 4x07165 I:7164	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI17	3x07166 4x07166 I:7165	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI18</b>						
RISE DI18	3x07171 4x07171 I:7170	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				

FALL DI18	3x07172 4x07172 I:7171	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI18	3x07173 4x07173 I:7172	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI18	3x07174 4x07174 I:7173	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI18	3x07175 4x07175 I:7174	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI18	3x07176 4x07176 I:7175	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI19</b>						
RISE DI19	3x07181 4x07181 I:7180	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI19	3x07182 4x07182 I:7181	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI19	3x07183 4x07183 I:7182	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI19	3x07184 4x07184 I:7183	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI19	3x07185 4x07185 I:7184	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI19	3x07186 4x07186 I:7185	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI20</b>						

RISE DI20	3x07191 4x07191 I:7190	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI20	3x07192 4x07192 I:7191	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI20	3x07193 4x07193 I:7192	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI20	3x07194 4x07194 I:7193	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI20	3x07195 4x07195 I:7194	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI20	3x07196 4x07196 I:7195	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI21</b>						
RISE DI21	3x07201 4x07201 I:7200	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI21	3x07202 4x07202 I:7201	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI21	3x07203 4x07203 I:7202	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI21	3x07204 4x07204 I:7203	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI21	3x07205 4x07205 I:7204	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				

LONG KEYPRESS END DI21	3x07206 4x07206 I:7205	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI22</b>						
RISE DI22	3x07211 4x07211 I:7210	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI22	3x07212 4x07212 I:7211	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI22	3x07213 4x07213 I:7212	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI22	3x07214 4x07214 I:7213	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI22	3x07215 4x07215 I:7214	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI22	3x07216 4x07216 I:7215	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI23</b>						
RISE DI23	3x07221 4x07221 I:7220	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI23	3x07222 4x07222 I:7221	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI23	3x07223 4x07223 I:7222	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI23	3x07224 4x07224 I:7223	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				



LONG KEYPRESS START DI23	3x07225 4x07225 I:7224	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI23	3x07226 4x07226 I:7225	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI24</b>						
RISE DI24	3x07231 4x07231 I:7230	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI24	3x07232 4x07232 I:7231	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI24	3x07233 4x07233 I:7232	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI24	3x07234 4x07234 I:7233	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI24	3x07235 4x07235 I:7234	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI24	3x07236 4x07236 I:7235	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI25</b>						
RISE DI25	3x07241 4x07241 I:7240	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI25	3x07242 4x07242 I:7241	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI25	3x07243 4x07243 I:7242	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				

SHORT KEYPRESS DI25	3x07244 4x07244 I:7243	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI25	3x07245 4x07245 I:7244	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI25	3x07246 4x07246 I:7245	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI26</b>						
RISE DI26	3x07251 4x07251 I:7250	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI26	3x07252 4x07252 I:7251	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI26	3x07253 4x07253 I:7252	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI26	3x07254 4x07254 I:7253	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI26	3x07255 4x07255 I:7254	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI26	3x07256 4x07256 I:7255	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI27</b>						
RISE DI27	3x07261 4x07261 I:7260	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI27	3x07262 4x07262 I:7261	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				

CHANGE DI27	3x07263 4x07263 I:7262	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI27	3x07264 4x07264 I:7263	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI27	3x07265 4x07265 I:7264	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI27	3x07266 4x07266 I:7265	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI28</b>						
RISE DI28	3x07271 4x07271 I:7270	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI28	3x07272 4x07272 I:7271	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI28	3x07273 4x07273 I:7272	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI28	3x07274 4x07274 I:7273	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI28	3x07275 4x07275 I:7274	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI28	3x07276 4x07276 I:7275	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI29</b>						
RISE DI29	3x07281 4x07281 I:7280	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				

FALL DI29	3x07282 4x07282 I:7281	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI29	3x07283 4x07283 I:7282	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI29	3x07284 4x07284 I:7283	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI29	3x07285 4x07285 I:7284	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI29	3x07286 4x07286 I:7285	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI30</b>						
RISE DI30	3x07291 4x07291 I:7290	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI30	3x07292 4x07292 I:7291	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI30	3x07293 4x07293 I:7292	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI30	3x07294 4x07294 I:7293	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI30	3x07295 4x07295 I:7294	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI30	3x07296 4x07296 I:7295	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI31</b>						

RISE DI31	3x07301 4x07301 I:7300	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI31	3x07302 4x07302 I:7301	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI31	3x07303 4x07303 I:7302	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI31	3x07304 4x07304 I:7303	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI31	3x07305 4x07305 I:7304	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI31	3x07306 4x07306 I:7305	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI32</b>						
RISE DI32	3x07311 4x07311 I:7310	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI32	3x07312 4x07312 I:7311	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI32	3x07313 4x07313 I:7312	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI32	3x07314 4x07314 I:7313	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI32	3x07315 4x07315 I:7314	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				

LONG KEYPRESS END DI32	3x07316 4x07316 I:7315	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI33</b>						
RISE DI33	3x07321 4x07321 I:7320	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI33	3x07322 4x07322 I:7321	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI33	3x07323 4x07323 I:7322	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI33	3x07324 4x07324 I:7323	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI33	3x07325 4x07325 I:7324	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI33	3x07326 4x07326 I:7325	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI34</b>						
RISE DI34	3x07331 4x07331 I:7330	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI34	3x07332 4x07332 I:7331	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI34	3x07333 4x07333 I:7332	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI34	3x07334 4x07334 I:7333	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				

LONG KEYPRESS START DI34	3x07335 4x07335 I:7334	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI34	3x07336 4x07336 I:7335	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI35</b>						
RISE DI35	3x07341 4x07341 I:7340	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI35	3x07342 4x07342 I:7341	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI35	3x07343 4x07343 I:7342	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI35	3x07344 4x07344 I:7343	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI35	3x07345 4x07345 I:7344	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI35	3x07346 4x07346 I:7345	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI36</b>						
RISE DI36	3x07351 4x07351 I:7350	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI36	3x07352 4x07352 I:7351	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI36	3x07353 4x07353 I:7352	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				

SHORT KEYPRESS DI36	3x07354 4x07354 I:7353	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI36	3x07355 4x07355 I:7354	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI36	3x07356 4x07356 I:7355	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI37</b>						
RISE DI37	3x07361 4x07361 I:7360	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI37	3x07362 4x07362 I:7361	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI37	3x07363 4x07363 I:7362	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI37	3x07364 4x07364 I:7363	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI37	3x07365 4x07365 I:7364	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI37	3x07366 4x07366 I:7365	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI38</b>						
RISE DI38	3x07371 4x07371 I:7370	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI38	3x07372 4x07372 I:7371	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				



CHANGE DI38	3x07373 4x07373 I:7372	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI38	3x07374 4x07374 I:7373	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI38	3x07375 4x07375 I:7374	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI38	3x07376 4x07376 I:7375	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI39</b>						
RISE DI39	3x07381 4x07381 I:7380	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI39	3x07382 4x07382 I:7381	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI39	3x07383 4x07383 I:7382	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI39	3x07384 4x07384 I:7383	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI39	3x07385 4x07385 I:7384	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI39	3x07386 4x07386 I:7385	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI40</b>						
RISE DI40	3x07391 4x07391 I:7390	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				

FALL DI40	3x07392 4x07392 I:7391	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI40	3x07393 4x07393 I:7392	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI40	3x07394 4x07394 I:7393	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI40	3x07395 4x07395 I:7394	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI40	3x07396 4x07396 I:7395	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI41</b>						
RISE DI41	3x07401 4x07401 I:7400	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI41	3x07402 4x07402 I:7401	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI41	3x07403 4x07403 I:7402	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI41	3x07404 4x07404 I:7403	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI41	3x07405 4x07405 I:7404	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI41	3x07406 4x07406 I:7405	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI42</b>						

RISE DI42	3x07411 4x07411 I:7410	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI42	3x07412 4x07412 I:7411	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI42	3x07413 4x07413 I:7412	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI42	3x07414 4x07414 I:7413	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI42	3x07415 4x07415 I:7414	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI42	3x07416 4x07416 I:7415	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI43</b>						
RISE DI43	3x07421 4x07421 I:7420	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI43	3x07422 4x07422 I:7421	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI43	3x07423 4x07423 I:7422	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI43	3x07424 4x07424 I:7423	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI43	3x07425 4x07425 I:7424	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				

LONG KEYPRESS END DI43	3x07426 4x07426 I:7425	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI44</b>						
RISE DI44	3x07431 4x07431 I:7430	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI44	3x07432 4x07432 I:7431	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI44	3x07433 4x07433 I:7432	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI44	3x07434 4x07434 I:7433	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI44	3x07435 4x07435 I:7434	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI44	3x07436 4x07436 I:7435	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI45</b>						
RISE DI45	3x07441 4x07441 I:7440	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI45	3x07442 4x07442 I:7441	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI45	3x07443 4x07443 I:7442	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI45	3x07444 4x07444 I:7443	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				

LONG KEYPRESS START DI45	3x07445 4x07445 I:7444	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI45	3x07446 4x07446 I:7445	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI46</b>						
RISE DI46	3x07451 4x07451 I:7450	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI46	3x07452 4x07452 I:7451	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI46	3x07453 4x07453 I:7452	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI46	3x07454 4x07454 I:7453	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI46	3x07455 4x07455 I:7454	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI46	3x07456 4x07456 I:7455	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI47</b>						
RISE DI47	3x07461 4x07461 I:7460	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI47	3x07462 4x07462 I:7461	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI47	3x07463 4x07463 I:7462	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				

SHORT KEYPRESS DI47	3x07464 4x07464 I:7463	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI47	3x07465 4x07465 I:7464	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI47	3x07466 4x07466 I:7465	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI48</b>						
RISE DI48	3x07471 4x07471 I:7470	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI48	3x07472 4x07472 I:7471	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI48	3x07473 4x07473 I:7472	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI48	3x07474 4x07474 I:7473	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI48	3x07475 4x07475 I:7474	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI48	3x07476 4x07476 I:7475	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI49</b>						
RISE DI49	3x07481 4x07481 I:7480	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI49	3x07482 4x07482 I:7481	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				

CHANGE DI49	3x07483 4x07483 I:7482	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI49	3x07484 4x07484 I:7483	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI49	3x07485 4x07485 I:7484	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI49	3x07486 4x07486 I:7485	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI50</b>						
RISE DI50	3x07491 4x07491 I:7490	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI50	3x07492 4x07492 I:7491	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI50	3x07493 4x07493 I:7492	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI50	3x07494 4x07494 I:7493	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI50	3x07495 4x07495 I:7494	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI50	3x07496 4x07496 I:7495	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI51</b>						
RISE DI51	3x07501 4x07501 I:7500	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				

FALL DI51	3x07502 4x07502 I:7501	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI51	3x07503 4x07503 I:7502	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI51	3x07504 4x07504 I:7503	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI51	3x07505 4x07505 I:7504	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI51	3x07506 4x07506 I:7505	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI52</b>						
RISE DI52	3x07511 4x07511 I:7510	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI52	3x07512 4x07512 I:7511	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI52	3x07513 4x07513 I:7512	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI52	3x07514 4x07514 I:7513	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI52	3x07515 4x07515 I:7514	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI52	3x07516 4x07516 I:7515	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI53</b>						



RISE DI53	3x07521 4x07521 I:7520	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI53	3x07522 4x07522 I:7521	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI53	3x07523 4x07523 I:7522	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI53	3x07524 4x07524 I:7523	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI53	3x07525 4x07525 I:7524	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI53	3x07526 4x07526 I:7525	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI54</b>						
RISE DI54	3x07531 4x07531 I:7530	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI54	3x07532 4x07532 I:7531	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI54	3x07533 4x07533 I:7532	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI54	3x07534 4x07534 I:7533	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI54	3x07535 4x07535 I:7534	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				

LONG KEYPRESS END DI54	3x07536 4x07536 I:7535	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI55</b>						
RISE DI55	3x07541 4x07541 I:7540	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI55	3x07542 4x07542 I:7541	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI55	3x07543 4x07543 I:7542	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI55	3x07544 4x07544 I:7543	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI55	3x07545 4x07545 I:7544	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI55	3x07546 4x07546 I:7545	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI56</b>						
RISE DI56	3x07551 4x07551 I:7550	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI56	3x07552 4x07552 I:7551	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI56	3x07553 4x07553 I:7552	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI56	3x07554 4x07554 I:7553	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				

LONG KEYPRESS START DI56	3x07555 4x07555 I:7554	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI56	3x07556 4x07556 I:7555	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI57</b>						
RISE DI57	3x07561 4x07561 I:7560	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI57	3x07562 4x07562 I:7561	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI57	3x07563 4x07563 I:7562	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI57	3x07564 4x07564 I:7563	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI57	3x07565 4x07565 I:7564	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI57	3x07566 4x07566 I:7565	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI58</b>						
RISE DI58	3x07571 4x07571 I:7570	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI58	3x07572 4x07572 I:7571	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI58	3x07573 4x07573 I:7572	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				

SHORT KEYPRESS DI58	3x07574 4x07574 I:7573	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI58	3x07575 4x07575 I:7574	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI58	3x07576 4x07576 I:7575	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI59</b>						
RISE DI59	3x07581 4x07581 I:7580	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI59	3x07582 4x07582 I:7581	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI59	3x07583 4x07583 I:7582	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI59	3x07584 4x07584 I:7583	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI59	3x07585 4x07585 I:7584	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI59	3x07586 4x07586 I:7585	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI60</b>						
RISE DI60	3x07591 4x07591 I:7590	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI60	3x07592 4x07592 I:7591	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				

CHANGE DI60	3x07593 4x07593 I:7592	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI60	3x07594 4x07594 I:7593	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI60	3x07595 4x07595 I:7594	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI60	3x07596 4x07596 I:7595	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI61</b>						
RISE DI61	3x07601 4x07601 I:7600	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI61	3x07602 4x07602 I:7601	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI61	3x07603 4x07603 I:7602	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI61	3x07604 4x07604 I:7603	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI61	3x07605 4x07605 I:7604	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI61	3x07606 4x07606 I:7605	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI62</b>						
RISE DI62	3x07611 4x07611 I:7610	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				

FALL DI62	3x07612 4x07612 I:7611	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI62	3x07613 4x07613 I:7612	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI62	3x07614 4x07614 I:7613	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI62	3x07615 4x07615 I:7614	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI62	3x07616 4x07616 I:7615	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI63</b>						
RISE DI63	3x07621 4x07621 I:7620	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI63	3x07622 4x07622 I:7621	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI63	3x07623 4x07623 I:7622	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI63	3x07624 4x07624 I:7623	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI63	3x07625 4x07625 I:7624	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI63	3x07626 4x07626 I:7625	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
<b>DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI64</b>						

RISE DI64	3x07631 4x07631 I:7630	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
FALL DI64	3x07632 4x07632 I:7631	4,0x0004 B:00 04			UINT16 R/O	
		4 event(s)				
CHANGE DI64	3x07633 4x07633 I:7632	6,0x0006 B:00 06			UINT16 R/O	
		6 event(s)				
SHORT KEYPRESS DI64	3x07634 4x07634 I:7633	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS START DI64	3x07635 4x07635 I:7634	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
LONG KEYPRESS END DI64	3x07636 4x07636 I:7635	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				

Register NAME Command NAME	MODBUS Register ASCII Command	Register VALUE ASCII Command	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
<b>INITIAL &amp; WATCHDOG STATUS FOR ALL DIGITAL OUTPUTS</b>						
FRAM INTIAL & WATCHDOG STATUS OF DO1-DO16	3x59001 4x59001 l:59000	????		0xFFFF	UINT16 R/W	NO
		Actual init & watchdog state of DO1:0=OFF	1			
		Actual init & watchdog state of DO2:0=OFF	1			
		Actual init & watchdog state of DO3:0=OFF	1			
		Actual init & watchdog state of DO4:0=OFF	1			
		Actual init & watchdog state of DO5:0=OFF	1			
		Actual init & watchdog state of DO6:0=OFF	1			
		Actual init & watchdog state of DO7:0=OFF	1			
		Actual init & watchdog state of DO8:0=OFF	1			
		Actual init & watchdog state of DO9:0=OFF	1			
		Actual init & watchdog state of DO10:0=OFF	1			
		Actual init & watchdog state of DO11:0=OFF	1			
		Actual init & watchdog state of DO12:0=OFF	1			
		Actual init & watchdog state of DO13:0=OFF	1			
		Actual init & watchdog state of DO14:0=OFF	1			
		Actual init & watchdog state of DO15:0=OFF	1			
		Actual init & watchdog state of DO16:0=OFF	1			
Current FRAM setting of initial and watchdog state of all digital outputs. This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured Bit 0: =0:DO1 is OFF, =1:DO1 is ON Bit 1: =0:DO2 is OFF, =1:DO2 is ON ... Bit 14: =0:DO15 is OFF, =1:DO15 is ON Bit 15: =0:DO16 is OFF, =1:DO16 is ON						
Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FRAM						
FRAM INTIAL & WATCHDOG STATUS OF DO17-DO32	3x59002 4x59002 l:59001	????		0xFFFF	UINT16 R/W	NO
		Actual init & watchdog state of DO17:0=OFF	1			
		Actual init & watchdog state of DO18:0=OFF	1			
		Actual init & watchdog state of DO19:0=OFF	1			
		Actual init & watchdog state of DO20:0=OFF	1			
		Actual init & watchdog state of DO21:0=OFF	1			
		Actual init & watchdog state of DO22:0=OFF	1			
		Actual init & watchdog state of DO23:0=OFF	1			
		Actual init & watchdog state of DO24:0=OFF	1			
		Actual init & watchdog state of DO25:0=OFF	1			



		Actual init & watchdog state of DO26:0=OFF	1		
		Actual init & watchdog state of DO27:0=OFF	1		
		Actual init & watchdog state of DO28:0=OFF	1		
		Actual init & watchdog state of DO29:0=OFF	1		
		Actual init & watchdog state of DO30:0=OFF	1		
		Actual init & watchdog state of DO31:0=OFF	1		
		Actual init & watchdog state of DO32:0=OFF	1		

Current FRAM setting of initial and watchdog state of all digital outputs. This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured

Bit 0: =0:DO17 is OFF, =1:DO17 is ON

Bit 1: =0:DO18 is OFF, =1:DO18 is ON

...

Bit 14: =0:DO31 is OFF, =1:DO31 is ON

Bit 15: =0:DO32 is OFF, =1:DO32 is ON

Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FRAM

FRAM INTIAL & WATCHDOG STATUS OF DO33-DO48	3x59003 4x59003 l:59002	????		0xFFFF	UINT16 R/W	NO
		Actual init & watchdog state of DO33:0=OFF	1			
		Actual init & watchdog state of DO34:0=OFF	1			
		Actual init & watchdog state of DO35:0=OFF	1			
		Actual init & watchdog state of DO36:0=OFF	1			
		Actual init & watchdog state of DO37:0=OFF	1			
		Actual init & watchdog state of DO38:0=OFF	1			
		Actual init & watchdog state of DO39:0=OFF	1			
		Actual init & watchdog state of DO40:0=OFF	1			
		Actual init & watchdog state of DO41:0=OFF	1			
		Actual init & watchdog state of DO42:0=OFF	1			
		Actual init & watchdog state of DO43:0=OFF	1			
		Actual init & watchdog state of DO44:0=OFF	1			
		Actual init & watchdog state of DO45:0=OFF	1			
		Actual init & watchdog state of DO46:0=OFF	1			
		Actual init & watchdog state of DO47:0=OFF	1			
		Actual init & watchdog state of DO48:0=OFF	1			

Current FRAM setting of initial and watchdog state of all digital outputs. This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured

Bit 0: =0:DO33 is OFF, =1:DO33 is ON

Bit 1: =0:DO34 is OFF, =1:DO34 is ON

...

Bit 14: =0:DO47 is OFF, =1:DO47 is ON

Bit 15: =0:DO48 is OFF, =1:DO48 is ON

Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FRAM

FRAM INTIAL & WATCHDOG STATUS OF DO49-DO60	3x59004 4x59004 l:59003	????		#BEZUG!	UINT16 R/W	NO
		Actual init & watchdog state of DO49:0=OFF	1			
		Actual init & watchdog state of DO50:0=OFF	1			

		Actual init & watchdog state of DO51:0=OFF	1		
		Actual init & watchdog state of DO52:0=OFF	1		
		Actual init & watchdog state of DO53:0=OFF	1		
		Actual init & watchdog state of DO54:0=OFF	1		
		Actual init & watchdog state of DO55:0=OFF	1		
		Actual init & watchdog state of DO56:0=OFF	1		
		Actual init & watchdog state of DO57:0=OFF	1		
		Actual init & watchdog state of DO58:0=OFF	1		
		Actual init & watchdog state of DO59:0=OFF	1		
		Actual init & watchdog state of DO60:0=OFF	1		

Current FRAM setting of initial and watchdog state of all digital outputs. This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured

Bit 0: =0:DO49 is OFF, =1:DO49 is ON

Bit 1: =0:DO50 is OFF, =1:DO50 is ON

...

Bit 10: =0:DO59 is OFF, =1:DO59 is ON

Bit 11: =0:DO60 is OFF, =1:DO60 is ON

Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FRAM

#### DIGITAL OUTPUTS:ENABLE OPEN WIRE DETECTION WHILE ON

INITIAL & WATCHDOG SETUP	3x59005	????		0xFFFF	UINT16 R/W	NO
ENABLE OPEN WIRE DETECTION	4x59005					
WHILE ON DO1-DO16	1:59004					
		Initial setup of open wire detection while ON for DO1:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO2:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO3:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO4:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO5:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO6:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO7:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO8:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO9:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO10:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO11:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO12:0=DISABLED	1			

		Initial setup of open wire detection while ON for DO13:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO14:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO15:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO16:0=DISABLED	1		
Current FRAM setting for intial and watchdog state for open wire detection while ON for digital output DOx. This state is used after power on and after a communcation watchdog timeout, if a watchdog time is configured Bit 0: =0:Open wire detection for DO1 is DISABLED, =1:Open wire detection for DO1 is ENABLED Bit 1: =0:Open wire detection for DO2 is DISABLED, =1:Open wire detection for DO2 is ENABLED ... Bit 14: =0:Open wire detection for DO15 is DISABLED, =1:Open wire detection for DO15 is ENABLED Bit 15: =0:Open wire detection for DO16 is DISABLED, =1:Open wire detection for DO16 is ENABLED					
Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FRAM					
INITIAL & WATCHDOG SETUP ENABLE OPEN WIRE DETECTION WHILE ON DO17-DO32	3x59006 4x59006 1:59005	????		0xFFFF	UINT16 R/W NO
		Initial setup of open wire detection while ON for DO17:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO18:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO19:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO20:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO21:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO22:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO23:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO24:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO25:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO26:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO27:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO28:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO29:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO30:0=DISABLED	1		

		Initial setup of open wire detection while ON for DO31:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO32:0=DISABLED	1		
Current FRAM setting for intial and watchdog state for open wire detection while ON for digital output DOx. This state is used after power on and after a communcation watchdog timeout, if a watchdog time is configured Bit 0: =0:Open wire detection for DO17 is DISABLED, =1:Open wire detection for DO17 is ENABLED Bit 1: =0:Open wire detection for DO18 is DISABLED, =1:Open wire detection for DO18 is ENABLED ... Bit 14: =0:Open wire detection for DO31 is DISABLED, =1:Open wire detection for DO31 is ENABLED Bit 15: =0:Open wire detection for DO32 is DISABLED, =1:Open wire detection for DO32 is ENABLED					
Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FRAM					
INITIAL & WATCHDOG SETUP ENABLE OPEN WIRE DETECTION WHILE ON DO33-DO48	3x59007 4x59007 1:59006	????		0xFFFF	UINT16 R/W NO
		Initial setup of open wire detection while ON for DO33:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO34:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO35:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO36:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO37:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO38:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO39:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO40:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO41:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO42:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO43:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO44:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO45:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO46:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO47:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO48:0=DISABLED	1		

Current FRAM setting for intial and watchdog state for open wire detection while ON for digital output DOx. This state is used after power on and after a communcation watchdog timeout, if a watchdog time is configured

Bit 0: =0:Open wire detection for DO33 is DISABLED, =1:Open wire detection for DO33 is ENABLED

Bit 1: =0:Open wire detection for DO34 is DISABLED, =1:Open wire detection for DO34 is ENABLED

...

Bit 14: =0:Open wire detection for DO47 is DISABLED, =1:Open wire detection for DO47 is ENABLED

Bit 15: =0:Open wire detection for DO48 is DISABLED, =1:Open wire detection for DO48 is ENABLED

Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FRAM

INITIAL & WATCHDOG SETUP ENABLE OPEN WIRE DETECTION WHILE ON DO49-DO60	3x59008 4x59008 l:59007	????	#BEZUG!	UINT16 R/W	NO
		Initial setup of open wire detection while ON for DO49:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO50:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO51:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO52:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO53:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO54:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO55:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO56:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO57:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO58:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO59:0=DISABLED	1		
		Initial setup of open wire detection while ON for DO60:0=DISABLED	1		

Current FRAM setting for intial and watchdog state for open wire detection while ON for digital output DOx. This state is used after power on and after a communcation watchdog timeout, if a watchdog time is configured

Bit 0: =0:Open wire detection for DO49 is DISABLED, =1:Open wire detection for DO49 is ENABLED

Bit 1: =0:Open wire detection for DO50 is DISABLED, =1:Open wire detection for DO50 is ENABLED

...

Bit 10: =0:Open wire detection for DO59 is DISABLED, =1:Open wire detection for DO59 is ENABLED

Bit 11: =0:Open wire detection for DO60 is DISABLED, =1:Open wire detection for DO60 is ENABLED

Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FRAM

#### DIGITAL OUTPUTS:ENABLE OPEN WIRE DETECTION WHILE OFF

INITIAL & WATCHDOG SETUP ENABLE OPEN WIRE DETECTION WHILE OFF DO1-DO16	3x59009 4x59009 l:59008	????	0xFFFF	UINT16 R/W	NO

		Initial setup of open wire detection while OFF for DO1:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO2:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO3:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO4:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO5:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO6:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO7:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO8:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO9:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO10:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO11:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO12:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO13:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO14:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO15:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO16:0=DISABLED	1		

Current FRAM setting for intial and watchdog state for open wire detection while OFF for digital output DOx. This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured

Bit 0: =0:Open wire detection for DO1 is DISABLED, =1:Open wire detection for DO1 is ENABLED

Bit 1: =0:Open wire detection for DO2 is DISABLED, =1:Open wire detection for DO2 is ENABLED

...

Bit 14: =0:Open wire detection for DO15 is DISABLED, =1:Open wire detection for DO15 is ENABLED

Bit 15: =0:Open wire detection for DO16 is DISABLED, =1:Open wire detection for DO16 is ENABLED

Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FRAM

INITIAL & WATCHDOG SETUP ENABLE OPEN WIRE DETECTION WHILE OFF DO17-DO32	3x59010 4x59010 1:59009	????		0xFFFF	UINT16 R/W	NO
		Initial setup of open wire detection while OFF for DO17:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO18:0=DISABLED	1			

		Initial setup of open wire detection while OFF for DO19:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO20:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO21:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO22:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO23:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO24:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO25:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO26:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO27:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO28:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO29:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO30:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO31:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO32:0=DISABLED	1			
<p>Current FRAM setting for intial and watchdog state for open wire detection while OFF for digital output DOx. This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured</p> <p>Bit 0: =0:Open wire detection for DO17 is DISABLED, =1:Open wire detection for DO17 is ENABLED</p> <p>Bit 1: =0:Open wire detection for DO18 is DISABLED, =1:Open wire detection for DO18 is ENABLED</p> <p>...</p> <p>Bit 14: =0:Open wire detection for DO31 is DISABLED, =1:Open wire detection for DO31 is ENABLED</p> <p>Bit 15: =0:Open wire detection for DO32 is DISABLED, =1:Open wire detection for DO32 is ENABLED</p> <p>Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FRAM</p>						
INITIAL & WATCHDOG SETUP ENABLE OPEN WIRE DETECTION WHILE OFF DO33-DO48	3x59011 4x59011 1:59010	????		0xFFFF	UINT16 R/W	NO
		Initial setup of open wire detection while OFF for DO33:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO34:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO35:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO36:0=DISABLED	1			

		Initial setup of open wire detection while OFF for DO37:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO38:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO39:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO40:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO41:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO42:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO43:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO44:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO45:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO46:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO47:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO48:0=DISABLED	1		

Current FRAM setting for initial and watchdog state for open wire detection while OFF for digital output DOx. This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured

Bit 0: =0:Open wire detection for DO33 is DISABLED, =1:Open wire detection for DO33 is ENABLED

Bit 1: =0:Open wire detection for DO34 is DISABLED, =1:Open wire detection for DO34 is ENABLED

...

Bit 14: =0:Open wire detection for DO47 is DISABLED, =1:Open wire detection for DO47 is ENABLED

Bit 15: =0:Open wire detection for DO48 is DISABLED, =1:Open wire detection for DO48 is ENABLED

Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FRAM

INITIAL & WATCHDOG SETUP ENABLE OPEN WIRE DETECTION WHILE OFF DO49-DO60	3x59010 4x59010 1:59009	???		#BEZUG!	UINT16 R/W	NO
		Initial setup of open wire detection while OFF for DO49:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO50:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO51:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO52:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO53:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO54:0=DISABLED	1			



		Initial setup of open wire detection while OFF for DO55:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO56:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO57:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO58:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO59:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO60:0=DISABLED	1		

Current FRAM setting for initial and watchdog state for open wire detection while OFF for digital output DOx. This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured

Bit 0: =0:Open wire detection for DO49 is DISABLED, =1:Open wire detection for DO49 is ENABLED

Bit 1: =0:Open wire detection for DO50 is DISABLED, =1:Open wire detection for DO50 is ENABLED

...

Bit 10: =0:Open wire detection for DO59 is DISABLED, =1:Open wire detection for DO59 is ENABLED

Bit 11: =0:Open wire detection for DO60 is DISABLED, =1:Open wire detection for DO60 is ENABLED

Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FRAM

#### DIGITAL OUTPUTS:ENABLE SHORTCUT DETECTION WHILE OFF

INITIAL & WATCHDOG SETUP ENABLE SHORTCUT TO VDD DETECTION WHILE OFF DO1-DO16	3x59011 4x59011 1:59010	????		0xFFFF	UINT16 R/W	NO
		Initial setup of shortcut to VDD detection while OFF for DO1:0=DISABLED	1			
		Initial setup of shortcut to VDD detection while OFF for DO2:0=DISABLED	1			
		Initial setup of shortcut to VDD detection while OFF for DO3:0=DISABLED	1			
		Initial setup of shortcut to VDD detection while OFF for DO4:0=DISABLED	1			
		Initial setup of shortcut to VDD detection while OFF for DO5:0=DISABLED	1			
		Initial setup of shortcut to VDD detection while OFF for DO6:0=DISABLED	1			
		Initial setup of shortcut to VDD detection while OFF for DO7:0=DISABLED	1			
		Initial setup of shortcut to VDD detection while OFF for DO8:0=DISABLED	1			
		Initial setup of shortcut to VDD detection while OFF for DO9:0=DISABLED	1			
		Initial setup of shortcut to VDD detection while OFF for DO10:0=DISABLED	1			
		Initial setup of shortcut to VDD detection while OFF for DO11:0=DISABLED	1			

		Initial setup of shortcut to VDD detection while OFF for DO12:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO13:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO14:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO15:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO16:0=DISABLED	1		

Current FRAM setting for initial and watchdog state for shortcut to VDD detection while OFF for digital output DOx. This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured

Bit 0: =0:Shortcut detection for DO1 is DISABLED, =1:Shortcut detection for DO1 is ENABLED

Bit 1: =0:Shortcut detection for DO2 is DISABLED, =1:Shortcut detection for DO2 is ENABLED

...

Bit 14: =0:Shortcut detection for DO15 is DISABLED, =1:Shortcut detection for DO15 is ENABLED

Bit 15: =0:Shortcut detection for DO16 is DISABLED, =1:Shortcut detection for DO16 is ENABLED

Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FRAM

INITIAL & WATCHDOG SETUP ENABLE SHORTCUT TO VDD DETECTION WHILE OFF DO17-DO32	3x59012 4x59012 1:59011	????		0xFFFF	UINT16 R/W	NO
		Initial setup of shortcut to VDD detection while OFF for DO17:0=DISABLED	1			
		Initial setup of shortcut to VDD detection while OFF for DO18:0=DISABLED	1			
		Initial setup of shortcut to VDD detection while OFF for DO19:0=DISABLED	1			
		Initial setup of shortcut to VDD detection while OFF for DO20:0=DISABLED	1			
		Initial setup of shortcut to VDD detection while OFF for DO21:0=DISABLED	1			
		Initial setup of shortcut to VDD detection while OFF for DO22:0=DISABLED	1			
		Initial setup of shortcut to VDD detection while OFF for DO23:0=DISABLED	1			
		Initial setup of shortcut to VDD detection while OFF for DO24:0=DISABLED	1			
		Initial setup of shortcut to VDD detection while OFF for DO25:0=DISABLED	1			
		Initial setup of shortcut to VDD detection while OFF for DO26:0=DISABLED	1			
		Initial setup of shortcut to VDD detection while OFF for DO27:0=DISABLED	1			
		Initial setup of shortcut to VDD detection while OFF for DO28:0=DISABLED	1			

		Initial setup of shortcut to VDD detection while OFF for DO29:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO30:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO31:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO32:0=DISABLED	1		
<p>Current FRAM setting for initial and watchdog state for shortcut to VDD detection while OFF for digital output DOx. This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured</p> <p>Bit 0: =0:Shortcut detection for DO17 is DISABLED, =1:Shortcut detection for DO17 is ENABLED</p> <p>Bit 1: =0:Shortcut detection for DO18 is DISABLED, =1:Shortcut detection for DO18 is ENABLED</p> <p>...</p> <p>Bit 14: =0:Shortcut detection for DO31 is DISABLED, =1:Shortcut detection for DO31 is ENABLED</p> <p>Bit 15: =0:Shortcut detection for DO32 is DISABLED, =1:Shortcut detection for DO32 is ENABLED</p> <p>Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FRAM</p>					
INITIAL & WATCHDOG SETUP ENABLE SHORTCUT TO VDD DETECTION WHILE OFF DO33-DO48	3x59013 4x59013 1:59012	????		0xFFFF	UINT16 R/W
		Initial setup of shortcut to VDD detection while OFF for DO33:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO34:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO35:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO36:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO37:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO38:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO39:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO40:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO41:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO42:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO43:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO44:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO45:0=DISABLED	1		

		Initial setup of shortcut to VDD detection while OFF for DO46:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO47:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO48:0=DISABLED	1		
<p>Current FRAM setting for initial and watchdog state for shortcut to VDD detection while OFF for digital output DOx. This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured</p> <p>Bit 0: =0:Shortcut detection for DO33 is DISABLED, =1:Shortcut detection for DO33 is ENABLED            Bit 1: =0:Shortcut detection for DO34 is DISABLED, =1:Shortcut detection for DO34 is ENABLED            ...            Bit 14: =0:Shortcut detection for DO47 is DISABLED, =1:Shortcut detection for DO47 is ENABLED            Bit 15: =0:Shortcut detection for DO48 is DISABLED, =1:Shortcut detection for DO48 is ENABLED</p> <p>Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FRAM</p>					
INITIAL & WATCHDOG SETUP ENABLE SHORTCUT TO VDD DETECTION WHILE OFF DO49-DO60	3x59014 4x59014 1:59013	????		#BEZUG!	UINT16 R/W
		Initial setup of shortcut to VDD detection while OFF for DO49:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO50:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO51:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO52:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO53:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO54:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO55:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO56:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO57:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO58:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO59:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO60:0=DISABLED	1		

Current FRAM setting for initial and watchdog state for shortcut to VDD detection while OFF for digital output DOx. This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured

Bit 0: =0:Shortcut detection for DO49 is DISABLED, =1:Shortcut detection for DO49 is ENABLED

Bit 1: =0:Shortcut detection for DO50 is DISABLED, =1:Shortcut detection for DO50 is ENABLED

...

Bit 10: =0:Shortcut detection for DO59 is DISABLED, =1:Shortcut detection for DO59 is ENABLED

Bit 11: =0:Shortcut detection for DO60 is DISABLED, =1:Shortcut detection for DO60 is ENABLED

Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FRAM

cd