

Command NAME	ASCII command type	ASCII command structure	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
ASCII COMMANDS						
HEART BEAT	ASCII READ COMMAND	#HB<CR> Result: #HB<CR>			ASCII	
	TX	#1,HB<CR>				
	RX	#1,HB<CR>				
Sends an Heartbeat to test the communcation						
GET VERSION	ASCII READ COMMAND	#VERSION<CR> Result: #VERSION:<VersionHi>,<VersionMed>,<VersionLo><CR>			ASCII	
	TX	#1,VERSION<CR>				
	RX	#1,VERSION:1.2.00<CR>				
		Current SW version:1.2.00				
Returns the version number of the module VersionHi: Version number high (1..255) VersionMed: Version number medium (1..255) VersionLo: Version number low (1..255)						
GET TYPE	ASCII READ COMMAND	#TYPE<CR> Result: #TYPE:<Type><CR>			ASCII	
	TX	#1,TYPE<CR>				
	RX	#1,TYPE:RESI-64DI-SIO<CR>				
		Current module type:RESI-64DI-SIO				
Returns the current module type						
GET FEATURES	ASCII READ COMMAND	#FTRS<CR> Result: #FTRS:<Type><CR>			ASCII	
	TX	#1,FTRS<CR>				
	RX	#1,FTRS:RESI-64DI-SIO,RS485,DI:64<CR>				
		Current module type:#1,FTRS:RESI-64DI-SIO,RS485,DI:64<CR>				
Returns the current module features						
GET OWNER	ASCII READ COMMAND	#OWNER<CR> Result: #OWNER:<Owner><CR>			ASCII	
	TX	#1,OWNER<CR>				
	RX	#1,OWNER:RESI<CR>				
		Current owner:RESI				
Returns the current owner of the module						

GET CREATOR	ASCII READ COMMAND	#CREATOR<CR> Result: #CREATOR:<Creator><CR>	ASCII	
	TX	#1,CREATOR<CR>		
	RX	#1,CREATOR:DI HC SIGL,MSC<CR>		
		Current creator:DI HC SIGL,MSC		
Returns the current creator of the module				
GET COPYRIGHT	ASCII READ COMMAND	#COPYRIGHT<CR> Result: #COPYRIGHT:<Copyright><CR>	ASCII	
	TX	#1,COPYRIGHT<CR>		
	RX	#1,COPYRIGHT:2015-24 BY RESI AND DI HC SIGL,MSC WWW.RESI.CC<CR>		
		Current copyright:2015-24 BY RESI AND DI HC SIGL,MSC WWW.RESI.CC		
Returns the current copyright of the module				
GET SERIAL NUMBER	ASCII READ COMMAND	#SN<CR> Result: #SN:<Serial><CR>	ASCII	
	TX	#1,SN<CR>		
	RX	#1,SN:38002D001457435535343920<CR>		
		Current serial number:38002D001457435535343920		
Returns the current serial number of the module				
GET INTERNAL STATUS	ASCII READ COMMAND	#INTSTAT<CR> Result: #INTSTAT:<Status><CR>	ASCII	
	TX	#1,INTSTAT<CR>		
	RX	#1,INTSTAT:I2C1:0,I2C2:0,FRAM:20<CR>		
Returns the device specific internal status				
GET DIP SWITCH	ASCII READ COMMAND	#GDIP<CR> Result: #GDIP:<DIPSwitchDec>,<DIPSwitchHex><CR>	ASCII	
	TX	#1,GDIP<CR>		
	RX	#1,GDIP:65,0x41<CR>		
		Current DIP SWITCH settings:0100.0001		
Returns the current setting of the Dip switches as decimal number and as hexadecimal number. DIPSwitchDec DIPSwitchHex The current value of the DIP switches: Bit 0: DIP Switch 1 (=0:OFF, =1:ON) Bit 1: DIP Switch 2 (=0:OFF, =1:ON) Bit 2: DIP Switch 3 (=0:OFF, =1:ON) Bit 3: DIP Switch 4 (=0:OFF, =1:ON) Bit 4: DIP Switch 5, if available (=0:OFF, =1:ON) Bit 5: DIP Switch 6, if available (=0:OFF, =1:ON) Bit 6: DIP Switch 7, if available (=0:OFF, =1:ON) Bit 7: DIP Switch 8, if available (=0:OFF, =1:ON)				
ASCII COMMANDS				

SET MODBUS ADDRESS	ASCII WRITE COMMAND	#SMBADR:<UNITID> <CR> Result: #OK<CR>	ASCII	NO
	UNITID	123		
	TX	#1,SMBADR:123<CR>		
	RX	N/A		
Redefines the unit ID of the module. This change will affect the MODBUS/RTU communication immediately. As a Unit IO you can use the values 0dec to 255dec.				
HINT: The new settings are activated after a system reboot or power off on cycle!				
SET MODBUS BAUDRATE	ASCII WRITE COMMAND	#SMBBAUD:<BAUD> <CR> Result: #OK<CR>	ASCII	NO
	BAUD	128000:128000BD		
	TX	#1,SMBBAUD:128000<CR>		
	RX	N/A		
Sets a new baud rate in the FLASH For ULTRA SLIM IOs RESI-xxx-SIO: This baudrate is only used, if DIP switch mode DIP1=ON+DIP2=ON (BR) (default is 57600bd) For BIG IOs RESI-xxx-SIO: This baudrate is only used, if DIP switch mode DIP7=ON (PARAMETER) (default is 57600bd) The following baudrates are allowed: 300bd, 600bd, 900bd, 1200bd, 2400bd, 4800bd, 9600bd, 19200bd, 38400bd, 57600bd, 115200bd, 128000bd 230400bd, 250000bd, 256000bd				
HINT: The new setup parameters will be active after a restart of the module.				
SET MODBUS PARITY	ASCII WRITE COMMAND	#SMBPAR:<PARITY> <CR> Result: #OK<CR>	ASCII	NO
	PARITY	ODD:ODD PARITY		
	TX	#1,SMBPAR:ODD<CR>		
	RX	N/A		
Sets a new parity for the serial interface. MBParity: NONE: no parity EVEN: even parity ODD: odd parity				
HINT: The new setup parameters will be active after a restart of the module.				
SET MODBUS STOPS	ASCII WRITE COMMAND	#SMBSTOP:<STOPBIT> <CR> Result: #OK<CR>	ASCII	NO
	STOPBIT	TWO:TWO STOPBITS		
	TX	#1,SMBSTOP:TWO<CR>		
	RX	N/A		

Sets a new amount of stop bits for the serial interface.

MBStops

ONE: one stop bit

TWO: two stop bits

HINT: The new setup parameters will be active after a restart of the module.

SET MODBUS PARAMS	ASCII WRITE COMMAND	#SMBPARAMS:<UNITID>,<BAUD>,<PARITY>,<STOPBIT><CR> Result: #OK<CR>	ASCII	NO
	UNITID	3		
	BAUD	115200:115200BD		
	PARITY	EVEN:EVEN PARITY		
	STOPBIT	TWO:TWO STOPBITS		
	TX	#1,SMBPARAMS:3,115200,EVEN,TWO<CR>		
	RX	N/A		

Sets all parameters for serial interface

GET MODBUS ADDRESS	ASCII READ COMMAND	#GMBADR<CR> Result: #GMBADR:<MBUnitDec>,<MBFLASHDec>,<MBUnitHex>,<MBFLASHHex><CR>	ASCII	
	TX	#1,GMBADR<CR>		
	RX	#1,GMBADR:1,15,0x1,0xF<CR>		
		Current MODBUS unit ID:1,15,0x1,0xF		

Shows the current used MODBUS/RTU or ASCII unit address and shows also the stored unit address in the FLASH memory, which is only used if the DIP switch for the bus address is set to 0.

MBUnitDec,MBUnitHex

The current used MODBUS/RTU unit or ASCII address for communication

MBFLASHDec,MBFLASHHex

The internal stored MODBUS/RTU unit address or ASCII address from the FLASH memory, if the DIP switch DIP3 is OFF.

GET MODBUS BAUDRATE	ASCII READ COMMAND	#GMBBAUD<CR> Result: #GMBBAUD:<BaudRate><CR>	ASCII	
	TX	#1,GMBBAUD<CR>		
	RX	#1,GMBBAUD:115200,0x1C200<CR>		
		Current baudrate:115200,0x1C200		

This is the current configured baud rate in the FLASH

For ULTRA SLIM IOs RESI-xxx-SIO: This baudrate is only used, if DIP switch mode DIP1=ON+DIP2=ON (BR) (default is 57600bd)

For BIG IOs RESI-xxx-SIO: This baudrate is only used, if DIP switch mode DIP7=ON (PARAMETER) (default is 57600bd)

The following baudrates are allowed:

300bd, 600bd, 900bd, 1200bd, 2400bd, 4800bd,

9600bd, 19200bd, 38400bd, 57600bd, 115200bd, 128000bd

230400bd, 250000bd, 256000bd

GET MODBUS PARITY	ASCII READ COMMAND	#GMBPAR<CR> Result: #GMBPAR:<MBParity><CR>	ASCII	
	TX	#1,GMBPAR<CR>		
	RX	#1,GMBPAR:NONE<CR>		

		Current parity:NONE		
Shows the current configured parity of the serial interface. MBParity NONE: no parity EVEN: even parity ODD: odd parity				
GET MODBUS STOP	ASCII READ COMMAND	#GMBSTOP<CR> Result: #GMBSTOP:<MBStop> <CR>	ASCII	
	TX	#1,GMBSTOP<CR>		
	RX	#1,GMBPAR:ONE<CR>		
		Current stopbit(s):ONE		
Shows the current configured parity of the serial interface. MBParity NONE: no parity EVEN: even parity ODD: odd parity				
GET MODBUS PARAMS	ASCII READ COMMAND	#GMBPARAMS<CR> Result: #GMBPARAMS:<MBUnitDec>,<MBFLASHDec>,<MBUnitHex>,<MBFLASHHex>,<MBBaudrateDec>,<MBBaudrateHex>,<MBParity>,<MBStops> <CR>	ASCII	
	TX	#1,GMBPARAMS<CR>		
	RX	#1,GMBPARAMS:1,0x1,15,0xF,115200,0x1C200,NONE,ONE<CR>		
		Current MODBUS unit ID used:1		
		Current MODBUS unit ID in FLASH:15		
		Current baudrate in FLASH:115200		
		Current parity in FLASH:NONE		
		Current stopbit(s) in FLASH:ONE		
Returns the complete settings for serial interface				
ASCII COMMANDS				
RESET	ASCII WRITE COMMAND	#RST<CR> Result: #OK<CR>	ASCII	NO
	TX	#1,RST<CR>		
	RX	N/A		
Executes a software reset (Reboot) of the module.				
FACTORY RESET	ASCII WRITE COMMAND	#FRST<CR> Result: #OK<CR>	ASCII	NO
	TX	#1,FRST<CR>		
	RX	N/A		
Performs a factory reset of all internal saved parameters				
SET MODBUS WATCHDOG TIMER	ASCII WRITE COMMAND	#SMBWATCHDOG:<WDTIME> <CR> Result: #OK<CR>	ASCII	YES

	WDTIME	100		
	TX	#1,SMBWATCHDOG:100<CR>		
	RX	#1,OK<CR>		
Enables or disables the WATCHDOG Timer for the IO module. WDTIME: 1..65535: Time for Watchdog in 1/100s =0: Watchdog is deactivated HINT: The Watchdog is internally handled every 100ms. If the IO module receives no valid frame within this time period, the outputs are set to predefined values!				
GET MODBUS WATCHDOG TIMER	ASCII READ COMMAND	#GMBWATCHDOG<CR> Result: #GMBWATCHDOG:<WDTIME> <CR>	ASCII	
	TX	#1,GMBWATCHDOG<CR>		
	RX	#1,GMBWATCHDOG:0,0x0<CR>		
		Current watchdog time:0 -> 0,0s		
Shows the actual configured time for the telegram watchdog function of the IO module. WDTIME: 1..65535: Time for Watchdog in 1/100s =0: Watchdog is deactivated HINT: The Watchdog is internally handled every 100ms. If the IO module receives no valid frame within this time period, the outputs are set to predefined values!				
CPU PARAMETERS				
GET CPU VOLTAGE	ASCII READ COMMAND	#GCPUTEMP<CR> Result: #GCPUTEMP:<CPUTemp> <CR>	ASCII	
	TX	#1,GCPUTEMP<CR>		
	RX	#1,GCPUTEMP:41.0951<CR>		
		Current internal temperature of CPU:41.0951°C		
Current internal temperature of CPU in ° Celsius.				
GET CPU VOLTAGE	ASCII READ COMMAND	#GCPUVOLT<CR> Result: #GCPUVOLT:<CPUVoltage> <CR>	ASCII	
	TX	#1,GCPUVOLT<CR>		
	RX	#1,GCPUVOLT:3.3908<CR>		
		Current supply voltage of CPU:3.3908V		
Current internal supply voltage of CPU in Volt.				
GET CPU BACKUP	ASCII READ COMMAND	#GCPUBACK<CR> Result: #GCPUBACK:<CPUBackupVoltage> <CR>	ASCII	
	TX	#1,GCPUBACK<CR>		
	RX	#1,GCPUBACK:0.0004<CR>		
		Current backup voltage of CPU:0.0004V		
Current internal backup capacitor voltage of CPU in Volt.				

Register NAME	MODBUS Register	Register VALUE	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
PRODUCT DATA						
HW_GROUP	3x65201 4x65201 I:65200	16384,0x4000 B:40 00			UINT16 R/O	
This is the group of hardware of the current product						
SW_GROUP	3x65202 4x65202 I:65201	32773,0x8005 B:80 05			UINT16 R/O	
This is the group of software of the current product						
SW_VERSION	3x65203 4x65203 I:65202	4608,0x1200 B:12 00			UINT16 R/O	
		SW VERSION:1.2.0				
This is the current software version of the firmware						
SW_AUTHOR	3x65204 4x65204 I:65203	18771,0x4953 B:49 53			UINT16 R/O	
This is the current software author of the firmware						
MODBUS SETTINGS						
UNIT_ID	3x65222 4x65222 I:65221	1,0x0001 B:00 01			UINT16 R/O	
		UNIT ID:1				
If the host reads this register, the current defined unit ID is returned.						
FLASH UNIT_ID	3x65223 4x65223 I:65222	15,0x000F B:00 0F		27	UINT16 R/W	NO
		UNIT ID:15				
If the host reads this register, the current defined unit ID from the FLASH is returned. This UnitID is used if DIP switch for UnitID is set to 15						
HINT:This settings will be active after you repower or reset your device !!						
BAUD_RATE	3x65224 4x65224 I:65223	115200,0x0001C200 B:00 01 C2 00	57600	57600	UINT32 R/W	NO
		115200Bd		ENTER BAUD RATE		
This is the current configured baud rate in the FLASH For ULTRA SLIM IOs RESI-xxx-SIO: This baudrate is only used, if DIP switch mode DIP1=ON+DIP2=ON (BR) (default is 57600bd) For BIG IOs RESI-xxx-SIO: This baudrate is only used, if DIP switch mode DIP7=ON (PARAMETER) (default is 57600bd)						

Valid baud rates are:

300bd, 600bd, 900bd, 1200bd, 2400bd, 4800bd,
9600bd, 19200bd, 38400bd, 57600bd, 115200bd, 128000bd
230400bd, 250000bd, 256000bd

HINT:This settings will be active after you repower or reset your device !!

PARITY	3x65226 4x65226 I:65225	0,0x0000 B:00 00		1:EVEN PARITY	UINT16 R/W	NO
		NO PARITY		SELECT PARITY		

If the register is read out, the currently set parity of the serial interface is returned.

Writing a value to this register will change the new parity in FLASH. This will only take effect after a restart of the module. This can be triggered by writing to the RESET SYSTEM register.

Parity values are

0: no parity

1: even parity

2: odd parity

STOP BITS	3x65227 4x65227 I:65226	1,0x0001 B:00 01		2:TWO STOPBITS	UINT16 R/W	NO
		ONE STOPBIT		SELECT STOPBITS		

If the register is read out, the currently set number of stop bits of the serial interface is returned.

Writing a value to this register will change the new number of stop bits in the FLASH. This will only take effect after a restart of the module. This can be triggered by writing to the RESET SYSTEM register.

Values for stop bits are

1: one stop bit

2: two stop bits

MODBUS TIMING	3x65228 4x65228 I:65227	0,0x0000 B:00 00		10	UINT16 R/W	NO
		Actual timing:0ms				

If the host reads this register, the current defined timing for MODBUS telegrams is returned. This timing is a time in ms which extends the standard 1.5 character timeout between two consecutive bytes on the serial line.

If you write a new value to this register, the new settings are stored into the internal FLASH. Reboot the device to activate the new settings.

MODBUS WATCHDOG TIME	3x65229 4x65229 I:65228	0,0x0000 B:00 00		50	UINT16 R/W	YES
		Actual watchdog time in 1/100s:0 -> 0,0s				

Writing a value onto this register defines a new time for the internal communication watchdog timer. The value is a timespan in 1/100s.

=0: The communication watchdog is disabled

=1..65535: Communication watchdog will be triggered after x 1/100s pause on communication line

In case of an communication watchdog, the module sets all outputs to the states defined in the configuration output registers

Reading this register will return the current stored time from the internal FLASH

CPU DATA

SERIAL1	3x65521 4x65521 I:65520	56,0x0038 B:00 38			UINT16 R/O	
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Serial number of module as 96 bit unsigned integer number						
SERIAL2	3x65522 4x65522 I:65521	45,0x002D B:00 2D			UINT16 R/O	
SERIAL3	3x65523 4x65523 I:65522	22292,0x5714 B:57 14			UINT16 R/O	
SERIAL4	3x65524 4x65524 I:65523	21827,0x5543 B:55 43			UINT16 R/O	
SERIAL5	3x65525 4x65525 I:65524	13365,0x3435 B:34 35			UINT16 R/O	
SERIAL6	3x65526 4x65526 I:65525	8249,0x2039 B:20 39			UINT16 R/O	
	SERIAL:38002D001457435535343920					
Serial number of module as 96 bit unsigned integer number						
CPU TEMPERATURE	3x65527 4x65527 I:65526	4115,0x1013 B:10 13			UINT16 R/O	
	Current internal temperature of CPU:41,15°C					
Current internal temperature of CPU in ° Celsius multiplied by 100.						
CPU VOLTAGE	3x65528 4x65528 I:65527	339,0x0153 B:01 53			UINT16 R/O	
	Current supply voltage of CPU:3,39V					
Current internal supply voltage of CPU in Volt multiplied by 100.						
CPU BACKUP VOLTAGE	3x65529 4x65529 I:65528	0,0x0000 B:00 00			UINT16 R/O	
	Current backup voltage of CPU:0,00V					
Current internal backup capacitor voltage of CPU in Volt multiplied by 100.						
DIP SWITCH STATUS						
DIP SWITCH	3x65300 4x65300 I:65299	65,0x0041 B:00 41			UINT16 R/O	
Returns the current setting of the Dip switches. Bit 0: DIP Switch 1 (=0:OFF, =1:ON) Bit 1: DIP Switch 2 (=0:OFF, =1:ON) Bit 2: DIP Switch 3 (=0:OFF, =1:ON) Bit 3: DIP Switch 4 (=0:OFF, =1:ON) Bit 4: DIP Switch 5 (=0:OFF, =1:ON) Bit 5: DIP Switch 6 (=0:OFF, =1:ON) Bit 6: DIP Switch 7 (=0:OFF, =1:ON) Bit 7: DIP Switch 8 (=0:OFF, =1:ON)						

CONVERTER STATUS						
CONVERTER STATUS	3x65534 4x65534 l:65533	0,0x0000 B:00 00			UINT16 R/O	
Current status of the converter						
FACTORY RESET	3x65535 4x65535 l:65534	0,0x0000 B:00 00		1:PERFORM FACTORY RESET	UINT16 R/W	NO
Performs a factory reset of all internal saved parameters						
SOFTWARE RESET						
RESET	1x65536 2x65536 l:65535	0,0x00 B:00		N/A:NO CHANGE	BIT R/W	NO
Performs a software reset, whenever 1 is written to this register. If the host writes to this register 1, the module executes a soft reset (reboot).						
RESET	3x65536 4x65535 l:65535	0,0x0000 B:00 00		N/A:NO CHANGE	UINT16 R/W	NO
Performs a software reset, whenever 1 is written to this register. If the host writes to this register 1, the module executes a soft reset (reboot).						

Command NAME	ASCII command type	ASCII command structure	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
ASCII COMMANDS						
DIGITAL INPUTS						
GET DIGITAL INPUTS	ASCII READ COMMAND	#GDIS<CR> Result: #GDIS:<DIS1Dec>,<DIS2Dec>,<DIS3Dec>,<DIS4Dec>,<DIS1Hex>,<DIS2Hex>,<DIS3Hex>,<DIS4Hex><CR>			ASCII	
	TX	#1,GDIS<CR>				
	RX	#1,GDIS:0,2147483648,0,2147483648,0x0,0x80000000,0x0,0x80000000<CR>				
		Actual status of digital inputs DI1-DI32:0000.0000.0000.0000.0000.0000.0000.0000				
		Actual status of digital inputs DI33-DI64:1000.0000.0000.0000.0000.0000.0000.0000				
		Actual status of digital inputs DI65-DI96:0000.0000.0000.0000.0000.0000.0000.0000				
		Actual status of digital inputs DI97-DI128:1000.0000.0000.0000.0000.0000.0000.0000				
Returns the actual state of all digital inputs as decimal number and as hexadecimal number. DIS1Dec, DIS1Hex The current state of all digital inputs: Bit 0: State of DI1 (=0:OFF, =1:ON) Bit 1: State of DI2 (=0:OFF, =1:ON) Bit 2: State of DI3 (=0:OFF, =1:ON) ... Bit 29: State of DI30 (=0:OFF, =1:ON) Bit 30: State of DI31 (=0:OFF, =1:ON) Bit 31: State of DI32 (=0:OFF, =1:ON)						
DIS2Dec, DIS2Hex The current state of all digital inputs: Bit 0: State of DI33 (=0:OFF, =1:ON) Bit 1: State of DI34 (=0:OFF, =1:ON) Bit 2: State of DI35 (=0:OFF, =1:ON) ... Bit 29: State of DI62 (=0:OFF, =1:ON) Bit 30: State of DI63 (=0:OFF, =1:ON) Bit 31: State of DI64 (=0:OFF, =1:ON)						
DIS3Dec, DIS3Hex The current state of all digital inputs: Bit 0: State of DI65 (=0:OFF, =1:ON) Bit 1: State of DI66 (=0:OFF, =1:ON) Bit 2: State of DI67 (=0:OFF, =1:ON) ... Bit 29: State of DI94 (=0:OFF, =1:ON) Bit 30: State of DI95 (=0:OFF, =1:ON) Bit 31: State of DI96 (=0:OFF, =1:ON)						

[illegible]

		Actual counter for changes on DI120:0		
		Actual counter for changes on DI121:0		
		Actual counter for changes on DI122:0		
		Actual counter for changes on DI123:0		
		Actual counter for changes on DI124:0		
		Actual counter for changes on DI125:0		
		Actual counter for changes on DI126:0		
		Actual counter for changes on DI127:0		
		Actual counter for changes on DI128:1		
<PART>: 1..8, 1=DI1-DI16, 2=DI17-DI32, 3=DI33-DI48, 4=DI49-DI64, 5=DI65-DI80, 6=DI81-DI96, 7=DI97-DI112, 8=DI113-DI128				
Returns for each digital input the counter for changes. As soon as the module detects a signal change on a digital input, the change counter for the affected digital input is incremented by 1. A signal change can be: Detection of a short keypress Detection of the start of a long keypress Detection of a release of a long keypress				
The parameter <PART> defines the part of the digital inputs. The command returns maximal 16 digital inputs.				
CHANGE DIx	ASCII READ COMMAND	#CDI<DINR> <CR> Result: #CDI<DINR>:<ChangesDec>,<ChangesHex> <CR>	ASCII	
	DINR	1		
	TX	#1,CDI1<CR>		
	RX	#1,CDI1:0,0x0<CR>		
		Actual counter for changes on digital input DI1:0		
<DINR>: 1=DI1..128=DI128				
Returns for digital input <DINR> the counter for signal changes. As soon as the module detects a signal change on a digital input, the change counter for the affected digital input is incremented by 1. A signal change can be: Detection of a short keypress Detection of the start of a long keypress Detection of a release of a long keypress				
SHORT KEY ALL DIS PART x	ASCII READ COMMAND	#SKADISP<PART> <CR> Result: #SKADISP<PART>:<ShortKeyDInDec>,...,<ShortKeyDIn+15Dec>, <ShortKeyDInHex>,...,<ShortKeyDIn+15Hex> <CR>	ASCII	
	PART	8		
	TX	#1,SKADISP8<CR>		
	RX	#1,SKADISP4:0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0<CR>		
		Actual counter for short keypress events on DI113:0		
		Actual counter for short keypress events on DI114:0		
		Actual counter for short keypress events on DI115:0		
		Actual counter for short keypress events on DI116:0		
		Actual counter for short keypress events on DI117:0		
		Actual counter for short keypress events on DI118:0		
		Actual counter for short keypress events on DI119:0		

[illegible]

		Actual counter for long keypress start events on DI125:0		
		Actual counter for long keypress start events on DI126:0		
		Actual counter for long keypress start events on DI127:0		
		Actual counter for long keypress start events on DI128:1		
<PART>: 1..8, 1=DI1-DI16, 2=DI17-DI32, 3=DI33-DI48, 4=DI49-DI64, 5=DI65-DI80, 6=DI81-DI96, 7=DI97-DI112, 8=DI113-DI128				
Returns for each digital input the counter for long keypress start events. As soon as the module detects the start of a long keypress on a digital input, the counter for the affected digital input is incremented by 1.				
The parameter <PART> defines the part of the digital inputs. The command returns maximal 16 digital inputs.				
LONG KEY START DiX	ASCII READ COMMAND	#LKSDI<DINR> <CR> Result: #LKSDI<DINR>:<LongKeyStartDec>,<LongKeyStartHex> <CR>	ASCII	
	DINR	1		
	TX	#1,LKSDI1<CR>		
	RX	#1,LKSDI1:0,0x0<CR>		
		Actual counter for long keypress start events on digital input DI1:0		
<DINR>: 1=DI1..128=DI128				
Returns for digital input <DINR> the counter for long keypress start events. As soon as the module detects the start of a long keypress on a digital input, the counter for the affected digital input is incremented by 1.				
LONG KEY END ALL DIS PART x	ASCII READ COMMAND	#LKEADISP<PART> <CR> Result: #LKEADISP<PART>:<LongKeyEndDInDec>,...,<LongKeyEndDIn+15Dec>, <LongKeyEndDInHex>,...,<LongKeyEndDIn+15Hex> <CR>	ASCII	
	PART	8		
	TX	#1,LKEADISP8<CR>		
	RX	#1,LKEADISP4:0,0,0,0,0,0,0,0,0,0,0,0,0,0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0<CR>		
		Actual counter for long keypress end events on DI113:0		
		Actual counter for long keypress end events on DI114:0		
		Actual counter for long keypress end events on DI115:0		
		Actual counter for long keypress end events on DI116:0		
		Actual counter for long keypress end events on DI117:0		
		Actual counter for long keypress end events on DI118:0		
		Actual counter for long keypress end events on DI119:0		
		Actual counter for long keypress end events on DI120:0		
		Actual counter for long keypress end events on DI121:0		
		Actual counter for long keypress end events on DI122:0		
		Actual counter for long keypress end events on DI123:0		
		Actual counter for long keypress end events on DI124:0		
		Actual counter for long keypress end events on DI125:0		
		Actual counter for long keypress end events on DI126:0		
		Actual counter for long keypress end events on DI127:0		
		Actual counter for long keypress end events on DI128:0		
<PART>: 1..8, 1=DI1-DI16, 2=DI17-DI32, 3=DI33-DI48, 4=DI49-DI64, 5=DI65-DI80, 6=DI81-DI96, 7=DI97-DI112, 8=DI113-DI128				

The parameter <PART> defines the part of the digital inputs. The command returns maximal 16 digital inputs.

<DINR>: 1=D11..128=D1128

RISE ALL DIS	ASCII	#RADISP<PART><CR>	ASCII	
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<PART>: 1.8, 1=DI1-DI16, 2=DI17-DI32, 3=DI33-DI48, 4=DI49-DI64, 5=DI65-DI80, 6=DI81-DI96, 7=DI97-DI112, 8=DI113-DI128

The parameter <PART> defines the part of the digital inputs. The command returns maximal 16 digital inputs.

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Returns for digital input <DINR> the counter for falling edges.

As soon as the module detects a falling edge on a digital input, the falling edge counter for the affected digital input is incremented by 1.

RESET COUNTERS	ASCII WRITE COMMAND	#RC<CR> Result: #OK<CR>	ASCII	NO
	TX	#1,RC<CR>		
	RX	N/A		

Resets all internal counters for digital inputs and events on this digital inputs to 0.

DIGITAL INPUTS EVENTS

EVENTS ON	ASCII WRITE COMMAND	#EVTON<CR> Result: #OK<CR>	ASCII	NO
	TX	#1,EVTON<CR>		
	RX	#1,OK<CR>		

Activates event sending of changes on digital inputs

Whenever a change is detected on the digital inputs, the IO module sends immediately

#<BusAdr>,EVT:DIS:<AlIDISasDec>,<AlIDISasHex> <CR>

EVENTS OFF	ASCII WRITE COMMAND	#EVTOFF<CR> Result: #OK<CR>	ASCII	NO
	TX	#1,EVTOFF<CR>		
	RX	#1,OK<CR>		

Deactivates event sending of changes on digital inputs

Whenever a change is detected on the digital inputs, the IO module sends immediately

#<BusAdr>,EVT:DIS:<AlIDISasDec>,<AlIDISasHex> <CR>

Register NAME Command NAME	MODBUS Register ASCII Command	Register VALUE ASCII Command	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
STATUS DIGITAL INPUTS						
DI1	1x00001 2x00001 I:0	0,0x00 B:00			BIT R/O	
		Actual state of DI1:0=OFF				
Current state of the digital input DIx =0:DI is OFF, =1:DI is ON						
DI2	1x00002 2x00002 I:1	0,0x00 B:00			BIT R/O	
		Actual state of DI2:0=OFF				
DI3	1x00003 2x00003 I:2	0,0x00 B:00			BIT R/O	
		Actual state of DI3:0=OFF				
DI4	1x00004 2x00004 I:3	0,0x00 B:00			BIT R/O	
		Actual state of DI4:0=OFF				
DI5	1x00005 2x00005 I:4	0,0x00 B:00			BIT R/O	
		Actual state of DI5:0=OFF				
DI6	1x00006 2x00006 I:5	0,0x00 B:00			BIT R/O	
		Actual state of DI6:0=OFF				
DI7	1x00007 2x00007 I:6	0,0x00 B:00			BIT R/O	
		Actual state of DI7:0=OFF				
DI8	1x00008 2x00008 I:7	0,0x00 B:00			BIT R/O	
		Actual state of DI8:0=OFF				
DI9	1x00009 2x00009 I:8	0,0x00 B:00			BIT R/O	
		Actual state of DI9:0=OFF				

DI10	1x00010 2x00010 I:9	0,0x00 B:00			BIT R/O	
		Actual state of DI10:0=OFF				
DI11	1x00011 2x00011 I:10	0,0x00 B:00			BIT R/O	
		Actual state of DI11:0=OFF				
DI12	1x00012 2x00012 I:11	0,0x00 B:00			BIT R/O	
		Actual state of DI12:0=OFF				
DI13	1x00013 2x00013 I:12	0,0x00 B:00			BIT R/O	
		Actual state of DI13:0=OFF				
DI14	1x00014 2x00014 I:13	0,0x00 B:00			BIT R/O	
		Actual state of DI14:0=OFF				
DI15	1x00015 2x00015 I:14	0,0x00 B:00			BIT R/O	
		Actual state of DI15:0=OFF				
DI16	1x00016 2x00016 I:15	0,0x00 B:00			BIT R/O	
		Actual state of DI16:0=OFF				
DI17	1x00017 2x00017 I:16	0,0x00 B:00			BIT R/O	
		Actual state of DI17:0=OFF				
DI18	1x00018 2x00018 I:17	0,0x00 B:00			BIT R/O	
		Actual state of DI18:0=OFF				
DI19	1x00019 2x00019 I:18	0,0x00 B:00			BIT R/O	
		Actual state of DI19:0=OFF				
DI20	1x00020 2x00020 I:19	0,0x00 B:00			BIT R/O	
		Actual state of DI20:0=OFF				

DI21	1x00021 2x00021 I:20	0,0x00 B:00			BIT R/O	
		Actual state of DI21:0=OFF				
DI22	1x00022 2x00022 I:21	0,0x00 B:00			BIT R/O	
		Actual state of DI22:0=OFF				
DI23	1x00023 2x00023 I:22	0,0x00 B:00			BIT R/O	
		Actual state of DI23:0=OFF				
DI24	1x00024 2x00024 I:23	0,0x00 B:00			BIT R/O	
		Actual state of DI24:0=OFF				
DI25	1x00025 2x00025 I:24	0,0x00 B:00			BIT R/O	
		Actual state of DI25:0=OFF				
DI26	1x00026 2x00026 I:25	0,0x00 B:00			BIT R/O	
		Actual state of DI26:0=OFF				
DI27	1x00027 2x00027 I:26	0,0x00 B:00			BIT R/O	
		Actual state of DI27:0=OFF				
DI28	1x00028 2x00028 I:27	0,0x00 B:00			BIT R/O	
		Actual state of DI28:0=OFF				
DI29	1x00029 2x00029 I:28	0,0x00 B:00			BIT R/O	
		Actual state of DI29:0=OFF				
DI30	1x00030 2x00030 I:29	0,0x00 B:00			BIT R/O	
		Actual state of DI30:0=OFF				
DI31	1x00031 2x00031 I:30	0,0x00 B:00			BIT R/O	
		Actual state of DI31:0=OFF				

DI32	1x00032 2x00032 I:31	0,0x00 B:00			BIT R/O	
		Actual state of DI32:0=OFF				
DI33	1x00033 2x00033 I:32	0,0x00 B:00			BIT R/O	
		Actual state of DI33:0=OFF				
DI34	1x00034 2x00034 I:33	0,0x00 B:00			BIT R/O	
		Actual state of DI34:0=OFF				
DI35	1x00035 2x00035 I:34	0,0x00 B:00			BIT R/O	
		Actual state of DI35:0=OFF				
DI36	1x00036 2x00036 I:35	0,0x00 B:00			BIT R/O	
		Actual state of DI36:0=OFF				
DI37	1x00037 2x00037 I:36	0,0x00 B:00			BIT R/O	
		Actual state of DI37:0=OFF				
DI38	1x00038 2x00038 I:37	0,0x00 B:00			BIT R/O	
		Actual state of DI38:0=OFF				
DI39	1x00039 2x00039 I:38	0,0x00 B:00			BIT R/O	
		Actual state of DI39:0=OFF				
DI40	1x00040 2x00040 I:39	0,0x00 B:00			BIT R/O	
		Actual state of DI40:0=OFF				
DI41	1x00041 2x00041 I:40	0,0x00 B:00			BIT R/O	
		Actual state of DI41:0=OFF				
DI42	1x00042 2x00042 I:41	0,0x00 B:00			BIT R/O	
		Actual state of DI42:0=OFF				

DI43	1x00043 2x00043 I:42	0,0x00 B:00			BIT R/O	
		Actual state of DI43:0=OFF				
DI44	1x00044 2x00044 I:43	0,0x00 B:00			BIT R/O	
		Actual state of DI44:0=OFF				
DI45	1x00045 2x00045 I:44	0,0x00 B:00			BIT R/O	
		Actual state of DI45:0=OFF				
DI46	1x00046 2x00046 I:45	0,0x00 B:00			BIT R/O	
		Actual state of DI46:0=OFF				
DI47	1x00047 2x00047 I:46	0,0x00 B:00			BIT R/O	
		Actual state of DI47:0=OFF				
DI48	1x00048 2x00048 I:47	0,0x00 B:00			BIT R/O	
		Actual state of DI48:0=OFF				
DI49	1x00049 2x00049 I:48	0,0x00 B:00			BIT R/O	
		Actual state of DI49:0=OFF				
DI50	1x00050 2x00050 I:49	0,0x00 B:00			BIT R/O	
		Actual state of DI50:0=OFF				
DI51	1x00051 2x00051 I:50	0,0x00 B:00			BIT R/O	
		Actual state of DI51:0=OFF				
DI52	1x00052 2x00052 I:51	0,0x00 B:00			BIT R/O	
		Actual state of DI52:0=OFF				
DI53	1x00053 2x00053 I:52	0,0x00 B:00			BIT R/O	
		Actual state of DI53:0=OFF				

DI54	1x00054 2x00054 I:53	0,0x00 B:00			BIT R/O	
		Actual state of DI54:0=OFF				
DI55	1x00055 2x00055 I:54	0,0x00 B:00			BIT R/O	
		Actual state of DI55:0=OFF				
DI56	1x00056 2x00056 I:55	0,0x00 B:00			BIT R/O	
		Actual state of DI56:0=OFF				
DI57	1x00057 2x00057 I:56	0,0x00 B:00			BIT R/O	
		Actual state of DI57:0=OFF				
DI58	1x00058 2x00058 I:57	0,0x00 B:00			BIT R/O	
		Actual state of DI58:0=OFF				
DI59	1x00059 2x00059 I:58	0,0x00 B:00			BIT R/O	
		Actual state of DI59:0=OFF				
DI60	1x00060 2x00060 I:59	0,0x00 B:00			BIT R/O	
		Actual state of DI60:0=OFF				
DI61	1x00061 2x00061 I:60	0,0x00 B:00			BIT R/O	
		Actual state of DI61:0=OFF				
DI62	1x00062 2x00062 I:61	0,0x00 B:00			BIT R/O	
		Actual state of DI62:0=OFF				
DI63	1x00063 2x00063 I:62	0,0x00 B:00			BIT R/O	
		Actual state of DI63:0=OFF				
DI64	1x00064 2x00064 I:63	0,0x00 B:00			BIT R/O	
		Actual state of DI64:0=OFF				

DI65	1x00065 2x00065 I:64	0,0x00 B:00			BIT R/O	
		Actual state of DI65:0=OFF				
DI66	1x00066 2x00066 I:65	0,0x00 B:00			BIT R/O	
		Actual state of DI66:0=OFF				
DI67	1x00067 2x00067 I:66	0,0x00 B:00			BIT R/O	
		Actual state of DI67:0=OFF				
DI68	1x00068 2x00068 I:67	0,0x00 B:00			BIT R/O	
		Actual state of DI68:0=OFF				
DI69	1x00069 2x00069 I:68	0,0x00 B:00			BIT R/O	
		Actual state of DI69:0=OFF				
DI70	1x00070 2x00070 I:69	0,0x00 B:00			BIT R/O	
		Actual state of DI70:0=OFF				
DI71	1x00071 2x00071 I:70	0,0x00 B:00			BIT R/O	
		Actual state of DI71:0=OFF				
DI72	1x00072 2x00072 I:71	0,0x00 B:00			BIT R/O	
		Actual state of DI72:0=OFF				
DI73	1x00073 2x00073 I:72	0,0x00 B:00			BIT R/O	
		Actual state of DI73:0=OFF				
DI74	1x00074 2x00074 I:73	0,0x00 B:00			BIT R/O	
		Actual state of DI74:0=OFF				
DI75	1x00075 2x00075 I:74	0,0x00 B:00			BIT R/O	
		Actual state of DI75:0=OFF				

DI76	1x00076 2x00076 I:75	0,0x00 B:00			BIT R/O	
		Actual state of DI76:0=OFF				
DI77	1x00077 2x00077 I:76	0,0x00 B:00			BIT R/O	
		Actual state of DI77:0=OFF				
DI78	1x00078 2x00078 I:77	0,0x00 B:00			BIT R/O	
		Actual state of DI78:0=OFF				
DI79	1x00079 2x00079 I:78	0,0x00 B:00			BIT R/O	
		Actual state of DI79:0=OFF				
DI80	1x00080 2x00080 I:79	0,0x00 B:00			BIT R/O	
		Actual state of DI80:0=OFF				
DI81	1x00081 2x00081 I:80	0,0x00 B:00			BIT R/O	
		Actual state of DI81:0=OFF				
DI82	1x00082 2x00082 I:81	0,0x00 B:00			BIT R/O	
		Actual state of DI82:0=OFF				
DI83	1x00083 2x00083 I:82	0,0x00 B:00			BIT R/O	
		Actual state of DI83:0=OFF				
DI84	1x00084 2x00084 I:83	0,0x00 B:00			BIT R/O	
		Actual state of DI84:0=OFF				
DI85	1x00085 2x00085 I:84	0,0x00 B:00			BIT R/O	
		Actual state of DI85:0=OFF				
DI86	1x00086 2x00086 I:85	0,0x00 B:00			BIT R/O	
		Actual state of DI86:0=OFF				

DI87	1x00087 2x00087 I:86	0,0x00 B:00			BIT R/O	
		Actual state of DI87:0=OFF				
DI88	1x00088 2x00088 I:87	0,0x00 B:00			BIT R/O	
		Actual state of DI88:0=OFF				
DI89	1x00089 2x00089 I:88	0,0x00 B:00			BIT R/O	
		Actual state of DI89:0=OFF				
DI90	1x00090 2x00090 I:89	0,0x00 B:00			BIT R/O	
		Actual state of DI90:0=OFF				
DI91	1x00091 2x00091 I:90	0,0x00 B:00			BIT R/O	
		Actual state of DI91:0=OFF				
DI92	1x00092 2x00092 I:91	0,0x00 B:00			BIT R/O	
		Actual state of DI92:0=OFF				
DI93	1x00093 2x00093 I:92	0,0x00 B:00			BIT R/O	
		Actual state of DI93:0=OFF				
DI94	1x00094 2x00094 I:93	0,0x00 B:00			BIT R/O	
		Actual state of DI94:0=OFF				
DI95	1x00095 2x00095 I:94	0,0x00 B:00			BIT R/O	
		Actual state of DI95:0=OFF				
DI96	1x00096 2x00096 I:95	0,0x00 B:00			BIT R/O	
		Actual state of DI96:0=OFF				
DI97	1x00097 2x00097 I:96	0,0x00 B:00			BIT R/O	
		Actual state of DI97:0=OFF				

DI98	1x00098 2x00098 I:97	0,0x00 B:00			BIT R/O	
		Actual state of DI98:0=OFF				
DI99	1x00099 2x00099 I:98	0,0x00 B:00			BIT R/O	
		Actual state of DI99:0=OFF				
DI100	1x00100 2x00100 I:99	0,0x00 B:00			BIT R/O	
		Actual state of DI100:0=OFF				
DI101	1x00101 2x00101 I:100	0,0x00 B:00			BIT R/O	
		Actual state of DI101:0=OFF				
DI102	1x00102 2x00102 I:101	0,0x00 B:00			BIT R/O	
		Actual state of DI102:0=OFF				
DI103	1x00103 2x00103 I:102	0,0x00 B:00			BIT R/O	
		Actual state of DI103:0=OFF				
DI104	1x00104 2x00104 I:103	0,0x00 B:00			BIT R/O	
		Actual state of DI104:0=OFF				
DI105	1x00105 2x00105 I:104	0,0x00 B:00			BIT R/O	
		Actual state of DI105:0=OFF				
DI106	1x00106 2x00106 I:105	0,0x00 B:00			BIT R/O	
		Actual state of DI106:0=OFF				
DI107	1x00107 2x00107 I:106	0,0x00 B:00			BIT R/O	
		Actual state of DI107:0=OFF				
DI108	1x00108 2x00108 I:107	0,0x00 B:00			BIT R/O	
		Actual state of DI108:0=OFF				

DI109	1x00109 2x00109 I:108	0,0x00 B:00			BIT R/O	
		Actual state of DI109:0=OFF				
DI110	1x00110 2x00110 I:109	0,0x00 B:00			BIT R/O	
		Actual state of DI110:0=OFF				
DI111	1x00111 2x00111 I:110	0,0x00 B:00			BIT R/O	
		Actual state of DI111:0=OFF				
DI112	1x00112 2x00112 I:111	0,0x00 B:00			BIT R/O	
		Actual state of DI112:0=OFF				
DI113	1x00113 2x00113 I:112	0,0x00 B:00			BIT R/O	
		Actual state of DI113:0=OFF				
DI114	1x00114 2x00114 I:113	0,0x00 B:00			BIT R/O	
		Actual state of DI114:0=OFF				
DI115	1x00115 2x00115 I:114	0,0x00 B:00			BIT R/O	
		Actual state of DI115:0=OFF				
DI116	1x00116 2x00116 I:115	0,0x00 B:00			BIT R/O	
		Actual state of DI116:0=OFF				
DI117	1x00117 2x00117 I:116	0,0x00 B:00			BIT R/O	
		Actual state of DI117:0=OFF				
DI118	1x00118 2x00118 I:117	0,0x00 B:00			BIT R/O	
		Actual state of DI118:0=OFF				
DI119	1x00119 2x00119 I:118	0,0x00 B:00			BIT R/O	
		Actual state of DI119:0=OFF				

DI120	1x00120 2x00120 I:119	0,0x00 B:00			BIT R/O	
		Actual state of DI120:0=OFF				
DI121	1x00121 2x00121 I:120	0,0x00 B:00			BIT R/O	
		Actual state of DI121:0=OFF				
DI122	1x00122 2x00122 I:121	0,0x00 B:00			BIT R/O	
		Actual state of DI122:0=OFF				
DI123	1x00123 2x00123 I:122	0,0x00 B:00			BIT R/O	
		Actual state of DI123:0=OFF				
DI124	1x00124 2x00124 I:123	0,0x00 B:00			BIT R/O	
		Actual state of DI124:0=OFF				
DI125	1x00125 2x00125 I:124	0,0x00 B:00			BIT R/O	
		Actual state of DI125:0=OFF				
DI126	1x00126 2x00126 I:125	0,0x00 B:00			BIT R/O	
		Actual state of DI126:0=OFF				
DI127	1x00127 2x00127 I:126	0,0x00 B:00			BIT R/O	
		Actual state of DI127:0=OFF				
DI128	1x00128 2x00128 I:127	0,0x00 B:00			BIT R/O	
		Actual state of DI128:0=OFF				
DIGITAL INPUTS: RESET						
RESET COUNTERS	1x10000 2x10000 I:9999	0,0x00 B:00		1:PERFORM RESET	BIT R/W	NO

If this register is written to 1, all internal edge counters and event counters are set to 0. 0 is always returned when reading.

STATUS REAL DIGITAL INPUTS

REAL DI1	1x15001 2x14001 I:15000	0,0x00 B:00			BIT R/O		
		Actual state of REAL DI1:0=OFF					
Current state of the digital input DIx with the internal software filter to suppress glitches or spike on thie line =0:DI is OFF, =1:DI is ON							
REAL DI2	1x15002 2x15002 I:15001	0,0x00 B:00			BIT R/O		
		Actual state of REAL DI2:0=OFF					
REAL DI3	1x15003 2x15003 I:15002	0,0x00 B:00			BIT R/O		
		Actual state of REAL DI3:0=OFF					
REAL DI4	1x15004 2x15004 I:15003	0,0x00 B:00			BIT R/O		
		Actual state of REAL DI4:0=OFF					
REAL DI5	1x15005 2x15005 I:15004	0,0x00 B:00			BIT R/O		
		Actual state of REAL DI5:0=OFF					
REAL DI6	1x15006 2x15006 I:15005	0,0x00 B:00			BIT R/O		
		Actual state of REAL DI6:0=OFF					
REAL DI7	1x15007 2x15007 I:15006	0,0x00 B:00			BIT R/O		
		Actual state of REAL DI7:0=OFF					
REAL DI8	1x15008 2x15008 I:15007	0,0x00 B:00			BIT R/O		
		Actual state of REAL DI8:0=OFF					
REAL DI9	1x15009 2x15009 I:15008	0,0x00 B:00			BIT R/O		
		Actual state of REAL DI9:0=OFF					
REAL DI10	1x15010 2x15010 I:15009	0,0x00 B:00			BIT R/O		
		Actual state of REAL DI10:0=OFF					
REAL DI11	1x15011 2x15011 I:15010	0,0x00 B:00			BIT R/O		

		Actual state of REAL DI11:0=OFF				
REAL DI12	1x15012 2x15012 I:15011	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI12:0=OFF				
REAL DI13	1x15013 2x15013 I:15012	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI13:0=OFF				
REAL DI14	1x15014 2x15014 I:15013	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI14:0=OFF				
REAL DI15	1x15015 2x15015 I:15014	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI15:0=OFF				
REAL DI16	1x15016 2x15016 I:15015	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI16:0=OFF				
REAL DI17	1x15017 2x15017 I:15016	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI17:0=OFF				
REAL DI18	1x15018 2x15018 I:15017	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI18:0=OFF				
REAL DI19	1x15019 2x15019 I:15018	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI19:0=OFF				
REAL DI20	1x15020 2x15020 I:15019	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI20:0=OFF				
REAL DI21	1x15021 2x15021 I:15020	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI21:0=OFF				
REAL DI22	1x15022 2x15022 I:15021	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI22:0=OFF				

REAL DI23	1x15023 2x15023 I:15022	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI23:0=OFF				
REAL DI24	1x15024 2x15024 I:15023	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI24:0=OFF				
REAL DI25	1x15025 2x15025 I:15024	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI25:0=OFF				
REAL DI26	1x15026 2x15026 I:15025	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI26:0=OFF				
REAL DI27	1x15027 2x15027 I:15026	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI27:0=OFF				
REAL DI28	1x15028 2x15028 I:15027	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI28:0=OFF				
REAL DI29	1x15029 2x15029 I:15028	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI29:0=OFF				
REAL DI30	1x15030 2x15030 I:15029	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI30:0=OFF				
REAL DI31	1x15031 2x15031 I:15030	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI31:0=OFF				
REAL DI32	1x15032 2x15032 I:15031	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI32:0=OFF				
REAL DI33	1x15033 2x15033 I:15032	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI33:0=OFF				

REAL DI34	1x15034 2x15034 I:15033	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI34:0=OFF				
REAL DI35	1x15035 2x15035 I:15034	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI35:0=OFF				
REAL DI36	1x15036 2x15036 I:15035	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI36:0=OFF				
REAL DI37	1x15037 2x15037 I:15036	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI37:0=OFF				
REAL DI38	1x15038 2x15038 I:15037	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI38:0=OFF				
REAL DI39	1x15039 2x15039 I:15038	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI39:0=OFF				
REAL DI40	1x15040 2x15040 I:15039	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI40:0=OFF				
REAL DI41	1x15041 2x15041 I:15040	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI41:0=OFF				
REAL DI42	1x15042 2x15042 I:15041	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI42:0=OFF				
REAL DI43	1x15043 2x15043 I:15042	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI43:0=OFF				
REAL DI44	1x15044 2x15044 I:15043	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI44:0=OFF				

REAL DI45	1x15045 2x15045 I:15044	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI45:0=OFF				
REAL DI46	1x15046 2x15046 I:15045	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI46:0=OFF				
REAL DI47	1x15047 2x15047 I:15046	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI47:0=OFF				
REAL DI48	1x15048 2x15048 I:15047	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI48:0=OFF				
REAL DI49	1x15049 2x15049 I:15048	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI49:0=OFF				
REAL DI50	1x15050 2x15050 I:15049	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI50:0=OFF				
REAL DI51	1x15051 2x15051 I:15050	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI51:0=OFF				
REAL DI52	1x15052 2x15052 I:15051	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI52:0=OFF				
REAL DI53	1x15053 2x15053 I:15052	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI53:0=OFF				
REAL DI54	1x15054 2x15054 I:15053	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI54:0=OFF				
REAL DI55	1x15055 2x15055 I:15054	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI55:0=OFF				

REAL DI56	1x15056 2x15056 I:15055	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI56:0=OFF				
REAL DI57	1x15057 2x15057 I:15056	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI57:0=OFF				
REAL DI58	1x15058 2x15058 I:15057	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI58:0=OFF				
REAL DI59	1x15059 2x15059 I:15058	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI59:0=OFF				
REAL DI60	1x15060 2x15060 I:15059	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI60:0=OFF				
REAL DI61	1x15061 2x15061 I:15060	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI61:0=OFF				
REAL DI62	1x15062 2x15062 I:15061	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI62:0=OFF				
REAL DI63	1x15063 2x15063 I:15062	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI63:0=OFF				
REAL DI64	1x15064 2x15064 I:15063	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI64:0=OFF				
REAL DI65	1x15065 2x15065 I:15064	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI65:0=OFF				
REAL DI66	1x15066 2x15066 I:15065	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI66:0=OFF				

REAL DI67	1x15067 2x15067 I:15066	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI67:0=OFF				
REAL DI68	1x15068 2x15068 I:15067	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI68:0=OFF				
REAL DI69	1x15069 2x15069 I:15068	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI69:0=OFF				
REAL DI70	1x15070 2x15070 I:15069	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI70:0=OFF				
REAL DI71	1x15071 2x15071 I:15070	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI71:0=OFF				
REAL DI72	1x15072 2x15072 I:15071	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI72:0=OFF				
REAL DI73	1x15073 2x15073 I:15072	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI73:0=OFF				
REAL DI74	1x15074 2x15074 I:15073	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI74:0=OFF				
REAL DI75	1x15075 2x15075 I:15074	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI75:0=OFF				
REAL DI76	1x15076 2x15076 I:15075	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI76:0=OFF				
REAL DI77	1x15077 2x15077 I:15076	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI77:0=OFF				

REAL DI78	1x15078 2x15078 I:15077	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI78:0=OFF				
REAL DI79	1x15079 2x15079 I:15078	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI79:0=OFF				
REAL DI80	1x15080 2x15080 I:15079	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI80:0=OFF				
REAL DI81	1x15081 2x15081 I:15080	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI81:0=OFF				
REAL DI82	1x15082 2x15082 I:15081	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI82:0=OFF				
REAL DI83	1x15083 2x15083 I:15082	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI83:0=OFF				
REAL DI84	1x15084 2x15084 I:15083	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI84:0=OFF				
REAL DI85	1x15085 2x15085 I:15084	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI85:0=OFF				
REAL DI86	1x15086 2x15086 I:15085	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI86:0=OFF				
REAL DI87	1x15087 2x15087 I:15086	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI87:0=OFF				
REAL DI88	1x15088 2x15088 I:15087	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI88:0=OFF				

REAL DI89	1x15089 2x15089 I:15088	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI89:0=OFF				
REAL DI90	1x15090 2x15090 I:15089	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI90:0=OFF				
REAL DI91	1x15091 2x15091 I:15090	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI91:0=OFF				
REAL DI92	1x15092 2x15092 I:15091	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI92:0=OFF				
REAL DI93	1x15093 2x15093 I:15092	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI93:0=OFF				
REAL DI94	1x15094 2x15094 I:15093	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI94:0=OFF				
REAL DI95	1x15095 2x15095 I:15094	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI95:0=OFF				
REAL DI96	1x15096 2x15096 I:15095	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI96:0=OFF				
REAL DI97	1x15097 2x15097 I:15096	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI97:0=OFF				
REAL DI98	1x15098 2x15098 I:15097	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI98:0=OFF				
REAL DI99	1x15099 2x15099 I:15098	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI99:0=OFF				

REAL DI100	1x15100 2x15100 I:15099	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI100:0=OFF				
REAL DI101	1x15101 2x15101 I:15100	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI101:0=OFF				
REAL DI102	1x15102 2x15102 I:15101	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI102:0=OFF				
REAL DI103	1x15103 2x15103 I:15102	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI103:0=OFF				
REAL DI104	1x15104 2x15104 I:15103	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI104:0=OFF				
REAL DI105	1x15105 2x15105 I:15104	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI105:0=OFF				
REAL DI106	1x15106 2x15106 I:15105	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI106:0=OFF				
REAL DI107	1x15107 2x15107 I:15106	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI107:0=OFF				
REAL DI108	1x15108 2x15108 I:15107	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI108:0=OFF				
REAL DI109	1x15109 2x15109 I:15108	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI109:0=OFF				
REAL DI110	1x15110 2x15110 I:15109	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI110:0=OFF				

REAL DI111	1x15111 2x15111 I:15110	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI111:0=OFF				
REAL DI112	1x15112 2x15112 I:15111	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI112:0=OFF				
REAL DI113	1x15113 2x15113 I:15112	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI113:0=OFF				
REAL DI114	1x15114 2x15114 I:15113	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI114:0=OFF				
REAL DI115	1x15115 2x15115 I:15114	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI115:0=OFF				
REAL DI116	1x15116 2x15116 I:15115	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI116:0=OFF				
REAL DI117	1x15117 2x15117 I:15116	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI117:0=OFF				
REAL DI118	1x15118 2x15118 I:15117	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI118:0=OFF				
REAL DI119	1x15119 2x15119 I:15118	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI119:0=OFF				
REAL DI120	1x15120 2x15120 I:15119	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI120:0=OFF				
REAL DI121	1x15121 2x15121 I:15120	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI121:0=OFF				

REAL DI122	1x15122 2x15122 I:15121	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI122:0=OFF				
REAL DI123	1x15123 2x15123 I:15122	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI123:0=OFF				
REAL DI124	1x15124 2x15124 I:15123	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI124:0=OFF				
REAL DI125	1x15125 2x15125 I:15124	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI125:0=OFF				
REAL DI126	1x15126 2x15126 I:15125	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI126:0=OFF				
REAL DI127	1x15127 2x15127 I:15126	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI127:0=OFF				
REAL DI128	1x15128 2x15128 I:15127	0,0x00 B:00			BIT R/O	
		Actual state of REAL DI128:0=OFF				
STATUS DIGITAL INPUTS						
UNFILTERED DI1	1x15129 2x15129 I:15128	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI1:0=OFF				
Current state of the real digital input DIx without the internal software filter to suppress glitches or spike on thie line =0:DI is OFF, =1:DI is ON						
UNFILTERED DI2	1x15130 2x15130 I:15129	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI2:0=OFF				
UNFILTERED DI3	1x15131 2x15131 I:15130	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI3:0=OFF				

UNFILTERED DI4	1x15132 2x15132 I:15131	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI4:0=OFF				
UNFILTERED DI5	1x15133 2x15133 I:15132	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI5:0=OFF				
UNFILTERED DI6	1x15134 2x15134 I:15133	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI6:0=OFF				
UNFILTERED DI7	1x15135 2x15135 I:15134	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI7:0=OFF				
UNFILTERED DI8	1x15136 2x15136 I:15135	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI8:0=OFF				
UNFILTERED DI9	1x15137 2x15137 I:15136	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI9:0=OFF				
UNFILTERED DI10	1x15138 2x15138 I:15137	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI10:0=OFF				
UNFILTERED DI11	1x15139 2x15139 I:15138	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI11:0=OFF				
UNFILTERED DI12	1x15140 2x15140 I:15139	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI12:0=OFF				
UNFILTERED DI13	1x15141 2x15141 I:15140	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI13:0=OFF				
UNFILTERED DI14	1x15142 2x15142 I:15141	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI14:0=OFF				

UNFILTERED DI15	1x15143 2x15143 I:15142	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI15:0=OFF				
UNFILTERED DI16	1x15144 2x15144 I:15143	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI16:0=OFF				
UNFILTERED DI17	1x15145 2x15145 I:15144	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI17:0=OFF				
UNFILTERED DI18	1x15146 2x15146 I:15145	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI18:0=OFF				
UNFILTERED DI19	1x15147 2x15147 I:15146	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI19:0=OFF				
UNFILTERED DI20	1x15148 2x15148 I:15147	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI20:0=OFF				
UNFILTERED DI21	1x15149 2x15149 I:15148	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI21:0=OFF				
UNFILTERED DI22	1x15150 2x15150 I:15149	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI22:0=OFF				
UNFILTERED DI23	1x15151 2x15151 I:15150	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI23:0=OFF				
UNFILTERED DI24	1x15152 2x15152 I:15151	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI24:0=OFF				
UNFILTERED DI25	1x15153 2x15153 I:15152	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI25:0=OFF				

UNFILTERED DI26	1x15154 2x15154 I:15153	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI26:0=OFF				
UNFILTERED DI27	1x15155 2x15155 I:15154	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI27:0=OFF				
UNFILTERED DI28	1x15156 2x15156 I:15155	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI28:0=OFF				
UNFILTERED DI29	1x15157 2x15157 I:15156	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI29:0=OFF				
UNFILTERED DI30	1x15158 2x15158 I:15157	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI30:0=OFF				
UNFILTERED DI31	1x15159 2x15159 I:15158	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI31:0=OFF				
UNFILTERED DI32	1x15160 2x15160 I:15159	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI32:0=OFF				
UNFILTERED DI33	1x15161 2x15161 I:15160	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI33:0=OFF				
UNFILTERED DI34	1x15162 2x15162 I:15161	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI34:0=OFF				
UNFILTERED DI35	1x15163 2x15163 I:15162	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI35:0=OFF				
UNFILTERED DI36	1x15164 2x15164 I:15163	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI36:0=OFF				

UNFILTERED DI37	1x15165 2x15165 I:15164	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI37:0=OFF				
UNFILTERED DI38	1x15166 2x15166 I:15165	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI38:0=OFF				
UNFILTERED DI39	1x15167 2x15167 I:15166	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI39:0=OFF				
UNFILTERED DI40	1x15168 2x15168 I:15167	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI40:0=OFF				
UNFILTERED DI41	1x15169 2x15169 I:15168	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI41:0=OFF				
UNFILTERED DI42	1x15170 2x15170 I:15169	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI42:0=OFF				
UNFILTERED DI43	1x15171 2x15171 I:15170	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI43:0=OFF				
UNFILTERED DI44	1x15172 2x15172 I:15171	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI44:0=OFF				
UNFILTERED DI45	1x15173 2x15173 I:15172	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI45:0=OFF				
UNFILTERED DI46	1x15174 2x15174 I:15173	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI46:0=OFF				
UNFILTERED DI47	1x15175 2x15175 I:15174	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI47:0=OFF				

UNFILTERED DI48	1x15176 2x15176 I:15175	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI48:0=OFF				
UNFILTERED DI49	1x15177 2x15177 I:15176	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI49:0=OFF				
UNFILTERED DI50	1x15178 2x15178 I:15177	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI50:0=OFF				
UNFILTERED DI51	1x15179 2x15179 I:15178	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI51:0=OFF				
UNFILTERED DI52	1x15180 2x15180 I:15179	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI52:0=OFF				
UNFILTERED DI53	1x15181 2x15181 I:15180	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI53:0=OFF				
UNFILTERED DI54	1x15182 2x15182 I:15181	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI54:0=OFF				
UNFILTERED DI55	1x15183 2x15183 I:15182	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI55:0=OFF				
UNFILTERED DI56	1x15184 2x15184 I:15183	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI56:0=OFF				
UNFILTERED DI57	1x15185 2x15185 I:15184	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI57:0=OFF				
UNFILTERED DI58	1x15186 2x15186 I:15185	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI58:0=OFF				

UNFILTERED DI59	1x15187 2x15187 I:15186	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI59:0=OFF				
UNFILTERED DI60	1x15188 2x15188 I:15187	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI60:0=OFF				
UNFILTERED DI61	1x15189 2x15189 I:15188	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI61:0=OFF				
UNFILTERED DI62	1x15190 2x15190 I:15189	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI62:0=OFF				
UNFILTERED DI63	1x15191 2x15191 I:15190	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI63:0=OFF				
UNFILTERED DI64	1x15192 2x15192 I:15191	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI64:0=OFF				
UNFILTERED DI65	1x15193 2x15193 I:15192	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI65:0=OFF				
UNFILTERED DI66	1x15194 2x15194 I:15193	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI66:0=OFF				
UNFILTERED DI67	1x15195 2x15195 I:15194	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI67:0=OFF				
UNFILTERED DI68	1x15196 2x15196 I:15195	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI68:0=OFF				
UNFILTERED DI69	1x15197 2x15197 I:15196	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI69:0=OFF				

UNFILTERED DI70	1x15198 2x15198 I:15197	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI70:0=OFF				
UNFILTERED DI71	1x15199 2x15199 I:15198	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI71:0=OFF				
UNFILTERED DI72	1x15200 2x15200 I:15199	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI72:0=OFF				
UNFILTERED DI73	1x15201 2x15201 I:15200	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI73:0=OFF				
UNFILTERED DI74	1x15202 2x15202 I:15201	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI74:0=OFF				
UNFILTERED DI75	1x15203 2x15203 I:15202	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI75:0=OFF				
UNFILTERED DI76	1x15204 2x15204 I:15203	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI76:0=OFF				
UNFILTERED DI77	1x15205 2x15205 I:15204	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI77:0=OFF				
UNFILTERED DI78	1x15206 2x15206 I:15205	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI78:0=OFF				
UNFILTERED DI79	1x15207 2x15207 I:15206	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI79:0=OFF				
UNFILTERED DI80	1x15208 2x15208 I:15207	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI80:0=OFF				

UNFILTERED DI81	1x15209 2x15209 I:15208	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI81:0=OFF				
UNFILTERED DI82	1x15210 2x15210 I:15209	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI82:0=OFF				
UNFILTERED DI83	1x15211 2x15211 I:15210	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI83:0=OFF				
UNFILTERED DI84	1x15212 2x15212 I:15211	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI84:0=OFF				
UNFILTERED DI85	1x15213 2x15213 I:15212	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI85:0=OFF				
UNFILTERED DI86	1x15214 2x15214 I:15213	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI86:0=OFF				
UNFILTERED DI87	1x15215 2x15215 I:15214	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI87:0=OFF				
UNFILTERED DI88	1x15216 2x15216 I:15215	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI88:0=OFF				
UNFILTERED DI89	1x15217 2x15217 I:15216	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI89:0=OFF				
UNFILTERED DI90	1x15218 2x15218 I:15217	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI90:0=OFF				
UNFILTERED DI91	1x15219 2x15219 I:15218	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI91:0=OFF				

UNFILTERED DI92	1x15220 2x15220 I:15219	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI92:0=OFF				
UNFILTERED DI93	1x15221 2x15221 I:15220	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI93:0=OFF				
UNFILTERED DI94	1x15222 2x15222 I:15221	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI94:0=OFF				
UNFILTERED DI95	1x15223 2x15223 I:15222	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI95:0=OFF				
UNFILTERED DI96	1x15224 2x15224 I:15223	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI96:0=OFF				
UNFILTERED DI97	1x15225 2x15225 I:15224	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI97:0=OFF				
UNFILTERED DI98	1x15226 2x15226 I:15225	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI98:0=OFF				
UNFILTERED DI99	1x15227 2x15227 I:15226	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI99:0=OFF				
UNFILTERED DI100	1x15228 2x15228 I:15227	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI100:0=OFF				
UNFILTERED DI101	1x15229 2x15229 I:15228	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI101:0=OFF				
UNFILTERED DI102	1x15230 2x15230 I:15229	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI102:0=OFF				

UNFILTERED DI103	1x15231 2x15231 I:15230	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI103:0=OFF				
UNFILTERED DI104	1x15232 2x15232 I:15231	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI104:0=OFF				
UNFILTERED DI105	1x15233 2x15233 I:15232	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI105:0=OFF				
UNFILTERED DI106	1x15234 2x15234 I:15233	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI106:0=OFF				
UNFILTERED DI107	1x15235 2x15235 I:15234	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI107:0=OFF				
UNFILTERED DI108	1x15236 2x15236 I:15235	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI108:0=OFF				
UNFILTERED DI109	1x15237 2x15237 I:15236	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI109:0=OFF				
UNFILTERED DI110	1x15238 2x15238 I:15237	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI110:0=OFF				
UNFILTERED DI111	1x15239 2x15239 I:15238	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI111:0=OFF				
UNFILTERED DI112	1x15240 2x15240 I:15239	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI112:0=OFF				
UNFILTERED DI113	1x15241 2x15241 I:15240	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI113:0=OFF				

UNFILTERED DI114	1x15242 2x15242 I:15241	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI114:0=OFF				
UNFILTERED DI115	1x15243 2x15243 I:15242	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI115:0=OFF				
UNFILTERED DI116	1x15244 2x15244 I:15243	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI116:0=OFF				
UNFILTERED DI117	1x15245 2x15245 I:15244	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI117:0=OFF				
UNFILTERED DI118	1x15246 2x15246 I:15245	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI118:0=OFF				
UNFILTERED DI119	1x15247 2x15247 I:15246	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI119:0=OFF				
UNFILTERED DI120	1x15248 2x15248 I:15247	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI120:0=OFF				
UNFILTERED DI121	1x15249 2x15249 I:15248	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI121:0=OFF				
UNFILTERED DI122	1x15250 2x15250 I:15249	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI122:0=OFF				
UNFILTERED DI123	1x15251 2x15251 I:15250	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI123:0=OFF				
UNFILTERED DI124	1x15252 2x15252 I:15251	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI124:0=OFF				

UNFILTERED DI125	1x15253 2x15253 I:15252	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI125:0=OFF				
UNFILTERED DI126	1x15254 2x15254 I:15253	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI126:0=OFF				
UNFILTERED DI127	1x15255 2x15255 I:15254	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI127:0=OFF				
UNFILTERED DI128	1x15256 2x15256 I:15255	0,0x00 B:00			BIT R/O	
		Actual state of UNFILTERED DI128:0=OFF				

Register NAME Command NAME	MODBUS Register ASCII Command	Register VALUE ASCII Command	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
DIGITAL INPUTS: DIGITAL INPUT HAS CHANGED IT'S STATE						
DI HAS CHANGED DI1	1x20001 2x20001 I:20000	0,0x00 B:00			BIT R/O	
If the digital input has changed this bit inverts its last state						
DI HAS CHANGED DI2	1x20002 2x20002 I:20001	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI3	1x20003 2x20003 I:20002	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI4	1x20004 2x20004 I:20003	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI5	1x20005 2x20005 I:20004	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI6	1x20006 2x20006 I:20005	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI7	1x20007 2x20007 I:20006	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI8	1x20008 2x20008 I:20007	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI9	1x20009 2x20009 I:20008	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI10	1x20010 2x20010 I:20009	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI11	1x20011 2x20011 I:20010	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI12	1x20012 2x20012 I:20011	0,0x00 B:00			BIT R/O	

DI HAS CHANGED DI13	1x20013 2x20013 I:20012	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI14	1x20014 2x20014 I:20013	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI15	1x20015 2x20015 I:20014	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI16	1x20016 2x20016 I:20015	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI17	1x20017 2x20017 I:20016	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI18	1x20018 2x20018 I:20017	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI19	1x20019 2x20019 I:20018	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI20	1x20020 2x20020 I:20019	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI21	1x20021 2x20021 I:20020	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI22	1x20022 2x20022 I:20021	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI23	1x20023 2x20023 I:20022	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI24	1x20024 2x20024 I:20023	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI25	1x20025 2x20025 I:20024	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI26	1x20026 2x20026 I:20025	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI27	1x20027 2x20027 I:20026	0,0x00 B:00			BIT R/O	

DI HAS CHANGED DI28	1x20028 2x20028 I:20027	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI29	1x20029 2x20029 I:20028	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI30	1x20030 2x20030 I:20029	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI31	1x20031 2x20031 I:20030	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI32	1x20032 2x20032 I:20031	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI33	1x20033 2x20033 I:20032	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI34	1x20034 2x20034 I:20033	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI35	1x20035 2x20035 I:20034	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI36	1x20036 2x20036 I:20035	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI37	1x20037 2x20037 I:20036	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI38	1x20038 2x20038 I:20037	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI39	1x20039 2x20039 I:20038	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI40	1x20040 2x20040 I:20039	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI41	1x20041 2x20041 I:20040	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI42	1x20042 2x20042 I:20041	0,0x00 B:00			BIT R/O	

DI HAS CHANGED DI43	1x20043 2x20043 I:20042	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI44	1x20044 2x20044 I:20043	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI45	1x20045 2x20045 I:20044	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI46	1x20046 2x20046 I:20045	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI47	1x20047 2x20047 I:20046	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI48	1x20048 2x20048 I:20047	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI49	1x20049 2x20049 I:20048	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI50	1x20050 2x20050 I:20049	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI51	1x20051 2x20051 I:20050	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI52	1x20052 2x20052 I:20051	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI53	1x20053 2x20053 I:20052	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI54	1x20054 2x20054 I:20053	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI55	1x20055 2x20055 I:20054	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI56	1x20056 2x20056 I:20055	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI57	1x20057 2x20057 I:20056	0,0x00 B:00			BIT R/O	

DI HAS CHANGED DI58	1x20058 2x20058 I:20057	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI59	1x20059 2x20059 I:20058	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI60	1x20060 2x20060 I:20059	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI61	1x20061 2x20061 I:20060	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI62	1x20062 2x20062 I:20061	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI63	1x20063 2x20063 I:20062	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI64	1x20064 2x20064 I:20063	1,0x01 B:01			BIT R/O	
DI HAS CHANGED DI65	1x20065 2x20065 I:20064	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI66	1x20066 2x20066 I:20065	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI67	1x20067 2x20067 I:20066	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI68	1x20068 2x20068 I:20067	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI69	1x20069 2x20069 I:20068	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI70	1x20070 2x20070 I:20069	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI71	1x20071 2x20071 I:20070	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI72	1x20072 2x20072 I:20071	0,0x00 B:00			BIT R/O	

DI HAS CHANGED DI73	1x20073 2x20073 I:20072	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI74	1x20074 2x20074 I:20073	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI75	1x20075 2x20075 I:20074	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI76	1x20076 2x20076 I:20075	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI77	1x20077 2x20077 I:20076	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI78	1x20078 2x20078 I:20077	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI79	1x20079 2x20079 I:20078	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI80	1x20080 2x20080 I:20079	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI81	1x20081 2x20081 I:20080	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI82	1x20082 2x20082 I:20081	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI83	1x20083 2x20083 I:20082	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI84	1x20084 2x20084 I:20083	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI85	1x20085 2x20085 I:20084	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI86	1x20086 2x20086 I:20085	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI87	1x20087 2x20087 I:20086	0,0x00 B:00			BIT R/O	

DI HAS CHANGED DI88	1x20088 2x20088 I:20087	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI89	1x20089 2x20089 I:20088	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI90	1x20090 2x20090 I:20089	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI91	1x20091 2x20091 I:20090	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI92	1x20092 2x20092 I:20091	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI93	1x20093 2x20093 I:20092	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI94	1x20094 2x20094 I:20093	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI95	1x20095 2x20095 I:20094	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI96	1x20096 2x20096 I:20095	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI97	1x20097 2x20097 I:20096	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI98	1x20098 2x20098 I:20097	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI99	1x20099 2x20099 I:20098	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI100	1x20100 2x20100 I:20099	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI101	1x20101 2x20101 I:20100	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI102	1x20102 2x20102 I:20101	0,0x00 B:00			BIT R/O	

DI HAS CHANGED DI103	1x20103 2x20103 I:20102	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI104	1x20104 2x20104 I:20103	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI105	1x20105 2x20105 I:20104	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI106	1x20106 2x20106 I:20105	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI107	1x20107 2x20107 I:20106	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI108	1x20108 2x20108 I:20107	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI109	1x20109 2x20109 I:20108	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI110	1x20110 2x20110 I:20109	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI111	1x20111 2x20111 I:20110	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI112	1x20112 2x20112 I:20111	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI113	1x20113 2x20113 I:20112	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI114	1x20114 2x20114 I:20113	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI115	1x20115 2x20115 I:20114	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI116	1x20116 2x20116 I:20115	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI117	1x20117 2x20117 I:20116	0,0x00 B:00			BIT R/O	

DI HAS CHANGED DI118	1x20118 2x20118 I:20117	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI119	1x20119 2x20119 I:20118	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI120	1x20120 2x20120 I:20119	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI121	1x20121 2x20121 I:20120	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI122	1x20122 2x20122 I:20121	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI123	1x20123 2x20123 I:20122	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI124	1x20124 2x20124 I:20123	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI125	1x20125 2x20125 I:20124	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI126	1x20126 2x20126 I:20125	1,0x01 B:01			BIT R/O	
DI HAS CHANGED DI127	1x20127 2x20127 I:20126	0,0x00 B:00			BIT R/O	
DI HAS CHANGED DI128	1x20128 2x20128 I:20127	1,0x01 B:01			BIT R/O	
DIGITAL INPUTS: SHORT KEYPRESS EVENT ON DIGITAL INPUT DETECTED						
SHORT KEYPRESS ON DI1	1x20129 2x20129 I:20128	0,0x00 B:00			BIT R/O	
If a short keypress event was detected on the digital input this bit inverts its last state						
SHORT KEYPRESS ON DI2	1x20130 2x20130 I:20129	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI3	1x20131 2x20131 I:20130	0,0x00 B:00			BIT R/O	

SHORT KEYPRESS ON DI4	1x20132 2x20132 I:20131	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI5	1x20133 2x20133 I:20132	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI6	1x20134 2x20134 I:20133	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI7	1x20135 2x20135 I:20134	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI8	1x20136 2x20136 I:20135	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI9	1x20137 2x20137 I:20136	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI10	1x20138 2x20138 I:20137	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI11	1x20139 2x20139 I:20138	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI12	1x20140 2x20140 I:20139	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI13	1x20141 2x20141 I:20140	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI14	1x20142 2x20142 I:20141	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI15	1x20143 2x20143 I:20142	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI16	1x20144 2x20144 I:20143	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI17	1x20145 2x20145 I:20144	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI18	1x20146 2x20146 I:20145	0,0x00 B:00			BIT R/O	

SHORT KEYPRESS ON DI19	1x20147 2x20147 I:20146	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI20	1x20148 2x20148 I:20147	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI21	1x20149 2x20149 I:20148	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI22	1x20150 2x20150 I:20149	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI23	1x20151 2x20151 I:20150	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI24	1x20152 2x20152 I:20151	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI25	1x20153 2x20153 I:20152	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI26	1x20154 2x20154 I:20153	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI27	1x20155 2x20155 I:20154	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI28	1x20156 2x20156 I:20155	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI29	1x20157 2x20157 I:20156	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI30	1x20158 2x20158 I:20157	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI31	1x20159 2x20159 I:20158	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI32	1x20160 2x20160 I:20159	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI33	1x20161 2x20161 I:20160	0,0x00 B:00			BIT R/O	

SHORT KEYPRESS ON DI34	1x20162 2x20162 I:20161	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI35	1x20163 2x20163 I:20162	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI36	1x20164 2x20164 I:20163	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI37	1x20165 2x20165 I:20164	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI38	1x20166 2x20166 I:20165	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI39	1x20167 2x20167 I:20166	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI40	1x20168 2x20168 I:20167	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI41	1x20169 2x20169 I:20168	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI42	1x20170 2x20170 I:20169	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI43	1x20171 2x20171 I:20170	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI44	1x20172 2x20172 I:20171	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI45	1x20173 2x20173 I:20172	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI46	1x20174 2x20174 I:20173	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI47	1x20175 2x20175 I:20174	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI48	1x20176 2x20176 I:20175	0,0x00 B:00			BIT R/O	

SHORT KEYPRESS ON DI49	1x20177 2x20177 I:20176	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI50	1x20178 2x20178 I:20177	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI51	1x20179 2x20179 I:20178	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI52	1x20180 2x20180 I:20179	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI53	1x20181 2x20181 I:20180	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI54	1x20182 2x20182 I:20181	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI55	1x20183 2x20183 I:20182	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI56	1x20184 2x20184 I:20183	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI57	1x20185 2x20185 I:20184	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI58	1x20186 2x20186 I:20185	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI59	1x20187 2x20187 I:20186	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI60	1x20188 2x20188 I:20187	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI61	1x20189 2x20189 I:20188	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI62	1x20190 2x20190 I:20189	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI63	1x20191 2x20191 I:20190	0,0x00 B:00			BIT R/O	

SHORT KEYPRESS ON DI64	1x20192 2x20192 I:20191	1,0x01 B:01			BIT R/O	
SHORT KEYPRESS ON DI65	1x20193 2x20193 I:20192	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI66	1x20194 2x20194 I:20193	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI67	1x20195 2x20195 I:20194	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI68	1x20196 2x20196 I:20195	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI69	1x20197 2x20197 I:20196	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI70	1x20198 2x20198 I:20197	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI71	1x20199 2x20199 I:20198	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI72	1x20200 2x20200 I:20199	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI73	1x20201 2x20201 I:20200	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI74	1x20202 2x20202 I:20201	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI75	1x20203 2x20203 I:20202	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI76	1x20204 2x20204 I:20203	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI77	1x20205 2x20205 I:20204	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI78	1x20206 2x20206 I:20205	0,0x00 B:00			BIT R/O	

SHORT KEYPRESS ON DI79	1x20207 2x20207 I:20206	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI80	1x20208 2x20208 I:20207	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI81	1x20209 2x20209 I:20208	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI82	1x20210 2x20210 I:20209	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI83	1x20211 2x20211 I:20210	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI84	1x20212 2x20212 I:20211	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI85	1x20213 2x20213 I:20212	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI86	1x20214 2x20214 I:20213	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI87	1x20215 2x20215 I:20214	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI88	1x20216 2x20216 I:20215	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI89	1x20217 2x20217 I:20216	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI90	1x20218 2x20218 I:20217	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI91	1x20219 2x20219 I:20218	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI92	1x20220 2x20220 I:20219	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI93	1x20221 2x20221 I:20220	0,0x00 B:00			BIT R/O	

SHORT KEYPRESS ON DI94	1x20222 2x20222 I:20221	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI95	1x20223 2x20223 I:20222	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI96	1x20224 2x20224 I:20223	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI97	1x20225 2x20225 I:20224	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI98	1x20226 2x20226 I:20225	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI99	1x20227 2x20227 I:20226	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI100	1x20228 2x20228 I:20227	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI101	1x20229 2x20229 I:20228	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI102	1x20230 2x20230 I:20229	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI103	1x20231 2x20231 I:20230	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI104	1x20232 2x20232 I:20231	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI105	1x20233 2x20233 I:20232	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI106	1x20234 2x20234 I:20233	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI107	1x20235 2x20235 I:20234	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI108	1x20236 2x20236 I:20235	0,0x00 B:00			BIT R/O	

SHORT KEYPRESS ON DI109	1x20237 2x20237 I:20236	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI110	1x20238 2x20238 I:20237	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI111	1x20239 2x20239 I:20238	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI112	1x20240 2x20240 I:20239	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI113	1x20241 2x20241 I:20240	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI114	1x20242 2x20242 I:20241	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI115	1x20243 2x20243 I:20242	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI116	1x20244 2x20244 I:20243	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI117	1x20245 2x20245 I:20244	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI118	1x20246 2x20246 I:20245	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI119	1x20247 2x20247 I:20246	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI120	1x20248 2x20248 I:20247	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI121	1x20249 2x20249 I:20248	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI122	1x20250 2x20250 I:20249	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI123	1x20251 2x20251 I:20250	0,0x00 B:00			BIT R/O	

SHORT KEYPRESS ON DI124	1x20252 2x20252 I:20251	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI125	1x20253 2x20253 I:20252	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI126	1x20254 2x20254 I:20253	1,0x01 B:01			BIT R/O	
SHORT KEYPRESS ON DI127	1x20255 2x20255 I:20254	0,0x00 B:00			BIT R/O	
SHORT KEYPRESS ON DI128	1x20256 2x20256 I:20255	1,0x01 B:01			BIT R/O	
DIGITAL INPUTS: LONG KEYPRESS START EVENT ON DIGITAL INPUT DETECTED						
LONG KEYPRESS START ON DI1	1x20257 2x20257 I:20256	0,0x00 B:00			BIT R/O	
If a long keypress start event was detected on the digital input this bit inverts its last state						
LONG KEYPRESS START ON DI2	1x20258 2x20258 I:20257	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI3	1x20259 2x20259 I:20258	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI4	1x20260 2x20260 I:20259	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI5	1x20261 2x20261 I:20260	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI6	1x20262 2x20262 I:20261	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI7	1x20263 2x20263 I:20262	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI8	1x20264 2x20264 I:20263	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI9	1x20265 2x20265 I:20264	0,0x00 B:00			BIT R/O	

LONG KEYPRESS START ON DI10	1x20266 2x20266 I:20265	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI11	1x20267 2x20267 I:20266	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI12	1x20268 2x20268 I:20267	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI13	1x20269 2x20269 I:20268	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI14	1x20270 2x20270 I:20269	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI15	1x20271 2x20271 I:20270	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI16	1x20272 2x20272 I:20271	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI17	1x20273 2x20273 I:20272	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI18	1x20274 2x20274 I:20273	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI19	1x20275 2x20275 I:20274	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI20	1x20276 2x20276 I:20275	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI21	1x20277 2x20277 I:20276	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI22	1x20278 2x20278 I:20277	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI23	1x20279 2x20279 I:20278	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI24	1x20280 2x20280 I:20279	0,0x00 B:00			BIT R/O	

LONG KEYPRESS START ON DI25	1x20281 2x20281 I:20280	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI26	1x20282 2x20282 I:20281	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI27	1x20283 2x20283 I:20282	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI28	1x20284 2x20284 I:20283	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI29	1x20285 2x20285 I:20284	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI30	1x20286 2x20286 I:20285	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI31	1x20287 2x20287 I:20286	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI32	1x20288 2x20288 I:20287	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI33	1x20289 2x20289 I:20288	1,0x01 B:01			BIT R/O	
LONG KEYPRESS START ON DI34	1x20290 2x20290 I:20289	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI35	1x20291 2x20291 I:20290	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI36	1x20292 2x20292 I:20291	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI37	1x20293 2x20293 I:20292	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI38	1x20294 2x20294 I:20293	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI39	1x20295 2x20295 I:20294	0,0x00 B:00			BIT R/O	

LONG KEYPRESS START ON DI40	1x20296 2x20296 I:20295	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI41	1x20297 2x20297 I:20296	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI42	1x20298 2x20298 I:20297	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI43	1x20299 2x20299 I:20298	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI44	1x20300 2x20300 I:20299	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI45	1x20301 2x20301 I:20300	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI46	1x20302 2x20302 I:20301	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI47	1x20303 2x20303 I:20302	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI48	1x20304 2x20304 I:20303	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI49	1x20305 2x20305 I:20304	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI50	1x20306 2x20306 I:20305	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI51	1x20307 2x20307 I:20306	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI52	1x20308 2x20308 I:20307	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI53	1x20309 2x20309 I:20308	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI54	1x20310 2x20310 I:20309	0,0x00 B:00			BIT R/O	

LONG KEYPRESS START ON DI55	1x20311 2x20311 I:20310	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI56	1x20312 2x20312 I:20311	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI57	1x20313 2x20313 I:20312	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI58	1x20314 2x20314 I:20313	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI59	1x20315 2x20315 I:20314	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI60	1x20316 2x20316 I:20315	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI61	1x20317 2x20317 I:20316	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI62	1x20318 2x20318 I:20317	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI63	1x20319 2x20319 I:20318	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI64	1x20320 2x20320 I:20319	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI65	1x20321 2x20321 I:20320	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI66	1x20322 2x20322 I:20321	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI67	1x20323 2x20323 I:20322	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI68	1x20324 2x20324 I:20323	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI69	1x20325 2x20325 I:20324	0,0x00 B:00			BIT R/O	

LONG KEYPRESS START ON DI70	1x20326 2x20326 I:20325	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI71	1x20327 2x20327 I:20326	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI72	1x20328 2x20328 I:20327	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI73	1x20329 2x20329 I:20328	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI74	1x20330 2x20330 I:20329	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI75	1x20331 2x20331 I:20330	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI76	1x20332 2x20332 I:20331	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI77	1x20333 2x20333 I:20332	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI78	1x20334 2x20334 I:20333	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI79	1x20335 2x20335 I:20334	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI80	1x20336 2x20336 I:20335	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI81	1x20337 2x20337 I:20336	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI82	1x20338 2x20338 I:20337	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI83	1x20339 2x20339 I:20338	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI84	1x20340 2x20340 I:20339	0,0x00 B:00			BIT R/O	

LONG KEYPRESS START ON DI85	1x20341 2x20341 I:20340	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI86	1x20342 2x20342 I:20341	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI87	1x20343 2x20343 I:20342	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI88	1x20344 2x20344 I:20343	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI89	1x20345 2x20345 I:20344	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI90	1x20346 2x20346 I:20345	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI91	1x20347 2x20347 I:20346	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI92	1x20348 2x20348 I:20347	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI93	1x20349 2x20349 I:20348	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI94	1x20350 2x20350 I:20349	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI95	1x20351 2x20351 I:20350	1,0x01 B:01			BIT R/O	
LONG KEYPRESS START ON DI96	1x20352 2x20352 I:20351	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI97	1x20353 2x20353 I:20352	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI98	1x20354 2x20354 I:20353	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI99	1x20355 2x20355 I:20354	0,0x00 B:00			BIT R/O	

LONG KEYPRESS START ON DI100	1x20356 2x20356 I:20355	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI101	1x20357 2x20357 I:20356	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI102	1x20358 2x20358 I:20357	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI103	1x20359 2x20359 I:20358	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI104	1x20360 2x20360 I:20359	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI105	1x20361 2x20361 I:20360	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI106	1x20362 2x20362 I:20361	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI107	1x20363 2x20363 I:20362	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI108	1x20364 2x20364 I:20363	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI109	1x20365 2x20365 I:20364	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI110	1x20366 2x20366 I:20365	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI111	1x20367 2x20367 I:20366	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI112	1x20368 2x20368 I:20367	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI113	1x20369 2x20369 I:20368	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI114	1x20370 2x20370 I:20369	0,0x00 B:00			BIT R/O	

LONG KEYPRESS START ON DI115	1x20371 2x20371 I:20370	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI116	1x20372 2x20372 I:20371	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI117	1x20373 2x20373 I:20372	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI118	1x20374 2x20374 I:20373	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI119	1x20375 2x20375 I:20374	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI120	1x20376 2x20376 I:20375	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI121	1x20377 2x20377 I:20376	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI122	1x20378 2x20378 I:20377	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI123	1x20379 2x20379 I:20378	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI124	1x20380 2x20380 I:20379	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI125	1x20381 2x20381 I:20380	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI126	1x20382 2x20382 I:20381	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI127	1x20383 2x20383 I:20382	0,0x00 B:00			BIT R/O	
LONG KEYPRESS START ON DI128	1x20384 2x20384 I:20383	0,0x00 B:00			BIT R/O	
DIGITAL INPUTS: LONG KEYPRESS END EVENT ON DIGITAL INPUT DETECTED						

LONG KEYPRESS END ON DI1	1x20385 2x20385 I:20384	0,0x00 B:00			BIT R/O	
If a long keypress end event was detected on the digital input this bit inverts its last state						
LONG KEYPRESS END ON DI2	1x20386 2x20386 I:20385	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI3	1x20387 2x20387 I:20386	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI4	1x20388 2x20388 I:20387	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI5	1x20389 2x20389 I:20388	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI6	1x20390 2x20390 I:20389	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI7	1x20391 2x20391 I:20390	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI8	1x20392 2x20392 I:20391	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI9	1x20393 2x20393 I:20392	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI10	1x20394 2x20394 I:20393	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI11	1x20395 2x20395 I:20394	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI12	1x20396 2x20396 I:20395	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI13	1x20397 2x20397 I:20396	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI14	1x20398 2x20398 I:20397	0,0x00 B:00			BIT R/O	

LONG KEYPRESS END ON DI15	1x20399 2x20399 I:20398	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI16	1x20400 2x20400 I:20399	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI17	1x20401 2x20401 I:20400	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI18	1x20402 2x20402 I:20401	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI19	1x20403 2x20403 I:20402	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI20	1x20404 2x20404 I:20403	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI21	1x20405 2x20405 I:20404	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI22	1x20406 2x20406 I:20405	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI23	1x20407 2x20407 I:20406	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI24	1x20408 2x20408 I:20407	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI25	1x20409 2x20409 I:20408	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI26	1x20410 2x20410 I:20409	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI27	1x20411 2x20411 I:20410	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI28	1x20412 2x20412 I:20411	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI29	1x20413 2x20413 I:20412	0,0x00 B:00			BIT R/O	

LONG KEYPRESS END ON DI30	1x20414 2x20414 I:20413	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI31	1x20415 2x20415 I:20414	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI32	1x20416 2x20416 I:20415	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI33	1x20417 2x20417 I:20416	1,0x01 B:01			BIT R/O	
LONG KEYPRESS END ON DI34	1x20418 2x20418 I:20417	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI35	1x20419 2x20419 I:20418	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI36	1x20420 2x20420 I:20419	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI37	1x20421 2x20421 I:20420	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI38	1x20422 2x20422 I:20421	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI39	1x20423 2x20423 I:20422	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI40	1x20424 2x20424 I:20423	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI41	1x20425 2x20425 I:20424	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI42	1x20426 2x20426 I:20425	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI43	1x20427 2x20427 I:20426	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI44	1x20428 2x20428 I:20427	0,0x00 B:00			BIT R/O	

LONG KEYPRESS END ON DI45	1x20429 2x20429 I:20428	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI46	1x20430 2x20430 I:20429	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI47	1x20431 2x20431 I:20430	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI48	1x20432 2x20432 I:20431	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI49	1x20433 2x20433 I:20432	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI50	1x20434 2x20434 I:20433	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI51	1x20435 2x20435 I:20434	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI52	1x20436 2x20436 I:20435	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI53	1x20437 2x20437 I:20436	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI54	1x20438 2x20438 I:20437	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI55	1x20439 2x20439 I:20438	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI56	1x20440 2x20440 I:20439	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI57	1x20441 2x20441 I:20440	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI58	1x20442 2x20442 I:20441	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI59	1x20443 2x20443 I:20442	0,0x00 B:00			BIT R/O	

LONG KEYPRESS END ON DI60	1x20444 2x20444 I:20443	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI61	1x20445 2x20445 I:20444	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI62	1x20446 2x20446 I:20445	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI63	1x20447 2x20447 I:20446	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI64	1x20448 2x20448 I:20447	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI65	1x20449 2x20449 I:20448	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI66	1x20450 2x20450 I:20449	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI67	1x20451 2x20451 I:20450	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI68	1x20452 2x20452 I:20451	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI69	1x20453 2x20453 I:20452	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI70	1x20454 2x20454 I:20453	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI71	1x20455 2x20455 I:20454	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI72	1x20456 2x20456 I:20455	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI73	1x20457 2x20457 I:20456	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI74	1x20458 2x20458 I:20457	0,0x00 B:00			BIT R/O	

LONG KEYPRESS END ON DI75	1x20459 2x20459 I:20458	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI76	1x20460 2x20460 I:20459	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI77	1x20461 2x20461 I:20460	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI78	1x20462 2x20462 I:20461	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI79	1x20463 2x20463 I:20462	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI80	1x20464 2x20464 I:20463	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI81	1x20465 2x20465 I:20464	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI82	1x20466 2x20466 I:20465	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI83	1x20467 2x20467 I:20466	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI84	1x20468 2x20468 I:20467	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI85	1x20469 2x20469 I:20468	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI86	1x20470 2x20470 I:20469	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI87	1x20471 2x20471 I:20470	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI88	1x20472 2x20472 I:20471	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI89	1x20473 2x20473 I:20472	0,0x00 B:00			BIT R/O	

LONG KEYPRESS END ON DI90	1x20474 2x20474 I:20473	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI91	1x20475 2x20475 I:20474	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI92	1x20476 2x20476 I:20475	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI93	1x20477 2x20477 I:20476	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI94	1x20478 2x20478 I:20477	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI95	1x20479 2x20479 I:20478	1,0x01 B:01			BIT R/O	
LONG KEYPRESS END ON DI96	1x20480 2x20480 I:20479	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI97	1x20481 2x20481 I:20480	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI98	1x20482 2x20482 I:20481	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI99	1x20483 2x20483 I:20482	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI100	1x20484 2x20484 I:20483	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI101	1x20485 2x20485 I:20484	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI102	1x20486 2x20486 I:20485	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI103	1x20487 2x20487 I:20486	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI104	1x20488 2x20488 I:20487	0,0x00 B:00			BIT R/O	

LONG KEYPRESS END ON DI105	1x20489 2x20489 I:20488	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI106	1x20490 2x20490 I:20489	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI107	1x20491 2x20491 I:20490	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI108	1x20492 2x20492 I:20491	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI109	1x20493 2x20493 I:20492	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI110	1x20494 2x20494 I:20493	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI111	1x20495 2x20495 I:20494	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI112	1x20496 2x20496 I:20495	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI113	1x20497 2x20497 I:20496	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI114	1x20498 2x20498 I:20497	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI115	1x20499 2x20499 I:20498	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI116	1x20500 2x20500 I:20499	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI117	1x20501 2x20501 I:20500	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI118	1x20502 2x20502 I:20501	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI119	1x20503 2x20503 I:20502	0,0x00 B:00			BIT R/O	

LONG KEYPRESS END ON DI120	1x20504 2x20504 I:20503	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI121	1x20505 2x20505 I:20504	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI122	1x20506 2x20506 I:20505	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI123	1x20507 2x20507 I:20506	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI124	1x20508 2x20508 I:20507	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI125	1x20509 2x20509 I:20508	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI126	1x20510 2x20510 I:20509	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI127	1x20511 2x20511 I:20510	0,0x00 B:00			BIT R/O	
LONG KEYPRESS END ON DI128	1x20512 2x20512 I:20511	0,0x00 B:00			BIT R/O	
DIGITAL INPUTS: RISING EDGE ON DIGITAL INPUT DETECTED						
RISING EDGE ON DI1	1x20513 2x20513 I:20512	0,0x00 B:00			BIT R/O	
If a rising edge was detected on the digital input this bit inverts its last state						
RISING EDGE ON DI2	1x20514 2x20514 I:20513	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI3	1x20515 2x20515 I:20514	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI4	1x20516 2x20516 I:20515	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI5	1x20517 2x20517 I:20516	0,0x00 B:00			BIT R/O	

RISING EDGE ON DI6	1x20518 2x20518 I:20517	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI7	1x20519 2x20519 I:20518	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI8	1x20520 2x20520 I:20519	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI9	1x20521 2x20521 I:20520	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI10	1x20522 2x20522 I:20521	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI11	1x20523 2x20523 I:20522	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI12	1x20524 2x20524 I:20523	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI13	1x20525 2x20525 I:20524	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI14	1x20526 2x20526 I:20525	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI15	1x20527 2x20527 I:20526	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI16	1x20528 2x20528 I:20527	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI17	1x20529 2x20529 I:20528	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI18	1x20530 2x20530 I:20529	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI19	1x20531 2x20531 I:20530	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI20	1x20532 2x20532 I:20531	0,0x00 B:00			BIT R/O	

RISING EDGE ON DI21	1x20533 2x20533 I:20532	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI22	1x20534 2x20534 I:20533	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI23	1x20535 2x20535 I:20534	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI24	1x20536 2x20536 I:20535	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI25	1x20537 2x20537 I:20536	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI26	1x20538 2x20538 I:20537	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI27	1x20539 2x20539 I:20538	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI28	1x20540 2x20540 I:20539	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI29	1x20541 2x20541 I:20540	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI30	1x20542 2x20542 I:20541	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI31	1x20543 2x20543 I:20542	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI32	1x20544 2x20544 I:20543	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI33	1x20545 2x20545 I:20544	1,0x01 B:01			BIT R/O	
RISING EDGE ON DI34	1x20546 2x20546 I:20545	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI35	1x20547 2x20547 I:20546	0,0x00 B:00			BIT R/O	

RISING EDGE ON DI36	1x20548 2x20548 I:20547	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI37	1x20549 2x20549 I:20548	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI38	1x20550 2x20550 I:20549	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI39	1x20551 2x20551 I:20550	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI40	1x20552 2x20552 I:20551	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI41	1x20553 2x20553 I:20552	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI42	1x20554 2x20554 I:20553	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI43	1x20555 2x20555 I:20554	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI44	1x20556 2x20556 I:20555	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI45	1x20557 2x20557 I:20556	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI46	1x20558 2x20558 I:20557	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI47	1x20559 2x20559 I:20558	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI48	1x20560 2x20560 I:20559	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI49	1x20561 2x20561 I:20560	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI50	1x20562 2x20562 I:20561	0,0x00 B:00			BIT R/O	

RISING EDGE ON DI51	1x20563 2x20563 I:20562	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI52	1x20564 2x20564 I:20563	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI53	1x20565 2x20565 I:20564	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI54	1x20566 2x20566 I:20565	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI55	1x20567 2x20567 I:20566	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI56	1x20568 2x20568 I:20567	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI57	1x20569 2x20569 I:20568	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI58	1x20570 2x20570 I:20569	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI59	1x20571 2x20571 I:20570	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI60	1x20572 2x20572 I:20571	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI61	1x20573 2x20573 I:20572	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI62	1x20574 2x20574 I:20573	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI63	1x20575 2x20575 I:20574	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI64	1x20576 2x20576 I:20575	1,0x01 B:01			BIT R/O	
RISING EDGE ON DI65	1x20577 2x20577 I:20576	0,0x00 B:00			BIT R/O	

RISING EDGE ON DI66	1x20578 2x20578 I:20577	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI67	1x20579 2x20579 I:20578	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI68	1x20580 2x20580 I:20579	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI69	1x20581 2x20581 I:20580	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI70	1x20582 2x20582 I:20581	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI71	1x20583 2x20583 I:20582	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI72	1x20584 2x20584 I:20583	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI73	1x20585 2x20585 I:20584	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI74	1x20586 2x20586 I:20585	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI75	1x20587 2x20587 I:20586	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI76	1x20588 2x20588 I:20587	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI77	1x20589 2x20589 I:20588	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI78	1x20590 2x20590 I:20589	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI79	1x20591 2x20591 I:20590	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI80	1x20592 2x20592 I:20591	0,0x00 B:00			BIT R/O	

RISING EDGE ON DI81	1x20593 2x20593 I:20592	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI82	1x20594 2x20594 I:20593	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI83	1x20595 2x20595 I:20594	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI84	1x20596 2x20596 I:20595	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI85	1x20597 2x20597 I:20596	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI86	1x20598 2x20598 I:20597	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI87	1x20599 2x20599 I:20598	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI88	1x20600 2x20600 I:20599	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI89	1x20601 2x20601 I:20600	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI90	1x20602 2x20602 I:20601	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI91	1x20603 2x20603 I:20602	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI92	1x20604 2x20604 I:20603	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI93	1x20605 2x20605 I:20604	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI94	1x20606 2x20606 I:20605	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI95	1x20607 2x20607 I:20606	1,0x01 B:01			BIT R/O	

RISING EDGE ON DI96	1x20608 2x20608 I:20607	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI97	1x20609 2x20609 I:20608	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI98	1x20610 2x20610 I:20609	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI99	1x20611 2x20611 I:20610	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI100	1x20612 2x20612 I:20611	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI101	1x20613 2x20613 I:20612	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI102	1x20614 2x20614 I:20613	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI103	1x20615 2x20615 I:20614	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI104	1x20616 2x20616 I:20615	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI105	1x20617 2x20617 I:20616	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI106	1x20618 2x20618 I:20617	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI107	1x20619 2x20619 I:20618	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI108	1x20620 2x20620 I:20619	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI109	1x20621 2x20621 I:20620	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI110	1x20622 2x20622 I:20621	0,0x00 B:00			BIT R/O	

RISING EDGE ON DI111	1x20623 2x20623 I:20622	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI112	1x20624 2x20624 I:20623	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI113	1x20625 2x20625 I:20624	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI114	1x20626 2x20626 I:20625	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI115	1x20627 2x20627 I:20626	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI116	1x20628 2x20628 I:20627	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI117	1x20629 2x20629 I:20628	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI118	1x20630 2x20630 I:20629	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI119	1x20631 2x20631 I:20630	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI120	1x20632 2x20632 I:20631	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI121	1x20633 2x20633 I:20632	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI122	1x20634 2x20634 I:20633	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI123	1x20635 2x20635 I:20634	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI124	1x20636 2x20636 I:20635	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI125	1x20637 2x20637 I:20636	0,0x00 B:00			BIT R/O	

RISING EDGE ON DI126	1x20638 2x20638 I:20637	1,0x01 B:01			BIT R/O	
RISING EDGE ON DI127	1x20639 2x20639 I:20638	0,0x00 B:00			BIT R/O	
RISING EDGE ON DI128	1x20640 2x20640 I:20639	1,0x01 B:01			BIT R/O	
DIGITAL INPUTS: FALLING EDGE ON DIGITAL INPUT DETECTED						
FALLING EDGE ON DI1	1x20641 2x20641 I:20640	0,0x00 B:00			BIT R/O	
If a falling edge was detected on the digital input this bit inverts its last state						
FALLING EDGE ON DI2	1x20642 2x20642 I:20641	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI3	1x20643 2x20643 I:20642	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI4	1x20644 2x20644 I:20643	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI5	1x20645 2x20645 I:20644	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI6	1x20646 2x20646 I:20645	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI7	1x20647 2x20647 I:20646	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI8	1x20648 2x20648 I:20647	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI9	1x20649 2x20649 I:20648	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI10	1x20650 2x20650 I:20649	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI11	1x20651 2x20651 I:20650	0,0x00 B:00			BIT R/O	

FALLING EDGE ON DI12	1x20652 2x20652 I:20651	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI13	1x20653 2x20653 I:20652	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI14	1x20654 2x20654 I:20653	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI15	1x20655 2x20655 I:20654	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI16	1x20656 2x20656 I:20655	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI17	1x20657 2x20657 I:20656	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI18	1x20658 2x20658 I:20657	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI19	1x20659 2x20659 I:20658	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI20	1x20660 2x20660 I:20659	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI21	1x20661 2x20661 I:20660	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI22	1x20662 2x20662 I:20661	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI23	1x20663 2x20663 I:20662	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI24	1x20664 2x20664 I:20663	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI25	1x20665 2x20665 I:20664	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI26	1x20666 2x20666 I:20665	0,0x00 B:00			BIT R/O	

FALLING EDGE ON DI27	1x20667 2x20667 I:20666	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI28	1x20668 2x20668 I:20667	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI29	1x20669 2x20669 I:20668	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI30	1x20670 2x20670 I:20669	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI31	1x20671 2x20671 I:20670	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI32	1x20672 2x20672 I:20671	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI33	1x20673 2x20673 I:20672	1,0x01 B:01			BIT R/O	
FALLING EDGE ON DI34	1x20674 2x20674 I:20673	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI35	1x20675 2x20675 I:20674	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI36	1x20676 2x20676 I:20675	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI37	1x20677 2x20677 I:20676	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI38	1x20678 2x20678 I:20677	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI39	1x20679 2x20679 I:20678	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI40	1x20680 2x20680 I:20679	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI41	1x20681 2x20681 I:20680	0,0x00 B:00			BIT R/O	

FALLING EDGE ON DI42	1x20682 2x20682 I:20681	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI43	1x20683 2x20683 I:20682	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI44	1x20684 2x20684 I:20683	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI45	1x20685 2x20685 I:20684	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI46	1x20686 2x20686 I:20685	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI47	1x20687 2x20687 I:20686	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI48	1x20688 2x20688 I:20687	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI49	1x20689 2x20689 I:20688	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI50	1x20690 2x20690 I:20689	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI51	1x20691 2x20691 I:20690	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI52	1x20692 2x20692 I:20691	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI53	1x20693 2x20693 I:20692	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI54	1x20694 2x20694 I:20693	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI55	1x20695 2x20695 I:20694	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI56	1x20696 2x20696 I:20695	0,0x00 B:00			BIT R/O	

FALLING EDGE ON DI57	1x20697 2x20697 I:20696	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI58	1x20698 2x20698 I:20697	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI59	1x20699 2x20699 I:20698	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI60	1x20700 2x20700 I:20699	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI61	1x20701 2x20701 I:20700	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI62	1x20702 2x20702 I:20701	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI63	1x20703 2x20703 I:20702	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI64	1x20704 2x20704 I:20703	1,0x01 B:01			BIT R/O	
FALLING EDGE ON DI65	1x20705 2x20705 I:20704	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI66	1x20706 2x20706 I:20705	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI67	1x20707 2x20707 I:20706	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI68	1x20708 2x20708 I:20707	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI69	1x20709 2x20709 I:20708	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI70	1x20710 2x20710 I:20709	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI71	1x20711 2x20711 I:20710	0,0x00 B:00			BIT R/O	

FALLING EDGE ON DI72	1x20712 2x20712 I:20711	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI73	1x20713 2x20713 I:20712	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI74	1x20714 2x20714 I:20713	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI75	1x20715 2x20715 I:20714	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI76	1x20716 2x20716 I:20715	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI77	1x20717 2x20717 I:20716	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI78	1x20718 2x20718 I:20717	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI79	1x20719 2x20719 I:20718	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI80	1x20720 2x20720 I:20719	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI81	1x20721 2x20721 I:20720	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI82	1x20722 2x20722 I:20721	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI83	1x20723 2x20723 I:20722	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI84	1x20724 2x20724 I:20723	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI85	1x20725 2x20725 I:20724	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI86	1x20726 2x20726 I:20725	0,0x00 B:00			BIT R/O	

FALLING EDGE ON DI87	1x20727 2x20727 I:20726	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI88	1x20728 2x20728 I:20727	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI89	1x20729 2x20729 I:20728	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI90	1x20730 2x20730 I:20729	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI91	1x20731 2x20731 I:20730	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI92	1x20732 2x20732 I:20731	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI93	1x20733 2x20733 I:20732	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI94	1x20734 2x20734 I:20733	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI95	1x20735 2x20735 I:20734	1,0x01 B:01			BIT R/O	
FALLING EDGE ON DI96	1x20736 2x20736 I:20735	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI97	1x20737 2x20737 I:20736	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI98	1x20738 2x20738 I:20737	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI99	1x20739 2x20739 I:20738	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI100	1x20740 2x20740 I:20739	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI101	1x20741 2x20741 I:20740	0,0x00 B:00			BIT R/O	

FALLING EDGE ON DI102	1x20742 2x20742 I:20741	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI103	1x20743 2x20743 I:20742	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI104	1x20744 2x20744 I:20743	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI105	1x20745 2x20745 I:20744	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI106	1x20746 2x20746 I:20745	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI107	1x20747 2x20747 I:20746	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI108	1x20748 2x20748 I:20747	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI109	1x20749 2x20749 I:20748	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI110	1x20750 2x20750 I:20749	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI111	1x20751 2x20751 I:20750	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI112	1x20752 2x20752 I:20751	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI113	1x20753 2x20753 I:20752	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI114	1x20754 2x20754 I:20753	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI115	1x20755 2x20755 I:20754	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI116	1x20756 2x20756 I:20755	0,0x00 B:00			BIT R/O	

FALLING EDGE ON DI117	1x20757 2x20757 I:20756	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI118	1x20758 2x20758 I:20757	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI119	1x20759 2x20759 I:20758	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI120	1x20760 2x20760 I:20759	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI121	1x20761 2x20761 I:20760	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI122	1x20762 2x20762 I:20761	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI123	1x20763 2x20763 I:20762	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI124	1x20764 2x20764 I:20763	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI125	1x20765 2x20765 I:20764	0,0x00 B:00			BIT R/O	
FALLING EDGE ON DI126	1x20766 2x20766 I:20765	1,0x01 B:01			BIT R/O	
FALLING EDGE ON DI127	1x20767 2x20767 I:20766	1,0x01 B:01			BIT R/O	
FALLING EDGE ON DI128	1x20768 2x20768 I:20767	1,0x01 B:01			BIT R/O	

Register NAME Command NAME	MODBUS Register ASCII Command	Register VALUE ASCII Command	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
STATUS DIGITAL INPUTS						
DI1	3x00001 4x00001 I:0	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI1:0=OFF				
Current state of the digital input DIx =0:DI is OFF, =1:DI is ON						
DI2	3x00002 4x00002 I:1	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI2:0=OFF				
DI3	3x00003 4x00003 I:2	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI3:0=OFF				
DI4	3x00004 4x00004 I:3	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI4:0=OFF				
DI5	3x00005 4x00005 I:4	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI5:0=OFF				
DI6	3x00006 4x00006 I:5	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI6:0=OFF				
DI7	3x00007 4x00007 I:6	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI7:0=OFF				
DI8	3x00008 4x00008 I:7	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI8:0=OFF				
DI9	3x00009 4x00009 I:8	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI9:0=OFF				

DI10	3x00010 4x00010 I:9	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI10:0=OFF				
DI11	3x00011 4x00011 I:10	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI11:0=OFF				
DI12	3x00012 4x00012 I:11	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI12:0=OFF				
DI13	3x00013 4x00013 I:12	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI13:0=OFF				
DI14	3x00014 4x00014 I:13	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI14:0=OFF				
DI15	3x00015 4x00015 I:14	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI15:0=OFF				
DI16	3x00016 4x00016 I:15	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI16:0=OFF				
DI17	3x00017 4x00017 I:16	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI17:0=OFF				
DI18	3x00018 4x00018 I:17	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI18:0=OFF				
DI19	3x00019 4x00019 I:18	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI19:0=OFF				
DI20	3x00020 4x00020 I:19	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI20:0=OFF				

DI21	3x00021 4x00021 I:20	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI21:0=OFF				
DI22	3x00022 4x00022 I:21	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI22:0=OFF				
DI23	3x00023 4x00023 I:22	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI23:0=OFF				
DI24	3x00024 4x00024 I:23	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI24:1=ON				
DI25	3x00025 4x00025 I:24	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI25:0=OFF				
DI26	3x00026 4x00026 I:25	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI26:0=OFF				
DI27	3x00027 4x00027 I:26	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI27:0=OFF				
DI28	3x00028 4x00028 I:27	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI28:0=OFF				
DI29	3x00029 4x00029 I:28	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI29:0=OFF				
DI30	3x00030 4x00030 I:29	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI30:0=OFF				
DI31	3x00031 4x00031 I:30	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI31:0=OFF				

DI32	3x00032 4x00032 I:31	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI32:0=OFF				
DI33	3x00033 4x00033 I:32	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI33:0=OFF				
DI34	3x00034 4x00034 I:33	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI34:0=OFF				
DI35	3x00035 4x00035 I:34	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI35:0=OFF				
DI36	3x00036 4x00036 I:35	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI36:0=OFF				
DI37	3x00037 4x00037 I:36	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI37:0=OFF				
DI38	3x00038 4x00038 I:37	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI38:0=OFF				
DI39	3x00039 4x00039 I:38	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI39:0=OFF				
DI40	3x00040 4x00040 I:39	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI40:0=OFF				
DI41	3x00041 4x00041 I:40	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI41:0=OFF				
DI42	3x00042 4x00042 I:41	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI42:0=OFF				

DI43	3x00043 4x00043 I:42	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI43:0=OFF				
DI44	3x00044 4x00044 I:43	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI44:0=OFF				
DI45	3x00045 4x00045 I:44	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI45:0=OFF				
DI46	3x00046 4x00046 I:45	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI46:1=ON				
DI47	3x00047 4x00047 I:46	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI47:0=OFF				
DI48	3x00048 4x00048 I:47	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI48:0=OFF				
DI49	3x00049 4x00049 I:48	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI49:0=OFF				
DI50	3x00050 4x00050 I:49	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI50:0=OFF				
DI51	3x00051 4x00051 I:50	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI51:0=OFF				
DI52	3x00052 4x00052 I:51	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI52:0=OFF				
DI53	3x00053 4x00053 I:52	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI53:0=OFF				

DI54	3x00054 4x00054 I:53	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI54:0=OFF				
DI55	3x00055 4x00055 I:54	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI55:0=OFF				
DI56	3x00056 4x00056 I:55	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI56:0=OFF				
DI57	3x00057 4x00057 I:56	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI57:0=OFF				
DI58	3x00058 4x00058 I:57	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI58:0=OFF				
DI59	3x00059 4x00059 I:58	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI59:0=OFF				
DI60	3x00060 4x00060 I:59	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI60:0=OFF				
DI61	3x00061 4x00061 I:60	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI61:0=OFF				
DI62	3x00062 4x00062 I:61	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI62:0=OFF				
DI63	3x00063 4x00063 I:62	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI63:0=OFF				
DI64	3x00064 4x00064 I:63	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI64:0=OFF				

DI65	3x00065 4x00065 I:64	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI65:0=OFF				
DI66	3x00066 4x00066 I:65	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI66:0=OFF				
DI67	3x00067 4x00067 I:66	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI67:0=OFF				
DI68	3x00068 4x00068 I:67	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI68:0=OFF				
DI69	3x00069 4x00069 I:68	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI69:0=OFF				
DI70	3x00070 4x00070 I:69	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI70:0=OFF				
DI71	3x00071 4x00071 I:70	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI71:0=OFF				
DI72	3x00072 4x00072 I:71	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI72:0=OFF				
DI73	3x00073 4x00073 I:72	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI73:0=OFF				
DI74	3x00074 4x00074 I:73	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI74:0=OFF				
DI75	3x00075 4x00075 I:74	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI75:0=OFF				

DI76	3x00076 4x00076 I:75	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI76:0=OFF				
DI77	3x00077 4x00077 I:76	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI77:0=OFF				
DI78	3x00078 4x00078 I:77	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI78:0=OFF				
DI79	3x00079 4x00079 I:78	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI79:0=OFF				
DI80	3x00080 4x00080 I:79	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI80:0=OFF				
DI81	3x00081 4x00081 I:80	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI81:0=OFF				
DI82	3x00082 4x00082 I:81	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI82:0=OFF				
DI83	3x00083 4x00083 I:82	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI83:0=OFF				
DI84	3x00084 4x00084 I:83	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI84:0=OFF				
DI85	3x00085 4x00085 I:84	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI85:0=OFF				
DI86	3x00086 4x00086 I:85	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI86:0=OFF				

DI87	3x00087 4x00087 I:86	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI87:0=OFF				
DI88	3x00088 4x00088 I:87	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI88:0=OFF				
DI89	3x00089 4x00089 I:88	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI89:0=OFF				
DI90	3x00090 4x00090 I:89	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI90:0=OFF				
DI91	3x00091 4x00091 I:90	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI91:0=OFF				
DI92	3x00092 4x00092 I:91	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI92:0=OFF				
DI93	3x00093 4x00093 I:92	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI93:0=OFF				
DI94	3x00094 4x00094 I:93	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI94:0=OFF				
DI95	3x00095 4x00095 I:94	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI95:0=OFF				
DI96	3x00096 4x00096 I:95	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI96:0=OFF				
DI97	3x00097 4x00097 I:96	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI97:0=OFF				

DI98	3x00098 4x00098 I:97	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI98:0=OFF				
DI99	3x00099 4x00099 I:98	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI99:0=OFF				
DI100	3x00100 4x00100 I:99	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI100:0=OFF				
DI101	3x00101 4x00101 I:100	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI101:0=OFF				
DI102	3x00102 4x00102 I:101	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI102:0=OFF				
DI103	3x00103 4x00103 I:102	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI103:0=OFF				
DI104	3x00104 4x00104 I:103	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI104:0=OFF				
DI105	3x00105 4x00105 I:104	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI105:0=OFF				
DI106	3x00106 4x00106 I:105	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI106:0=OFF				
DI107	3x00107 4x00107 I:106	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI107:0=OFF				
DI108	3x00108 4x00108 I:107	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI108:0=OFF				

DI109	3x00109 4x00109 I:108	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI109:0=OFF				
DI110	3x00110 4x00110 I:109	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI110:0=OFF				
DI111	3x00111 4x00111 I:110	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI111:0=OFF				
DI112	3x00112 4x00112 I:111	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI112:0=OFF				
DI113	3x00113 4x00113 I:112	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI113:0=OFF				
DI114	3x00114 4x00114 I:113	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI114:0=OFF				
DI115	3x00115 4x00115 I:114	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI115:0=OFF				
DI116	3x00116 4x00116 I:115	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI116:0=OFF				
DI117	3x00117 4x00117 I:116	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI117:0=OFF				
DI118	3x00118 4x00118 I:117	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI118:0=OFF				
DI119	3x00119 4x00119 I:118	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI119:0=OFF				

DI120	3x00120 4x00120 I:119	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI120:0=OFF				
DI121	3x00121 4x00121 I:120	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI121:0=OFF				
DI122	3x00122 4x00122 I:121	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI122:0=OFF				
DI123	3x00123 4x00123 I:122	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI123:0=OFF				
DI124	3x00124 4x00124 I:123	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI124:0=OFF				
DI125	3x00125 4x00125 I:124	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI125:0=OFF				
DI126	3x00126 4x00126 I:125	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI126:0=OFF				
DI127	3x00127 4x00127 I:126	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI127:0=OFF				
DI128	3x00128 4x00128 I:127	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI128:0=OFF				
DIGITAL INPUTS: RESET						
RESET COUNTERS	3x10000 4x10000 I:9999	0,0x0000 B:00 00		1:PERFORM RESET	UINT16 R/W	NO
If this register is written to 1, all internal edge counters and event counters are set to 0. 0 is always returned when reading.						
HAS DIS CHANGED	3x10001 4x10001 I:10000	3,0x0003 B:00 03			UINT16 R/O	
		3 event(s)				

As soon as the module registrates an event on one of the available digital inputs, this global event counter is incremented by 1. Possible events are: Detection of a short keypress Detection of the start of a long keypress Detection of the end of a long keypress						
STATUS OF ALL DIS DI1..DI16	3x10002 4x10002 I:10001	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI1:0=OFF				
		Actual state of DI2:0=OFF				
		Actual state of DI3:0=OFF				
		Actual state of DI4:0=OFF				
		Actual state of DI5:0=OFF				
		Actual state of DI6:0=OFF				
		Actual state of DI7:0=OFF				
		Actual state of DI8:0=OFF				
		Actual state of DI9:0=OFF				
		Actual state of DI10:0=OFF				
		Actual state of DI11:0=OFF				
		Actual state of DI12:0=OFF				
		Actual state of DI13:0=OFF				
		Actual state of DI14:0=OFF				
		Actual state of DI15:0=OFF				
		Actual state of DI16:0=OFF				
Actual state of all digital inputs DI1..DI16 Bit 0: =0:DI1 is OFF, =1:DI1 is ON Bit 1: =0:DI2 is OFF, =1:DI2 is ON ... Bit 14: =0:DI15 is OFF, =1:DI15 is ON Bit 15: =0:DI16 is OFF, =1:DI16 is ON						
STATUS OF ALL DIS DI17..DI32	3x10003 4x10003 I:10002	128,0x0080 B:00 80			UINT16 R/O	
		Actual state of DI17:0=OFF				
		Actual state of DI18:0=OFF				
		Actual state of DI19:0=OFF				
		Actual state of DI20:0=OFF				
		Actual state of DI21:0=OFF				
		Actual state of DI22:0=OFF				
		Actual state of DI23:0=OFF				
		Actual state of DI24:1=ON				
		Actual state of DI25:0=OFF				
		Actual state of DI26:0=OFF				
		Actual state of DI27:0=OFF				
		Actual state of DI28:0=OFF				
		Actual state of DI29:0=OFF				

		Actual state of DI30:0=OFF			
		Actual state of DI31:0=OFF			
		Actual state of DI32:0=OFF			
Actual state of all digital inputs DI17..DI32 Bit 0: =0:DI17 is OFF, =1:DI17 is ON Bit 1: =0:DI18 is OFF, =1:DI18 is ON ... Bit 30: =0:DI31 is OFF, =1:DI31 is ON Bit 31: =0:DI32 is OFF, =1:DI32 is ON					
STATUS OF ALL DIS DI33..DI48	3x10004 4x10004 I:10003	128,0x0080 B:00 80			UINT16 R/O
		Actual state of DI33:0=OFF			
		Actual state of DI34:0=OFF			
		Actual state of DI35:0=OFF			
		Actual state of DI36:0=OFF			
		Actual state of DI37:0=OFF			
		Actual state of DI38:0=OFF			
		Actual state of DI39:0=OFF			
		Actual state of DI40:1=ON			
		Actual state of DI41:0=OFF			
		Actual state of DI42:0=OFF			
		Actual state of DI43:0=OFF			
		Actual state of DI44:0=OFF			
		Actual state of DI45:0=OFF			
		Actual state of DI46:0=OFF			
		Actual state of DI47:0=OFF			
		Actual state of DI48:0=OFF			
Actual state of all digital inputs DI33..DI48 Bit 0: =0:DI33 is OFF, =1:DI33 is ON Bit 1: =0:DI34 is OFF, =1:DI34 is ON ... Bit 30: =0:DI47 is OFF, =1:DI47 is ON Bit 31: =0:DI48 is OFF, =1:DI48 is ON					
STATUS OF ALL DIS DI49..DI64	3x10005 4x10005 I:10004	128,0x0080 B:00 80			UINT16 R/O
		Actual state of DI49:0=OFF			
		Actual state of DI50:0=OFF			
		Actual state of DI51:0=OFF			
		Actual state of DI52:0=OFF			
		Actual state of DI53:0=OFF			
		Actual state of DI54:0=OFF			
		Actual state of DI55:0=OFF			
		Actual state of DI56:1=ON			

		Actual state of DI57:0=OFF			
		Actual state of DI58:0=OFF			
		Actual state of DI59:0=OFF			
		Actual state of DI60:0=OFF			
		Actual state of DI61:0=OFF			
		Actual state of DI62:0=OFF			
		Actual state of DI63:0=OFF			
		Actual state of DI64:0=OFF			
Actual state of all digital inputs DI49..DI64 Bit 0: =0:DI49 is OFF, =1:DI49 is ON Bit 1: =0:DI50 is OFF, =1:DI50 is ON ... Bit 30: =0:DI63 is OFF, =1:DI63 is ON Bit 31: =0:DI64 is OFF, =1:DI64 is ON					
STATUS OF ALL DIS DI65..DI80	3x10006 4x10006 I:10005	0,0x0000 B:00 00			UINT16 R/O
		Actual state of DI65:0=OFF			
		Actual state of DI66:0=OFF			
		Actual state of DI67:0=OFF			
		Actual state of DI68:0=OFF			
		Actual state of DI69:0=OFF			
		Actual state of DI70:0=OFF			
		Actual state of DI71:0=OFF			
		Actual state of DI72:0=OFF			
		Actual state of DI73:0=OFF			
		Actual state of DI74:0=OFF			
		Actual state of DI75:0=OFF			
		Actual state of DI76:0=OFF			
		Actual state of DI77:0=OFF			
		Actual state of DI78:0=OFF			
		Actual state of DI79:0=OFF			
		Actual state of DI80:0=OFF			
Actual state of all digital inputs DI65..DI80 Bit 0: =0:DI65 is OFF, =1:DI65 is ON Bit 1: =0:DI66 is OFF, =1:DI66 is ON ... Bit 14: =0:DI79 is OFF, =1:DI79 is ON Bit 15: =0:DI80 is OFF, =1:DI80 is ON					
STATUS OF ALL DIS DI81..DI96	3x10007 4x10007 I:10006	128,0x0080 B:00 80			UINT16 R/O
		Actual state of DI81:0=OFF			
		Actual state of DI82:0=OFF			
		Actual state of DI83:0=OFF			
		Actual state of DI84:0=OFF			

		Actual state of DI85:0=OFF			
		Actual state of DI86:0=OFF			
		Actual state of DI87:0=OFF			
		Actual state of DI88:1=ON			
		Actual state of DI89:0=OFF			
		Actual state of DI90:0=OFF			
		Actual state of DI91:0=OFF			
		Actual state of DI92:0=OFF			
		Actual state of DI93:0=OFF			
		Actual state of DI94:0=OFF			
		Actual state of DI95:0=OFF			
		Actual state of DI96:0=OFF			
Actual state of all digital inputs DI81..DI96 Bit 0: =0:DI81 is OFF, =1:DI81 is ON Bit 1: =0:DI82 is OFF, =1:DI82 is ON ... Bit 30: =0:DI95 is OFF, =1:DI95 is ON Bit 31: =0:DI96 is OFF, =1:DI96 is ON					
STATUS OF ALL DIS DI97..DI112	3x10008 4x10008 I:10007	128,0x0080 B:00 80			UINT16 R/O
		Actual state of DI97:0=OFF			
		Actual state of DI98:0=OFF			
		Actual state of DI99:0=OFF			
		Actual state of DI100:0=OFF			
		Actual state of DI101:0=OFF			
		Actual state of DI102:0=OFF			
		Actual state of DI103:0=OFF			
		Actual state of DI104:1=ON			
		Actual state of DI105:0=OFF			
		Actual state of DI106:0=OFF			
		Actual state of DI107:0=OFF			
		Actual state of DI108:0=OFF			
		Actual state of DI109:0=OFF			
		Actual state of DI110:0=OFF			
		Actual state of DI111:0=OFF			
		Actual state of DI112:0=OFF			
Actual state of all digital inputs DI97..DI112 Bit 0: =0:DI97 is OFF, =1:DI97 is ON Bit 1: =0:DI98 is OFF, =1:DI98 is ON ... Bit 30: =0:DI111 is OFF, =1:DI111 is ON Bit 31: =0:DI112 is OFF, =1:DI112 is ON					
STATUS OF ALL DIS DI113..DI128	3x10009 4x10009 I:10008	128,0x0080 B:00 80			UINT16 R/O

	Actual state of DI113:0=OFF		
	Actual state of DI114:0=OFF		
	Actual state of DI115:0=OFF		
	Actual state of DI116:0=OFF		
	Actual state of DI117:0=OFF		
	Actual state of DI118:0=OFF		
	Actual state of DI119:0=OFF		
	Actual state of DI120:1=ON		
	Actual state of DI121:0=OFF		
	Actual state of DI122:0=OFF		
	Actual state of DI123:0=OFF		
	Actual state of DI124:0=OFF		
	Actual state of DI125:0=OFF		
	Actual state of DI126:0=OFF		
	Actual state of DI127:0=OFF		
	Actual state of DI128:0=OFF		

Actual state of all digital inputs DI113..DI128

Bit 0: =0:DI113 is OFF, =1:DI113 is ON

Bit 1: =0:DI114 is OFF, =1:DI114 is ON

...

Bit 30: =0:DI127 is OFF, =1:DI127 is ON

Bit 31: =0:DI128 is OFF, =1:DI128 is ON

STATUS REAL DIGITAL INPUTS

DI1	3x15001 4x15001 I:15000	0,0x0000 B:00 00			UINT16 R/O	
	Actual state of DI1:0=OFF					
Current state of the digital input DIx with the internal software filter to suppress glitches or spike on this line =0:DI is OFF, =1:DI is ON						
DI2	3x15002 4x15002 I:15001	0,0x0000 B:00 00			UINT16 R/O	
	Actual state of DI2:0=OFF					
DI3	3x15003 4x15003 I:15002	0,0x0000 B:00 00			UINT16 R/O	
	Actual state of DI3:0=OFF					
DI4	3x15004 4x15004 I:15003	0,0x0000 B:00 00			UINT16 R/O	
	Actual state of DI4:0=OFF					
DI5	3x15005 4x15005 I:15004	0,0x0000 B:00 00			UINT16 R/O	
	Actual state of DI5:0=OFF					

DI6	3x15006 4x15006 I:15005	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI6:0=OFF				
DI7	3x15007 4x15007 I:15006	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI7:0=OFF				
DI8	3x15008 4x15008 I:15007	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI8:0=OFF				
DI9	3x15009 4x15009 I:15008	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI9:0=OFF				
DI10	3x15010 4x15010 I:15009	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI10:0=OFF				
DI11	3x15011 4x15011 I:15010	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI11:0=OFF				
DI12	3x15012 4x15012 I:15011	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI12:0=OFF				
DI13	3x15013 4x15013 I:15012	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI13:0=OFF				
DI14	3x15014 4x15014 I:15013	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI14:0=OFF				
DI15	3x15015 4x15015 I:15014	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI15:0=OFF				
DI16	3x15016 4x15016 I:15015	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI16:0=OFF				

DI17	3x15017 4x15017 I:15016	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI17:0=OFF				
DI18	3x15018 4x15018 I:15017	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI18:0=OFF				
DI19	3x15019 4x15019 I:15018	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI19:0=OFF				
DI20	3x15020 4x15020 I:15019	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI20:0=OFF				
DI21	3x15021 4x15021 I:15020	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI21:0=OFF				
DI22	3x15022 4x15022 I:15021	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI22:0=OFF				
DI23	3x15023 4x15023 I:15022	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI23:0=OFF				
DI24	3x15024 4x15024 I:15023	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI24:1=ON				
DI25	3x15025 4x15025 I:15024	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI25:0=OFF				
DI26	3x15026 4x15026 I:15025	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI26:0=OFF				
DI27	3x15027 4x15027 I:15026	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI27:0=OFF				

DI28	3x15028 4x15028 I:15027	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI28:0=OFF				
DI29	3x15029 4x15029 I:15028	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI29:0=OFF				
DI30	3x15030 4x15030 I:15029	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI30:0=OFF				
DI31	3x15031 4x15031 I:15030	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI31:0=OFF				
DI32	3x15032 4x15032 I:15031	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI32:0=OFF				
DI33	3x15033 4x15033 I:15032	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI33:0=OFF				
DI34	3x15034 4x15034 I:15033	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI34:0=OFF				
DI35	3x15035 4x15035 I:15034	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI35:0=OFF				
DI36	3x15036 4x15036 I:15035	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI36:0=OFF				
DI37	3x15037 4x15037 I:15036	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI37:1=ON				
DI38	3x15038 4x15038 I:15037	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI38:0=OFF				

DI39	3x15039 4x15039 I:15038	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI39:0=OFF				
DI40	3x15040 4x15040 I:15039	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI40:0=OFF				
DI41	3x15041 4x15041 I:15040	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI41:0=OFF				
DI42	3x15042 4x15042 I:15041	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI42:0=OFF				
DI43	3x15043 4x15043 I:15042	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI43:0=OFF				
DI44	3x15044 4x15044 I:15043	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI44:0=OFF				
DI45	3x15045 4x15045 I:15044	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI45:0=OFF				
DI46	3x15046 4x15046 I:15045	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI46:0=OFF				
DI47	3x15047 4x15047 I:15046	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI47:0=OFF				
DI48	3x15048 4x15048 I:15047	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI48:0=OFF				
DI49	3x15049 4x15049 I:15048	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI49:0=OFF				

DI50	3x15050 4x15050 I:15049	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI50:1=ON				
DI51	3x15051 4x15051 I:15050	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI51:0=OFF				
DI52	3x15052 4x15052 I:15051	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI52:0=OFF				
DI53	3x15053 4x15053 I:15052	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI53:0=OFF				
DI54	3x15054 4x15054 I:15053	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI54:0=OFF				
DI55	3x15055 4x15055 I:15054	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI55:0=OFF				
DI56	3x15056 4x15056 I:15055	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI56:0=OFF				
DI57	3x15057 4x15057 I:15056	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI57:0=OFF				
DI58	3x15058 4x15058 I:15057	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI58:0=OFF				
DI59	3x15059 4x15059 I:15058	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI59:0=OFF				
DI60	3x15060 4x15060 I:15059	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI60:1=ON				

DI61	3x15061 4x15061 I:15060	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI61:1=ON				
DI62	3x15062 4x15062 I:15061	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI62:1=ON				
DI63	3x15063 4x15063 I:15062	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI63:1=ON				
DI64	3x15064 4x15064 I:15063	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI64:1=ON				
DI65	3x15065 4x15065 I:15064	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI65:0=OFF				
DI66	3x15066 4x15066 I:15065	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI66:0=OFF				
DI67	3x15067 4x15067 I:15066	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI67:0=OFF				
DI68	3x15068 4x15068 I:15067	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI68:0=OFF				
DI69	3x15069 4x15069 I:15068	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI69:0=OFF				
DI70	3x15070 4x15070 I:15069	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI70:0=OFF				
DI71	3x15071 4x15071 I:15070	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI71:0=OFF				

DI72	3x15072 4x15072 I:15071	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI72:0=OFF				
DI73	3x15073 4x15073 I:15072	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI73:0=OFF				
DI74	3x15074 4x15074 I:15073	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI74:0=OFF				
DI75	3x15075 4x15075 I:15074	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI75:0=OFF				
DI76	3x15076 4x15076 I:15075	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI76:0=OFF				
DI77	3x15077 4x15077 I:15076	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI77:0=OFF				
DI78	3x15078 4x15078 I:15077	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI78:0=OFF				
DI79	3x15079 4x15079 I:15078	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI79:0=OFF				
DI80	3x15080 4x15080 I:15079	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI80:0=OFF				
DI81	3x15081 4x15081 I:15080	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI81:0=OFF				
DI82	3x15082 4x15082 I:15081	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI82:0=OFF				

DI83	3x15083 4x15083 I:15082	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI83:0=OFF				
DI84	3x15084 4x15084 I:15083	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI84:0=OFF				
DI85	3x15085 4x15085 I:15084	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI85:0=OFF				
DI86	3x15086 4x15086 I:15085	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI86:1=ON				
DI87	3x15087 4x15087 I:15086	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI87:0=OFF				
DI88	3x15088 4x15088 I:15087	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI88:0=OFF				
DI89	3x15089 4x15089 I:15088	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI89:0=OFF				
DI90	3x15090 4x15090 I:15089	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI90:0=OFF				
DI91	3x15091 4x15091 I:15090	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI91:0=OFF				
DI92	3x15092 4x15092 I:15091	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI92:0=OFF				
DI93	3x15093 4x15093 I:15092	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI93:0=OFF				

DI94	3x15094 4x15094 I:15093	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI94:0=OFF				
DI95	3x15095 4x15095 I:15094	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI95:0=OFF				
DI96	3x15096 4x15096 I:15095	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI96:0=OFF				
DI97	3x15097 4x15097 I:15096	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI97:0=OFF				
DI98	3x15098 4x15098 I:15097	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI98:0=OFF				
DI99	3x15099 4x15099 I:15098	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI99:1=ON				
DI100	3x15100 4x15100 I:15099	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI100:0=OFF				
DI101	3x15101 4x15101 I:15100	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI101:0=OFF				
DI102	3x15102 4x15102 I:15101	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI102:0=OFF				
DI103	3x15103 4x15103 I:15102	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI103:0=OFF				
DI104	3x15104 4x15104 I:15103	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI104:0=OFF				

DI105	3x15105 4x15105 I:15104	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI105:0=OFF				
DI106	3x15106 4x15106 I:15105	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI106:0=OFF				
DI107	3x15107 4x15107 I:15106	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI107:0=OFF				
DI108	3x15108 4x15108 I:15107	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI108:0=OFF				
DI109	3x15109 4x15109 I:15108	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI109:0=OFF				
DI110	3x15110 4x15110 I:15109	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI110:0=OFF				
DI111	3x15111 4x15111 I:15110	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI111:0=OFF				
DI112	3x15112 4x15112 I:15111	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI112:1=ON				
DI113	3x15113 4x15113 I:15112	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI113:0=OFF				
DI114	3x15114 4x15114 I:15113	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI114:0=OFF				
DI115	3x15115 4x15115 I:15114	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI115:0=OFF				

DI116	3x15116 4x15116 I:15115	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI116:0=OFF				
DI117	3x15117 4x15117 I:15116	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI117:0=OFF				
DI118	3x15118 4x15118 I:15117	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI118:0=OFF				
DI119	3x15119 4x15119 I:15118	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI119:0=OFF				
DI120	3x15120 4x15120 I:15119	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI120:0=OFF				
DI121	3x15121 4x15121 I:15120	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of DI121:0=OFF				
DI122	3x15122 4x15122 I:15121	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI122:1=ON				
DI123	3x15123 4x15123 I:15122	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI123:1=ON				
DI124	3x15124 4x15124 I:15123	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI124:1=ON				
DI125	3x15125 4x15125 I:15124	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI125:1=ON				
DI126	3x15126 4x15126 I:15125	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI126:1=ON				

DI127	3x15127 4x15127 I:15126	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI127:1=ON				
DI128	3x15128 4x15128 I:15127	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of DI128:1=ON				
STATUS DIGITAL INPUTS						
UNFILTERED DI1	3x15065 4x15065 I:15064	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI1:0=OFF				
Current state of the real digital input DIx without the internal software filter to suppress glitches or spike on this line =0:DI is OFF, =1:DI is ON						
UNFILTERED DI2	3x15066 4x15066 I:15065	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI2:0=OFF				
UNFILTERED DI3	3x15067 4x15067 I:15066	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI3:0=OFF				
UNFILTERED DI4	3x15068 4x15068 I:15067	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI4:0=OFF				
UNFILTERED DI5	3x15069 4x15069 I:15068	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI5:0=OFF				
UNFILTERED DI6	3x15070 4x15070 I:15069	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI6:0=OFF				
UNFILTERED DI7	3x15071 4x15071 I:15070	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI7:0=OFF				
UNFILTERED DI8	3x15072 4x15072 I:15071	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI8:0=OFF				
UNFILTERED DI9	3x15073 4x15073 I:15072	0,0x0000 B:00 00			UINT16 R/O	

		Actual state of UNFILTERED DI9:0=OFF				
UNFILTERED DI10	3x15074 4x15074 I:15073	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI10:0=OFF				
UNFILTERED DI11	3x15075 4x15075 I:15074	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI11:0=OFF				
UNFILTERED DI12	3x15076 4x15076 I:15075	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI12:0=OFF				
UNFILTERED DI13	3x15077 4x15077 I:15076	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI13:0=OFF				
UNFILTERED DI14	3x15078 4x15078 I:15077	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI14:0=OFF				
UNFILTERED DI15	3x15079 4x15079 I:15078	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI15:0=OFF				
UNFILTERED DI16	3x15080 4x15080 I:15079	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI16:0=OFF				
UNFILTERED DI17	3x15081 4x15081 I:15080	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI17:0=OFF				
UNFILTERED DI18	3x15082 4x15082 I:15081	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI18:0=OFF				
UNFILTERED DI19	3x15083 4x15083 I:15082	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI19:0=OFF				
UNFILTERED DI20	3x15084 4x15084 I:15083	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI20:0=OFF				

UNFILTERED DI21	3x15085 4x15085 I:15084	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI21:0=OFF				
UNFILTERED DI22	3x15086 4x15086 I:15085	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI22:0=OFF				
UNFILTERED DI23	3x15087 4x15087 I:15086	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI23:0=OFF				
UNFILTERED DI24	3x15088 4x15088 I:15087	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of UNFILTERED DI24:1=ON				
UNFILTERED DI25	3x15089 4x15089 I:15088	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI25:0=OFF				
UNFILTERED DI26	3x15090 4x15090 I:15089	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI26:0=OFF				
UNFILTERED DI27	3x15091 4x15091 I:15090	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI27:0=OFF				
UNFILTERED DI28	3x15092 4x15092 I:15091	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI28:0=OFF				
UNFILTERED DI29	3x15093 4x15093 I:15092	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI29:0=OFF				
UNFILTERED DI30	3x15094 4x15094 I:15093	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI30:0=OFF				
UNFILTERED DI31	3x15095 4x15095 I:15094	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI31:0=OFF				

UNFILTERED DI32	3x15096 4x15096 I:15095	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI32:0=OFF				
UNFILTERED DI33	3x15097 4x15097 I:15096	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI33:0=OFF				
UNFILTERED DI34	3x15098 4x15098 I:15097	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI34:0=OFF				
UNFILTERED DI35	3x15099 4x15099 I:15098	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI35:0=OFF				
UNFILTERED DI36	3x15100 4x15100 I:15099	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI36:0=OFF				
UNFILTERED DI37	3x15101 4x15101 I:15100	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI37:0=OFF				
UNFILTERED DI38	3x15102 4x15102 I:15101	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI38:0=OFF				
UNFILTERED DI39	3x15103 4x15103 I:15102	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of UNFILTERED DI39:1=ON				
UNFILTERED DI40	3x15104 4x15104 I:15103	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI40:0=OFF				
UNFILTERED DI41	3x15105 4x15105 I:15104	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI41:0=OFF				
UNFILTERED DI42	3x15106 4x15106 I:15105	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI42:0=OFF				

UNFILTERED DI43	3x15107 4x15107 I:15106	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI43:0=OFF				
UNFILTERED DI44	3x15108 4x15108 I:15107	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI44:0=OFF				
UNFILTERED DI45	3x15109 4x15109 I:15108	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI45:0=OFF				
UNFILTERED DI46	3x15110 4x15110 I:15109	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI46:0=OFF				
UNFILTERED DI47	3x15111 4x15111 I:15110	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI47:0=OFF				
UNFILTERED DI48	3x15112 4x15112 I:15111	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI48:0=OFF				
UNFILTERED DI49	3x15113 4x15113 I:15112	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI49:0=OFF				
UNFILTERED DI50	3x15114 4x15114 I:15113	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI50:0=OFF				
UNFILTERED DI51	3x15115 4x15115 I:15114	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI51:0=OFF				
UNFILTERED DI52	3x15116 4x15116 I:15115	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI52:0=OFF				
UNFILTERED DI53	3x15117 4x15117 I:15116	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI53:0=OFF				

UNFILTERED DI54	3x15118 4x15118 I:15117	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of UNFILTERED DI54:1=ON				
UNFILTERED DI55	3x15119 4x15119 I:15118	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI55:0=OFF				
UNFILTERED DI56	3x15120 4x15120 I:15119	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI56:0=OFF				
UNFILTERED DI57	3x15121 4x15121 I:15120	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI57:0=OFF				
UNFILTERED DI58	3x15122 4x15122 I:15121	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI58:0=OFF				
UNFILTERED DI59	3x15123 4x15123 I:15122	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI59:0=OFF				
UNFILTERED DI60	3x15124 4x15124 I:15123	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI60:0=OFF				
UNFILTERED DI61	3x15125 4x15125 I:15124	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI61:0=OFF				
UNFILTERED DI62	3x15126 4x15126 I:15125	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI62:0=OFF				
UNFILTERED DI63	3x15127 4x15127 I:15126	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI63:0=OFF				
UNFILTERED DI64	3x15128 4x15128 I:15127	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI64:0=OFF				

UNFILTERED DI65	3x15129 4x15129 I:15128	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI65:0=OFF				
UNFILTERED DI66	3x15130 4x15130 I:15129	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI66:0=OFF				
UNFILTERED DI67	3x15131 4x15131 I:15130	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI67:0=OFF				
UNFILTERED DI68	3x15132 4x15132 I:15131	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI68:0=OFF				
UNFILTERED DI69	3x15133 4x15133 I:15132	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI69:0=OFF				
UNFILTERED DI70	3x15134 4x15134 I:15133	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI70:0=OFF				
UNFILTERED DI71	3x15135 4x15135 I:15134	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI71:0=OFF				
UNFILTERED DI72	3x15136 4x15136 I:15135	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI72:0=OFF				
UNFILTERED DI73	3x15137 4x15137 I:15136	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI73:0=OFF				
UNFILTERED DI74	3x15138 4x15138 I:15137	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI74:0=OFF				
UNFILTERED DI75	3x15139 4x15139 I:15138	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI75:0=OFF				

UNFILTERED DI76	3x15140 4x15140 I:15139	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI76:0=OFF				
UNFILTERED DI77	3x15141 4x15141 I:15140	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI77:0=OFF				
UNFILTERED DI78	3x15142 4x15142 I:15141	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI78:0=OFF				
UNFILTERED DI79	3x15143 4x15143 I:15142	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI79:0=OFF				
UNFILTERED DI80	3x15144 4x15144 I:15143	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI80:0=OFF				
UNFILTERED DI81	3x15145 4x15145 I:15144	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI81:0=OFF				
UNFILTERED DI82	3x15146 4x15146 I:15145	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI82:0=OFF				
UNFILTERED DI83	3x15147 4x15147 I:15146	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI83:0=OFF				
UNFILTERED DI84	3x15148 4x15148 I:15147	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI84:0=OFF				
UNFILTERED DI85	3x15149 4x15149 I:15148	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI85:0=OFF				
UNFILTERED DI86	3x15150 4x15150 I:15149	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of UNFILTERED DI86:1=ON				

UNFILTERED DI87	3x15151 4x15151 I:15150	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI87:0=OFF				
UNFILTERED DI88	3x15152 4x15152 I:15151	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI88:0=OFF				
UNFILTERED DI89	3x15153 4x15153 I:15152	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI89:0=OFF				
UNFILTERED DI90	3x15154 4x15154 I:15153	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI90:0=OFF				
UNFILTERED DI91	3x15155 4x15155 I:15154	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI91:0=OFF				
UNFILTERED DI92	3x15156 4x15156 I:15155	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI92:0=OFF				
UNFILTERED DI93	3x15157 4x15157 I:15156	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI93:0=OFF				
UNFILTERED DI94	3x15158 4x15158 I:15157	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI94:0=OFF				
UNFILTERED DI95	3x15159 4x15159 I:15158	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI95:0=OFF				
UNFILTERED DI96	3x15160 4x15160 I:15159	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI96:0=OFF				
UNFILTERED DI97	3x15161 4x15161 I:15160	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI97:0=OFF				

UNFILTERED DI98	3x15162 4x15162 I:15161	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI98:0=OFF				
UNFILTERED DI99	3x15163 4x15163 I:15162	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI99:0=OFF				
UNFILTERED DI100	3x15164 4x15164 I:15163	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI100:0=OFF				
UNFILTERED DI101	3x15165 4x15165 I:15164	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of UNFILTERED DI101:1=ON				
UNFILTERED DI102	3x15166 4x15166 I:15165	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI102:0=OFF				
UNFILTERED DI103	3x15167 4x15167 I:15166	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI103:0=OFF				
UNFILTERED DI104	3x15168 4x15168 I:15167	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI104:0=OFF				
UNFILTERED DI105	3x15169 4x15169 I:15168	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI105:0=OFF				
UNFILTERED DI106	3x15170 4x15170 I:15169	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI106:0=OFF				
UNFILTERED DI107	3x15171 4x15171 I:15170	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI107:0=OFF				
UNFILTERED DI108	3x15172 4x15172 I:15171	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI108:0=OFF				

UNFILTERED DI109	3x15173 4x15173 I:15172	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI109:0=OFF				
UNFILTERED DI110	3x15174 4x15174 I:15173	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI110:0=OFF				
UNFILTERED DI111	3x15175 4x15175 I:15174	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI111:0=OFF				
UNFILTERED DI112	3x15176 4x15176 I:15175	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI112:0=OFF				
UNFILTERED DI113	3x15177 4x15177 I:15176	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI113:0=OFF				
UNFILTERED DI114	3x15178 4x15178 I:15177	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI114:0=OFF				
UNFILTERED DI115	3x15179 4x15179 I:15178	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI115:0=OFF				
UNFILTERED DI116	3x15180 4x15180 I:15179	1,0x0001 B:00 01			UINT16 R/O	
		Actual state of UNFILTERED DI116:1=ON				
UNFILTERED DI117	3x15181 4x15181 I:15180	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI117:0=OFF				
UNFILTERED DI118	3x15182 4x15182 I:15181	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI118:0=OFF				
UNFILTERED DI119	3x15183 4x15183 I:15182	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI119:0=OFF				

UNFILTERED DI120	3x15184 4x15184 I:15183	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI120:0=OFF				
UNFILTERED DI121	3x15185 4x15185 I:15184	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI121:0=OFF				
UNFILTERED DI122	3x15186 4x15186 I:15185	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI122:0=OFF				
UNFILTERED DI123	3x15187 4x15187 I:15186	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI123:0=OFF				
UNFILTERED DI124	3x15188 4x15188 I:15187	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI124:0=OFF				
UNFILTERED DI125	3x15189 4x15189 I:15188	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI125:0=OFF				
UNFILTERED DI126	3x15190 4x15190 I:15189	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI126:0=OFF				
UNFILTERED DI127	3x15191 4x15191 I:15190	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI127:0=OFF				
UNFILTERED DI128	3x15192 4x15192 I:15191	0,0x0000 B:00 00			UINT16 R/O	
		Actual state of UNFILTERED DI128:0=OFF				

Register NAME Command NAME	MODBUS Register ASCII Command	Register VALUE ASCII Command	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
DIGITAL INPUTS						
STATUS DI1 A	3x05001 4x05001 I:5000	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
Status for the digital input DIx Bit 0-4: Lower 5 bits of CHANGE COUNTER Bit 5-9: Lower 5 bits of RISING EDGE COUNTER Bit 10-14: Lower 5 bits of FALLING EDGE COUNTER Bit 15: Current Status of DIx =0: DIx si OFF, =1: DIx is ON						
STATUS DI1 B	3x05002 4x05002 I:5001	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
Status for the digital input DIx Bit 0-4: Lower 5 bits of SHORT KEYPRESS EVENTS Bit 5-9: Lower 5 bits of LONG KEYPRESS START EVENTS Bit 10-14: Lower 5 bits of LONG KEYPRESS END EVENTS Bit 15: Current Status of DIx =0: DIx si OFF, =1: DIx is ON						
STATUS DI2 A	3x05003 4x05003 I:5002	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI2 B	3x05004 4x05004 I:5003	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI3 A	3x05005 4x05005 I:5004	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI3 B	3x05006 4x05006 I:5005	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI4 A	3x05007 4x05007 I:5006	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI4 B	3x05008 4x05008 I:5007	0,0x0000 B:00 00			UINT16 R/O	

		DI:0,SKE:0,LKSE:0,LKEE:0			
STATUS DI5 A	3x05009 4x05009 I:5008	0,0x0000 B:00 00		UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0			
STATUS DI5 B	3x05010 4x05010 I:5009	0,0x0000 B:00 00		UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0			
STATUS DI6 A	3x05011 4x05011 I:5010	0,0x0000 B:00 00		UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0			
STATUS DI6 B	3x05012 4x05012 I:5011	0,0x0000 B:00 00		UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0			
STATUS DI7 A	3x05013 4x05013 I:5012	0,0x0000 B:00 00		UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0			
STATUS DI7 B	3x05014 4x05014 I:5013	0,0x0000 B:00 00		UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0			
STATUS DI8 A	3x05015 4x05015 I:5014	0,0x0000 B:00 00		UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0			
STATUS DI8 B	3x05016 4x05016 I:5015	0,0x0000 B:00 00		UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0			
STATUS DI9 A	3x05017 4x05017 I:5016	0,0x0000 B:00 00		UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0			
STATUS DI9 B	3x05018 4x05018 I:5017	0,0x0000 B:00 00		UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0			
STATUS DI10 A	3x05019 4x05019 I:5018	0,0x0000 B:00 00		UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0			

STATUS DI10 B	3x05020 4x05020 I:5019	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI11 A	3x05021 4x05021 I:5020	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI11 B	3x05022 4x05022 I:5021	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI12 A	3x05023 4x05023 I:5022	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI12 B	3x05024 4x05024 I:5023	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI13 A	3x05025 4x05025 I:5024	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI13 B	3x05026 4x05026 I:5025	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI14 A	3x05027 4x05027 I:5026	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI14 B	3x05028 4x05028 I:5027	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI15 A	3x05029 4x05029 I:5028	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI15 B	3x05030 4x05030 I:5029	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				

STATUS DI16 A	3x05031 4x05031 I:5030	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI16 B	3x05032 4x05032 I:5031	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI17 A	3x05033 4x05033 I:5032	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI17 B	3x05034 4x05034 I:5033	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI18 A	3x05035 4x05035 I:5034	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI18 B	3x05036 4x05036 I:5035	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI19 A	3x05037 4x05037 I:5036	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI19 B	3x05038 4x05038 I:5037	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI20 A	3x05039 4x05039 I:5038	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI20 B	3x05040 4x05040 I:5039	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI21 A	3x05041 4x05041 I:5040	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				

STATUS DI21 B	3x05042 4x05042 I:5041	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI22 A	3x05043 4x05043 I:5042	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI22 B	3x05044 4x05044 I:5043	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI23 A	3x05045 4x05045 I:5044	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI23 B	3x05046 4x05046 I:5045	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI24 A	3x05047 4x05047 I:5046	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI24 B	3x05048 4x05048 I:5047	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI25 A	3x05049 4x05049 I:5048	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI25 B	3x05050 4x05050 I:5049	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI26 A	3x05051 4x05051 I:5050	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI26 B	3x05052 4x05052 I:5051	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				

STATUS DI27 A	3x05053 4x05053 I:5052	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI27 B	3x05054 4x05054 I:5053	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI28 A	3x05055 4x05055 I:5054	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI28 B	3x05056 4x05056 I:5055	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI29 A	3x05057 4x05057 I:5056	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI29 B	3x05058 4x05058 I:5057	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI30 A	3x05059 4x05059 I:5058	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI30 B	3x05060 4x05060 I:5059	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI31 A	3x05061 4x05061 I:5060	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI31 B	3x05062 4x05062 I:5061	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI32 A	3x05063 4x05063 I:5062	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				

STATUS DI32 B	3x05064 4x05064 I:5063	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI33 A	3x05065 4x05065 I:5064	1058,0x0422 B:04 22			UINT16 R/O	
		DI:1,CC:2,REC:1,FEC:1				
STATUS DI33 B	3x05066 4x05066 I:5065	1056,0x0420 B:04 20			UINT16 R/O	
		DI:1,SKE:0,LKSE:1,LKEE:1				
STATUS DI34 A	3x05067 4x05067 I:5066	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI34 B	3x05068 4x05068 I:5067	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI35 A	3x05069 4x05069 I:5068	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI35 B	3x05070 4x05070 I:5069	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI36 A	3x05071 4x05071 I:5070	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI36 B	3x05072 4x05072 I:5071	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI37 A	3x05073 4x05073 I:5072	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI37 B	3x05074 4x05074 I:5073	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				

STATUS DI38 A	3x05075 4x05075 I:5074	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI38 B	3x05076 4x05076 I:5075	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI39 A	3x05077 4x05077 I:5076	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI39 B	3x05078 4x05078 I:5077	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI40 A	3x05079 4x05079 I:5078	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI40 B	3x05080 4x05080 I:5079	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI41 A	3x05081 4x05081 I:5080	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI41 B	3x05082 4x05082 I:5081	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI42 A	3x05083 4x05083 I:5082	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI42 B	3x05084 4x05084 I:5083	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI43 A	3x05085 4x05085 I:5084	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				

STATUS DI43 B	3x05086 4x05086 I:5085	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI44 A	3x05087 4x05087 I:5086	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI44 B	3x05088 4x05088 I:5087	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI45 A	3x05089 4x05089 I:5088	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI45 B	3x05090 4x05090 I:5089	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI46 A	3x05091 4x05091 I:5090	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI46 B	3x05092 4x05092 I:5091	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI47 A	3x05093 4x05093 I:5092	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI47 B	3x05094 4x05094 I:5093	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI48 A	3x05095 4x05095 I:5094	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI48 B	3x05096 4x05096 I:5095	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				

STATUS DI49 A	3x05097 4x05097 I:5096	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI49 B	3x05098 4x05098 I:5097	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI50 A	3x05099 4x05099 I:5098	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI50 B	3x05100 4x05100 I:5099	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI51 A	3x05101 4x05101 I:5100	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI51 B	3x05102 4x05102 I:5101	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI52 A	3x05103 4x05103 I:5102	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI52 B	3x05104 4x05104 I:5103	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI53 A	3x05105 4x05105 I:5104	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI53 B	3x05106 4x05106 I:5105	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI54 A	3x05107 4x05107 I:5106	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				

STATUS DI54 B	3x05108 4x05108 I:5107	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI55 A	3x05109 4x05109 I:5108	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI55 B	3x05110 4x05110 I:5109	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI56 A	3x05111 4x05111 I:5110	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI56 B	3x05112 4x05112 I:5111	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI57 A	3x05113 4x05113 I:5112	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI57 B	3x05114 4x05114 I:5113	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI58 A	3x05115 4x05115 I:5114	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI58 B	3x05116 4x05116 I:5115	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI59 A	3x05117 4x05117 I:5116	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI59 B	3x05118 4x05118 I:5117	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				

STATUS DI60 A	3x05119 4x05119 I:5118	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI60 B	3x05120 4x05120 I:5119	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI61 A	3x05121 4x05121 I:5120	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI61 B	3x05122 4x05122 I:5121	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI62 A	3x05123 4x05123 I:5122	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI62 B	3x05124 4x05124 I:5123	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI63 A	3x05125 4x05125 I:5124	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI63 B	3x05126 4x05126 I:5125	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI64 A	3x05127 4x05127 I:5126	1057,0x0421 B:04 21			UINT16 R/O	
		DI:1,CC:1,REC:1,FEC:1				
STATUS DI64 B	3x05128 4x05128 I:5127	1,0x0001 B:00 01			UINT16 R/O	
		DI:0,SKE:1,LKSE:0,LKEE:0				
STATUS DI65 A	3x05129 4x05129 I:5128	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				

STATUS DI65 B	3x05130 4x05130 I:5129	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI66 A	3x05131 4x05131 I:5130	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI66 B	3x05132 4x05132 I:5131	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI67 A	3x05133 4x05133 I:5132	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI67 B	3x05134 4x05134 I:5133	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI68 A	3x05135 4x05135 I:5134	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI68 B	3x05136 4x05136 I:5135	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI69 A	3x05137 4x05137 I:5136	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI69 B	3x05138 4x05138 I:5137	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI70 A	3x05139 4x05139 I:5138	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI70 B	3x05140 4x05140 I:5139	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				

STATUS DI71 A	3x05141 4x05141 I:5140	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI71 B	3x05142 4x05142 I:5141	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI72 A	3x05143 4x05143 I:5142	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI72 B	3x05144 4x05144 I:5143	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI73 A	3x05145 4x05145 I:5144	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI73 B	3x05146 4x05146 I:5145	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI74 A	3x05147 4x05147 I:5146	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI74 B	3x05148 4x05148 I:5147	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI75 A	3x05149 4x05149 I:5148	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI75 B	3x05150 4x05150 I:5149	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI76 A	3x05151 4x05151 I:5150	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				

STATUS DI76 B	3x05152 4x05152 I:5151	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI77 A	3x05153 4x05153 I:5152	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI77 B	3x05154 4x05154 I:5153	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI78 A	3x05155 4x05155 I:5154	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI78 B	3x05156 4x05156 I:5155	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI79 A	3x05157 4x05157 I:5156	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI79 B	3x05158 4x05158 I:5157	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI80 A	3x05159 4x05159 I:5158	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI80 B	3x05160 4x05160 I:5159	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI81 A	3x05161 4x05161 I:5160	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI81 B	3x05162 4x05162 I:5161	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				

STATUS DI82 A	3x05163 4x05163 I:5162	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI82 B	3x05164 4x05164 I:5163	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI83 A	3x05165 4x05165 I:5164	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI83 B	3x05166 4x05166 I:5165	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI84 A	3x05167 4x05167 I:5166	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI84 B	3x05168 4x05168 I:5167	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI85 A	3x05169 4x05169 I:5168	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI85 B	3x05170 4x05170 I:5169	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI86 A	3x05171 4x05171 I:5170	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI86 B	3x05172 4x05172 I:5171	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI87 A	3x05173 4x05173 I:5172	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				

STATUS DI87 B	3x05174 4x05174 I:5173	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI88 A	3x05175 4x05175 I:5174	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI88 B	3x05176 4x05176 I:5175	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI89 A	3x05177 4x05177 I:5176	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI89 B	3x05178 4x05178 I:5177	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI90 A	3x05179 4x05179 I:5178	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI90 B	3x05180 4x05180 I:5179	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI91 A	3x05181 4x05181 I:5180	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI91 B	3x05182 4x05182 I:5181	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI92 A	3x05183 4x05183 I:5182	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI92 B	3x05184 4x05184 I:5183	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				

STATUS DI93 A	3x05185 4x05185 I:5184	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI93 B	3x05186 4x05186 I:5185	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI94 A	3x05187 4x05187 I:5186	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI94 B	3x05188 4x05188 I:5187	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI95 A	3x05189 4x05189 I:5188	1058,0x0422 B:04 22			UINT16 R/O	
		DI:1,CC:2,REC:1,FEC:1				
STATUS DI95 B	3x05190 4x05190 I:5189	1056,0x0420 B:04 20			UINT16 R/O	
		DI:1,SKE:0,LKSE:1,LKEE:1				
STATUS DI96 A	3x05191 4x05191 I:5190	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI96 B	3x05192 4x05192 I:5191	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI97 A	3x05193 4x05193 I:5192	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI97 B	3x05194 4x05194 I:5193	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI98 A	3x05195 4x05195 I:5194	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				

STATUS DI98 B	3x05196 4x05196 I:5195	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI99 A	3x05197 4x05197 I:5196	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI99 B	3x05198 4x05198 I:5197	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI100 A	3x05199 4x05199 I:5198	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI100 B	3x05200 4x05200 I:5199	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI101 A	3x05201 4x05201 I:5200	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI101 B	3x05202 4x05202 I:5201	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI102 A	3x05203 4x05203 I:5202	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI102 B	3x05204 4x05204 I:5203	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI103 A	3x05205 4x05205 I:5204	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI103 B	3x05206 4x05206 I:5205	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				

STATUS DI104 A	3x05207 4x05207 I:5206	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI104 B	3x05208 4x05208 I:5207	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI105 A	3x05209 4x05209 I:5208	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI105 B	3x05210 4x05210 I:5209	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI106 A	3x05211 4x05211 I:5210	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI106 B	3x05212 4x05212 I:5211	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI107 A	3x05213 4x05213 I:5212	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI107 B	3x05214 4x05214 I:5213	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI108 A	3x05215 4x05215 I:5214	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI108 B	3x05216 4x05216 I:5215	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI109 A	3x05217 4x05217 I:5216	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				

STATUS DI109 B	3x05218 4x05218 I:5217	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI110 A	3x05219 4x05219 I:5218	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI110 B	3x05220 4x05220 I:5219	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI111 A	3x05221 4x05221 I:5220	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI111 B	3x05222 4x05222 I:5221	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI112 A	3x05223 4x05223 I:5222	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI112 B	3x05224 4x05224 I:5223	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI113 A	3x05225 4x05225 I:5224	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI113 B	3x05226 4x05226 I:5225	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI114 A	3x05227 4x05227 I:5226	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI114 B	3x05228 4x05228 I:5227	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				

STATUS DI115 A	3x05229 4x05229 I:5228	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI115 B	3x05230 4x05230 I:5229	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI116 A	3x05231 4x05231 I:5230	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI116 B	3x05232 4x05232 I:5231	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI117 A	3x05233 4x05233 I:5232	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI117 B	3x05234 4x05234 I:5233	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI118 A	3x05235 4x05235 I:5234	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI118 B	3x05236 4x05236 I:5235	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI119 A	3x05237 4x05237 I:5236	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI119 B	3x05238 4x05238 I:5237	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI120 A	3x05239 4x05239 I:5238	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				

STATUS DI120 B	3x05240 4x05240 I:5239	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI121 A	3x05241 4x05241 I:5240	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI121 B	3x05242 4x05242 I:5241	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI122 A	3x05243 4x05243 I:5242	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI122 B	3x05244 4x05244 I:5243	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI123 A	3x05245 4x05245 I:5244	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI123 B	3x05246 4x05246 I:5245	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI124 A	3x05247 4x05247 I:5246	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI124 B	3x05248 4x05248 I:5247	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI125 A	3x05249 4x05249 I:5248	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI125 B	3x05250 4x05250 I:5249	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				

STATUS DI126 A	3x05251 4x05251 I:5250	1057,0x0421 B:04 21			UINT16 R/O	
		DI:1,CC:1,REC:1,FEC:1				
STATUS DI126 B	3x05252 4x05252 I:5251	1,0x0001 B:00 01			UINT16 R/O	
		DI:0,SKE:1,LKSE:0,LKEE:0				
STATUS DI127 A	3x05253 4x05253 I:5252	1057,0x0421 B:04 21			UINT16 R/O	
		DI:1,CC:1,REC:1,FEC:1				
STATUS DI127 B	3x05254 4x05254 I:5253	1,0x0001 B:00 01			UINT16 R/O	
		DI:0,SKE:1,LKSE:0,LKEE:0				
STATUS DI128 A	3x05255 4x05255 I:5254	1057,0x0421 B:04 21			UINT16 R/O	
		DI:1,CC:1,REC:1,FEC:1				
STATUS DI128 B	3x05256 4x05256 I:5255	1,0x0001 B:00 01			UINT16 R/O	
		DI:0,SKE:1,LKSE:0,LKEE:0				
STATUS						
FILTER PATTERN DI1	3x05257 4x05257 I:5256	0,0x00000000 B:00 00 00 00			UINT32 R/O	
The internal pattern for corresponding digital input for AC/DC filtering. The internal used state is created out of this internal pattern via oversampling.						
FILTER PATTERN DI2	3x05259 4x05259 I:5258	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI3	3x05261 4x05261 I:5260	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI4	3x05263 4x05263 I:5262	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI5	3x05265 4x05265 I:5264	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI6	3x05267 4x05267 I:5266	0,0x00000000 B:00 00 00 00			UINT32 R/O	

FILTER PATTERN DI7	3x05269 4x05269 I:5268	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI8	3x05271 4x05271 I:5270	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI9	3x05273 4x05273 I:5272	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI10	3x05275 4x05275 I:5274	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI11	3x05277 4x05277 I:5276	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI12	3x05279 4x05279 I:5278	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI13	3x05281 4x05281 I:5280	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI14	3x05283 4x05283 I:5282	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI15	3x05285 4x05285 I:5284	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI16	3x05287 4x05287 I:5286	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI17	3x05289 4x05289 I:5288	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI18	3x05291 4x05291 I:5290	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI19	3x05293 4x05293 I:5292	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI20	3x05295 4x05295 I:5294	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI21	3x05297 4x05297 I:5296	0,0x00000000 B:00 00 00 00			UINT32 R/O	

FILTER PATTERN DI22	3x05299 4x05299 I:5298	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI23	3x05301 4x05301 I:5300	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI24	3x05303 4x05303 I:5302	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI25	3x05305 4x05305 I:5304	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI26	3x05307 4x05307 I:5306	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI27	3x05309 4x05309 I:5308	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI28	3x05311 4x05311 I:5310	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI29	3x05313 4x05313 I:5312	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI30	3x05315 4x05315 I:5314	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI31	3x05317 4x05317 I:5316	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI32	3x05319 4x05319 I:5318	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI33	3x05321 4x05321 I:5320	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI34	3x05323 4x05323 I:5322	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI35	3x05325 4x05325 I:5324	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI36	3x05327 4x05327 I:5326	0,0x00000000 B:00 00 00 00			UINT32 R/O	

FILTER PATTERN DI37	3x05329 4x05329 I:5328	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI38	3x05331 4x05331 I:5330	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI39	3x05333 4x05333 I:5332	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI40	3x05335 4x05335 I:5334	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI41	3x05337 4x05337 I:5336	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI42	3x05339 4x05339 I:5338	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI43	3x05341 4x05341 I:5340	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI44	3x05343 4x05343 I:5342	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI45	3x05345 4x05345 I:5344	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI46	3x05347 4x05347 I:5346	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI47	3x05349 4x05349 I:5348	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI48	3x05351 4x05351 I:5350	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI49	3x05353 4x05353 I:5352	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI50	3x05355 4x05355 I:5354	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI51	3x05357 4x05357 I:5356	0,0x00000000 B:00 00 00 00			UINT32 R/O	

FILTER PATTERN DI52	3x05359 4x05359 I:5358	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI53	3x05361 4x05361 I:5360	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI54	3x05363 4x05363 I:5362	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI55	3x05365 4x05365 I:5364	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI56	3x05367 4x05367 I:5366	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI57	3x05369 4x05369 I:5368	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI58	3x05371 4x05371 I:5370	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI59	3x05373 4x05373 I:5372	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI60	3x05375 4x05375 I:5374	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI61	3x05377 4x05377 I:5376	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI62	3x05379 4x05379 I:5378	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI63	3x05381 4x05381 I:5380	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI64	3x05383 4x05383 I:5382	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI65	3x05385 4x05385 I:5384	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI66	3x05387 4x05387 I:5386	0,0x00000000 B:00 00 00 00			UINT32 R/O	

FILTER PATTERN DI67	3x05389 4x05389 I:5388	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI68	3x05391 4x05391 I:5390	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI69	3x05393 4x05393 I:5392	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI70	3x05395 4x05395 I:5394	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI71	3x05397 4x05397 I:5396	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI72	3x05399 4x05399 I:5398	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI73	3x05401 4x05401 I:5400	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI74	3x05403 4x05403 I:5402	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI75	3x05405 4x05405 I:5404	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI76	3x05407 4x05407 I:5406	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI77	3x05409 4x05409 I:5408	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI78	3x05411 4x05411 I:5410	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI79	3x05413 4x05413 I:5412	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI80	3x05415 4x05415 I:5414	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI81	3x05417 4x05417 I:5416	0,0x00000000 B:00 00 00 00			UINT32 R/O	

FILTER PATTERN DI82	3x05419 4x05419 I:5418	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI83	3x05421 4x05421 I:5420	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI84	3x05423 4x05423 I:5422	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI85	3x05425 4x05425 I:5424	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI86	3x05427 4x05427 I:5426	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI87	3x05429 4x05429 I:5428	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI88	3x05431 4x05431 I:5430	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI89	3x05433 4x05433 I:5432	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI90	3x05435 4x05435 I:5434	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI91	3x05437 4x05437 I:5436	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI92	3x05439 4x05439 I:5438	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI93	3x05441 4x05441 I:5440	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI94	3x05443 4x05443 I:5442	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI95	3x05445 4x05445 I:5444	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI96	3x05447 4x05447 I:5446	0,0x00000000 B:00 00 00 00			UINT32 R/O	

FILTER PATTERN DI97	3x05449 4x05449 I:5448	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI98	3x05451 4x05451 I:5450	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI99	3x05453 4x05453 I:5452	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI100	3x05455 4x05455 I:5454	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI101	3x05457 4x05457 I:5456	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI102	3x05459 4x05459 I:5458	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI103	3x05461 4x05461 I:5460	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI104	3x05463 4x05463 I:5462	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI105	3x05465 4x05465 I:5464	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI106	3x05467 4x05467 I:5466	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI107	3x05469 4x05469 I:5468	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI108	3x05471 4x05471 I:5470	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI109	3x05473 4x05473 I:5472	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI110	3x05475 4x05475 I:5474	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI111	3x05477 4x05477 I:5476	0,0x00000000 B:00 00 00 00			UINT32 R/O	

FILTER PATTERN DI112	3x05479 4x05479 I:5478	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI113	3x05481 4x05481 I:5480	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI114	3x05483 4x05483 I:5482	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI115	3x05485 4x05485 I:5484	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI116	3x05487 4x05487 I:5486	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI117	3x05489 4x05489 I:5488	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI118	3x05491 4x05491 I:5490	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI119	3x05493 4x05493 I:5492	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI120	3x05495 4x05495 I:5494	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI121	3x05497 4x05497 I:5496	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI122	3x05499 4x05499 I:5498	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI123	3x05501 4x05501 I:5500	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI124	3x05503 4x05503 I:5502	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI125	3x05505 4x05505 I:5504	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI126	3x05507 4x05507 I:5506	0,0x00000000 B:00 00 00 00			UINT32 R/O	

FILTER PATTERN DI127	3x05509 4x05509 I:5508	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI128	3x05511 4x05511 I:5510	0,0x00000000 B:00 00 00 00			UINT32 R/O	
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI1						
RISE DI1	3x07001 4x07001 I:7000	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
Counter for rising edges on the digital input DIx. If the module detects a rising edge on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.						
FALL DI1	3x07002 4x07002 I:7001	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
Counter for falling edges on the digital input DIx. If the module detects a falling edge on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.						
CHANGE DI1	3x07003 4x07003 I:7002	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
Counter for events on the digital input DIx. If the module detects an event on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0. The following events are available: Detection of a short keypress Detection of the start of a long keypress Detection of the end of a long keypress						
SHORT KEYPRESS DI1	3x07004 4x07004 I:7003	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
Counter for short keypress events on the digital input DIx. If the module detects a short keypress on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.						
LONG KEYPRESS START DI1	3x07005 4x07005 I:7004	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
Counter for start events of long keypress actions on the digital input DIx. If the module detects the start of a long keypress action on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.						
LONG KEYPRESS END DI1	3x07006 4x07006 I:7005	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)			
Counter for end events of long keypress actions on the digital input DIx. If the module detects the end of a long keypress action on the digital input, this counter is incremented by 1. After power on or a soft reset this counter is set always to 0. With the function RESET COUNTER this counter is also set to 0.					
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI2					
RISE DI2	3x07011 4x07011 I:7010	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
FALL DI2	3x07012 4x07012 I:7011	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
CHANGE DI2	3x07013 4x07013 I:7012	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
SHORT KEYPRESS DI2	3x07014 4x07014 I:7013	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
LONG KEYPRESS START DI2	3x07015 4x07015 I:7014	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
LONG KEYPRESS END DI2	3x07016 4x07016 I:7015	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI3					
RISE DI3	3x07021 4x07021 I:7020	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
FALL DI3	3x07022 4x07022 I:7021	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
CHANGE DI3	3x07023 4x07023 I:7022	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
SHORT KEYPRESS DI3	3x07024 4x07024 I:7023	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			

LONG KEYPRESS START DI3	3x07025 4x07025 I:7024	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI3	3x07026 4x07026 I:7025	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI4						
RISE DI4	3x07031 4x07031 I:7030	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI4	3x07032 4x07032 I:7031	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI4	3x07033 4x07033 I:7032	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI4	3x07034 4x07034 I:7033	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI4	3x07035 4x07035 I:7034	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI4	3x07036 4x07036 I:7035	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI5						
RISE DI5	3x07041 4x07041 I:7040	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI5	3x07042 4x07042 I:7041	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI5	3x07043 4x07043 I:7042	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

SHORT KEYPRESS DI5	3x07044 4x07044 I:7043	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI5	3x07045 4x07045 I:7044	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI5	3x07046 4x07046 I:7045	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI6						
RISE DI6	3x07051 4x07051 I:7050	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI6	3x07052 4x07052 I:7051	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI6	3x07053 4x07053 I:7052	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI6	3x07054 4x07054 I:7053	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI6	3x07055 4x07055 I:7054	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI6	3x07056 4x07056 I:7055	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI7						
RISE DI7	3x07061 4x07061 I:7060	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI7	3x07062 4x07062 I:7061	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

CHANGE DI7	3x07063 4x07063 I:7062	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI7	3x07064 4x07064 I:7063	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI7	3x07065 4x07065 I:7064	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI7	3x07066 4x07066 I:7065	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI8						
RISE DI8	3x07071 4x07071 I:7070	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI8	3x07072 4x07072 I:7071	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI8	3x07073 4x07073 I:7072	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI8	3x07074 4x07074 I:7073	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI8	3x07075 4x07075 I:7074	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI8	3x07076 4x07076 I:7075	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI9						
RISE DI9	3x07081 4x07081 I:7080	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

FALL DI9	3x07082 4x07082 I:7081	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI9	3x07083 4x07083 I:7082	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI9	3x07084 4x07084 I:7083	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI9	3x07085 4x07085 I:7084	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI9	3x07086 4x07086 I:7085	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI10						
RISE DI10	3x07091 4x07091 I:7090	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI10	3x07092 4x07092 I:7091	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI10	3x07093 4x07093 I:7092	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI10	3x07094 4x07094 I:7093	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI10	3x07095 4x07095 I:7094	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI10	3x07096 4x07096 I:7095	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI11						

RISE DI11	3x07101 4x07101 I:7100	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI11	3x07102 4x07102 I:7101	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI11	3x07103 4x07103 I:7102	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI11	3x07104 4x07104 I:7103	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI11	3x07105 4x07105 I:7104	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI11	3x07106 4x07106 I:7105	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI12						
RISE DI12	3x07111 4x07111 I:7110	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI12	3x07112 4x07112 I:7111	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI12	3x07113 4x07113 I:7112	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI12	3x07114 4x07114 I:7113	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI12	3x07115 4x07115 I:7114	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

LONG KEYPRESS END DI12	3x07116 4x07116 I:7115	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI13						
RISE DI13	3x07121 4x07121 I:7120	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI13	3x07122 4x07122 I:7121	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI13	3x07123 4x07123 I:7122	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI13	3x07124 4x07124 I:7123	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI13	3x07125 4x07125 I:7124	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI13	3x07126 4x07126 I:7125	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI14						
RISE DI14	3x07131 4x07131 I:7130	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI14	3x07132 4x07132 I:7131	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI14	3x07133 4x07133 I:7132	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI14	3x07134 4x07134 I:7133	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

LONG KEYPRESS START DI14	3x07135 4x07135 I:7134	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI14	3x07136 4x07136 I:7135	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI15						
RISE DI15	3x07141 4x07141 I:7140	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI15	3x07142 4x07142 I:7141	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI15	3x07143 4x07143 I:7142	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI15	3x07144 4x07144 I:7143	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI15	3x07145 4x07145 I:7144	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI15	3x07146 4x07146 I:7145	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI16						
RISE DI16	3x07151 4x07151 I:7150	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI16	3x07152 4x07152 I:7151	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI16	3x07153 4x07153 I:7152	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

SHORT KEYPRESS DI16	3x07154 4x07154 I:7153	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI16	3x07155 4x07155 I:7154	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI16	3x07156 4x07156 I:7155	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI17						
RISE DI17	3x07161 4x07161 I:7160	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI17	3x07162 4x07162 I:7161	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI17	3x07163 4x07163 I:7162	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI17	3x07164 4x07164 I:7163	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI17	3x07165 4x07165 I:7164	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI17	3x07166 4x07166 I:7165	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI18						
RISE DI18	3x07171 4x07171 I:7170	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI18	3x07172 4x07172 I:7171	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

CHANGE DI18	3x07173 4x07173 I:7172	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI18	3x07174 4x07174 I:7173	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI18	3x07175 4x07175 I:7174	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI18	3x07176 4x07176 I:7175	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI19						
RISE DI19	3x07181 4x07181 I:7180	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI19	3x07182 4x07182 I:7181	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI19	3x07183 4x07183 I:7182	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI19	3x07184 4x07184 I:7183	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI19	3x07185 4x07185 I:7184	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI19	3x07186 4x07186 I:7185	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI20						
RISE DI20	3x07191 4x07191 I:7190	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

FALL DI20	3x07192 4x07192 I:7191	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI20	3x07193 4x07193 I:7192	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI20	3x07194 4x07194 I:7193	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI20	3x07195 4x07195 I:7194	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI20	3x07196 4x07196 I:7195	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI21						
RISE DI21	3x07201 4x07201 I:7200	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI21	3x07202 4x07202 I:7201	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI21	3x07203 4x07203 I:7202	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI21	3x07204 4x07204 I:7203	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI21	3x07205 4x07205 I:7204	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI21	3x07206 4x07206 I:7205	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI22						

RISE DI22	3x07211 4x07211 I:7210	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI22	3x07212 4x07212 I:7211	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI22	3x07213 4x07213 I:7212	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI22	3x07214 4x07214 I:7213	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI22	3x07215 4x07215 I:7214	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI22	3x07216 4x07216 I:7215	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI23						
RISE DI23	3x07221 4x07221 I:7220	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI23	3x07222 4x07222 I:7221	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI23	3x07223 4x07223 I:7222	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI23	3x07224 4x07224 I:7223	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI23	3x07225 4x07225 I:7224	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

LONG KEYPRESS END DI23	3x07226 4x07226 I:7225	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI24						
RISE DI24	3x07231 4x07231 I:7230	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI24	3x07232 4x07232 I:7231	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI24	3x07233 4x07233 I:7232	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI24	3x07234 4x07234 I:7233	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI24	3x07235 4x07235 I:7234	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI24	3x07236 4x07236 I:7235	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI25						
RISE DI25	3x07241 4x07241 I:7240	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI25	3x07242 4x07242 I:7241	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI25	3x07243 4x07243 I:7242	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI25	3x07244 4x07244 I:7243	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)				
LONG KEYPRESS START DI25	3x07245 4x07245 I:7244	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI25	3x07246 4x07246 I:7245	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI26						
RISE DI26	3x07251 4x07251 I:7250	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI26	3x07252 4x07252 I:7251	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI26	3x07253 4x07253 I:7252	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI26	3x07254 4x07254 I:7253	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI26	3x07255 4x07255 I:7254	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI26	3x07256 4x07256 I:7255	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI27						
RISE DI27	3x07261 4x07261 I:7260	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI27	3x07262 4x07262 I:7261	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

CHANGE DI27	3x07263 4x07263 I:7262	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI27	3x07264 4x07264 I:7263	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI27	3x07265 4x07265 I:7264	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI27	3x07266 4x07266 I:7265	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI28						
RISE DI28	3x07271 4x07271 I:7270	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI28	3x07272 4x07272 I:7271	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI28	3x07273 4x07273 I:7272	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI28	3x07274 4x07274 I:7273	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI28	3x07275 4x07275 I:7274	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI28	3x07276 4x07276 I:7275	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI29						
RISE DI29	3x07281 4x07281 I:7280	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)			
FALL DI29	3x07282 4x07282 I:7281	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
CHANGE DI29	3x07283 4x07283 I:7282	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
SHORT KEYPRESS DI29	3x07284 4x07284 I:7283	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
LONG KEYPRESS START DI29	3x07285 4x07285 I:7284	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
LONG KEYPRESS END DI29	3x07286 4x07286 I:7285	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI30					
RISE DI30	3x07291 4x07291 I:7290	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
FALL DI30	3x07292 4x07292 I:7291	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
CHANGE DI30	3x07293 4x07293 I:7292	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
SHORT KEYPRESS DI30	3x07294 4x07294 I:7293	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
LONG KEYPRESS START DI30	3x07295 4x07295 I:7294	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
LONG KEYPRESS END DI30	3x07296 4x07296 I:7295	0,0x0000 B:00 00			UINT16 R/O

		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI31					
RISE DI31	3x07301 4x07301 I:7300	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
FALL DI31	3x07302 4x07302 I:7301	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
CHANGE DI31	3x07303 4x07303 I:7302	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
SHORT KEYPRESS DI31	3x07304 4x07304 I:7303	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
LONG KEYPRESS START DI31	3x07305 4x07305 I:7304	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
LONG KEYPRESS END DI31	3x07306 4x07306 I:7305	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI32					
RISE DI32	3x07311 4x07311 I:7310	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
FALL DI32	3x07312 4x07312 I:7311	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
CHANGE DI32	3x07313 4x07313 I:7312	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
SHORT KEYPRESS DI32	3x07314 4x07314 I:7313	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			

LONG KEYPRESS START DI32	3x07315 4x07315 I:7314	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI32	3x07316 4x07316 I:7315	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI33						
RISE DI33	3x07321 4x07321 I:7320	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI33	3x07322 4x07322 I:7321	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI33	3x07323 4x07323 I:7322	2,0x0002 B:00 02			UINT16 R/O	
		2 event(s)				
SHORT KEYPRESS DI33	3x07324 4x07324 I:7323	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI33	3x07325 4x07325 I:7324	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS END DI33	3x07326 4x07326 I:7325	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI34						
RISE DI34	3x07331 4x07331 I:7330	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI34	3x07332 4x07332 I:7331	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI34	3x07333 4x07333 I:7332	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)			
SHORT KEYPRESS DI34	3x07334 4x07334 I:7333	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
LONG KEYPRESS START DI34	3x07335 4x07335 I:7334	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
LONG KEYPRESS END DI34	3x07336 4x07336 I:7335	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI35					
RISE DI35	3x07341 4x07341 I:7340	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
FALL DI35	3x07342 4x07342 I:7341	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
CHANGE DI35	3x07343 4x07343 I:7342	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
SHORT KEYPRESS DI35	3x07344 4x07344 I:7343	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
LONG KEYPRESS START DI35	3x07345 4x07345 I:7344	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
LONG KEYPRESS END DI35	3x07346 4x07346 I:7345	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI36					
RISE DI36	3x07351 4x07351 I:7350	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			

FALL DI36	3x07352 4x07352 I:7351	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI36	3x07353 4x07353 I:7352	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI36	3x07354 4x07354 I:7353	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI36	3x07355 4x07355 I:7354	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI36	3x07356 4x07356 I:7355	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI37						
RISE DI37	3x07361 4x07361 I:7360	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI37	3x07362 4x07362 I:7361	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI37	3x07363 4x07363 I:7362	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI37	3x07364 4x07364 I:7363	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI37	3x07365 4x07365 I:7364	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI37	3x07366 4x07366 I:7365	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI38						
RISE DI38	3x07371 4x07371 I:7370	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI38	3x07372 4x07372 I:7371	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI38	3x07373 4x07373 I:7372	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI38	3x07374 4x07374 I:7373	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI38	3x07375 4x07375 I:7374	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI38	3x07376 4x07376 I:7375	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI39						
RISE DI39	3x07381 4x07381 I:7380	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI39	3x07382 4x07382 I:7381	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI39	3x07383 4x07383 I:7382	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI39	3x07384 4x07384 I:7383	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI39	3x07385 4x07385 I:7384	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)			
LONG KEYPRESS END DI39	3x07386 4x07386 I:7385	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI40					
RISE DI40	3x07391 4x07391 I:7390	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
FALL DI40	3x07392 4x07392 I:7391	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
CHANGE DI40	3x07393 4x07393 I:7392	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
SHORT KEYPRESS DI40	3x07394 4x07394 I:7393	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
LONG KEYPRESS START DI40	3x07395 4x07395 I:7394	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
LONG KEYPRESS END DI40	3x07396 4x07396 I:7395	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI41					
RISE DI41	3x07401 4x07401 I:7400	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
FALL DI41	3x07402 4x07402 I:7401	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
CHANGE DI41	3x07403 4x07403 I:7402	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			

SHORT KEYPRESS DI41	3x07404 4x07404 I:7403	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI41	3x07405 4x07405 I:7404	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI41	3x07406 4x07406 I:7405	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI42						
RISE DI42	3x07411 4x07411 I:7410	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI42	3x07412 4x07412 I:7411	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI42	3x07413 4x07413 I:7412	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI42	3x07414 4x07414 I:7413	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI42	3x07415 4x07415 I:7414	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI42	3x07416 4x07416 I:7415	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI43						
RISE DI43	3x07421 4x07421 I:7420	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI43	3x07422 4x07422 I:7421	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)			
CHANGE DI43	3x07423 4x07423 I:7422	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
SHORT KEYPRESS DI43	3x07424 4x07424 I:7423	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
LONG KEYPRESS START DI43	3x07425 4x07425 I:7424	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
LONG KEYPRESS END DI43	3x07426 4x07426 I:7425	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI44					
RISE DI44	3x07431 4x07431 I:7430	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
FALL DI44	3x07432 4x07432 I:7431	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
CHANGE DI44	3x07433 4x07433 I:7432	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
SHORT KEYPRESS DI44	3x07434 4x07434 I:7433	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
LONG KEYPRESS START DI44	3x07435 4x07435 I:7434	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
LONG KEYPRESS END DI44	3x07436 4x07436 I:7435	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI45					

RISE DI45	3x07441 4x07441 I:7440	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI45	3x07442 4x07442 I:7441	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI45	3x07443 4x07443 I:7442	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI45	3x07444 4x07444 I:7443	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI45	3x07445 4x07445 I:7444	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI45	3x07446 4x07446 I:7445	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI46						
RISE DI46	3x07451 4x07451 I:7450	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI46	3x07452 4x07452 I:7451	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI46	3x07453 4x07453 I:7452	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI46	3x07454 4x07454 I:7453	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI46	3x07455 4x07455 I:7454	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

LONG KEYPRESS END DI46	3x07456 4x07456 I:7455	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI47						
RISE DI47	3x07461 4x07461 I:7460	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI47	3x07462 4x07462 I:7461	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI47	3x07463 4x07463 I:7462	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI47	3x07464 4x07464 I:7463	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI47	3x07465 4x07465 I:7464	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI47	3x07466 4x07466 I:7465	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI48						
RISE DI48	3x07471 4x07471 I:7470	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI48	3x07472 4x07472 I:7471	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI48	3x07473 4x07473 I:7472	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI48	3x07474 4x07474 I:7473	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)				
LONG KEYPRESS START DI48	3x07475 4x07475 I:7474	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI48	3x07476 4x07476 I:7475	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI49						
RISE DI49	3x07481 4x07481 I:7480	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI49	3x07482 4x07482 I:7481	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI49	3x07483 4x07483 I:7482	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI49	3x07484 4x07484 I:7483	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI49	3x07485 4x07485 I:7484	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI49	3x07486 4x07486 I:7485	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI50						
RISE DI50	3x07491 4x07491 I:7490	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI50	3x07492 4x07492 I:7491	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

CHANGE DI50	3x07493 4x07493 I:7492	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI50	3x07494 4x07494 I:7493	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI50	3x07495 4x07495 I:7494	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI50	3x07496 4x07496 I:7495	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI51						
RISE DI51	3x07501 4x07501 I:7500	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI51	3x07502 4x07502 I:7501	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI51	3x07503 4x07503 I:7502	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI51	3x07504 4x07504 I:7503	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI51	3x07505 4x07505 I:7504	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI51	3x07506 4x07506 I:7505	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI52						
RISE DI52	3x07511 4x07511 I:7510	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)				
FALL DI52	3x07512 4x07512 I:7511	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI52	3x07513 4x07513 I:7512	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI52	3x07514 4x07514 I:7513	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI52	3x07515 4x07515 I:7514	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI52	3x07516 4x07516 I:7515	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI53						
RISE DI53	3x07521 4x07521 I:7520	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI53	3x07522 4x07522 I:7521	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI53	3x07523 4x07523 I:7522	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI53	3x07524 4x07524 I:7523	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI53	3x07525 4x07525 I:7524	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI53	3x07526 4x07526 I:7525	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI54					
RISE DI54	3x07531 4x07531 I:7530	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
FALL DI54	3x07532 4x07532 I:7531	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
CHANGE DI54	3x07533 4x07533 I:7532	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
SHORT KEYPRESS DI54	3x07534 4x07534 I:7533	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
LONG KEYPRESS START DI54	3x07535 4x07535 I:7534	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
LONG KEYPRESS END DI54	3x07536 4x07536 I:7535	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI55					
RISE DI55	3x07541 4x07541 I:7540	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
FALL DI55	3x07542 4x07542 I:7541	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
CHANGE DI55	3x07543 4x07543 I:7542	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
SHORT KEYPRESS DI55	3x07544 4x07544 I:7543	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			

LONG KEYPRESS START DI55	3x07545 4x07545 I:7544	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI55	3x07546 4x07546 I:7545	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI56						
RISE DI56	3x07551 4x07551 I:7550	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI56	3x07552 4x07552 I:7551	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI56	3x07553 4x07553 I:7552	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI56	3x07554 4x07554 I:7553	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI56	3x07555 4x07555 I:7554	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI56	3x07556 4x07556 I:7555	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI57						
RISE DI57	3x07561 4x07561 I:7560	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI57	3x07562 4x07562 I:7561	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI57	3x07563 4x07563 I:7562	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)			
SHORT KEYPRESS DI57	3x07564 4x07564 I:7563	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
LONG KEYPRESS START DI57	3x07565 4x07565 I:7564	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
LONG KEYPRESS END DI57	3x07566 4x07566 I:7565	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI58					
RISE DI58	3x07571 4x07571 I:7570	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
FALL DI58	3x07572 4x07572 I:7571	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
CHANGE DI58	3x07573 4x07573 I:7572	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
SHORT KEYPRESS DI58	3x07574 4x07574 I:7573	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
LONG KEYPRESS START DI58	3x07575 4x07575 I:7574	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
LONG KEYPRESS END DI58	3x07576 4x07576 I:7575	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI59					
RISE DI59	3x07581 4x07581 I:7580	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			

FALL DI59	3x07582 4x07582 I:7581	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI59	3x07583 4x07583 I:7582	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI59	3x07584 4x07584 I:7583	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI59	3x07585 4x07585 I:7584	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI59	3x07586 4x07586 I:7585	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI60						
RISE DI60	3x07591 4x07591 I:7590	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI60	3x07592 4x07592 I:7591	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI60	3x07593 4x07593 I:7592	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI60	3x07594 4x07594 I:7593	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI60	3x07595 4x07595 I:7594	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI60	3x07596 4x07596 I:7595	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI61						
RISE DI61	3x07601 4x07601 I:7600	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI61	3x07602 4x07602 I:7601	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI61	3x07603 4x07603 I:7602	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI61	3x07604 4x07604 I:7603	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI61	3x07605 4x07605 I:7604	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI61	3x07606 4x07606 I:7605	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI62						
RISE DI62	3x07611 4x07611 I:7610	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
FALL DI62	3x07612 4x07612 I:7611	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
CHANGE DI62	3x07613 4x07613 I:7612	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
SHORT KEYPRESS DI62	3x07614 4x07614 I:7613	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS START DI62	3x07615 4x07615 I:7614	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)			
LONG KEYPRESS END DI62	3x07616 4x07616 I:7615	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI63					
RISE DI63	3x07621 4x07621 I:7620	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
FALL DI63	3x07622 4x07622 I:7621	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
CHANGE DI63	3x07623 4x07623 I:7622	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
SHORT KEYPRESS DI63	3x07624 4x07624 I:7623	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
LONG KEYPRESS START DI63	3x07625 4x07625 I:7624	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
LONG KEYPRESS END DI63	3x07626 4x07626 I:7625	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI64					
RISE DI64	3x07631 4x07631 I:7630	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
FALL DI64	3x07632 4x07632 I:7631	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
CHANGE DI64	3x07633 4x07633 I:7632	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			

SHORT KEYPRESS DI64	3x07634 4x07634 I:7633	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI64	3x07635 4x07635 I:7634	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI64	3x07636 4x07636 I:7635	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI65						
RISE DI65	3x07641 4x07641 I:7640	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI65	3x07642 4x07642 I:7641	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI65	3x07643 4x07643 I:7642	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI65	3x07644 4x07644 I:7643	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI65	3x07645 4x07645 I:7644	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI65	3x07646 4x07646 I:7645	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI66						
RISE DI66	3x07651 4x07651 I:7650	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI66	3x07652 4x07652 I:7651	1,0x0001 B:00 01			UINT16 R/O	

		1 event(s)				
CHANGE DI66	3x07653 4x07653 I:7652	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI66	3x07654 4x07654 I:7653	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI66	3x07655 4x07655 I:7654	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI66	3x07656 4x07656 I:7655	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI67						
RISE DI67	3x07661 4x07661 I:7660	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI67	3x07662 4x07662 I:7661	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI67	3x07663 4x07663 I:7662	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI67	3x07664 4x07664 I:7663	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI67	3x07665 4x07665 I:7664	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI67	3x07666 4x07666 I:7665	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI68						

RISE DI68	3x07671 4x07671 I:7670	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI68	3x07672 4x07672 I:7671	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI68	3x07673 4x07673 I:7672	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI68	3x07674 4x07674 I:7673	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI68	3x07675 4x07675 I:7674	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI68	3x07676 4x07676 I:7675	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI69						
RISE DI69	3x07681 4x07681 I:7680	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI69	3x07682 4x07682 I:7681	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI69	3x07683 4x07683 I:7682	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI69	3x07684 4x07684 I:7683	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI69	3x07685 4x07685 I:7684	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

LONG KEYPRESS END DI69	3x07686 4x07686 I:7685	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI70						
RISE DI70	3x07691 4x07691 I:7690	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI70	3x07692 4x07692 I:7691	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI70	3x07693 4x07693 I:7692	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI70	3x07694 4x07694 I:7693	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI70	3x07695 4x07695 I:7694	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI70	3x07696 4x07696 I:7695	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI71						
RISE DI71	3x07701 4x07701 I:7700	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI71	3x07702 4x07702 I:7701	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI71	3x07703 4x07703 I:7702	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI71	3x07704 4x07704 I:7703	1,0x0001 B:00 01			UINT16 R/O	

		1 event(s)				
LONG KEYPRESS START DI71	3x07705 4x07705 I:7704	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI71	3x07706 4x07706 I:7705	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI72						
RISE DI72	3x07711 4x07711 I:7710	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI72	3x07712 4x07712 I:7711	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI72	3x07713 4x07713 I:7712	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI72	3x07714 4x07714 I:7713	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI72	3x07715 4x07715 I:7714	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI72	3x07716 4x07716 I:7715	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI73						
RISE DI73	3x07721 4x07721 I:7720	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI73	3x07722 4x07722 I:7721	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				

CHANGE DI73	3x07723 4x07723 I:7722	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI73	3x07724 4x07724 I:7723	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI73	3x07725 4x07725 I:7724	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI73	3x07726 4x07726 I:7725	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI74						
RISE DI74	3x07731 4x07731 I:7730	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI74	3x07732 4x07732 I:7731	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI74	3x07733 4x07733 I:7732	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI74	3x07734 4x07734 I:7733	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI74	3x07735 4x07735 I:7734	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI74	3x07736 4x07736 I:7735	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI75						
RISE DI75	3x07741 4x07741 I:7740	1,0x0001 B:00 01			UINT16 R/O	

		1 event(s)				
FALL DI75	3x07742 4x07742 I:7741	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI75	3x07743 4x07743 I:7742	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI75	3x07744 4x07744 I:7743	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI75	3x07745 4x07745 I:7744	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI75	3x07746 4x07746 I:7745	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI76						
RISE DI76	3x07751 4x07751 I:7750	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI76	3x07752 4x07752 I:7751	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI76	3x07753 4x07753 I:7752	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI76	3x07754 4x07754 I:7753	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI76	3x07755 4x07755 I:7754	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI76	3x07756 4x07756 I:7755	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI77					
RISE DI77	3x07761 4x07761 I:7760	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
FALL DI77	3x07762 4x07762 I:7761	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
CHANGE DI77	3x07763 4x07763 I:7762	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
SHORT KEYPRESS DI77	3x07764 4x07764 I:7763	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
LONG KEYPRESS START DI77	3x07765 4x07765 I:7764	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
LONG KEYPRESS END DI77	3x07766 4x07766 I:7765	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI78					
RISE DI78	3x07771 4x07771 I:7770	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
FALL DI78	3x07772 4x07772 I:7771	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
CHANGE DI78	3x07773 4x07773 I:7772	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
SHORT KEYPRESS DI78	3x07774 4x07774 I:7773	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			

LONG KEYPRESS START DI78	3x07775 4x07775 I:7774	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI78	3x07776 4x07776 I:7775	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI79						
RISE DI79	3x07781 4x07781 I:7780	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI79	3x07782 4x07782 I:7781	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI79	3x07783 4x07783 I:7782	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI79	3x07784 4x07784 I:7783	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI79	3x07785 4x07785 I:7784	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI79	3x07786 4x07786 I:7785	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI80						
RISE DI80	3x07791 4x07791 I:7790	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI80	3x07792 4x07792 I:7791	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI80	3x07793 4x07793 I:7792	1,0x0001 B:00 01			UINT16 R/O	

		1 event(s)				
SHORT KEYPRESS DI80	3x07794 4x07794 I:7793	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI80	3x07795 4x07795 I:7794	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI80	3x07796 4x07796 I:7795	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI81						
RISE DI81	3x07801 4x07801 I:7800	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI81	3x07802 4x07802 I:7801	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI81	3x07803 4x07803 I:7802	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI81	3x07804 4x07804 I:7803	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI81	3x07805 4x07805 I:7804	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI81	3x07806 4x07806 I:7805	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI82						
RISE DI82	3x07811 4x07811 I:7810	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				

FALL DI82	3x07812 4x07812 I:7811	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI82	3x07813 4x07813 I:7812	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI82	3x07814 4x07814 I:7813	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI82	3x07815 4x07815 I:7814	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI82	3x07816 4x07816 I:7815	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI83						
RISE DI83	3x07821 4x07821 I:7820	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI83	3x07822 4x07822 I:7821	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI83	3x07823 4x07823 I:7822	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI83	3x07824 4x07824 I:7823	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI83	3x07825 4x07825 I:7824	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI83	3x07826 4x07826 I:7825	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI84						
RISE DI84	3x07831 4x07831 I:7830	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI84	3x07832 4x07832 I:7831	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI84	3x07833 4x07833 I:7832	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI84	3x07834 4x07834 I:7833	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI84	3x07835 4x07835 I:7834	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI84	3x07836 4x07836 I:7835	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI85						
RISE DI85	3x07841 4x07841 I:7840	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI85	3x07842 4x07842 I:7841	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI85	3x07843 4x07843 I:7842	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI85	3x07844 4x07844 I:7843	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI85	3x07845 4x07845 I:7844	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)			
LONG KEYPRESS END DI85	3x07846 4x07846 I:7845	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI86					
RISE DI86	3x07851 4x07851 I:7850	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
FALL DI86	3x07852 4x07852 I:7851	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
CHANGE DI86	3x07853 4x07853 I:7852	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
SHORT KEYPRESS DI86	3x07854 4x07854 I:7853	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
LONG KEYPRESS START DI86	3x07855 4x07855 I:7854	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
LONG KEYPRESS END DI86	3x07856 4x07856 I:7855	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI87					
RISE DI87	3x07861 4x07861 I:7860	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
FALL DI87	3x07862 4x07862 I:7861	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
CHANGE DI87	3x07863 4x07863 I:7862	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			

SHORT KEYPRESS DI87	3x07864 4x07864 I:7863	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI87	3x07865 4x07865 I:7864	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI87	3x07866 4x07866 I:7865	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI88						
RISE DI88	3x07871 4x07871 I:7870	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI88	3x07872 4x07872 I:7871	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI88	3x07873 4x07873 I:7872	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI88	3x07874 4x07874 I:7873	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI88	3x07875 4x07875 I:7874	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI88	3x07876 4x07876 I:7875	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI89						
RISE DI89	3x07881 4x07881 I:7880	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI89	3x07882 4x07882 I:7881	1,0x0001 B:00 01			UINT16 R/O	

		1 event(s)				
CHANGE DI89	3x07883 4x07883 I:7882	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI89	3x07884 4x07884 I:7883	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI89	3x07885 4x07885 I:7884	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI89	3x07886 4x07886 I:7885	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI90						
RISE DI90	3x07891 4x07891 I:7890	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI90	3x07892 4x07892 I:7891	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI90	3x07893 4x07893 I:7892	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI90	3x07894 4x07894 I:7893	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI90	3x07895 4x07895 I:7894	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI90	3x07896 4x07896 I:7895	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI91						

RISE DI91	3x07901 4x07901 I:7900	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI91	3x07902 4x07902 I:7901	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI91	3x07903 4x07903 I:7902	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI91	3x07904 4x07904 I:7903	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI91	3x07905 4x07905 I:7904	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI91	3x07906 4x07906 I:7905	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI92						
RISE DI92	3x07911 4x07911 I:7910	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI92	3x07912 4x07912 I:7911	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI92	3x07913 4x07913 I:7912	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI92	3x07914 4x07914 I:7913	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI92	3x07915 4x07915 I:7914	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

LONG KEYPRESS END DI92	3x07916 4x07916 I:7915	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI93						
RISE DI93	3x07921 4x07921 I:7920	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI93	3x07922 4x07922 I:7921	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI93	3x07923 4x07923 I:7922	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI93	3x07924 4x07924 I:7923	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI93	3x07925 4x07925 I:7924	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI93	3x07926 4x07926 I:7925	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI94						
RISE DI94	3x07931 4x07931 I:7930	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI94	3x07932 4x07932 I:7931	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI94	3x07933 4x07933 I:7932	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI94	3x07934 4x07934 I:7933	1,0x0001 B:00 01			UINT16 R/O	

		1 event(s)				
LONG KEYPRESS START DI94	3x07935 4x07935 I:7934	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI94	3x07936 4x07936 I:7935	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI95						
RISE DI95	3x07941 4x07941 I:7940	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI95	3x07942 4x07942 I:7941	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI95	3x07943 4x07943 I:7942	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI95	3x07944 4x07944 I:7943	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI95	3x07945 4x07945 I:7944	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI95	3x07946 4x07946 I:7945	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI96						
RISE DI96	3x07951 4x07951 I:7950	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI96	3x07952 4x07952 I:7951	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				

CHANGE DI96	3x07953 4x07953 I:7952	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI96	3x07954 4x07954 I:7953	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI96	3x07955 4x07955 I:7954	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI96	3x07956 4x07956 I:7955	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI97						
RISE DI97	3x07961 4x07961 I:7960	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI97	3x07962 4x07962 I:7961	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI97	3x07963 4x07963 I:7962	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI97	3x07964 4x07964 I:7963	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI97	3x07965 4x07965 I:7964	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI97	3x07966 4x07966 I:7965	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI98						
RISE DI98	3x07971 4x07971 I:7970	1,0x0001 B:00 01			UINT16 R/O	

		1 event(s)				
FALL DI98	3x07972 4x07972 I:7971	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI98	3x07973 4x07973 I:7972	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI98	3x07974 4x07974 I:7973	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI98	3x07975 4x07975 I:7974	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI98	3x07976 4x07976 I:7975	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI99						
RISE DI99	3x07981 4x07981 I:7980	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI99	3x07982 4x07982 I:7981	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI99	3x07983 4x07983 I:7982	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI99	3x07984 4x07984 I:7983	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI99	3x07985 4x07985 I:7984	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI99	3x07986 4x07986 I:7985	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI100					
RISE DI100	3x07991 4x07991 I:7990	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
FALL DI100	3x07992 4x07992 I:7991	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
CHANGE DI100	3x07993 4x07993 I:7992	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
SHORT KEYPRESS DI100	3x07994 4x07994 I:7993	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
LONG KEYPRESS START DI100	3x07995 4x07995 I:7994	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
LONG KEYPRESS END DI100	3x07996 4x07996 I:7995	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI101					
RISE DI101	3x08001 4x08001 I:8000	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
FALL DI101	3x08002 4x08002 I:8001	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
CHANGE DI101	3x08003 4x08003 I:8002	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
SHORT KEYPRESS DI101	3x08004 4x08004 I:8003	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			

LONG KEYPRESS START DI101	3x08005 4x08005 I:8004	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI101	3x08006 4x08006 I:8005	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI102						
RISE DI102	3x08011 4x08011 I:8010	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI102	3x08012 4x08012 I:8011	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI102	3x08013 4x08013 I:8012	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI102	3x08014 4x08014 I:8013	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI102	3x08015 4x08015 I:8014	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI102	3x08016 4x08016 I:8015	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI103						
RISE DI103	3x08021 4x08021 I:8020	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI103	3x08022 4x08022 I:8021	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI103	3x08023 4x08023 I:8022	1,0x0001 B:00 01			UINT16 R/O	

		1 event(s)				
SHORT KEYPRESS DI103	3x08024 4x08024 I:8023	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI103	3x08025 4x08025 I:8024	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI103	3x08026 4x08026 I:8025	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI104						
RISE DI104	3x08031 4x08031 I:8030	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI104	3x08032 4x08032 I:8031	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI104	3x08033 4x08033 I:8032	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI104	3x08034 4x08034 I:8033	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI104	3x08035 4x08035 I:8034	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI104	3x08036 4x08036 I:8035	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI105						
RISE DI105	3x08041 4x08041 I:8040	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				

FALL DI105	3x08042 4x08042 I:8041	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI105	3x08043 4x08043 I:8042	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI105	3x08044 4x08044 I:8043	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI105	3x08045 4x08045 I:8044	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI105	3x08046 4x08046 I:8045	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI106						
RISE DI106	3x08051 4x08051 I:8050	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI106	3x08052 4x08052 I:8051	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI106	3x08053 4x08053 I:8052	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI106	3x08054 4x08054 I:8053	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI106	3x08055 4x08055 I:8054	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI106	3x08056 4x08056 I:8055	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI107

RISE DI107	3x08061 4x08061 I:8060	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI107	3x08062 4x08062 I:8061	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI107	3x08063 4x08063 I:8062	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI107	3x08064 4x08064 I:8063	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI107	3x08065 4x08065 I:8064	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI107	3x08066 4x08066 I:8065	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI108

RISE DI108	3x08071 4x08071 I:8070	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI108	3x08072 4x08072 I:8071	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI108	3x08073 4x08073 I:8072	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI108	3x08074 4x08074 I:8073	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI108	3x08075 4x08075 I:8074	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)			
LONG KEYPRESS END DI108	3x08076 4x08076 I:8075	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI109					
RISE DI109	3x08081 4x08081 I:8080	1,0x0001 B:00 01		UINT16 R/O	
		1 event(s)			
FALL DI109	3x08082 4x08082 I:8081	1,0x0001 B:00 01		UINT16 R/O	
		1 event(s)			
CHANGE DI109	3x08083 4x08083 I:8082	1,0x0001 B:00 01		UINT16 R/O	
		1 event(s)			
SHORT KEYPRESS DI109	3x08084 4x08084 I:8083	1,0x0001 B:00 01		UINT16 R/O	
		1 event(s)			
LONG KEYPRESS START DI109	3x08085 4x08085 I:8084	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
LONG KEYPRESS END DI109	3x08086 4x08086 I:8085	0,0x0000 B:00 00		UINT16 R/O	
		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI110					
RISE DI110	3x08091 4x08091 I:8090	1,0x0001 B:00 01		UINT16 R/O	
		1 event(s)			
FALL DI110	3x08092 4x08092 I:8091	1,0x0001 B:00 01		UINT16 R/O	
		1 event(s)			
CHANGE DI110	3x08093 4x08093 I:8092	1,0x0001 B:00 01		UINT16 R/O	
		1 event(s)			

SHORT KEYPRESS DI110	3x08094 4x08094 I:8093	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI110	3x08095 4x08095 I:8094	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI110	3x08096 4x08096 I:8095	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI111						
RISE DI111	3x08101 4x08101 I:8100	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI111	3x08102 4x08102 I:8101	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI111	3x08103 4x08103 I:8102	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI111	3x08104 4x08104 I:8103	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI111	3x08105 4x08105 I:8104	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI111	3x08106 4x08106 I:8105	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI112						
RISE DI112	3x08111 4x08111 I:8110	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI112	3x08112 4x08112 I:8111	1,0x0001 B:00 01			UINT16 R/O	

		1 event(s)				
CHANGE DI112	3x08113 4x08113 I:8112	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI112	3x08114 4x08114 I:8113	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI112	3x08115 4x08115 I:8114	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI112	3x08116 4x08116 I:8115	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI113						
RISE DI113	3x08121 4x08121 I:8120	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI113	3x08122 4x08122 I:8121	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI113	3x08123 4x08123 I:8122	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI113	3x08124 4x08124 I:8123	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI113	3x08125 4x08125 I:8124	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI113	3x08126 4x08126 I:8125	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI114						

RISE DI114	3x08131 4x08131 I:8130	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI114	3x08132 4x08132 I:8131	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI114	3x08133 4x08133 I:8132	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI114	3x08134 4x08134 I:8133	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI114	3x08135 4x08135 I:8134	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI114	3x08136 4x08136 I:8135	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI115						
RISE DI115	3x08141 4x08141 I:8140	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI115	3x08142 4x08142 I:8141	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI115	3x08143 4x08143 I:8142	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI115	3x08144 4x08144 I:8143	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI115	3x08145 4x08145 I:8144	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				

LONG KEYPRESS END DI115	3x08146 4x08146 I:8145	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI116						
RISE DI116	3x08151 4x08151 I:8150	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI116	3x08152 4x08152 I:8151	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI116	3x08153 4x08153 I:8152	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI116	3x08154 4x08154 I:8153	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI116	3x08155 4x08155 I:8154	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI116	3x08156 4x08156 I:8155	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI117						
RISE DI117	3x08161 4x08161 I:8160	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI117	3x08162 4x08162 I:8161	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI117	3x08163 4x08163 I:8162	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI117	3x08164 4x08164 I:8163	1,0x0001 B:00 01			UINT16 R/O	

		1 event(s)				
LONG KEYPRESS START DI117	3x08165 4x08165 I:8164	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI117	3x08166 4x08166 I:8165	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI118						
RISE DI118	3x08171 4x08171 I:8170	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI118	3x08172 4x08172 I:8171	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI118	3x08173 4x08173 I:8172	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI118	3x08174 4x08174 I:8173	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI118	3x08175 4x08175 I:8174	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI118	3x08176 4x08176 I:8175	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI119						
RISE DI119	3x08181 4x08181 I:8180	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI119	3x08182 4x08182 I:8181	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				

CHANGE DI119	3x08183 4x08183 I:8182	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI119	3x08184 4x08184 I:8183	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI119	3x08185 4x08185 I:8184	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI119	3x08186 4x08186 I:8185	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI120						
RISE DI120	3x08191 4x08191 I:8190	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI120	3x08192 4x08192 I:8191	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI120	3x08193 4x08193 I:8192	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI120	3x08194 4x08194 I:8193	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI120	3x08195 4x08195 I:8194	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI120	3x08196 4x08196 I:8195	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI121						
RISE DI121	3x08201 4x08201 I:8200	1,0x0001 B:00 01			UINT16 R/O	

		1 event(s)				
FALL DI121	3x08202 4x08202 I:8201	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI121	3x08203 4x08203 I:8202	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI121	3x08204 4x08204 I:8203	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI121	3x08205 4x08205 I:8204	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI121	3x08206 4x08206 I:8205	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI122						
RISE DI122	3x08211 4x08211 I:8210	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI122	3x08212 4x08212 I:8211	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI122	3x08213 4x08213 I:8212	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI122	3x08214 4x08214 I:8213	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI122	3x08215 4x08215 I:8214	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI122	3x08216 4x08216 I:8215	0,0x0000 B:00 00			UINT16 R/O	

		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI123					
RISE DI123	3x08221 4x08221 I:8220	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
FALL DI123	3x08222 4x08222 I:8221	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
CHANGE DI123	3x08223 4x08223 I:8222	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
SHORT KEYPRESS DI123	3x08224 4x08224 I:8223	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
LONG KEYPRESS START DI123	3x08225 4x08225 I:8224	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
LONG KEYPRESS END DI123	3x08226 4x08226 I:8225	0,0x0000 B:00 00			UINT16 R/O
		0 event(s)			
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI124					
RISE DI124	3x08231 4x08231 I:8230	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
FALL DI124	3x08232 4x08232 I:8231	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
CHANGE DI124	3x08233 4x08233 I:8232	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			
SHORT KEYPRESS DI124	3x08234 4x08234 I:8233	1,0x0001 B:00 01			UINT16 R/O
		1 event(s)			

LONG KEYPRESS START DI124	3x08235 4x08235 I:8234	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI124	3x08236 4x08236 I:8235	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI125						
RISE DI125	3x08241 4x08241 I:8240	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI125	3x08242 4x08242 I:8241	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI125	3x08243 4x08243 I:8242	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI125	3x08244 4x08244 I:8243	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI125	3x08245 4x08245 I:8244	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI125	3x08246 4x08246 I:8245	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI126						
RISE DI126	3x08251 4x08251 I:8250	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI126	3x08252 4x08252 I:8251	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI126	3x08253 4x08253 I:8252	1,0x0001 B:00 01			UINT16 R/O	

		1 event(s)				
SHORT KEYPRESS DI126	3x08254 4x08254 I:8253	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI126	3x08255 4x08255 I:8254	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI126	3x08256 4x08256 I:8255	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI127						
RISE DI127	3x08261 4x08261 I:8260	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
FALL DI127	3x08262 4x08262 I:8261	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI127	3x08263 4x08263 I:8262	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI127	3x08264 4x08264 I:8263	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI127	3x08265 4x08265 I:8264	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI127	3x08266 4x08266 I:8265	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
DIGITAL INPUTS: STATUS FOR DIGITAL INPUT DI128						
RISE DI128	3x08271 4x08271 I:8270	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				

FALL DI128	3x08272 4x08272 I:8271	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
CHANGE DI128	3x08273 4x08273 I:8272	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
SHORT KEYPRESS DI128	3x08274 4x08274 I:8273	1,0x0001 B:00 01			UINT16 R/O	
		1 event(s)				
LONG KEYPRESS START DI128	3x08275 4x08275 I:8274	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
LONG KEYPRESS END DI128	3x08276 4x08276 I:8275	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				