

Command NAME	ASCII command type	ASCII command structure	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
ASCII COMMANDS						
HEART BEAT	ASCII READ COMMAND	#HB<CR> Result: #HB<CR>			ASCII	
	TX	#1,HB<CR>				
	RX	#1,HB<CR>				
Sends an Heartbeat to test the communcation						
GET VERSION	ASCII READ COMMAND	#VERSION<CR> Result: #VERSION:<VersionHi>,<VersionMed>,<VersionLo><CR>			ASCII	
	TX	#1,VERSION<CR>				
	RX	#1,VERSION:1.2.00<CR>				
		Current SW version:1.2.00				
Returns the version number of the module VersionHi: Version number high (1..255) VersionMed: Version number medium (1..255) VersionLo: Version number low (1..255)						
GET TYPE	ASCII READ COMMAND	#TYPE<CR> Result: #TYPE:<Type><CR>			ASCII	
	TX	#1,TYPE<CR>				
	RX	#1,TYPE:RESI-24DO-SIO<CR>				
		Current module type:RESI-24DO-SIO				
Returns the current module type						
GET FEATURES	ASCII READ COMMAND	#FTRS<CR> Result: #FTRS:<Type><CR>			ASCII	
	TX	#1,FTRS<CR>				
	RX	#1,FTRS:RESI-24DO-SIO,RS485,DO:24,DOIC:MAX14915<CR>				
		Current module type:#1,FTRS:RESI-24DO-SIO,RS485,DO:24,DOIC:MAX14915<CR>				
Returns the current module features						
GET OWNER	ASCII READ COMMAND	#OWNER<CR> Result: #OWNER:<Owner><CR>			ASCII	
	TX	#1,OWNER<CR>				
	RX	#1,OWNER:RESI<CR>				
		Current owner:RESI				
Returns the current owner of the module						

GET CREATOR	ASCII READ COMMAND	#CREATOR<CR> Result: #CREATOR:<Creator><CR>	ASCII	
	TX	#1,CREATOR<CR>		
	RX	#1,CREATOR:DI HC SIGL,MSC<CR>		
		Current creator:DI HC SIGL,MSC		
Returns the current creator of the module				
GET COPYRIGHT	ASCII READ COMMAND	#COPYRIGHT<CR> Result: #COPYRIGHT:<Copyright><CR>	ASCII	
	TX	#1,COPYRIGHT<CR>		
	RX	#1,COPYRIGHT:2015-24 BY RESI AND DI HC SIGL,MSC WWW.RESI.CC<CR>		
		Current copyright:2015-24 BY RESI AND DI HC SIGL,MSC WWW.RESI.CC		
Returns the current copyright of the module				
GET SERIAL NUMBER	ASCII READ COMMAND	#SN<CR> Result: #SN:<Serial><CR>	ASCII	
	TX	#1,SN<CR>		
	RX	#1,SN:280029001457435535343920<CR>		
		Current serial number:280029001457435535343920		
Returns the current serial number of the module				
GET INTERNAL STATUS	ASCII READ COMMAND	#INTSTAT<CR> Result: #INTSTAT:<Status><CR>	ASCII	
	TX	#1,INTSTAT<CR>		
	RX	#1,INTSTAT:I2C1:0,I2C2:0,FRAM:44<CR>		
Returns the device specific internal status				
GET DIP SWITCH	ASCII READ COMMAND	#GDIP<CR> Result: #GDIP:<DIPSwitchDec>,<DIPSwitchHex><CR>	ASCII	
	TX	#1,GDIP<CR>		
	RX	#1,GDIP:65,0x41<CR>		
		Current DIP SWITCH settings:0100.0001		
Returns the current setting of the Dip switches as decimal number and as hexadecimal number. DIPSwitchDec DIPSwitchHex The current value of the DIP switches: Bit 0: DIP Switch 1 (=0:OFF, =1:ON) Bit 1: DIP Switch 2 (=0:OFF, =1:ON) Bit 2: DIP Switch 3 (=0:OFF, =1:ON) Bit 3: DIP Switch 4 (=0:OFF, =1:ON) Bit 4: DIP Switch 5, if available (=0:OFF, =1:ON) Bit 5: DIP Switch 6, if available (=0:OFF, =1:ON) Bit 6: DIP Switch 7, if available (=0:OFF, =1:ON) Bit 7: DIP Switch 8, if available (=0:OFF, =1:ON)				
ASCII COMMANDS				

SET MODBUS ADDRESS	ASCII WRITE COMMAND	#SMBADR:<UNITID> <CR> Result: #OK<CR>	ASCII	NO
	UNITID	123		
	TX	#1,SMBADR:123<CR>		
	RX	N/A		
Redefines the unit ID of the module. This change will affect the MODBUS/RTU communication immediately. As a Unit IO you can use the values 0dec to 255dec.				
HINT: The new settings are activated after a system reboot or power off on cycle!				
SET MODBUS BAUDRATE	ASCII WRITE COMMAND	#SMBBAUD:<BAUD> <CR> Result: #OK<CR>	ASCII	NO
	BAUD	128000:128000BD		
	TX	#1,SMBBAUD:128000<CR>		
	RX	N/A		
Sets a new baud rate in the FLASH For ULTRA SLIM IOs RESI-xxx-SIO: This baudrate is only used, if DIP switch mode DIP1=ON+DIP2=ON (BR) (default is 57600bd) For BIG IOs RESI-xxx-SIO: This baudrate is only used, if DIP switch mode DIP7=ON (PARAMETER) (default is 57600bd) The following baudrates are allowed: 300bd, 600bd, 900bd, 1200bd, 2400bd, 4800bd, 9600bd, 19200bd, 38400bd, 57600bd, 115200bd, 128000bd 230400bd, 250000bd, 256000bd				
HINT: The new setup parameters will be active after a restart of the module.				
SET MODBUS PARITY	ASCII WRITE COMMAND	#SMBPAR:<PARITY> <CR> Result: #OK<CR>	ASCII	NO
	PARITY	ODD:ODD PARITY		
	TX	#1,SMBPAR:ODD<CR>		
	RX	N/A		
Sets a new parity for the serial interface. MBParity: NONE: no parity EVEN: even parity ODD: odd parity				
HINT: The new setup parameters will be active after a restart of the module.				
SET MODBUS STOPS	ASCII WRITE COMMAND	#SMBSTOP:<STOPBIT> <CR> Result: #OK<CR>	ASCII	NO
	STOPBIT	TWO:TWO STOPBITS		
	TX	#1,SMBSTOP:TWO<CR>		
	RX	N/A		

Sets a new amount of stop bits for the serial interface. MBStops ONE: one stop bit TWO: two stop bits HINT: The new setup parameters will be active after a restart of the module.				
SET MODBUS PARAMS	ASCII WRITE COMMAND	#SMBPARAMS:<UNITID>,<BAUD>,<PARITY>,<STOPBIT> <CR> Result: #OK<CR>	ASCII	NO
	UNITID	3		
	BAUD	115200:115200BD		
	PARITY	EVEN:EVEN PARITY		
	STOPBIT	TWO:TWO STOPBITS		
	TX	#1,SMBPARAMS:3,115200,EVEN,TWO<CR>		
	RX	N/A		
Sets all parameters for serial interface				
GET MODBUS ADDRESS	ASCII READ COMMAND	#GMBADR<CR> Result: #GMBADR:<MBUnitDec>,<MBFLASHDec>,<MBUnitHex>,<MBFLASHHex> <CR>	ASCII	
	TX	#1,GMBADR<CR>		
	RX	#1,GMBADR:1,15,0x1,0xF<CR>		
		Current MODBUS unit ID:1,15,0x1,0xF		
Shows the current used MODBUS/RTU or ASCII unit address and shows also the stored unit address in the FLASH memory, which is only used if the DIP switch for the bus address is set to 0. MBUnitDec,MBUnitHex The current used MODBUS/RTU unit or ASCII address for communication MBFLASHDec,MBFLASHHex The internal stored MODBUS/RTU unit address or ASCII address from the FLASH memory, if the DIP switch DIP3 is OFF.				
GET MODBUS BAUDRATE	ASCII READ COMMAND	#GMBBAUD<CR> Result: #GMBBAUD:<BaudRate> <CR>	ASCII	
	TX	#1,GMBBAUD<CR>		
	RX	#1,GMBBAUD:115200,0x1C200<CR>		
		Current baudrate:115200,0x1C200		
This is the current configured baud rate in the FLASH For ULTRA SLIM IOs RESI-xxx-SIO: This baudrate is only used, if DIP switch mode DIP1=ON+DIP2=ON (BR) (default is 57600bd) For BIG IOs RESI-xxx-SIO: This baudrate is only used, if DIP switch mode DIP7=ON (PARAMETER) (default is 57600bd) The following baudrates are allowed: 300bd, 600bd, 900bd, 1200bd, 2400bd, 4800bd, 9600bd, 19200bd, 38400bd, 57600bd, 115200bd, 128000bd 230400bd, 250000bd, 256000bd				
GET MODBUS PARITY	ASCII READ COMMAND	#GMBPAR<CR> Result: #GMBPAR:<MBParity> <CR>	ASCII	
	TX	#1,GMBPAR<CR>		
	RX	#1,GMBPAR:NONE<CR>		

		Current parity:NONE		
Shows the current configured parity of the serial interface. MBParity NONE: no parity EVEN: even parity ODD: odd parity				
GET MODBUS STOP	ASCII READ COMMAND	#GMBSTOP<CR> Result: #GMBSTOP:<MBStop> <CR>	ASCII	
	TX	#1,GMBSTOP<CR>		
	RX	#1,GMBPAR:ONE<CR>		
		Current stopbit(s):ONE		
Shows the current configured parity of the serial interface. MBParity NONE: no parity EVEN: even parity ODD: odd parity				
GET MODBUS PARAMS	ASCII READ COMMAND	#GMBPARAMS<CR> Result: #GMBPARAMS:<MBUnitDec>,<MBFLASHDec>,<MBUnitHex>,<MBFLASHHex>,<MBBaudrateDec>,<MBBaudrateHex>,<MBParity>,<MBStops> <CR>	ASCII	
	TX	#1,GMBPARAMS<CR>		
	RX	#1,GMBPARAMS:1,0x1,15,0xF,115200,0x1C200,NONE,ONE <CR>		
		Current MODBUS unit ID used:1		
		Current MODBUS unit ID in FLASH:15		
		Current baudrate in FLASH:115200		
		Current parity in FLASH:NONE		
		Current stopbit(s) in FLASH:ONE		
Returns the complete settings for serial interface				
ASCII COMMANDS				
RESET	ASCII WRITE COMMAND	#RST<CR> Result: #OK<CR>	ASCII	NO
	TX	#1,RST<CR>		
	RX	N/A		
Executes a software reset (Reboot) of the module.				
FACTORY RESET	ASCII WRITE COMMAND	#FRST<CR> Result: #OK<CR>	ASCII	NO
	TX	#1,FRST<CR>		
	RX	N/A		
Performs a factory reset of all internal saved parameters				
SET MODBUS WATCHDOG TIMER	ASCII WRITE COMMAND	#SMBWATCHDOG:<WDTIME> <CR> Result: #OK<CR>	ASCII	YES

	WDTIME	100		
	TX	#1,SMBWATCHDOG:100<CR>		
	RX	#1,OK<CR>		
Enables or disables the WATCHDOG Timer for the IO module. WDTIME: 1..65535: Time for Watchdog in 1/100s =0: Watchdog is deactivated HINT: The Watchdog is internally handled every 100ms. If the IO module receives no valid frame within this time period, the outputs are set to predefined values!				
GET MODBUS WATCHDOG TIMER	ASCII READ COMMAND	#GMBWATCHDOG<CR> Result: #GMBWATCHDOG:<WDTIME> <CR>	ASCII	
	TX	#1,GMBWATCHDOG<CR>		
	RX	#1,GMBWATCHDOG:0,0x0<CR>		
		Current watchdog time:0 -> 0,0s		
Shows the actual configured time for the telegram watchdog function of the IO module. WDTIME: 1..65535: Time for Watchdog in 1/100s =0: Watchdog is deactivated HINT: The Watchdog is internally handled every 100ms. If the IO module receives no valid frame within this time period, the outputs are set to predefined values!				
CPU PARAMETERS				
GET CPU VOLTAGE	ASCII READ COMMAND	#GCPUTEMP<CR> Result: #GCPUTEMP:<CPUTemp> <CR>	ASCII	
	TX	#1,GCPUTEMP<CR>		
	RX	#1,GCPUTEMP:36.2003<CR>		
		Current internal temperature of CPU:36.2003°C		
Current internal temperature of CPU in ° Celsius.				
GET CPU VOLTAGE	ASCII READ COMMAND	#GCPUVOLT<CR> Result: #GCPUVOLT:<CPUVoltage> <CR>	ASCII	
	TX	#1,GCPUVOLT<CR>		
	RX	#1,GCPUVOLT:3.3725<CR>		
		Current supply voltage of CPU:3.3725V		
Current internal supply voltage of CPU in Volt.				
GET CPU BACKUP	ASCII READ COMMAND	#GCPUBACK<CR> Result: #GCPUBACK:<CPUBackupVoltage> <CR>	ASCII	
	TX	#1,GCPUBACK<CR>		
	RX	#1,GCPUBACK:0.0002<CR>		
		Current backup voltage of CPU:0.0002V		
Current internal backup capacitor voltage of CPU in Volt.				

Register NAME	MODBUS Register	Register VALUE	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
PRODUCT DATA						
HW_GROUP	3x65201 4x65201 I:65200	16384,0x4000 B:40 00			UINT16 R/O	
This is the group of hardware of the current product						
SW_GROUP	3x65202 4x65202 I:65201	32843,0x804B B:80 4B			UINT16 R/O	
This is the group of software of the current product						
SW_VERSION	3x65203 4x65203 I:65202	4608,0x1200 B:12 00			UINT16 R/O	
		SW VERSION:1.2.0				
This is the current software version of the firmware						
SW_AUTHOR	3x65204 4x65204 I:65203	18771,0x4953 B:49 53			UINT16 R/O	
This is the current software author of the firmware						
MODBUS SETTINGS						
UNIT_ID	3x65222 4x65222 I:65221	1,0x0001 B:00 01			UINT16 R/O	
		UNIT ID:1				
If the host reads this register, the current defined unit ID is returned.						
FLASH UNIT_ID	3x65223 4x65223 I:65222	15,0x000F B:00 0F		27	UINT16 R/W	NO
		UNIT ID:15				
If the host reads this register, the current defined unit ID from the FLASH is returned. This UnitID is used if DIP switch for UnitID is set to 15						
HINT:This settings will be active after you repower or reset your device !!						
BAUD_RATE	3x65224 4x65224 I:65223	115200,0x0001C200 B:00 01 C2 00	57600	57600	UINT32 R/W	NO
		115200Bd	ENTER BAUD RATE			
This is the current configured baud rate in the FLASH For ULTRA SLIM IOs RESI-xxx-SIO: This baudrate is only used, if DIP switch mode DIP1=ON+DIP2=ON (BR) (default is 57600bd) For BIG IOs RESI-xxx-SIO: This baudrate is only used, if DIP switch mode DIP7=ON (PARAMETER) (default is 57600bd)						

Valid baud rates are: 300bd, 600bd, 900bd, 1200bd, 2400bd, 4800bd, 9600bd, 19200bd, 38400bd, 57600bd, 115200bd, 128000bd 230400bd, 250000bd, 256000bd						
HINT:This settings will be active after you repower or reset your device !!						
PARITY	3x65226 4x65226 I:65225	0,0x0000 B:00 00		1:EVEN PARITY	UINT16 R/W	NO
		NO PARITY		SELECT PARITY		
If the register is read out, the currently set parity of the serial interface is returned. Writing a value to this register will change the new parity in FLASH. This will only take effect after a restart of the module. This can be triggered by writing to the RESET SYSTEM register.						
Parity values are 0: no parity 1: even parity 2: odd parity						
STOP BITS	3x65227 4x65227 I:65226	1,0x0001 B:00 01		2:TWO STOPBITS	UINT16 R/W	NO
		ONE STOPBIT		SELECT STOPBITS		
If the register is read out, the currently set number of stop bits of the serial interface is returned. Writing a value to this register will change the new number of stop bits in the FLASH. This will only take effect after a restart of the module. This can be triggered by writing to the RESET SYSTEM register.						
Values for stop bits are 1: one stop bit 2: two stop bits						
MODBUS TIMING	3x65228 4x65228 I:65227	0,0x0000 B:00 00		10	UINT16 R/W	NO
		Actual timing:0ms				
If the host reads this register, the current defined timing for MODBUS telegrams is returned. This timing is a time in ms which extends the standard 1.5 character timeout between two consecutive bytes on the serial line. If you write a new value to this register, the new settings are stored into the internal FLASH. Reboot the device to activate the new settings.						
MODBUS WATCHDOG TIME	3x65229 4x65229 I:65228	0,0x0000 B:00 00		50	UINT16 R/W	YES
		Actual watchdog time in 1/100s:0 -> 0,0s				
Writing a value onto this register defines a new time for the internal communication watchdog timer. The value is a timespan in 1/100s. =0: The communication watchdog is disabled =1..65535: Communication watchdog will be trigegred after x 1/100s pause on communication line In case of an communication watchdog, the module sets all outputs to the states defined in the configuration output registers Reading this registe will return the current stored time from the internal FLASH						
CPU DATA						
SERIAL1	3x65521 4x65521 I:65520	40,0x0028 B:00 28			UINT16 R/O	

Serial number of module as 96 bit unsigned integer number						
SERIAL2	3x65522 4x65522 I:65521	41,0x0029 B:00 29			UINT16 R/O	
SERIAL3	3x65523 4x65523 I:65522	22292,0x5714 B:57 14			UINT16 R/O	
SERIAL4	3x65524 4x65524 I:65523	21827,0x5543 B:55 43			UINT16 R/O	
SERIAL5	3x65525 4x65525 I:65524	13365,0x3435 B:34 35			UINT16 R/O	
SERIAL6	3x65526 4x65526 I:65525	8249,0x2039 B:20 39			UINT16 R/O	
	SERIAL:280029001457435535343920					
Serial number of module as 96 bit unsigned integer number						
CPU TEMPERATURE	3x65527 4x65527 I:65526	3646,0x0E3E B:0E 3E			UINT16 R/O	
	Current internal temperature of CPU:36,5°C					
Current internal temperature of CPU in ° Celsius multiplied by 10.						
CPU VOLTAGE	3x65528 4x65528 I:65527	337,0x0151 B:01 51			UINT16 R/O	
	Current supply voltage of CPU:3,37V					
Current internal supply voltage of CPU in Volt multiplied by 1000.						
CPU BACKUP VOLTAGE	3x65529 4x65529 I:65528	0,0x0000 B:00 00			UINT16 R/O	
	Current backup voltage of CPU:0,00V					
Current internal backup capacitor voltage of CPU in Volt multiplied by 1000.						
DIP SWITCH STATUS						
DIP SWITCH	3x65300 4x65300 I:65299	65,0x0041 B:00 41			UINT16 R/O	
Returns the current setting of the Dip switches. Bit 0: DIP Switch 1 (=0:OFF, =1:ON) Bit 1: DIP Switch 2 (=0:OFF, =1:ON) Bit 2: DIP Switch 3 (=0:OFF, =1:ON) Bit 3: DIP Switch 4 (=0:OFF, =1:ON) Bit 4: DIP Switch 5 (=0:OFF, =1:ON) Bit 5: DIP Switch 6 (=0:OFF, =1:ON) Bit 6: DIP Switch 7 (=0:OFF, =1:ON) Bit 7: DIP Switch 8 (=0:OFF, =1:ON)						

CONVERTER STATUS						
CONVERTER STATUS	3x65534 4x65534 I:65533	0,0x0000 B:00 00			UINT16 R/O	
Current status of the converter						
FACTORY RESET	3x65535 4x65535 I:65534	0,0x0000 B:00 00		1:PERFORM FACTORY RESET	UINT16 R/W	NO
Performs a factory reset of all internal saved parameters						
SOFTWARE RESET						
RESET	1x65536 2x65536 I:65535	0,0x00 B:00		N/A:NO CHANGE	BIT R/W	NO
Performs a software reset, whenever 1 is written to this register. If the host writes to this register 1, the module executes a soft reset (reboot).						
RESET	3x65536 4x65535 I:65535	0,0x0000 B:00 00		N/A:NO CHANGE	UINT16 R/W	NO
Performs a software reset, whenever 1 is written to this register. If the host writes to this register 1, the module executes a soft reset (reboot).						

Command NAME	ASCII command type	ASCII command structure	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
ASCII COMMANDS						
DIGITAL OUTPUTS						
SET DIGITAL OUTPUTS	ASCII WRITE COMMAND	#SDOS:<OutAlIDOS> <CR> Result: #OK<CR>			ASCII	YES
	DO1	0:OFF				
	DO2	0:OFF				
	DO3	0:OFF				
	DO4	0:OFF				
	DO5	0:OFF				
	DO6	0:OFF				
	DO7	0:OFF				
	DO8	0:OFF				
	DO9	0:OFF				
	DO10	0:OFF				
	DO11	0:OFF				
	DO12	0:OFF				
	DO13	0:OFF				
	DO14	0:OFF				
	DO15	0:OFF				
	DO16	0:OFF				
	DO17	0:OFF				
	DO18	0:OFF				
	DO19	0:OFF				
	DO20	0:OFF				
	DO21	0:OFF				
	DO22	0:OFF				
	DO23	0:OFF				
	DO24	0:OFF				
	TX	#1,SDOS:0<CR>				
	RX	#255,OK<CR>				

Sets all digital outputs to the new state OutAllDOS

The new state for all digital outputs

Bit 0: State of DO1 (=0:OFF, =1:ON)

Bit 1: State of DO2 (=0:OFF, =1:ON)

Bit 2: State of DO3 (=0:OFF, =1:ON)

...

Bit 21: State of DO22 (=0:OFF, =1:ON)

Bit 22: State of DO23 (=0:OFF, =1:ON)

Bit 23: State of DO24 (=0:OFF, =1:ON)

SET DIGITAL OUTPUT DOx	ASCII WRITE COMMAND	#SDO<DONR>:<Out> <CR> Result: #OK<CR>	ASCII	NO
	DONR	2		
	DOx	0:OFF		
	TX	#1,SDO2:0<CR>		
	RX	N/A		

<DONR>: 1=DO1..24=DO24

Sets the new state for digital output DOx. The state is defined with <Out>.

Out

The new state of the digital output DOx:

=0: digital output is OFF

=1: digital output is ON

GET DIGITAL OUTPUTS	ASCII READ COMMAND	#GDOS<CR> Result: #GDOS:<DOSDec>,<DOSHex> <CR>	ASCII	
	TX	#1,GDOS<CR>		
	RX	#1,GDOS:0,0x0<CR>		
		Actual status of digital outputs:00.0000.0000.0000.0000.0000.0000.0000		

Returns the actual state of the digital outputs as decimal number and as hexadecimal number.

DOSDec, DOSHex

The current state of the digital outputs:

Bit 0: State of DO1 (=0:OFF, =1:ON)

Bit 1: State of DO2 (=0:OFF, =1:ON)

Bit 2: State of DO3 (=0:OFF, =1:ON)

...

Bit 21: State of DO22 (=0:OFF, =1:ON)

Bit 22: State of DO23 (=0:OFF, =1:ON)

Bit 23: State of DO24 (=0:OFF, =1:ON)

GET DIGITAL OUTPUT DOx	ASCII READ COMMAND	#GDO<DONR> <CR> Result: #GDO<DONR>:<DOxDec>,<DOxHex> <CR>	ASCII	
	DONR	2		
	TX	#1,GDO2<CR>		
	RX	#1,GDO2:0,0x0<CR>		
		Actual status of digital output DO2:0=OFF		

Returns the actual state of the digital output DOx as decimal number and as hexadecimal number.
DOxDec, DOxHex
The current state of the digital output DOx:
=0: relay output is OFF
=1: relay output is ON

DIGITAL OUTPUTS: PULSE OUTPUT

PULSE DOx	ASCII WRITE COMMAND	#PDO<DONR>:<Time><CR> Result: #OK<CR>	ASCII	YES
	DONR	2		
	TIME	200		
	TX	#1,PDO2:200<CR>		
	RX	#255,OK<CR>		

<DONR>: 1=DO1..24=DO24

<Time>: 0..65535*100ms

This command switches the digital output DOx on for the pulse duration <PulseTimeIn100ms>*100ms.
PulseTimeIn100ms: A duration in 100ms units.
The corresponding digital output is switched on for this time period.

GET PULSE TIMER DOx	ASCII READ COMMAND	#GPT<DONR><CR> Result: #GPT:<TimeDec>,<TimeHex><CR>	ASCII	
	DONR	2		
	TX	#1,GPT2<CR>		
	RX	#1,GPT2:0,0x0<CR>		
		Actual pulse time for DO2:0,0s		

<DONR>: 1=DO1..24=DO24

Returns the remaining timer value of the pulse for digital output DOx in ms.
PulseTimeInMSDec, PulseTimeInMSHex
The remaining time of the pulse in Milliseconds

Command NAME	ASCII command type	ASCII command structure	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
ASCII COMMANDS						
DIGITAL OUTPUTS						
DIGITAL OUTPUTS: ENABLE OPEN WIRE DETECTION WHILE ON						
SET DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE ON	ASCII WRITE COMMAND	#SDOEOWDONS:<OpenWireDOS> <CR> Result: #OK<CR>			ASCII	YES
	DO1	0:OFF				
	DO2	0:OFF				
	DO3	0:OFF				
	DO4	0:OFF				
	DO5	0:OFF				
	DO6	0:OFF				
	DO7	0:OFF				
	DO8	0:OFF				
	DO9	0:OFF				
	DO10	0:OFF				
	DO11	0:OFF				
	DO12	0:OFF				
	DO13	0:OFF				
	DO14	0:OFF				
	DO15	0:OFF				
	DO16	0:OFF				
	DO17	0:OFF				
	DO18	0:OFF				
	DO19	0:OFF				
	DO20	0:OFF				
	DO21	0:OFF				
	DO22	0:OFF				
	DO23	0:OFF				
	DO24	0:OFF				
	TX	#1,SDOEOWDONS:0<CR>				
	RX	N/A				
Sets the open wire mode for all digital outputs to the new mode OpenWireDOS. This enables the diagnostic of open wire while the digital output is ON. The new state for all digital outputs Bit 0: New mode for DO1 (=0:DISABLED, =1:ENABLED) Bit 1: New mode for DO2 (=0:DISABLED, =1:ENABLED) ... Bit 22: New mode for DO23 (=0:DISABLED, =1:ENABLED) Bit 23: New mode for DO24 (=0:DISABLED, =1:ENABLED)						

SET DIGITAL OUTPUT ENABLE OPEN WIRE DETECTION WHILE ON	ASCII WRITE COMMAND	#SDOEOWDON<DONR>:<OpenWireDOx> <CR> Result: #OK<CR>	ASCII	YES
	DONR	2		
	DOx	0:DISABLE		
	TX	#1,SDOEOWDON2:0<CR>		
	RX	N/A		
<DONR>: 1=DO1..24=DO24				
<OpenWireDOx>: 0=DISABLE..1=ENABLE				
Sets the open wire mode for digital output DOx to the new mode OpenWireDOx. This enables the diagnostic of open wire while the digital output is ON. The new mode of the digital output DOx: =0: diagnostic mode for digital output is DISABLED =1: diagnostic mode for digital output is ENABLED				
GET DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE ON	ASCII READ COMMAND	#GDOEOWDONS<CR> Result: #GDOEOWDONS:<OpenWireDOSDec>,<OpenWireDOSHex> <CR>	ASCII	
	TX	#1,GDOEOWDONS<CR>		
	RX	#1,GDOEOWDONS:0,0x0<CR>		
		Actual mode for open wire diagnostic while ON of digital outputs:		
		DO1-DO24:0000.0000.0000.0000.0000.0000		
Returns the actual mode for open wire diagnostic while digital output is ON as decimal number and as hexadecimal number. OpenWireDOSDec, OpenWireDOSHex The current mode for open wire diagnostic while ON of the digital outputs: Bit 0: Open wire diagnostic mode of DO1 (=0:DISABLED, =1:ENABLED) Bit 1: Open wire diagnostic mode of DO2 (=0:DISABLED, =1:ENABLED) ... Bit 22: Open wire diagnostic mode of DO23 (=0:DISABLED, =1:ENABLED) Bit 23: Open wire diagnostic mode of DO24 (=0:DISABLED, =1:ENABLED)				
GET DIGITAL OUTPUT ENABLE OPEN WIRE DETECTION WHILE ON	ASCII READ COMMAND	#GDOEOWDON<DONR> <CR> Result: #GDOEOWDON<DONR>:<OpenWireDOxDec>,<OpenWireDOxHex> <CR>	ASCII	
	DONR	2		
	TX	#1,GDOEOWDON2<CR>		
	RX	#1,GDOEOWDON2:0,0x0<CR>		
		Actual open wire diagnostic mode while ON of digital output DO2:0=DISABLED		
<DONR>: 1=DO1..24=DO24				
Returns the actual open wire diagnostic mode while ON of the digital output DOx as decimal number and as hexadecimal number. OpenWireDOxDec, OpenWireDOxHex The current diagnostic mode of the digital output DOx: =0: open wire diagnostic mode for digital output is DISABLED =1: open wire diagnostic mode for digital output is ENABLED				
DIGITAL OUTPUTS: ENABLE OPEN WIRE DETECTION WHILE OFF				
SET DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE OFF	ASCII WRITE COMMAND	#SDOEOWDOFFS:<OpenWireDOS> <CR> Result: #OK<CR>	ASCII	YES
	DO1	0:OFF		
	DO2	0:OFF		

	DO3	0:OFF		
	DO4	0:OFF		
	DO5	0:OFF		
	DO6	0:OFF		
	DO7	0:OFF		
	DO8	0:OFF		
	DO9	0:OFF		
	DO10	0:OFF		
	DO11	0:OFF		
	DO12	0:OFF		
	DO13	0:OFF		
	DO14	0:OFF		
	DO15	0:OFF		
	DO16	0:OFF		
	DO17	0:OFF		
	DO18	0:OFF		
	DO19	0:OFF		
	DO20	0:OFF		
	DO21	0:OFF		
	DO22	0:OFF		
	DO23	0:OFF		
	DO24	0:OFF		
	TX	#1,SDOEOWDOFFS:0<CR>		
	RX	N/A		

Sets the open wire mode for all digital outputs to the new mode OpenWireDOS. This enables the diagnostic of open wire while the digital output is OFF.
The new state for all digital outputs
Bit 0: New mode for DO1 (=0:DISABLED, =1:ENABLED)
Bit 1: New mode for DO2 (=0:DISABLED, =1:ENABLED)
...
Bit 22: New mode for DO23 (=0:DISABLED, =1:ENABLED)
Bit 23: New mode for DO24 (=0:DISABLED, =1:ENABLED)

SET DIGITAL OUTPUT ENABLE OPEN WIRE DETECTION WHILE OFF	ASCII WRITE COMMAND DONR	#SDOEOWDOFF<DONR>:<OpenWireDOx> <CR> Result: #OK<CR> 2	ASCII	YES
	DOx	0:DISABLE		
	TX	#1,SDOEOWDOFF2:0<CR>		
	RX	N/A		

<DONR>: 1=DO1..24=DO24
<OpenWireDOx>: 0=DISABLE..1=ENABLE

Sets the open wire mode for digital output DOx to the new mode OpenWireDOx. This enables the diagnostic of open wire while the digital output is OFF.
The new mode of the digital output DOx:
=0: diagnostic mode for digital output is DISABLED
=1: diagnostic mode for digital output is ENABLED

GET DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE OFF	ASCII READ COMMAND	#GDOEOWDOFFS<CR> Result: #GDOEOWDOFFS:<OpenWireDOSDec>,<OpenWireDOSHex><CR>	ASCII	
	TX	#1,GDOEOWDOFFS<CR>		
	RX	#1,GDOEOWDOFFS:0,0x0<CR>		
		Actual mode for open wire diagnostic while OFF of digital outputs:		
		DO1-DO24:0000.0000.0000.0000.0000.0000		
Returns the actual mode for open wire diagnostic while digital output is OFF as decimal number and as hexadecimal number. OpenWireDOSDec, OpenWireDOSHex The current mode for open wire diagnostic while OFF of the digital outputs: Bit 0: Open wire diagnostic mode of DO1 (=0:DISABLED, =1:ENABLED) Bit 1: Open wire diagnostic mode of DO2 (=0:DISABLED, =1:ENABLED) ... Bit 22: Open wire diagnostic mode of DO23 (=0:DISABLED, =1:ENABLED) Bit 23: Open wire diagnostic mode of DO24 (=0:DISABLED, =1:ENABLED)				
GET DIGITAL OUTPUT ENABLE OPEN WIRE DETECTION WHILE OFF	ASCII READ COMMAND	#GDOEOWDOFF<DONR><CR> Result: #GDOEOWDOFF<DONR>:<OpenWireDOxDec>,<OpenWireDOxHex><CR>	ASCII	
	DONR	2		
	TX	#1,GDOEOWDOFF2<CR>		
	RX	#1,GDOEOWDOFF2:0,0x0<CR>		
		Actual open wire diagnostic mode while OFF of digital output DO2:0=DISABLED		
<DONR>: 1=DO1..24=DO24				
Returns the actual open wire diagnostic mode while OFF of the digital output DOx as decimal number and as hexadecimal number. OpenWireDOxDec, OpenWireDOxHex The current diagnostic mode of the digital output DOx: =0: open wire diagnostic mode for digital output is DISABLED =1: open wire diagnostic mode for digital output is ENABLED				
DIGITAL OUTPUTS: ENABLE SHORTCUT TO VDD DETECTION WHILE OFF				
SET DIGITAL OUTPUTS ENABLE SHORTCUT TO VDD DETECTION WHILE OFF	ASCII WRITE COMMAND	#SDOESVDDS:<ShortCutDOS><CR> Result: #OK<CR>	ASCII	YES
	DO1	0:OFF		
	DO2	0:OFF		
	DO3	0:OFF		
	DO4	0:OFF		
	DO5	0:OFF		
	DO6	0:OFF		
	DO7	0:OFF		
	DO8	0:OFF		
	DO9	0:OFF		
	DO10	0:OFF		
	DO11	0:OFF		
	DO12	0:OFF		
	DO13	0:OFF		
	DO14	0:OFF		

	DO15	0:OFF		
	DO16	0:OFF		
	DO17	0:OFF		
	DO18	0:OFF		
	DO19	0:OFF		
	DO20	0:OFF		
	DO21	0:OFF		
	DO22	0:OFF		
	DO23	0:OFF		
	DO24	0:OFF		
	TX	#1,SDOESVDDS:0<CR>		
	RX	N/A		
Sets the shortcut to VDD detection mode for all digital outputs to the new mode ShortCutDOS. This enables the diagnostic of shortcut to VDD while the digital output is OFF. The new state for all digital outputs Bit 0: New mode for DO1 (=0:DISABLED, =1:ENABLED) Bit 1: New mode for DO2 (=0:DISABLED, =1:ENABLED) ... Bit 22: New mode for DO23 (=0:DISABLED, =1:ENABLED) Bit 23: New mode for DO24 (=0:DISABLED, =1:ENABLED)				
SET DIGITAL OUTPUT ENABLE SHORTCUT TO VDD DETECTION WHILE OFF	ASCII WRITE COMMAND DONR	#SDOESVDD<DONR>:<ShortCutDOx><CR> Result: #OK<CR> 2	ASCII	YES
	DOx	0:DISABLE		
	TX	#1,SDOESVDD2:0<CR>		
	RX	N/A		
<DONR>: 1=DO1..24=DO24 <ShortCutDOx>: 0=DISABLE..1=ENABLE				
Sets the shortcut to VDD mode for digital output DOx to the new mode ShortCutDOx. This enables the diagnostic of shortcut to VDD while the digital output is OFF. The new mode of the digital output DOx: =0: diagnostic mode for digital output is DISABLED =1: diagnostic mode for digital output is ENABLED				
GET DIGITAL OUTPUTS ENABLE SHORT CUT TO VDD DETECTION WHILE OFF	ASCII READ COMMAND TX RX	#GDOESVDDS<CR> Result: #GDOESDDS:<ShortCutDOSDec>,<ShortCutDOSHex><CR> #1,GDOESVDDS<CR> #1,GDOESVDDS:0,0x0<CR>	ASCII	
		Actual mode for shortcut to VDD diagnostic while OFF of digital outputs: DO1-DO24:0000.0000.0000.0000.0000.0000		
Returns the actual mode for shortcut to VDD diagnostic while digital output is OFF as decimal number and as hexadecimal number. ShortCutDOSDec, ShortCutDOSHex The current mode for shortcut diagnostic while OFF of the digital outputs: Bit 0: Open wire diagnostic mode of DO1 (=0:DISABLED, =1:ENABLED) Bit 1: Open wire diagnostic mode of DO2 (=0:DISABLED, =1:ENABLED) ... Bit 22: Open wire diagnostic mode of DO23 (=0:DISABLED, =1:ENABLED) Bit 23: Open wire diagnostic mode of DO24 (=0:DISABLED, =1:ENABLED)				

GET DIGITAL OUTPUT ENABLE SHORT CUT TO VDD DETECTION WHILE OFF	ASCII READ COMMAND	#GDOESVDD<DONR> <CR> Result: #GDOESVDD<DONR>:<ShortCutDOxDec>,<ShortCutDOxHex> <CR>	ASCII	
	DONR	2		
	TX	#1,GDOESVDD2<CR>		
	RX	#1,GDOESVDD2:0,0x0<CR>		
		Actual shortcut to VDD diagnostic mode while OFF of digital output DO2:0=DISABLED		
<DONR>: 1=DO1..24=DO24				
Returns the actual short cut to VDD diagnostic mode while OFF of the digital output DOx as decimal number and as hexadecimal number. ShortCutDOxDec, ShortCutDOxHex The current diagnostic mode of the digital output DOx: =0: open wire diagnostic mode for digital output is DISABLED =1: open wire diagnostic mode for digital output is ENABLED				
DIGITAL OUTPUTS: SPI STATUS				
GET SPI STATUS DIGITAL OUTPUT GROUPS	ASCII READ COMMAND	#GSSDOGS<CR> Result: #GSSDOGS:<SPIDOGSDec>,<SPIDOGSHex> <CR>	ASCII	
	TX	#1,GSSDOGS<CR>		
	RX	#1,GSSDOGS:0,0x0<CR>		
		Actual SPI status of digital output groups:0000		
digital output group #1, chip #1:DO1-DO6 digital output group #2, chip #2:DO7-DO12 digital output group #3, chip #3:DO13-DO18 digital output group #4, chip #4:DO19-DO24				
Returns the actual SPI communication state of the corresponding output group as decimal number and as hexadecimal number. SPIDOGSDec,SPIDOGSHex The current SPI communication state of the digital output group: Bit 0: SPI communication state for digital output group #1 (=0:NO FAULT, =1:FAULT) Bit 1: SPI communication state for digital output group #2 (=0:NO FAULT, =1:FAULT) Bit 2: SPI communication state for digital output group #3 (=0:NO FAULT, =1:FAULT) Bit 3: SPI communication state for digital output group #4 (=0:NO FAULT, =1:FAULT)				
GET SPI STATUS DIGITAL OUTPUT GROUPx	ASCII READ COMMAND	#GSSDOG<DOGRP> <CR> Result: #GSSDOG<DOGRP>:<SPIDOGxDec>,<SPIDOGxHex> <CR>	ASCII	
	DOGRP	4		
	TX	#1,GSSDOG4<CR>		
	RX	#1,GSSDOG4:0,0x0<CR>		
		Actual SPI status of digital output group DOG4:0=NO FAULT		
<DOGRP>: 1=CHIP1..4=CHIP4				
digital output group #1, chip #1:DO1-DO6 digital output group #2, chip #2:DO7-DO12 digital output group #3, chip #3:DO13-DO18 digital output group #4, chip #4:DO19-DO24				
Returns the actual SPI communication state of the digital output group DOGRP as decimal number and as hexadecimal number. SPIDOGxDec, SPIDOGxHex The current SPI communication state of the digital output group DOGRP: =0: SPI communication state for output group is OK (NO FAULT) =1: SPI communication state for output group is FAULT				

DIGITAL OUTPUTS: INTERRUPT STATUS				
GET DIGITAL OUTPUTS INTERRUPT STATUS	ASCII READ COMMAND	#GDOINTS<CR> Result: #GDOINTS:<InterruptStatusDec>,<InterruptStatusHex> <CR>	ASCII	
	TX	#1,GDOINTS<CR>		
	RX	#1,GDOINTS:1077952576,0x40404040<CR>		
		Actual interrupt status of all digital output groups:		
		CHIP #1:0100.0000		
		CHIP #2:0100.0000		
		CHIP #3:0100.0000		
		CHIP #4:0100.0000		
digital output group #1, chip #1:DO1-DO6 digital output group #2, chip #2:DO7-DO12 digital output group #3, chip #3:DO13-DO18 digital output group #4, chip #4:DO19-DO24				
Returns the actual interrupt state of all output groups as decimal number and as hexadecimal number. InterruptStatusDec,InterruptStatusHex: The current interrupt state of digital output group 1-4 (CHIP1-4): For each chip 8 bits are used: CHIP#1:Bits 0-7, CHIP#2:Bits 8-15, CHIP#3:Bits 16-23, CHIP#4:Bits 24-31 Bit 0: Overload detected (0=OK,1=FAULT) Bit 1: Current limit detected(0=OK,1=FAULT) Bit 2: Open wire while OFF detected (0=OK,1=FAULT) Bit 3: Open wire while ON detected (0=OK,1=FAULT) Bit 4: Short to VDD while ON detected (0=OK,1=FAULT) Bit 5: Thermal error detected-shutdown (0=OK,1=FAULT) Bit 6: Supply error detected (0=OK,1=FAULT) Bit 7: Communication error detected (0=OK,1=FAULT)				
GET DIGITAL OUTPUT GROUPx INTERRUPT STATUS	ASCII READ COMMAND	#GDOINT<DOGRP> <CR> Result: #GDOINT<DOGRP>:<InterruptStatusDec>,<InterruptStatusHex> <CR>	ASCII	
	DOGRP	4		
	TX	#1,GDOINT4<CR>		
	RX	#1,GDOINT4:64,0x40<CR>		
		Actual interrupt status of digital output group 4:0100.0000		
<DOGRP>: 1=CHIP1..4=CHIP4 digital output group #1, chip #1:DO1-DO6 digital output group #2, chip #2:DO7-DO12 digital output group #3, chip #3:DO13-DO18 digital output group #4, chip #4:DO19-DO24				

Returns the actual interrupt state of the digital output group DOGRP as decimal number and as hexadecimal number. InterruptStatusDec, InterruptStatusHex Bit 0:Overload detected (0=OK,1=FAULT) Bit 1:Current limit detected(0=OK,1=FAULT) Bit 2:Open wire while OFF detected (0=OK,1=FAULT) Bit 3:Open wire while ON detected (0=OK,1=FAULT) Bit 4:Short to VDD while ON detected (0=OK,1=FAULT) Bit 5:Thermal error detected-shutdown (0=OK,1=FAULT) Bit 6:Supply error detected (0=OK,1=FAULT) Bit 7:Communication error detected (0=OK,1=FAULT)				
DIGITAL OUTPUTS: GLOBAL ERRORS				
GET DIGITAL OUTPUTS GLOBAL ERRORS	ASCII READ COMMAND	#GDOERRS<CR> Result: #GDOERRS:<GlobalErrorsDec>,<GlobalErrorsHex> <CR>	ASCII	
	TX	#1,GDOERRS<CR>		
	RX	#1,GDOERRS:471604252,0x1C1C1C1C<CR>		
		Actual global errors of all digital output groups:		
		CHIP #1:0001.1100		
		CHIP #2:0001.1100		
		CHIP #3:0001.1100		
		CHIP #4:0001.1100		
digital output group #1, chip #1:DO1-DO6 digital output group #2, chip #2:DO7-DO12 digital output group #3, chip #3:DO13-DO18 digital output group #4, chip #4:DO19-DO24				
Returns the actual global error state of all output groups as decimal number and as hexadecimal number. GlobalErrorsDec,GlobalErrorsHex: The current global error of all digital output groups 1-4 (CHIP1-4) For each chip 8 bits are used: CHIP#1:Bits 0-7, CHIP#2:Bits 8-15, CHIP#3:Bits 16-23, CHIP#4:Bits 24-31				
Bit 0: Internal under voltage detected (0=OK,1=FAULT) Bit 1: VA under voltage detected (<2.3V) (0=OK,1=FAULT) Bit 2: VDD not good detected (<17V) (0=OK,1=FAULT) Bit 3: VDD warning detected (<12V) (0=OK,1=FAULT) Bit 4: VDD under voltage detected (<8V) (0=OK,1=FAULT) Bit 5: Thermal shutdown (0=OK,1=FAULT) Bit 6: Synchronisation error detected (0=OK,1=FAULT) Bit 7: Watchdog error detected (0=OK,1=FAULT)				
GET DIGITAL OUTPUT GROUPx GLOBAL ERRORS	ASCII READ COMMAND	#GDOERR<DOGRP> <CR> Result: #GDOERR<DOGRP>:<GlobalErrorsDec>,<GlobalErrorsHex> <CR>	ASCII	
	DOGRP	4		
	TX	#1,GDOERR4<CR>		
	RX	#1,GDOERR4:28,0x1C<CR>		
		Actual global errors of digital output group 4:0001.1100		
<DOGRP>: 1=CHIP1..4=CHIP4				

digital output group #1, chip #1:DO1-DO6 digital output group #2, chip #2:DO7-DO12 digital output group #3, chip #3:DO13-DO18 digital output group #4, chip #4:DO19-DO24				
Returns the actual interrupt state of the digital output group DOGRP as decimal number and as hexadecimal number. InterruptStatusDec, InterruptStatusHex Bit 0:Internal under voltage detected (0=OK,1=FAULT) Bit 1:VA under voltage detected (<2.3V) (0=OK,1=FAULT) Bit 2:VDD not good detected (<17V) (0=OK,1=FAULT) Bit 3:VDD warning detected (<12V) (0=OK,1=FAULT) Bit 4:VDD under voltage detected (<8V) (0=OK,1=FAULT) Bit 5:Thermal shutdown (0=OK,1=FAULT) Bit 6:Synchronisation error detected (0=OK,1=FAULT) Bit 7:Watchdog error detected (0=OK,1=FAULT)				
DIGITAL OUTPUTS: THERMAL OVERLOAD DETECTION				
GET DIGITAL OUTPUTS THERMAL OVERLOAD DETECTION	ASCII READ COMMAND	#GDOTOS<CR> Result: #GDOTOS:<StatusDOSDec>,<StatusDOSHex> <CR>	ASCII	
	TX	#1,GDOTOS<CR>		
	RX	#1,GDOTOS:0,0x0<CR>		
		Actual thermal overload detection status of digital outputs:		
		DO1-DO24:0000.0000.0000.0000.0000.0000		
Returns the actual state of the thermal overload detection for all digital outputs as decimal number and as hexadecimal number. StatusDOSDec, StatusDOSHex The current detection state of the digital outputs: Bit 0: Thermal overload detected on DO1 (=0:NO, =1:YES) Bit 1: Thermal overload detected on DO2 (=0:NO, =1:YES) ... Bit 22: Thermal overload detected on DO23 (=0:NO, =1:YES) Bit 23: Thermal overload detected on DO24 (=0:NO, =1:YES)				
GET DIGITAL OUTPUT DOx THERMAL OVERLOAD DETECTION	ASCII READ COMMAND	#GDOTO<DONR> <CR> Result: #GDOTO<DONR>:<StatusDOxDec>,<StatusDOxHex> <CR>	ASCII	
	DONR	2		
	TX	#1,GDOTO2<CR>		
	RX	#1,GDOTO2:0,0x0<CR>		
		Thermal overload detected on DO2:0=NO		
<DONR>: 1=DO1..24=DO24				
Returns the actual state of the thermal overload detection for digital output DOx as decimal number and as hexadecimal number. StatusDOxDec, StatusDOxHex The current detection state for digital output DOx: =0: digital output is OK =1: FAULT detected on digital output				
DIGITAL OUTPUTS: CURRENT LIMIT DETECTION				
GET DIGITAL OUTPUTS CURRENT LIMIT DETECTION	ASCII READ COMMAND	#GDOCLS<CR> Result: #GDOCLS:<StatusDOSDec>,<StatusDOSHex> <CR>	ASCII	
	TX	#1,GDOCLS<CR>		

	RX	#1,GDOCLS:0,0x0<CR>		
		Actual current limit detection status of digital outputs:		
		DO1-DO24:0000.0000.0000.0000.0000.0000		
Returns the actual state of the current limit detection for all digital outputs as decimal number and as hexadecimal number. StatusDOSDec, StatusDOSHex The current detection state of the digital outputs: Bit 0: Current limit reached for DO1 (=0:NO, =1:YES) Bit 1: Current limit reached for DO2 (=0:NO, =1:YES) ... Bit 22: Current limit reached for DO23 (=0:NO, =1:YES) Bit 23: Current limit reached for DO24 (=0:NO, =1:YES)				
GET DIGITAL OUTPUT DOx CURRENT LIMIT DETECTION	ASCII READ COMMAND DONR	#GDOCL<DONR><CR> Result: #GDOCL<DONR>:<StatusDOxDec>,<StatusDOxHex><CR> 2	ASCII	
	TX	#1,GDOCL2<CR>		
	RX	#1,GDOCL2:0,0x0<CR>		
		Actual current limit detection status of DO2:0=OK		
<DONR>: 1=DO1..24=DO24				
Returns the actual state of the current limit detection for digital output DOx as decimal number and as hexadecimal number. StatusDOxDec, StatusDOxHex The current detection state for digital output DOx: =0: digital output is OK =1: FAULT detected on digital output				
DIGITAL OUTPUTS: OPEN WIRE DETECTION WHILE ON				
GET DIGITAL OUTPUTS OPEN WIRE FAULT DETECTION WHILE ON	ASCII READ COMMAND DONR	#GDOOWFONS<CR> Result: #GDOOWFONS:<StatusDOSDec>,<StatusDOSHex><CR> 2	ASCII	
	TX	#1,GDOOWFONS<CR>		
	RX	#1,GDOOWFONS:0,0x0<CR>		
		Actual open wire fault detection status while ON of digital outputs:		
		DO1-DO24:0000.0000.0000.0000.0000.0000		
Returns the actual state of the open wire fault detection while ON for all digital outputs as decimal number and as hexadecimal number. StatusDOSDec, StatusDOSHex The current detection state of the digital outputs: Bit 0: State of DO1 (=0:OK, =1:FAULT) Bit 1: State of DO2 (=0:OK, =1:FAULT) ... Bit 22: State of DO23 (=0:OK, =1:FAULT) Bit 23: State of DO24 (=0:OK, =1:FAULT)				
GET DIGITAL OUTPUT DOx OPEN WIRE FAULT DETECTION WHILE ON	ASCII READ COMMAND DONR	#GDOOWFON<DONR><CR> Result: #GDOOWFON<DONR>:<StatusDOxDec>,<StatusDOxHex><CR> 2	ASCII	
	TX	#1,GDOOWFON2<CR>		
	RX	#1,GDOOWFON2:0,0x0<CR>		
		Actual open wire fault detection status while ON of DO2:0=OK		
<DONR>: 1=DO1..24=DO24				

Returns the actual state of the open wire fault detection while ON for digital output DOx as decimal number and as hexadecimal number.

StatusDOxDec, StatusDOxHex

The current detection state for digital output DOx:

=0: digital output is OK

=1: FAULT detected on digital output

DIGITAL OUTPUTS: OPEN WIRE DETECTION WHILE OFF

GET DIGITAL OUTPUTS OPEN WIRE FAULT DETECTION WHILE OFF	ASCII READ COMMAND	#GDOOWFOFFS<CR> Result: #GDOOWFOFFS:<StatusDOSDec>,<StatusDOSHex><CR>	ASCII	
	TX	#1,GDOOWFOFFS<CR>		
	RX	#1,GDOOWFOFFS:0,0x0<CR>		
		Actual open wire fault detection status while OFF of digital outputs:		
		DO1-DO24:0000.0000.0000.0000.0000.0000		

Returns the actual state of the open wire fault detection while OFF for all digital outputs as decimal number and as hexadecimal number.

StatusDOSDec, StatusDOSHex

The current detection state of the digital outputs:

Bit 0: State of DO1 (=0:OK, =1:FAULT)

Bit 1: State of DO2 (=0:OK, =1:FAULT)

...

Bit 22: State of DO23 (=0:OK, =1:FAULT)

Bit 23: State of DO24 (=0:OK, =1:FAULT)

GET DIGITAL OUTPUT DOx OPEN WIRE FAULT DETECTION WHILE OFF	ASCII READ COMMAND	#GDOOWFOFF<DONR><CR> Result: #GDOOWFOFF<DONR>:<StatusDOxDec>,<StatusDOxHex><CR>	ASCII	
	DONR	2		
	TX	#1,GDOOWFOFF2<CR>		
	RX	#1,GDOOWFOFF2:0,0x0<CR>		
		Actual open wire fault detection status while OFF of DO2:0=OK		

<DONR>: 1=DO1..24=DO24

Returns the actual state of the open wire fault detection while OFF for digital output DOx as decimal number and as hexadecimal number.

StatusDOxDec, StatusDOxHex

The current detection state for digital output DOx:

=0: digital output is OK

=1: FAULT detected on digital output

DIGITAL OUTPUTS: SHORTCUT TO VDD DETECTION WHILE OFF

GET DIGITAL OUTPUTS SHORTCUT TO VDD FAULT DETECTION WHILE OFF	ASCII READ COMMAND	#GDOSVDDS<CR> Result: #GDOSVDDS:<StatusDOSDec>,<StatusDOSHex><CR>	ASCII	
	TX	#1,GDOSVDDS<CR>		
	RX	#1,GDOSVDDS:0,0x0<CR>		
		Actual shortcut to VDD fault detection status while OFF of digital outputs:		
		DO1-DO24:0000.0000.0000.0000.0000.0000		

Returns the actual state of the shortcut to VDD fault detection while OFF for all digital outputs as decimal number and as hexadecimal number. StatusDOSDec, StatusDOSHex The current detection state of the digital outputs: Bit 0: State of DO1 (=0:OK, =1:FAULT) Bit 1: State of DO2 (=0:OK, =1:FAULT) ... Bit 22: State of DO23 (=0:OK, =1:FAULT) Bit 23: State of DO24 (=0:OK, =1:FAULT)				
GET DIGITAL OUTPUT DOx SHORTCUT TO VDD FAULT DETECTION WHILE OFF	ASCII READ COMMAND DONR	#GDOSVDD<DONR> <CR> Result: #GDOSVDD<DONR>:<StatusDOxDec>,<StatusDOxHex> <CR> 2	ASCII	
	TX	#1,GDOSVDD2<CR>		
	RX	#1,GDOSVDD2:0,0x0<CR>		
		Actual shortcut to VDD fault detection status while OFF of DO2:0=OK		
<DONR>: 1=DO1..24=DO24				
Returns the actual state of the shortcut to VDD fault detection while OFF for digital output DOx as decimal number and as hexadecimal number. StatusDOxDec, StatusDOxHex The current detection state for digital output DOx: =0: digital output is OK =1: FAULT detected on digital output				
GET DIGITAL OUTPUTS CHIPSET NAME	ASCII READ COMMAND	#GDOCHIPSET<CR> Result: #GDOCHIPSET:<ChipSetName>	ASCII	
	TX	#1,GDOCHIPSET<CR>		
	RX	#1,GDOCHIPSET:MAX14915<CR>		
		Actual name of chipset for digital outputs:MAX14915		
Returns the actual name of the chipset of the digital outputs				

Command NAME	ASCII command type	ASCII command structure	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
ASCII COMMANDS						
DIGITAL OUTPUTS						
INITIAL & WATCHDOG STATE FOR DIGITAL OUTPUTS						
SET INITIAL & WATCHDOG STATE FOR DIGITAL OUTPUTS	ASCII WRITE COMMAND	#SCDOS:<OutAllDOS> <CR> Result: #OK<CR>			ASCII	YES
	DO1	0:OFF				
	DO2	0:OFF				
	DO3	0:OFF				
	DO4	0:OFF				
	DO5	0:OFF				
	DO6	0:OFF				
	DO7	0:OFF				
	DO8	0:OFF				
	DO9	0:OFF				
	DO10	0:OFF				
	DO11	0:OFF				
	DO12	0:OFF				
	DO13	0:OFF				
	DO14	0:OFF				
	DO15	0:OFF				
	DO16	0:OFF				
	DO17	0:OFF				
	DO18	0:OFF				
	DO19	0:OFF				
	DO20	0:OFF				
	DO21	0:OFF				
	DO22	0:OFF				
	DO23	0:OFF				
	DO24	0:OFF				
	TX	#1,SCDOS:0<CR>				
	RX	#255,OK<CR>				

This command sets all digital outputs to a new state for controller restart and watchdog function. The state is saved in FRAM.
This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured.

OutAllDOS
The new state for the digital outputs:
Bit 0: New state of DO1 (=0:OFF, =1:ON)
Bit 1: New state of DO2 (=0:OFF, =1:ON)
...
Bit 22: New state of DO23 (=0:OFF, =1:ON)
Bit 23: New state of DO24 (=0:OFF, =1:ON)

GET INITIAL & WATCHDOG STATE FOR DIGITAL OUTPUTS	ASCII READ COMMAND	#GCDOS<CR> Result: #GCDOS:<DOSDec>,<DOSHex><CR>	ASCII	
	TX	#1,GCDOS<CR>		
	RX	#1,GCDOS:0,0x0<CR>		
		Init & watchdog configuration for digital outputs:		
		DO1-DO24:0000.0000.0000.0000.0000.0000		

Returns the actual initial and watchdog state of the digital outputs as decimal number and as hexadecimal number. This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured

DOSDec, DOSHex
The current state of the digital outputs:
Bit 0: State of DO1 (=0:OFF, =1:ON)
Bit 1: State of DO2 (=0:OFF, =1:ON)
...
Bit 22: State of DO23 (=0:OFF, =1:ON)
Bit 23: State of DO24 (=0:OFF, =1:ON)

DIGITAL OUTPUTS: INIT & WATCHDOG ENABLE OPEN WIRE DETECTION WHILE ON

SET CONFIG DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE ON	ASCII WRITE COMMAND	#SCDOEOWDONS:<OpenWireDOS><CR> Result: #OK<CR>	ASCII	YES
	DO1	1:ENABLE		
	DO2	1:ENABLE		
	DO3	1:ENABLE		
	DO4	1:ENABLE		
	DO5	1:ENABLE		
	DO6	1:ENABLE		
	DO7	1:ENABLE		
	DO8	1:ENABLE		
	DO9	1:ENABLE		
	DO10	1:ENABLE		
	DO11	1:ENABLE		
	DO12	1:ENABLE		
	DO13	1:ENABLE		
	DO14	1:ENABLE		
	DO15	1:ENABLE		

	DO16	1:ENABLE		
	DO17	1:ENABLE		
	DO18	1:ENABLE		
	DO19	1:ENABLE		
	DO20	1:ENABLE		
	DO21	1:ENABLE		
	DO22	1:ENABLE		
	DO23	1:ENABLE		
	DO24	1:ENABLE		
	TX	#1,SCDOEOWDONS:16777215<CR>		
	RX	#255,OK<CR>		
Sets the open wire mode for all digital outputs to the new mode OpenWireDOS for init & watchdog. This enables the diagnostic of open wire while the digital output is ON. This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured The new state for all digital outputs Bit 0: New mode for DO1 (=0:DISABLED, =1:ENABLED) Bit 1: New mode for DO2 (=0:DISABLED, =1:ENABLED) ... Bit 22: New mode for DO23 (=0:DISABLED, =1:ENABLED) Bit 23: New mode for DO24 (=0:DISABLED, =1:ENABLED)				
GET CONFIG DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE ON	ASCII READ COMMAND	#GCDOEOWDONS<CR> Result: #GCDOEOWDONS:<OpenWireDOSDec>,<OpenWireDOSHex> <CR>	ASCII	
	TX	#1,GCDOEOWDONS<CR>		
	RX	#1,GCDOEOWDONS:0,0x0<CR>		
		Init & watchdog configuration for open wire diagnostic while ON of digital outputs:		
		DO1-DO24:0000.0000.0000.0000.0000.0000		
Returns the actual mode for open wire diagnostic while digital output is ON as decimal number and as hexadecimal number. This values are used after power on of the module an after a watchdog event. OpenWireDOSDec, OpenWireDOSHex The current mode for open wire diagnostic while ON of the digital outputs: Bit 0: Open wire diagnostic mode of DO1 (=0:DISABLED, =1:ENABLED) Bit 1: Open wire diagnostic mode of DO2 (=0:DISABLED, =1:ENABLED) ... Bit 22: Open wire diagnostic mode of DO23 (=0:DISABLED, =1:ENABLED) Bit 23: Open wire diagnostic mode of DO24 (=0:DISABLED, =1:ENABLED)				
DIGITAL OUTPUTS: INIT & WATCHDOG ENABLE OPEN WIRE DETECTION WHILE OFF				
SET CONFIG DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE OFF	ASCII WRITE COMMAND	#SCDOEOWDOFFS:<OpenWireDOS> <CR> Result: #OK<CR>	ASCII	YES
	DO1	1:ENABLE		
	DO2	1:ENABLE		
	DO3	1:ENABLE		
	DO4	1:ENABLE		
	DO5	1:ENABLE		
	DO6	1:ENABLE		

	DO7	1:ENABLE		
	DO8	1:ENABLE		
	DO9	1:ENABLE		
	DO10	1:ENABLE		
	DO11	1:ENABLE		
	DO12	1:ENABLE		
	DO13	1:ENABLE		
	DO14	1:ENABLE		
	DO15	1:ENABLE		
	DO16	1:ENABLE		
	DO17	1:ENABLE		
	DO18	1:ENABLE		
	DO19	1:ENABLE		
	DO20	1:ENABLE		
	DO21	1:ENABLE		
	DO22	1:ENABLE		
	DO23	1:ENABLE		
	DO24	1:ENABLE		
	TX	#1,SCDOEOWDOFFS:16777215<CR>		
	RX	#255,OK<CR>		

Sets the open wire mode for all digital outputs to the new mode OpenWireDOS for init & watchdog. This enables the diagnostic of open wire while the digital output is OFF.
This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured
The new state for all digital outputs
Bit 0: New mode for DO1 (=0:DISABLED, =1:ENABLED)
Bit 1: New mode for DO2 (=0:DISABLED, =1:ENABLED)
...
Bit 22: New mode for DO23 (=0:DISABLED, =1:ENABLED)
Bit 23: New mode for DO24 (=0:DISABLED, =1:ENABLED)

GET CONFIG DIGITAL OUTPUTS	ASCII	#GCDOEOWDOFFS<CR>	ASCII	
ENABLE OPEN WIRE	READ	Result:		
DETECTION WHILE OFF	COMMAND	#GCDOEOWDOFFS:<OpenWireDOSDec>,<OpenWireDOSHex><CR>		
	TX	#1,GCDOEOWDOFFS<CR>		
	RX	#1,GCDOEOWDOFFS:0,0x0<CR>		
		Init & watchdog configuration for open wire diagnostic while OFF of digital outputs:		
		DO1-DO24:0000.0000.0000.0000.0000.0000		

Returns the actual mode for open wire diagnostic while digital output is OFF as decimal number and as hexadecimal number.
This values are used after power on of the module an after a watchdog event.
OpenWireDOSDec, OpenWireDOSHex
The current mode for open wire diagnostic while OFF of the digital outputs:
Bit 0: Open wire diagnostic mode of DO1 (=0:DISABLED, =1:ENABLED)
Bit 1: Open wire diagnostic mode of DO2 (=0:DISABLED, =1:ENABLED)
...
Bit 22: Open wire diagnostic mode of DO23 (=0:DISABLED, =1:ENABLED)
Bit 23: Open wire diagnostic mode of DO24 (=0:DISABLED, =1:ENABLED)

DIGITAL OUTPUTS: INIT & WATCHDOG ENABLE SHORTCUT TO VDD DETECTION WHILE OFF

SET CONFIG DIGITAL OUTPUTS ENABLE SHORTCUT TO VDD DETECTION WHILE OFF	ASCII WRITE COMMAND	#SCDOESVDDS:<ShortCutDOS> <CR> Result: #OK<CR>	ASCII	YES
	DO1	1:ENABLE		
	DO2	1:ENABLE		
	DO3	1:ENABLE		
	DO4	1:ENABLE		
	DO5	1:ENABLE		
	DO6	1:ENABLE		
	DO7	1:ENABLE		
	DO8	1:ENABLE		
	DO9	1:ENABLE		
	DO10	1:ENABLE		
	DO11	1:ENABLE		
	DO12	1:ENABLE		
	DO13	1:ENABLE		
	DO14	1:ENABLE		
	DO15	1:ENABLE		
	DO16	1:ENABLE		
	DO17	1:ENABLE		
	DO18	1:ENABLE		
	DO19	1:ENABLE		
	DO20	1:ENABLE		
	DO21	1:ENABLE		
	DO22	1:ENABLE		
	DO23	1:ENABLE		
	DO24	1:ENABLE		
	TX	#1,SCDOESVDDS:16777215<CR>		
	RX	#255,OK<CR>		
Sets the shortcut to VDD detection mode for all digital outputs to the new mode ShortCutDOS for init & watchdog. This enables the diagnostic of shortcut to VDD while the digital output is OFF. This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured The new state for all digital outputs Bit 0: New mode for DO1 (=0:DISABLED, =1:ENABLED) Bit 1: New mode for DO2 (=0:DISABLED, =1:ENABLED) ... Bit 22: New mode for DO23 (=0:DISABLED, =1:ENABLED) Bit 23: New mode for DO24 (=0:DISABLED, =1:ENABLED)				
GET CONFIG DIGITAL OUTPUTS ENABLE SHORT CUT TO VDD DETECTION WHILE OFF	ASCII READ COMMAND	#GCDOESVDDS<CR> Result: #GCDOESDDS:<ShortCutDOSDec>,<ShortCutDOSHex><CR>	ASCII	
	TX	#1,GCDOESVDDS<CR>		
	RX	#1,GCDOESVDDS:0,0x0<CR>		
		Init & watchdog configuration for shortcut to VDD diagnostic while OFF of digital outputs:		

		DO1-DO24:0000.0000.0000.0000.0000.0000		
Returns the actual mode for shortcut to VDD diagnostic while digital output is OFF as decimal number and as hexadecimal number. This values are used after power on of the module an after a watchdog event. ShortCutDOSDec, ShortCutDOSHex The current mode for shortcut diagnostic while OFF of the digital outputs: Bit 0: Open wire diagnostic mode of DO1 (=0:DISABLED, =1:ENABLED) Bit 1: Open wire diagnostic mode of DO2 (=0:DISABLED, =1:ENABLED) ... Bit 22: Open wire diagnostic mode of DO23 (=0:DISABLED, =1:ENABLED) Bit 23: Open wire diagnostic mode of DO24 (=0:DISABLED, =1:ENABLED)				

Register NAME Command NAME	MODBUS Register ASCII Command	Register VALUE ASCII Command	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
STATUS DIGITAL OUTPUTS						
DO1	1x00001 2x00001 I:0	0,0x00 B:00		1	BIT R/W	NO
		Actual state of DO1:0=OFF		ENTER NEW STATE (0 or 1)		
Current state of the digital output DOx =0:DO is OFF, =1:DO is ON						
Writing on this register changes the state of the digital output						
DO2	1x00002 2x00002 I:1	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO2:0=OFF		ENTER NEW STATE (0 or 1)		
DO3	1x00003 2x00003 I:2	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO3:0=OFF		ENTER NEW STATE (0 or 1)		
DO4	1x00004 2x00004 I:3	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO4:0=OFF		ENTER NEW STATE (0 or 1)		
DO5	1x00005 2x00005 I:4	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO5:0=OFF		ENTER NEW STATE (0 or 1)		
DO6	1x00006 2x00006 I:5	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO6:0=OFF		ENTER NEW STATE (0 or 1)		
DO7	1x00007 2x00007 I:6	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO7:0=OFF		ENTER NEW STATE (0 or 1)		
DO8	1x00008 2x00008 I:7	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO8:0=OFF		ENTER NEW STATE (0 or 1)		
DO9	1x00009 2x00009 I:8	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO9:0=OFF		ENTER NEW STATE (0 or 1)		

DO10	1x00010 2x00010 I:9	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO10:0=OFF		ENTER NEW STATE (0 or 1)		
DO11	1x00011 2x00011 I:10	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO11:0=OFF		ENTER NEW STATE (0 or 1)		
DO12	1x00012 2x00012 I:11	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO12:0=OFF		ENTER NEW STATE (0 or 1)		
DO13	1x00013 2x00013 I:12	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO13:0=OFF		ENTER NEW STATE (0 or 1)		
DO14	1x00014 2x00014 I:13	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO14:0=OFF		ENTER NEW STATE (0 or 1)		
DO15	1x00015 2x00015 I:14	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO15:0=OFF		ENTER NEW STATE (0 or 1)		
DO16	1x00016 2x00016 I:15	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO16:0=OFF		ENTER NEW STATE (0 or 1)		
DO17	1x00017 2x00017 I:16	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO17:0=OFF		ENTER NEW STATE (0 or 1)		
DO18	1x00018 2x00018 I:17	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO18:0=OFF		ENTER NEW STATE (0 or 1)		
DO19	1x00019 2x00019 I:18	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO19:0=OFF		ENTER NEW STATE (0 or 1)		
DO20	1x00020 2x00020 I:19	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO20:0=OFF		ENTER NEW STATE (0 or 1)		

DO21	1x00021 2x00021 I:20	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO21:0=OFF		ENTER NEW STATE (0 or 1)		
DO22	1x00022 2x00022 I:21	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO22:0=OFF		ENTER NEW STATE (0 or 1)		
DO23	1x00023 2x00023 I:22	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO23:0=OFF		ENTER NEW STATE (0 or 1)		
DO24	1x00024 2x00024 I:23	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO24:0=OFF		ENTER NEW STATE (0 or 1)		
DIGITAL OUTPUTS: ENABLE OPEN WIRE DETECTION ON						
ENABLE OPEN WIRE DETECTION ON DO1	1x00025 2x00025 I:24	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state ON of DO1:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
Enables/disabled detection of an open wire in DO state ON for the digital output DOx =0:Open wire detection is OFF, =1:Open wire detection is ON						
Writing on this register changes the state of the open wire detection for this output						
ENABLE OPEN WIRE DETECTION ON DO2	1x00026 2x00026 I:25	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state ON of DO2:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO3	1x00027 2x00027 I:26	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state ON of DO3:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO4	1x00028 2x00028 I:27	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state ON of DO4:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO5	1x00029 2x00029 I:28	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state ON of DO5:0=OFF		ENTER NEW SETUP MODE (0 or 1)		

ENABLE OPEN WIRE DETECTION ON DO6	1x00030 2x00030 I:29	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO6:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO7	1x00031 2x00031 I:30	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO7:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO8	1x00032 2x00032 I:31	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO8:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO9	1x00033 2x00033 I:32	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO9:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO10	1x00034 2x00034 I:33	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO10:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO11	1x00035 2x00035 I:34	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO11:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO12	1x00036 2x00036 I:35	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO12:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO13	1x00037 2x00037 I:36	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO13:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO14	1x00038 2x00038 I:37	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO14:0=OFF		ENTER NEW SETUP MODE (0 or 1)			

ENABLE OPEN WIRE DETECTION ON DO15	1x00039 2x00039 I:38	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO15:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO16	1x00040 2x00040 I:39	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO16:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO17	1x00041 2x00041 I:40	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO17:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO18	1x00042 2x00042 I:41	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO18:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO19	1x00043 2x00043 I:42	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO19:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO20	1x00044 2x00044 I:43	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO20:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO21	1x00045 2x00045 I:44	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO21:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO22	1x00046 2x00046 I:45	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO22:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION ON DO23	1x00047 2x00047 I:46	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO23:0=OFF		ENTER NEW SETUP MODE (0 or 1)			

ENABLE OPEN WIRE DETECTION ON DO24	1x00048 2x00048 I:47	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state ON of DO24:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
DIGITAL OUTPUTS: ENABLE OPEN WIRE DETECTION OFF							
ENABLE OPEN WIRE DETECTION OFF DO1	1x00049 2x00049 I:48	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO1:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
Enables/disabled detection of an open wire in DO state OFF for the digital output DOx =0:Open wire detection is OFF, =1:Open wire detection is ON							
Writing on this register changes the state of the open wire detection for this output							
ENABLE OPEN WIRE DETECTION OFF DO2	1x00050 2x00050 I:49	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO2:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO3	1x00051 2x00051 I:50	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO3:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO4	1x00052 2x00052 I:51	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO4:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO5	1x00053 2x00053 I:52	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO5:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO6	1x00054 2x00054 I:53	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO6:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO7	1x00055 2x00055 I:54	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO7:0=OFF		ENTER NEW SETUP MODE (0 or 1)			

ENABLE OPEN WIRE DETECTION OFF DO8	1x00056 2x00056 I:55	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO8:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO9	1x00057 2x00057 I:56	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO9:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO10	1x00058 2x00058 I:57	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO10:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO11	1x00059 2x00059 I:58	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO11:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO12	1x00060 2x00060 I:59	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO12:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO13	1x00061 2x00061 I:60	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO13:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO14	1x00062 2x00062 I:61	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO14:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO15	1x00063 2x00063 I:62	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO15:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO16	1x00064 2x00064 I:63	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO16:0=OFF		ENTER NEW SETUP MODE (0 or 1)		

ENABLE OPEN WIRE DETECTION OFF DO17	1x00065 2x00065 I:64	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO17:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO18	1x00066 2x00066 I:65	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO18:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO19	1x00067 2x00067 I:66	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO19:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO20	1x00068 2x00068 I:67	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO20:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO21	1x00069 2x00069 I:68	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO21:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO22	1x00070 2x00070 I:69	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO22:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO23	1x00071 2x00071 I:70	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO23:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO24	1x00072 2x00072 I:71	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO24:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
DIGITAL OUTPUTS: ENABLE SHORT TO VDD DETECTION							
ENABLE SHORT TO VDD DETECTION DO1	1x00073 2x00073 I:72	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO1:0=OFF		ENTER NEW SETUP MODE (0 or 1)			

Enables/disabled detection of a shortcut to VDD in DO state OFF for the digital output DOx

=0:Shortcut to VDD detection is OFF, =1:Shortcut to VDD detection is ON

Writing on this register changes the state of the shortcut detection for this output

ENABLE SHORT TO VDD DETECTION DO2	1x00074 2x00074 I:73	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO2:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO3	1x00075 2x00075 I:74	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO3:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO4	1x00076 2x00076 I:75	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO4:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO5	1x00077 2x00077 I:76	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO5:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO6	1x00078 2x00078 I:77	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO6:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO7	1x00079 2x00079 I:78	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO7:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO8	1x00080 2x00080 I:79	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO8:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO9	1x00081 2x00081 I:80	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO9:0=OFF		ENTER NEW SETUP MODE (0 or 1)		

ENABLE SHORT TO VDD DETECTION DO10	1x00082 2x00082 I:81	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO10:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO11	1x00083 2x00083 I:82	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO11:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO12	1x00084 2x00084 I:83	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO12:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO13	1x00085 2x00085 I:84	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO13:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO14	1x00086 2x00086 I:85	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO14:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO15	1x00087 2x00087 I:86	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO15:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO16	1x00088 2x00088 I:87	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO16:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO17	1x00089 2x00089 I:88	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO17:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO18	1x00090 2x00090 I:89	0,0x00 B:00		1	BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO18:0=OFF		ENTER NEW SETUP MODE (0 or 1)		

ENABLE SHORT TO VDD DETECTION DO19	1x00091 2x00091 I:90	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO19:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO20	1x00092 2x00092 I:91	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO20:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO21	1x00093 2x00093 I:92	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO21:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO22	1x00094 2x00094 I:93	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO22:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO23	1x00095 2x00095 I:94	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO23:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO24	1x00096 2x00096 I:95	0,0x00 B:00		1		BIT R/W	NO
		Actual setup of open wire detection for state OFF of DO24:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
DIGITAL OUTPUTS: OPEN WIRE DETECTION STATUS WHILE ON							
OPEN WIRE FAULT WHILE ON DO1	1x00097 2x00097 I:96	0,0x00 B:00				BIT R/O	
		Actual detection state of an open wire fault in state ON for DO1:0=OK					
The current detection state of an open wire in the output state ON for the digital output DOx =0:No fault, =1:Fault-open wire detected							
OPEN WIRE FAULT WHILE ON DO2	1x00098 2x00098 I:97	0,0x00 B:00				BIT R/O	
		Actual detection state of an open wire fault in state ON for DO2:0=OK					
OPEN WIRE FAULT WHILE ON DO3	1x00099 2x00099 I:98	0,0x00 B:00				BIT R/O	

		Actual detection state of an open wire fault in state ON for DO3:0=OK				
OPEN WIRE FAULT WHILE ON DO4	1x00100 2x00100 I:99	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO4:0=OK				
OPEN WIRE FAULT WHILE ON DO5	1x00101 2x00101 I:100	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO5:0=OK				
OPEN WIRE FAULT WHILE ON DO6	1x00102 2x00102 I:101	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO6:0=OK				
OPEN WIRE FAULT WHILE ON DO7	1x00103 2x00103 I:102	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO7:0=OK				
OPEN WIRE FAULT WHILE ON DO8	1x00104 2x00104 I:103	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO8:0=OK				
OPEN WIRE FAULT WHILE ON DO9	1x00105 2x00105 I:104	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO9:0=OK				
OPEN WIRE FAULT WHILE ON DO10	1x00106 2x00106 I:105	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO10:0=OK				
OPEN WIRE FAULT WHILE ON DO11	1x00107 2x00107 I:106	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO11:0=OK				
OPEN WIRE FAULT WHILE ON DO12	1x00108 2x00108 I:107	0,0x00 B:00			BIT R/O	

		Actual detection state of an open wire fault in state ON for DO12:0=OK				
OPEN WIRE FAULT WHILE ON DO13	1x00109 2x00109 I:108	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO13:0=OK				
OPEN WIRE FAULT WHILE ON DO14	1x00110 2x00110 I:109	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO14:0=OK				
OPEN WIRE FAULT WHILE ON DO15	1x00111 2x00111 I:110	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO15:0=OK				
OPEN WIRE FAULT WHILE ON DO16	1x00112 2x00112 I:111	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO16:0=OK				
OPEN WIRE FAULT WHILE ON DO17	1x00113 2x00113 I:112	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO17:0=OK				
OPEN WIRE FAULT WHILE ON DO18	1x00114 2x00114 I:113	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO18:0=OK				
OPEN WIRE FAULT WHILE ON DO19	1x00115 2x00115 I:114	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO19:0=OK				
OPEN WIRE FAULT WHILE ON DO20	1x00116 2x00116 I:115	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO20:0=OK				
OPEN WIRE FAULT WHILE ON DO21	1x00117 2x00117 I:116	0,0x00 B:00			BIT R/O	

		Actual detection state of an open wire fault in state ON for DO21:0=OK				
OPEN WIRE FAULT WHILE ON DO22	1x00118 2x00118 I:117	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO22:0=OK				
OPEN WIRE FAULT WHILE ON DO23	1x00119 2x00119 I:118	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO23:0=OK				
OPEN WIRE FAULT WHILE ON DO24	1x00120 2x00120 I:119	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state ON for DO24:0=OK				
DIGITAL OUTPUTS: OPEN WIRE DETECTION STATUS WHILE OFF						
OPEN WIRE FAULT WHILE OFF DO1	1x00121 2x00121 I:120	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO1:0=OK				
The current detection state of an open wire in the output state OFF for the digital output DOx =0:No fault, =1:Fault-open wire detected						
OPEN WIRE FAULT WHILE OFF DO2	1x00122 2x00122 I:121	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO2:0=OK				
OPEN WIRE FAULT WHILE OFF DO3	1x00123 2x00123 I:122	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO3:0=OK				
OPEN WIRE FAULT WHILE OFF DO4	1x00124 2x00124 I:123	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO4:0=OK				
OPEN WIRE FAULT WHILE OFF DO5	1x00125 2x00125 I:124	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO5:0=OK				

OPEN WIRE FAULT WHILE OFF DO6	1x00126 2x00126 I:125	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO6:0=OK				
OPEN WIRE FAULT WHILE OFF DO7	1x00127 2x00127 I:126	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO7:0=OK				
OPEN WIRE FAULT WHILE OFF DO8	1x00128 2x00128 I:127	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO8:0=OK				
OPEN WIRE FAULT WHILE OFF DO9	1x00129 2x00129 I:128	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO9:0=OK				
OPEN WIRE FAULT WHILE OFF DO10	1x00130 2x00130 I:129	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO10:0=OK				
OPEN WIRE FAULT WHILE OFF DO11	1x00131 2x00131 I:130	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO11:0=OK				
OPEN WIRE FAULT WHILE OFF DO12	1x00132 2x00132 I:131	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO12:0=OK				
OPEN WIRE FAULT WHILE OFF DO13	1x00133 2x00133 I:132	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO13:0=OK				
OPEN WIRE FAULT WHILE OFF DO14	1x00134 2x00134 I:133	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO14:0=OK				

OPEN WIRE FAULT WHILE OFF DO15	1x00135 2x00135 I:134	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO15:0=OK				
OPEN WIRE FAULT WHILE OFF DO16	1x00136 2x00136 I:135	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO16:0=OK				
OPEN WIRE FAULT WHILE OFF DO17	1x00137 2x00137 I:136	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO17:0=OK				
OPEN WIRE FAULT WHILE OFF DO18	1x00138 2x00138 I:137	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO18:0=OK				
OPEN WIRE FAULT WHILE OFF DO19	1x00139 2x00139 I:138	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO19:0=OK				
OPEN WIRE FAULT WHILE OFF DO20	1x00140 2x00140 I:139	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO20:0=OK				
OPEN WIRE FAULT WHILE OFF DO21	1x00141 2x00141 I:140	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO21:0=OK				
OPEN WIRE FAULT WHILE OFF DO22	1x00142 2x00142 I:141	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO22:0=OK				
OPEN WIRE FAULT WHILE OFF DO23	1x00143 2x00143 I:142	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO23:0=OK				

OPEN WIRE FAULT WHILE OFF DO24	1x00144 2x00144 I:143	0,0x00 B:00			BIT R/O	
		Actual detection state of an open wire fault in state OFF for DO24:0=OK				
DIGITAL OUTPUTS: SHORTCUT DETECTION STATUS TO VDD WHILE OFF						
OPEN WIRE SHORTCUT TO VDD DO1	1x00145 2x00145 I:144	0,0x00 B:00			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO1:0=OK				
The current detection state of a shortcut to VDD in the output state OFF for the digital output DOx =0:No fault, =1:Fault-shortcut to VDD detected						
OPEN WIRE SHORTCUT TO VDD DO2	1x00146 2x00146 I:145	0,0x00 B:00			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO2:0=OK				
OPEN WIRE SHORTCUT TO VDD DO3	1x00147 2x00147 I:146	0,0x00 B:00			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO3:0=OK				
OPEN WIRE SHORTCUT TO VDD DO4	1x00148 2x00148 I:147	0,0x00 B:00			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO4:0=OK				
OPEN WIRE SHORTCUT TO VDD DO5	1x00149 2x00149 I:148	0,0x00 B:00			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO5:0=OK				
OPEN WIRE SHORTCUT TO VDD DO6	1x00150 2x00150 I:149	0,0x00 B:00			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO6:0=OK				

OPEN WIRE SHORTCUT TO VDD DO7	1x00151 2x00151 I:150	0,0x00 B:00			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO7:0=OK				
OPEN WIRE SHORTCUT TO VDD DO8	1x00152 2x00152 I:151	0,0x00 B:00			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO8:0=OK				
OPEN WIRE SHORTCUT TO VDD DO9	1x00153 2x00153 I:152	0,0x00 B:00			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO9:0=OK				
OPEN WIRE SHORTCUT TO VDD DO10	1x00154 2x00154 I:153	0,0x00 B:00			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO10:0=OK				
OPEN WIRE SHORTCUT TO VDD DO11	1x00155 2x00155 I:154	0,0x00 B:00			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO11:0=OK				
OPEN WIRE SHORTCUT TO VDD DO12	1x00156 2x00156 I:155	0,0x00 B:00			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO12:0=OK				
OPEN WIRE SHORTCUT TO VDD DO13	1x00157 2x00157 I:156	0,0x00 B:00			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO13:0=OK				
OPEN WIRE SHORTCUT TO VDD DO14	1x00158 2x00158 I:157	0,0x00 B:00			BIT R/O	

		Actual detection state of a shortcut to VDD in state OFF for VDD DO14:0=OK			
OPEN WIRE SHORTCUT TO VDD DO15	1x00159 2x00159 I:158	0,0x00 B:00			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO15:0=OK			
OPEN WIRE SHORTCUT TO VDD DO16	1x00160 2x00160 I:159	0,0x00 B:00			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO16:0=OK			
OPEN WIRE SHORTCUT TO VDD DO17	1x00161 2x00161 I:160	0,0x00 B:00			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO17:0=OK			
OPEN WIRE SHORTCUT TO VDD DO18	1x00162 2x00162 I:161	0,0x00 B:00			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO18:0=OK			
OPEN WIRE SHORTCUT TO VDD DO19	1x00163 2x00163 I:162	0,0x00 B:00			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO19:0=OK			
OPEN WIRE SHORTCUT TO VDD DO20	1x00164 2x00164 I:163	0,0x00 B:00			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO20:0=OK			
OPEN WIRE SHORTCUT TO VDD DO21	1x00165 2x00165 I:164	0,0x00 B:00			BIT R/O
		Actual detection state of a shortcut to VDD in state OFF for VDD DO21:0=OK			

OPEN WIRE SHORTCUT TO VDD DO22	1x00166 2x00166 I:165	0,0x00 B:00			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO22:0=OK				
OPEN WIRE SHORTCUT TO VDD DO23	1x00167 2x00167 I:166	0,0x00 B:00			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO23:0=OK				
OPEN WIRE SHORTCUT TO VDD DO24	1x00168 2x00168 I:167	0,0x00 B:00			BIT R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO24:0=OK				
DIGITAL OUTPUTS: THERMAL OVERLOAD DETECTION STATUS						
THERMAL OVERLOAD DETECTION STATUS DO1	1x00169 2x00169 I:168	0,0x00 B:00			BIT R/O	
		Actual detection state of a thermal overload for DO1:0=OK				
The current detection state of a thermal overload for the digital output DOx =0:No fault, =1:Fault-thermal overload detected						
THERMAL OVERLOAD DETECTION STATUS DO2	1x00170 2x00170 I:169	0,0x00 B:00			BIT R/O	
		Actual detection state of a thermal overload for DO2:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO3	1x00171 2x00171 I:170	0,0x00 B:00			BIT R/O	
		Actual detection state of a thermal overload for DO3:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO4	1x00172 2x00172 I:171	0,0x00 B:00			BIT R/O	
		Actual detection state of a thermal overload for DO4:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO5	1x00173 2x00173 I:172	0,0x00 B:00			BIT R/O	
		Actual detection state of a thermal overload for DO5:0=OK				

THERMAL OVERLOAD DETECTION STATUS DO6	1x00174 2x00174 I:173	0,0x00 B:00			BIT R/O	
		Actual detection state of a thermal overload for DO6:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO7	1x00175 2x00175 I:174	0,0x00 B:00			BIT R/O	
		Actual detection state of a thermal overload for DO7:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO8	1x00176 2x00176 I:175	0,0x00 B:00			BIT R/O	
		Actual detection state of a thermal overload for DO8:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO9	1x00177 2x00177 I:176	0,0x00 B:00			BIT R/O	
		Actual detection state of a thermal overload for DO9:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO10	1x00178 2x00178 I:177	0,0x00 B:00			BIT R/O	
		Actual detection state of a thermal overload for DO10:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO11	1x00179 2x00179 I:178	0,0x00 B:00			BIT R/O	
		Actual detection state of a thermal overload for DO11:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO12	1x00180 2x00180 I:179	0,0x00 B:00			BIT R/O	
		Actual detection state of a thermal overload for DO12:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO13	1x00181 2x00181 I:180	0,0x00 B:00			BIT R/O	
		Actual detection state of a thermal overload for DO13:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO14	1x00182 2x00182 I:181	0,0x00 B:00			BIT R/O	
		Actual detection state of a thermal overload for DO14:0=OK				

THERMAL OVERLOAD DETECTION STATUS DO15	1x00183 2x00183 I:182	0,0x00 B:00			BIT R/O	
		Actual detection state of a thermal overload for DO15:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO16	1x00184 2x00184 I:183	0,0x00 B:00			BIT R/O	
		Actual detection state of a thermal overload for DO16:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO17	1x00185 2x00185 I:184	0,0x00 B:00			BIT R/O	
		Actual detection state of a thermal overload for DO17:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO18	1x00186 2x00186 I:185	0,0x00 B:00			BIT R/O	
		Actual detection state of a thermal overload for DO18:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO19	1x00187 2x00187 I:186	0,0x00 B:00			BIT R/O	
		Actual detection state of a thermal overload for DO19:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO20	1x00188 2x00188 I:187	0,0x00 B:00			BIT R/O	
		Actual detection state of a thermal overload for DO20:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO21	1x00189 2x00189 I:188	0,0x00 B:00			BIT R/O	
		Actual detection state of a thermal overload for DO21:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO22	1x00190 2x00190 I:189	0,0x00 B:00			BIT R/O	
		Actual detection state of a thermal overload for DO22:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO23	1x00191 2x00191 I:190	0,0x00 B:00			BIT R/O	
		Actual detection state of a thermal overload for DO23:0=OK				

THERMAL OVERLOAD DETECTION STATUS DO24	1x00192 2x00192 I:191	0,0x00 B:00			BIT R/O	
		Actual detection state of a thermal overload for DO24:0=OK				
DIGITAL OUTPUTS: CURRENT LIMIT DETECTION STATUS WHILE ON						
CURRENT LIMIT DETECTION STATUS WHILE ON DO1	1x00193 2x00193 I:192	0,0x00 B:00			BIT R/O	
		Actual detection state of a current limit while ON for DO1:0=OK				
The current detection state of a current limit while output is ON for the digital output DOx =0:No fault, =1:Fault-current limit error						
CURRENT LIMIT DETECTION STATUS WHILE ON DO2	1x00194 2x00194 I:193	0,0x00 B:00			BIT R/O	
		Actual detection state of a current limit while ON for DO2:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO3	1x00195 2x00195 I:194	0,0x00 B:00			BIT R/O	
		Actual detection state of a current limit while ON for DO3:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO4	1x00196 2x00196 I:195	0,0x00 B:00			BIT R/O	
		Actual detection state of a current limit while ON for DO4:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO5	1x00197 2x00197 I:196	0,0x00 B:00			BIT R/O	
		Actual detection state of a current limit while ON for DO5:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO6	1x00198 2x00198 I:197	0,0x00 B:00			BIT R/O	
		Actual detection state of a current limit while ON for DO6:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO7	1x00199 2x00199 I:198	0,0x00 B:00			BIT R/O	
		Actual detection state of a current limit while ON for DO7:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO8	1x00200 2x00200 I:199	0,0x00 B:00			BIT R/O	

		Actual detection state of a current limit while ON for DO8:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO9	1x00201 2x00201 I:200	0,0x00 B:00			BIT R/O	
		Actual detection state of a current limit while ON for DO9:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO10	1x00202 2x00202 I:201	0,0x00 B:00			BIT R/O	
		Actual detection state of a current limit while ON for DO10:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO11	1x00203 2x00203 I:202	0,0x00 B:00			BIT R/O	
		Actual detection state of a current limit while ON for DO11:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO12	1x00204 2x00204 I:203	0,0x00 B:00			BIT R/O	
		Actual detection state of a current limit while ON for DO12:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO13	1x00205 2x00205 I:204	0,0x00 B:00			BIT R/O	
		Actual detection state of a current limit while ON for DO13:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO14	1x00206 2x00206 I:205	0,0x00 B:00			BIT R/O	
		Actual detection state of a current limit while ON for DO14:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO15	1x00207 2x00207 I:206	0,0x00 B:00			BIT R/O	
		Actual detection state of a current limit while ON for DO15:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO16	1x00208 2x00208 I:207	0,0x00 B:00			BIT R/O	
		Actual detection state of a current limit while ON for DO16:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO17	1x00209 2x00209 I:208	0,0x00 B:00			BIT R/O	

		Actual detection state of a current limit while ON for DO17:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO18	1x00210 2x00210 I:209	0,0x00 B:00			BIT R/O	
		Actual detection state of a current limit while ON for DO18:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO19	1x00211 2x00211 I:210	0,0x00 B:00			BIT R/O	
		Actual detection state of a current limit while ON for DO19:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO20	1x00212 2x00212 I:211	0,0x00 B:00			BIT R/O	
		Actual detection state of a current limit while ON for DO20:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO21	1x00213 2x00213 I:212	0,0x00 B:00			BIT R/O	
		Actual detection state of a current limit while ON for DO21:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO22	1x00214 2x00214 I:213	0,0x00 B:00			BIT R/O	
		Actual detection state of a current limit while ON for DO22:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO23	1x00215 2x00215 I:214	0,0x00 B:00			BIT R/O	
		Actual detection state of a current limit while ON for DO23:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO24	1x00216 2x00216 I:215	0,0x00 B:00			BIT R/O	
		Actual detection state of a current limit while ON for DO24:0=OK				
DIGITAL OUTPUTS: GLOBAL ERRORS						
CHIP #1:DO1-DO6						
GLOBAL ERRORS BIT 0	1x00217 2x00217 I:216	0,0x00 B:00			BIT R/O	
		BIT 0:Internal under voltage detected:0=OK				
GLOBAL ERRORS BIT 1	1x00218 2x00218 I:217	0,0x00 B:00			BIT R/O	

		BIT 1:VA under voltage detected (<2.3V):0=OK				
GLOBAL ERRORS BIT 2	1x00219 2x00219 I:218	1,0x01 B:01			BIT R/O	
		BIT 2:VDD not good detected (<17V):1=FAULT				
GLOBAL ERRORS BIT 3	1x00220 2x00220 I:219	1,0x01 B:01			BIT R/O	
		BIT 3:VDD warning detected (<12V):1=FAULT				
GLOBAL ERRORS BIT 4	1x00221 2x00221 I:220	1,0x01 B:01			BIT R/O	
		BIT 4:VDD under voltage detected (<8V):1=FAULT				
GLOBAL ERRORS BIT 5	1x00222 2x00222 I:221	0,0x00 B:00			BIT R/O	
		BIT 5:Thermal shutdown:0=OK				
GLOBAL ERRORS BIT 6	1x00223 2x00223 I:222	0,0x00 B:00			BIT R/O	
		BIT 6:Synchronisation error detected:0=OK				
GLOBAL ERRORS BIT 7	1x00224 2x00224 I:223	0,0x00 B:00			BIT R/O	
		BIT 7:Watchdog error detected:0=OK				
The global error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
DIGITAL OUTPUTS: GLOBAL ERRORS						
CHIP #2:DO7-DO12						
GLOBAL ERRORS BIT 0	1x00225 2x00225 I:224	0,0x00 B:00			BIT R/O	
		BIT 0:Internal under voltage detected:0=OK				
GLOBAL ERRORS BIT 1	1x00226 2x00226 I:225	0,0x00 B:00			BIT R/O	
		BIT 1:VA under voltage detected (<2.3V):0=OK				
GLOBAL ERRORS BIT 2	1x00227 2x00227 I:226	1,0x01 B:01			BIT R/O	
		BIT 2:VDD not good detected (<17V):1=FAULT				
GLOBAL ERRORS BIT 3	1x00228 2x00228 I:227	1,0x01 B:01			BIT R/O	
		BIT 3:VDD warning detected (<12V):1=FAULT				

GLOBAL ERRORS BIT 4	1x00229 2x00229 I:228	1,0x01 B:01			BIT R/O	
		BIT 4:VDD under voltage detected (<8V):1=FAULT				
GLOBAL ERRORS BIT 5	1x00230 2x00230 I:229	0,0x00 B:00			BIT R/O	
		BIT 5:Thermal shutdown:0=OK				
GLOBAL ERRORS BIT 6	1x00231 2x00231 I:230	0,0x00 B:00			BIT R/O	
		BIT 6:Synchronisation error detected:0=OK				
GLOBAL ERRORS BIT 7	1x00232 2x00232 I:231	0,0x00 B:00			BIT R/O	
		BIT 7:Watchdog error detected:0=OK				
The global error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
DIGITAL OUTPUTS: GLOBAL ERRORS						
CHIP #3:DO13-DO18						
GLOBAL ERRORS BIT 0	1x00233 2x00233 I:232	0,0x00 B:00			BIT R/O	
		BIT 0:Internal under voltage detected:0=OK				
GLOBAL ERRORS BIT 1	1x00234 2x00234 I:233	0,0x00 B:00			BIT R/O	
		BIT 1:VA under voltage detected (<2.3V):0=OK				
GLOBAL ERRORS BIT 2	1x00235 2x00235 I:234	1,0x01 B:01			BIT R/O	
		BIT 2:VDD not good detected (<17V):1=FAULT				
GLOBAL ERRORS BIT 3	1x00236 2x00236 I:235	1,0x01 B:01			BIT R/O	
		BIT 3:VDD warning detected (<12V):1=FAULT				
GLOBAL ERRORS BIT 4	1x00237 2x00237 I:236	1,0x01 B:01			BIT R/O	
		BIT 4:VDD under voltage detected (<8V):1=FAULT				
GLOBAL ERRORS BIT 5	1x00238 2x00238 I:237	0,0x00 B:00			BIT R/O	
		BIT 5:Thermal shutdown:0=OK				

GLOBAL ERRORS BIT 6	1x00239 2x00239 I:238	0,0x00 B:00			BIT R/O	
		BIT 6:Synchronisation error detected:0=OK				
GLOBAL ERRORS BIT 7	1x00240 2x00240 I:239	0,0x00 B:00			BIT R/O	
		BIT 7:Watchdog error detected:0=OK				
The global error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
DIGITAL OUTPUTS: GLOBAL ERRORS						
CHIP #4:DO19-DO24						
GLOBAL ERRORS BIT 0	1x00241 2x00241 I:240	0,0x00 B:00			BIT R/O	
		BIT 0:Internal under voltage detected:0=OK				
GLOBAL ERRORS BIT 1	1x00242 2x00242 I:241	0,0x00 B:00			BIT R/O	
		BIT 1:VA under voltage detected (<2.3V):0=OK				
GLOBAL ERRORS BIT 2	1x00243 2x00243 I:242	1,0x01 B:01			BIT R/O	
		BIT 2:VDD not good detected (<17V):1=FAULT				
GLOBAL ERRORS BIT 3	1x00244 2x00244 I:243	1,0x01 B:01			BIT R/O	
		BIT 3:VDD warning detected (<12V):1=FAULT				
GLOBAL ERRORS BIT 4	1x00245 2x00245 I:244	1,0x01 B:01			BIT R/O	
		BIT 4:VDD under voltage detected (<8V):1=FAULT				
GLOBAL ERRORS BIT 5	1x00246 2x00246 I:245	0,0x00 B:00			BIT R/O	
		BIT 5:Thermal shutdown:0=OK				
GLOBAL ERRORS BIT 6	1x00247 2x00247 I:246	0,0x00 B:00			BIT R/O	
		BIT 6:Synchronisation error detected:0=OK				
GLOBAL ERRORS BIT 7	1x00248 2x00248 I:247	0,0x00 B:00			BIT R/O	
		BIT 7:Watchdog error detected:0=OK				
The global error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						

DIGITAL OUTPUTS: INTERRUPT STATUS**CHIP #1:DO1-DO6**

INTERRUPT STATUS BIT 0	1x00249 2x00249 I:248	0,0x00 B:00			BIT R/O	
		BIT 0:Overload detected:0=OK				
INTERRUPT STATUS BIT 1	1x00250 2x00250 I:249	0,0x00 B:00			BIT R/O	
		BIT 1:Current limit detected:0=OK				
INTERRUPT STATUS BIT 2	1x00251 2x00251 I:250	0,0x00 B:00			BIT R/O	
		BIT 2:Open wire while OFF detected:0=OK				
INTERRUPT STATUS BIT 3	1x00252 2x00252 I:251	0,0x00 B:00			BIT R/O	
		BIT 3:Open wire while ON detected:0=OK				
INTERRUPT STATUS BIT 4	1x00253 2x00253 I:252	0,0x00 B:00			BIT R/O	
		BIT 4:Short to VDD while ON detected:0=OK				
INTERRUPT STATUS BIT 5	1x00254 2x00254 I:253	0,0x00 B:00			BIT R/O	
		BIT 5:Thermal error detected-shutdown:0=OK				
INTERRUPT STATUS BIT 6	1x00255 2x00255 I:254	1,0x01 B:01			BIT R/O	
		BIT 6:Supply error detected:1=FAULT				
INTERRUPT STATUS BIT 7	1x00256 2x00256 I:255	0,0x00 B:00			BIT R/O	
		BIT 7:Communication error detected:0=OK				

The global interrupt error state for the output group. Each bit stands for a different error
 =0:No fault, =1:Fault

DIGITAL OUTPUTS: INTERRUPT STATUS**CHIP #2:DO7-DO12**

INTERRUPT STATUS BIT 0	1x00257 2x00257 I:256	0,0x00 B:00			BIT R/O	
		BIT 0:Overload detected:0=OK				
INTERRUPT STATUS BIT 1	1x00258 2x00258 I:257	0,0x00 B:00			BIT R/O	
		BIT 1:Current limit detected:0=OK				

INTERRUPT STATUS BIT 2	1x00259 2x00259 I:258	0,0x00 B:00			BIT R/O	
		BIT 2:Open wire while OFF detected:0=OK				
INTERRUPT STATUS BIT 3	1x00260 2x00260 I:259	0,0x00 B:00			BIT R/O	
		BIT 3:Open wire while ON detected:0=OK				
INTERRUPT STATUS BIT 4	1x00261 2x00261 I:260	0,0x00 B:00			BIT R/O	
		BIT 4:Short to VDD while ON detected:0=OK				
INTERRUPT STATUS BIT 5	1x00262 2x00262 I:261	0,0x00 B:00			BIT R/O	
		BIT 5:Thermal error detected-shutdown:0=OK				
INTERRUPT STATUS BIT 6	1x00263 2x00263 I:262	1,0x01 B:01			BIT R/O	
		BIT 6:Supply error detected:1=FAULT				
INTERRUPT STATUS BIT 7	1x00264 2x00264 I:263	0,0x00 B:00			BIT R/O	
		BIT 7:Communication error detected:0=OK				

The global interrupt error state for the output group. Each bit stands for a different error
 =0:No fault, =1:Fault

DIGITAL OUTPUTS: INTERRUPT STATUS

CHIP #3:DO13-DO18

INTERRUPT STATUS BIT 0	1x00265 2x00265 I:264	0,0x00 B:00			BIT R/O	
		BIT 0:Overload detected:0=OK				
INTERRUPT STATUS BIT 1	1x00266 2x00266 I:265	0,0x00 B:00			BIT R/O	
		BIT 1:Current limit detected:0=OK				
INTERRUPT STATUS BIT 2	1x00267 2x00267 I:266	0,0x00 B:00			BIT R/O	
		BIT 2:Open wire while OFF detected:0=OK				
INTERRUPT STATUS BIT 3	1x00268 2x00268 I:267	0,0x00 B:00			BIT R/O	
		BIT 3:Open wire while ON detected:0=OK				

INTERRUPT STATUS BIT 4	1x00269 2x00269 I:268	0,0x00 B:00			BIT R/O	
		BIT 4:Short to VDD while ON detected:0=OK				
INTERRUPT STATUS BIT 5	1x00270 2x00270 I:269	0,0x00 B:00			BIT R/O	
		BIT 5:Thermal error detected-shutdown:0=OK				
INTERRUPT STATUS BIT 6	1x00271 2x00271 I:270	1,0x01 B:01			BIT R/O	
		BIT 6:Supply error detected:1=FAULT				
INTERRUPT STATUS BIT 7	1x00272 2x00272 I:271	0,0x00 B:00			BIT R/O	
		BIT 7:Communication error detected:0=OK				

The global interrupt error state for the output group. Each bit stands for a different error
=0:No fault, =1:Fault

DIGITAL OUTPUTS: INTERRUPT STATUS**CHIP #4:DO19-DO24**

INTERRUPT STATUS BIT 0	1x00273 2x00273 I:272	0,0x00 B:00			BIT R/O	
		BIT 0:Overload detected:0=OK				
INTERRUPT STATUS BIT 1	1x00274 2x00274 I:273	0,0x00 B:00			BIT R/O	
		BIT 1:Current limit detected:0=OK				
INTERRUPT STATUS BIT 2	1x00275 2x00275 I:274	0,0x00 B:00			BIT R/O	
		BIT 2:Open wire while OFF detected:0=OK				
INTERRUPT STATUS BIT 3	1x00276 2x00276 I:275	0,0x00 B:00			BIT R/O	
		BIT 3:Open wire while ON detected:0=OK				
INTERRUPT STATUS BIT 4	1x00277 2x00277 I:276	0,0x00 B:00			BIT R/O	
		BIT 4:Short to VDD while ON detected:0=OK				
INTERRUPT STATUS BIT 5	1x00278 2x00278 I:277	0,0x00 B:00			BIT R/O	
		BIT 5:Thermal error detected-shutdown:0=OK				

INTERRUPT STATUS BIT 6	1x00279 2x00279 I:278	1,0x01 B:01			BIT R/O	
		BIT 6:Supply error detected:1=FAULT				
INTERRUPT STATUS BIT 7	1x00280 2x00280 I:279	0,0x00 B:00			BIT R/O	
		BIT 7:Communication error detected:0=OK				
The global interrupt error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
SPI COMMUNICATION DIGITAL OUTPUTS						
SPI COMMUNICATION CHIP #1: DO1-DO6	1x00281 2x00281 I:280	0,0x00 B:00			BIT R/O	
		Actual SPI communication state:0=NO FAULT				
The current monitoring state of the SPI communication for the digital output group =0:No fault, =1:Fault						
SPI COMMUNICATION CHIP #2: DO7-DO12	1x00282 2x00282 I:281	0,0x00 B:00			BIT R/O	
		Actual SPI communication state:0=NO FAULT				
The current monitoring state of the SPI communication for the digital output group =0:No fault, =1:Fault						
SPI COMMUNICATION CHIP #3: DO13-DO18	1x00283 2x00283 I:282	0,0x00 B:00			BIT R/O	
		Actual SPI communication state:0=NO FAULT				
The current monitoring state of the SPI communication for the digital output group =0:No fault, =1:Fault						
SPI COMMUNICATION CHIP #4: DO19-DO24	1x00284 2x00284 I:283	0,0x00 B:00			BIT R/O	
		Actual SPI communication state:0=NO FAULT				
STATUS DIGITAL OUTPUTS						
DO1	1x16001 2x16001 I:16000	0,0x00 B:00		1	BIT R/W	NO
		Actual state of DO1:0=OFF		ENTER NEW STATE (0 or 1)		
Current state of the digital output DOx =0:DO is OFF, =1:DO is ON						
Writing on this register changes the state of the digital output						
DO2	1x16002 2x16002 I:16001	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO2:0=OFF		ENTER NEW STATE (0 or 1)		

DO3	1x16003 2x16003 I:16002	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO3:0=OFF		ENTER NEW STATE (0 or 1)		
DO4	1x16004 2x16004 I:16003	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO4:0=OFF		ENTER NEW STATE (0 or 1)		
DO5	1x16005 2x16005 I:16004	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO5:0=OFF		ENTER NEW STATE (0 or 1)		
DO6	1x16006 2x16006 I:16005	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO6:0=OFF		ENTER NEW STATE (0 or 1)		
DO7	1x16007 2x16007 I:16006	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO7:0=OFF		ENTER NEW STATE (0 or 1)		
DO8	1x16008 2x16008 I:16007	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO8:0=OFF		ENTER NEW STATE (0 or 1)		
DO9	1x16009 2x16009 I:16008	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO9:0=OFF		ENTER NEW STATE (0 or 1)		
DO10	1x16010 2x16010 I:16009	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO10:0=OFF		ENTER NEW STATE (0 or 1)		
DO11	1x16011 2x16011 I:16010	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO11:0=OFF		ENTER NEW STATE (0 or 1)		
DO12	1x16012 2x16012 I:16011	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO12:0=OFF		ENTER NEW STATE (0 or 1)		
DO13	1x16013 2x16013 I:16012	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO13:0=OFF		ENTER NEW STATE (0 or 1)		

DO14	1x16014 2x16014 I:16013	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO14:0=OFF		ENTER NEW STATE (0 or 1)		
DO15	1x16015 2x16015 I:16014	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO15:0=OFF		ENTER NEW STATE (0 or 1)		
DO16	1x16016 2x16016 I:16015	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO16:0=OFF		ENTER NEW STATE (0 or 1)		
DO17	1x16017 2x16017 I:16016	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO17:0=OFF		ENTER NEW STATE (0 or 1)		
DO18	1x16018 2x16018 I:16017	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO18:0=OFF		ENTER NEW STATE (0 or 1)		
DO19	1x16019 2x16019 I:16018	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO19:0=OFF		ENTER NEW STATE (0 or 1)		
DO20	1x16020 2x16020 I:16019	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO20:0=OFF		ENTER NEW STATE (0 or 1)		
DO21	1x16021 2x16021 I:16020	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO21:0=OFF		ENTER NEW STATE (0 or 1)		
DO22	1x16022 2x16022 I:16021	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO22:0=OFF		ENTER NEW STATE (0 or 1)		
DO23	1x16023 2x16023 I:16022	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO23:0=OFF		ENTER NEW STATE (0 or 1)		
DO24	1x16024 2x16024 I:16023	0,0x00 B:00		0	BIT R/W	NO
		Actual state of DO24:0=OFF		ENTER NEW STATE (0 or 1)		

Register NAME Command NAME	MODBUS Register ASCII Command	Register VALUE ASCII Command	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
STATUS DIGITAL OUTPUTS						
DO1	3x00001 4x00001 I:0	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual state of DO1:0=OFF		ENTER NEW STATE (0 or 1)		
Current state of the digital output DOx =0:DO is OFF, =1:DO is ON						
Writing on this register changes the state of the digital output						
DO2	3x00002 4x00002 I:1	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO2:0=OFF		ENTER NEW STATE (0 or 1)		
DO3	3x00003 4x00003 I:2	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO3:0=OFF		ENTER NEW STATE (0 or 1)		
DO4	3x00004 4x00004 I:3	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO4:0=OFF		ENTER NEW STATE (0 or 1)		
DO5	3x00005 4x00005 I:4	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO5:0=OFF		ENTER NEW STATE (0 or 1)		
DO6	3x00006 4x00006 I:5	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO6:0=OFF		ENTER NEW STATE (0 or 1)		
DO7	3x00007 4x00007 I:6	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO7:0=OFF		ENTER NEW STATE (0 or 1)		
DO8	3x00008 4x00008 I:7	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO8:0=OFF		ENTER NEW STATE (0 or 1)		
DO9	3x00009 4x00009 I:8	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO9:0=OFF		ENTER NEW STATE (0 or 1)		

DO10	3x00010 4x00010 I:9	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO10:0=OFF		ENTER NEW STATE (0 or 1)		
DO11	3x00011 4x00011 I:10	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO11:0=OFF		ENTER NEW STATE (0 or 1)		
DO12	3x00012 4x00012 I:11	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO12:0=OFF		ENTER NEW STATE (0 or 1)		
DO13	3x00013 4x00013 I:12	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO13:0=OFF		ENTER NEW STATE (0 or 1)		
DO14	3x00014 4x00014 I:13	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO14:0=OFF		ENTER NEW STATE (0 or 1)		
DO15	3x00015 4x00015 I:14	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO15:0=OFF		ENTER NEW STATE (0 or 1)		
DO16	3x00016 4x00016 I:15	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO16:0=OFF		ENTER NEW STATE (0 or 1)		
DO17	3x00017 4x00017 I:16	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO17:0=OFF		ENTER NEW STATE (0 or 1)		
DO18	3x00018 4x00018 I:17	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO18:0=OFF		ENTER NEW STATE (0 or 1)		
DO19	3x00019 4x00019 I:18	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO19:0=OFF		ENTER NEW STATE (0 or 1)		
DO20	3x00020 4x00020 I:19	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO20:0=OFF		ENTER NEW STATE (0 or 1)		

DO21	3x00021 4x00021 I:20	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO21:0=OFF		ENTER NEW STATE (0 or 1)		
DO22	3x00022 4x00022 I:21	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO22:0=OFF		ENTER NEW STATE (0 or 1)		
DO23	3x00023 4x00023 I:22	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO23:0=OFF		ENTER NEW STATE (0 or 1)		
DO24	3x00024 4x00024 I:23	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO24:0=OFF		ENTER NEW STATE (0 or 1)		
DIGITAL OUTPUTS: ENABLE OPEN WIRE DETECTION ON						
ENABLE OPEN WIRE DETECTION ON DO1	3x00025 4x00025 I:24	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO1:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
Enables/disabled detection of an open wire in DO state ON for the digital output DOx =0:Open wire detection is OFF, =1:Open wire detection is ON						
Writing on this register changes the state of the open wire detection for this output						
ENABLE OPEN WIRE DETECTION ON DO2	3x00026 4x00026 I:25	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO2:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO3	3x00027 4x00027 I:26	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO3:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO4	3x00028 4x00028 I:27	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO4:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO5	3x00029 4x00029 I:28	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO5:0=OFF		ENTER NEW SETUP MODE (0 or 1)		

ENABLE OPEN WIRE DETECTION ON DO6	3x00030 4x00030 I:29	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO6:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO7	3x00031 4x00031 I:30	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO7:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO8	3x00032 4x00032 I:31	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO8:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO9	3x00033 4x00033 I:32	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO9:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO10	3x00034 4x00034 I:33	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO10:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO11	3x00035 4x00035 I:34	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO11:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO12	3x00036 4x00036 I:35	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO12:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO13	3x00037 4x00037 I:36	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO13:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO14	3x00038 4x00038 I:37	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO14:0=OFF		ENTER NEW SETUP MODE (0 or 1)		

ENABLE OPEN WIRE DETECTION ON DO15	3x00039 4x00039 I:38	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO15:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO16	3x00040 4x00040 I:39	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO16:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO17	3x00041 4x00041 I:40	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO17:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO18	3x00042 4x00042 I:41	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO18:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO19	3x00043 4x00043 I:42	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO19:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO20	3x00044 4x00044 I:43	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO20:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO21	3x00045 4x00045 I:44	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO21:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO22	3x00046 4x00046 I:45	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO22:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO23	3x00047 4x00047 I:46	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO23:0=OFF		ENTER NEW SETUP MODE (0 or 1)		

ENABLE OPEN WIRE DETECTION ON DO24	3x00048 4x00048 I:47	0,0x0000 B:00 00		1		UINT16 R/W	NO
		Actual setup of open wire detection for state ON of DO24:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
DIGITAL OUTPUTS: ENABLE OPEN WIRE DETECTION OFF							
ENABLE OPEN WIRE DETECTION OFF DO1	3x00049 4x00049 I:48	0,0x0000 B:00 00		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO1:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
Enables/disabled detection of an open wire in DO state OFF for the digital output DOx =0:Open wire detection is OFF, =1:Open wire detection is ON							
Writing on this register changes the state of the open wire detection for this output							
ENABLE OPEN WIRE DETECTION OFF DO2	3x00050 4x00050 I:49	0,0x0000 B:00 00		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO2:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO3	3x00051 4x00051 I:50	0,0x0000 B:00 00		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO3:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO4	3x00052 4x00052 I:51	0,0x0000 B:00 00		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO4:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO5	3x00053 4x00053 I:52	0,0x0000 B:00 00		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO5:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO6	3x00054 4x00054 I:53	0,0x0000 B:00 00		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO6:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO7	3x00055 4x00055 I:54	0,0x0000 B:00 00		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO7:0=OFF		ENTER NEW SETUP MODE (0 or 1)			

ENABLE OPEN WIRE DETECTION OFF DO8	3x00056 4x00056 I:55	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO8:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO9	3x00057 4x00057 I:56	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO9:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO10	3x00058 4x00058 I:57	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO10:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO11	3x00059 4x00059 I:58	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO11:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO12	3x00060 4x00060 I:59	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO12:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO13	3x00061 4x00061 I:60	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO13:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO14	3x00062 4x00062 I:61	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO14:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO15	3x00063 4x00063 I:62	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO15:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO16	3x00064 4x00064 I:63	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO16:0=OFF		ENTER NEW SETUP MODE (0 or 1)		

ENABLE OPEN WIRE DETECTION OFF DO17	3x00065 4x00065 I:64	0,0x0000 B:00 00		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO17:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO18	3x00066 4x00066 I:65	0,0x0000 B:00 00		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO18:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO19	3x00067 4x00067 I:66	0,0x0000 B:00 00		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO19:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO20	3x00068 4x00068 I:67	0,0x0000 B:00 00		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO20:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO21	3x00069 4x00069 I:68	0,0x0000 B:00 00		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO21:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO22	3x00070 4x00070 I:69	0,0x0000 B:00 00		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO22:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO23	3x00071 4x00071 I:70	0,0x0000 B:00 00		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO23:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO24	3x00072 4x00072 I:71	0,0x0000 B:00 00		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO24:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
DIGITAL OUTPUTS: ENABLE SHORT TO VDD DETECTION							
ENABLE SHORT TO VDD DETECTION DO1	3x00073 4x00073 I:72	0,0x0000 B:00 00		1		UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO1:0=OFF		ENTER NEW SETUP MODE (0 or 1)			

Enables/disabled detection of a shortcut to VDD in DO state OFF for the digital output DOx

=0:Shortcut to VDD detection is OFF, =1:Shortcut to VDD detection is ON

Writing on this register changes the state of the shortcut detection for this output

ENABLE SHORT TO VDD DETECTION DO2	3x00074 4x00074 I:73	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO2:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO3	3x00075 4x00075 I:74	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO3:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO4	3x00076 4x00076 I:75	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO4:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO5	3x00077 4x00077 I:76	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO5:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO6	3x00078 4x00078 I:77	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO6:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO7	3x00079 4x00079 I:78	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO7:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO8	3x00080 4x00080 I:79	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO8:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO9	3x00081 4x00081 I:80	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO9:0=OFF		ENTER NEW SETUP MODE (0 or 1)		

ENABLE SHORT TO VDD DETECTION DO10	3x00082 4x00082 I:81	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO10:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO11	3x00083 4x00083 I:82	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO11:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO12	3x00084 4x00084 I:83	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO12:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO13	3x00085 4x00085 I:84	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO13:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO14	3x00086 4x00086 I:85	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO14:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO15	3x00087 4x00087 I:86	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO15:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO16	3x00088 4x00088 I:87	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO16:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO17	3x00089 4x00089 I:88	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO17:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO18	3x00090 4x00090 I:89	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO18:0=OFF		ENTER NEW SETUP MODE (0 or 1)		

ENABLE SHORT TO VDD DETECTION DO19	3x00091 4x00091 I:90	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO19:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO20	3x00092 4x00092 I:91	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO20:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO21	3x00093 4x00093 I:92	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO21:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO22	3x00094 4x00094 I:93	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO22:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO23	3x00095 4x00095 I:94	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO23:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO24	3x00096 4x00096 I:95	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual setup of open wire detection for state OFF of DO24:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
DIGITAL OUTPUTS: OPEN WIRE DETECTION STATUS WHILE ON						
OPEN WIRE FAULT WHILE ON DO1	3x00097 4x00097 I:96	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO1:0=OK				
The current detection state of an open wire in the output state ON for the digital output DOx =0:No fault, =1:Fault-open wire detected						
OPEN WIRE FAULT WHILE ON DO2	3x00098 4x00098 I:97	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO2:0=OK				
OPEN WIRE FAULT WHILE ON DO3	3x00099 4x00099 I:98	0,0x0000 B:00 00			UINT16 R/O	

		Actual detection state of an open wire fault in state ON for DO3:0=OK				
OPEN WIRE FAULT WHILE ON DO4	3x00100 4x00100 I:99	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO4:0=OK				
OPEN WIRE FAULT WHILE ON DO5	3x00101 4x00101 I:100	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO5:0=OK				
OPEN WIRE FAULT WHILE ON DO6	3x00102 4x00102 I:101	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO6:0=OK				
OPEN WIRE FAULT WHILE ON DO7	3x00103 4x00103 I:102	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO7:0=OK				
OPEN WIRE FAULT WHILE ON DO8	3x00104 4x00104 I:103	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO8:0=OK				
OPEN WIRE FAULT WHILE ON DO9	3x00105 4x00105 I:104	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO9:0=OK				
OPEN WIRE FAULT WHILE ON DO10	3x00106 4x00106 I:105	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO10:0=OK				
OPEN WIRE FAULT WHILE ON DO11	3x00107 4x00107 I:106	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO11:0=OK				
OPEN WIRE FAULT WHILE ON DO12	3x00108 4x00108 I:107	0,0x0000 B:00 00			UINT16 R/O	

		Actual detection state of an open wire fault in state ON for DO12:0=OK				
OPEN WIRE FAULT WHILE ON DO13	3x00109 4x00109 I:108	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO13:0=OK				
OPEN WIRE FAULT WHILE ON DO14	3x00110 4x00110 I:109	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO14:0=OK				
OPEN WIRE FAULT WHILE ON DO15	3x00111 4x00111 I:110	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO15:0=OK				
OPEN WIRE FAULT WHILE ON DO16	3x00112 4x00112 I:111	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO16:0=OK				
OPEN WIRE FAULT WHILE ON DO17	3x00113 4x00113 I:112	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO17:0=OK				
OPEN WIRE FAULT WHILE ON DO18	3x00114 4x00114 I:113	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO18:0=OK				
OPEN WIRE FAULT WHILE ON DO19	3x00115 4x00115 I:114	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO19:0=OK				
OPEN WIRE FAULT WHILE ON DO20	3x00116 4x00116 I:115	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO20:0=OK				
OPEN WIRE FAULT WHILE ON DO21	3x00117 4x00117 I:116	0,0x0000 B:00 00			UINT16 R/O	

		Actual detection state of an open wire fault in state ON for DO21:0=OK				
OPEN WIRE FAULT WHILE ON DO22	3x00118 4x00118 I:117	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO22:0=OK				
OPEN WIRE FAULT WHILE ON DO23	3x00119 4x00119 I:118	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO23:0=OK				
OPEN WIRE FAULT WHILE ON DO24	3x00120 4x00120 I:119	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state ON for DO24:0=OK				
DIGITAL OUTPUTS: OPEN WIRE DETECTION STATUS WHILE OFF						
OPEN WIRE FAULT WHILE OFF DO1	3x00121 4x00121 I:120	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO1:0=OK				
The current detection state of an open wire in the output state OFF for the digital output DOx =0:No fault, =1:Fault-open wire detected						
OPEN WIRE FAULT WHILE OFF DO2	3x00122 4x00122 I:121	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO2:0=OK				
OPEN WIRE FAULT WHILE OFF DO3	3x00123 4x00123 I:122	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO3:0=OK				
OPEN WIRE FAULT WHILE OFF DO4	3x00124 4x00124 I:123	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO4:0=OK				
OPEN WIRE FAULT WHILE OFF DO5	3x00125 4x00125 I:124	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO5:0=OK				

OPEN WIRE FAULT WHILE OFF DO6	3x00126 4x00126 I:125	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO6:0=OK				
OPEN WIRE FAULT WHILE OFF DO7	3x00127 4x00127 I:126	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO7:0=OK				
OPEN WIRE FAULT WHILE OFF DO8	3x00128 4x00128 I:127	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO8:0=OK				
OPEN WIRE FAULT WHILE OFF DO9	3x00129 4x00129 I:128	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO9:0=OK				
OPEN WIRE FAULT WHILE OFF DO10	3x00130 4x00130 I:129	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO10:0=OK				
OPEN WIRE FAULT WHILE OFF DO11	3x00131 4x00131 I:130	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO11:0=OK				
OPEN WIRE FAULT WHILE OFF DO12	3x00132 4x00132 I:131	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO12:0=OK				
OPEN WIRE FAULT WHILE OFF DO13	3x00133 4x00133 I:132	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO13:0=OK				
OPEN WIRE FAULT WHILE OFF DO14	3x00134 4x00134 I:133	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO14:0=OK				

OPEN WIRE FAULT WHILE OFF DO15	3x00135 4x00135 I:134	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO15:0=OK				
OPEN WIRE FAULT WHILE OFF DO16	3x00136 4x00136 I:135	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO16:0=OK				
OPEN WIRE FAULT WHILE OFF DO17	3x00137 4x00137 I:136	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO17:0=OK				
OPEN WIRE FAULT WHILE OFF DO18	3x00138 4x00138 I:137	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO18:0=OK				
OPEN WIRE FAULT WHILE OFF DO19	3x00139 4x00139 I:138	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO19:0=OK				
OPEN WIRE FAULT WHILE OFF DO20	3x00140 4x00140 I:139	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO20:0=OK				
OPEN WIRE FAULT WHILE OFF DO21	3x00141 4x00141 I:140	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO21:0=OK				
OPEN WIRE FAULT WHILE OFF DO22	3x00142 4x00142 I:141	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO22:0=OK				
OPEN WIRE FAULT WHILE OFF DO23	3x00143 4x00143 I:142	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO23:0=OK				

OPEN WIRE FAULT WHILE OFF DO24	3x00144 4x00144 I:143	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of an open wire fault in state OFF for DO24:0=OK				
DIGITAL OUTPUTS: SHORTCUT DETECTION STATUS TO VDD WHILE OFF						
OPEN WIRE SHORTCUT TO VDD DO1	3x00145 4x00145 I:144	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO1:0=OK				
The current detection state of a shortcut to VDD in the output state OFF for the digital output DOx =0:No fault, =1:Fault-shortcut to VDD detected						
OPEN WIRE SHORTCUT TO VDD DO2	3x00146 4x00146 I:145	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO2:0=OK				
OPEN WIRE SHORTCUT TO VDD DO3	3x00147 4x00147 I:146	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO3:0=OK				
OPEN WIRE SHORTCUT TO VDD DO4	3x00148 4x00148 I:147	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO4:0=OK				
OPEN WIRE SHORTCUT TO VDD DO5	3x00149 4x00149 I:148	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO5:0=OK				
OPEN WIRE SHORTCUT TO VDD DO6	3x00150 4x00150 I:149	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO6:0=OK				

OPEN WIRE SHORTCUT TO VDD DO7	3x00151 4x00151 I:150	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO7:0=OK				
OPEN WIRE SHORTCUT TO VDD DO8	3x00152 4x00152 I:151	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO8:0=OK				
OPEN WIRE SHORTCUT TO VDD DO9	3x00153 4x00153 I:152	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO9:0=OK				
OPEN WIRE SHORTCUT TO VDD DO10	3x00154 4x00154 I:153	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO10:0=OK				
OPEN WIRE SHORTCUT TO VDD DO11	3x00155 4x00155 I:154	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO11:0=OK				
OPEN WIRE SHORTCUT TO VDD DO12	3x00156 4x00156 I:155	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO12:0=OK				
OPEN WIRE SHORTCUT TO VDD DO13	3x00157 4x00157 I:156	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO13:0=OK				
OPEN WIRE SHORTCUT TO VDD DO14	3x00158 4x00158 I:157	0,0x0000 B:00 00			UINT16 R/O	

		Actual detection state of a shortcut to VDD in state OFF for VDD DO14:0=OK				
OPEN WIRE SHORTCUT TO VDD DO15	3x00159 4x00159 I:158	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO15:0=OK				
OPEN WIRE SHORTCUT TO VDD DO16	3x00160 4x00160 I:159	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO16:0=OK				
OPEN WIRE SHORTCUT TO VDD DO17	3x00161 4x00161 I:160	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO17:0=OK				
OPEN WIRE SHORTCUT TO VDD DO18	3x00162 4x00162 I:161	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO18:0=OK				
OPEN WIRE SHORTCUT TO VDD DO19	3x00163 4x00163 I:162	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO19:0=OK				
OPEN WIRE SHORTCUT TO VDD DO20	3x00164 4x00164 I:163	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO20:0=OK				
OPEN WIRE SHORTCUT TO VDD DO21	3x00165 4x00165 I:164	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO21:0=OK				

OPEN WIRE SHORTCUT TO VDD DO22	3x00166 4x00166 I:165	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO22:0=OK				
OPEN WIRE SHORTCUT TO VDD DO23	3x00167 4x00167 I:166	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO23:0=OK				
OPEN WIRE SHORTCUT TO VDD DO24	3x00168 4x00168 I:167	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a shortcut to VDD in state OFF for VDD DO24:0=OK				
DIGITAL OUTPUTS: THERMAL OVERLOAD DETECTION STATUS						
THERMAL OVERLOAD DETECTION STATUS DO1	3x00169 4x00169 I:168	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO1:0=OK				
The current detection state of a thermal overload for the digital output DOx =0:No fault, =1:Fault-thermal overload detected						
THERMAL OVERLOAD DETECTION STATUS DO2	3x00170 4x00170 I:169	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO2:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO3	3x00171 4x00171 I:170	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO3:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO4	3x00172 4x00172 I:171	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO4:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO5	3x00173 4x00173 I:172	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO5:0=OK				

THERMAL OVERLOAD DETECTION STATUS DO6	3x00174 4x00174 I:173	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO6:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO7	3x00175 4x00175 I:174	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO7:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO8	3x00176 4x00176 I:175	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO8:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO9	3x00177 4x00177 I:176	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO9:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO10	3x00178 4x00178 I:177	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO10:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO11	3x00179 4x00179 I:178	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO11:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO12	3x00180 4x00180 I:179	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO12:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO13	3x00181 4x00181 I:180	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO13:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO14	3x00182 4x00182 I:181	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO14:0=OK				

THERMAL OVERLOAD DETECTION STATUS DO15	3x00183 4x00183 I:182	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO15:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO16	3x00184 4x00184 I:183	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO16:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO17	3x00185 4x00185 I:184	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO17:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO18	3x00186 4x00186 I:185	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO18:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO19	3x00187 4x00187 I:186	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO19:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO20	3x00188 4x00188 I:187	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO20:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO21	3x00189 4x00189 I:188	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO21:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO22	3x00190 4x00190 I:189	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO22:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO23	3x00191 4x00191 I:190	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO23:0=OK				

THERMAL OVERLOAD DETECTION STATUS DO24	3x00192 4x00192 I:191	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a thermal overload for DO24:0=OK				
DIGITAL OUTPUTS: CURRENT LIMIT DETECTION STATUS WHILE ON						
CURRENT LIMIT DETECTION STATUS WHILE ON DO1	3x00193 4x00193 I:192	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO1:0=OK				
The current detection state of a current limit while output is ON for the digital output DOx =0:No fault, =1:Fault-current limit error						
CURRENT LIMIT DETECTION STATUS WHILE ON DO2	3x00194 4x00194 I:193	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO2:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO3	3x00195 4x00195 I:194	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO3:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO4	3x00196 4x00196 I:195	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO4:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO5	3x00197 4x00197 I:196	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO5:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO6	3x00198 4x00198 I:197	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO6:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO7	3x00199 4x00199 I:198	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO7:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO8	3x00200 4x00200 I:199	0,0x0000 B:00 00			UINT16 R/O	

		Actual detection state of a current limit while ON for DO8:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO9	3x00201 4x00201 I:200	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO9:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO10	3x00202 4x00202 I:201	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO10:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO11	3x00203 4x00203 I:202	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO11:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO12	3x00204 4x00204 I:203	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO12:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO13	3x00205 4x00205 I:204	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO13:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO14	3x00206 4x00206 I:205	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO14:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO15	3x00207 4x00207 I:206	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO15:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO16	3x00208 4x00208 I:207	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO16:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO17	3x00209 4x00209 I:208	0,0x0000 B:00 00			UINT16 R/O	

		Actual detection state of a current limit while ON for DO17:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO18	3x00210 4x00210 I:209	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO18:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO19	3x00211 4x00211 I:210	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO19:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO20	3x00212 4x00212 I:211	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO20:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO21	3x00213 4x00213 I:212	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO21:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO22	3x00214 4x00214 I:213	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO22:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO23	3x00215 4x00215 I:214	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO23:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO24	3x00216 4x00216 I:215	0,0x0000 B:00 00			UINT16 R/O	
		Actual detection state of a current limit while ON for DO24:0=OK				
DIGITAL OUTPUTS: GLOBAL ERRORS						
CHIP #1:DO1-DO6						
GLOBAL ERRORS BIT 0	3x00217 4x00217 I:216	0,0x0000 B:00 00			UINT16 R/O	
		BIT 0:Internal under voltage detected:0=OK				
GLOBAL ERRORS BIT 1	3x00218 4x00218 I:217	0,0x0000 B:00 00			UINT16 R/O	

		BIT 1:VA under voltage detected (<2.3V):0=OK				
GLOBAL ERRORS BIT 2	3x00219 4x00219 I:218	1,0x0001 B:00 01			UINT16 R/O	
		BIT 2:VDD not good detected (<17V):1=FAULT				
GLOBAL ERRORS BIT 3	3x00220 4x00220 I:219	1,0x0001 B:00 01			UINT16 R/O	
		BIT 3:VDD warning detected (<12V):1=FAULT				
GLOBAL ERRORS BIT 4	3x00221 4x00221 I:220	1,0x0001 B:00 01			UINT16 R/O	
		BIT 4:VDD under voltage detected (<8V):1=FAULT				
GLOBAL ERRORS BIT 5	3x00222 4x00222 I:221	0,0x0000 B:00 00			UINT16 R/O	
		BIT 5:Thermal shutdown:0=OK				
GLOBAL ERRORS BIT 6	3x00223 4x00223 I:222	0,0x0000 B:00 00			UINT16 R/O	
		BIT 6:Synchronisation error detected:0=OK				
GLOBAL ERRORS BIT 7	3x00224 4x00224 I:223	0,0x0000 B:00 00			UINT16 R/O	
		BIT 7:Watchdog error detected:0=OK				
The global error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
DIGITAL OUTPUTS: GLOBAL ERRORS						
CHIP #2:DO7-DO12						
GLOBAL ERRORS BIT 0	3x00225 4x00225 I:224	0,0x0000 B:00 00			UINT16 R/O	
		BIT 0:Internal under voltage detected:0=OK				
GLOBAL ERRORS BIT 1	3x00226 4x00226 I:225	0,0x0000 B:00 00			UINT16 R/O	
		BIT 1:VA under voltage detected (<2.3V):0=OK				
GLOBAL ERRORS BIT 2	3x00227 4x00227 I:226	1,0x0001 B:00 01			UINT16 R/O	
		BIT 2:VDD not good detected (<17V):1=FAULT				
GLOBAL ERRORS BIT 3	3x00228 4x00228 I:227	1,0x0001 B:00 01			UINT16 R/O	
		BIT 3:VDD warning detected (<12V):1=FAULT				

GLOBAL ERRORS BIT 4	3x00229 4x00229 I:228	1,0x0001 B:00 01			UINT16 R/O	
		BIT 4:VDD under voltage detected (<8V):1=FAULT				
GLOBAL ERRORS BIT 5	3x00230 4x00230 I:229	0,0x0000 B:00 00			UINT16 R/O	
		BIT 5:Thermal shutdown:0=OK				
GLOBAL ERRORS BIT 6	3x00231 4x00231 I:230	0,0x0000 B:00 00			UINT16 R/O	
		BIT 6:Synchronisation error detected:0=OK				
GLOBAL ERRORS BIT 7	3x00232 4x00232 I:231	0,0x0000 B:00 00			UINT16 R/O	
		BIT 7:Watchdog error detected:0=OK				
The global error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
DIGITAL OUTPUTS: GLOBAL ERRORS						
CHIP #3:DO13-DO18						
GLOBAL ERRORS BIT 0	3x00233 4x00233 I:232	0,0x0000 B:00 00			UINT16 R/O	
		BIT 0:Internal under voltage detected:0=OK				
GLOBAL ERRORS BIT 1	3x00234 4x00234 I:233	0,0x0000 B:00 00			UINT16 R/O	
		BIT 1:VA under voltage detected (<2.3V):0=OK				
GLOBAL ERRORS BIT 2	3x00235 4x00235 I:234	1,0x0001 B:00 01			UINT16 R/O	
		BIT 2:VDD not good detected (<17V):1=FAULT				
GLOBAL ERRORS BIT 3	3x00236 4x00236 I:235	1,0x0001 B:00 01			UINT16 R/O	
		BIT 3:VDD warning detected (<12V):1=FAULT				
GLOBAL ERRORS BIT 4	3x00237 4x00237 I:236	1,0x0001 B:00 01			UINT16 R/O	
		BIT 4:VDD under voltage detected (<8V):1=FAULT				
GLOBAL ERRORS BIT 5	3x00238 4x00238 I:237	0,0x0000 B:00 00			UINT16 R/O	
		BIT 5:Thermal shutdown:0=OK				

GLOBAL ERRORS BIT 6	3x00239 4x00239 I:238	0,0x0000 B:00 00			UINT16 R/O	
		BIT 6:Synchronisation error detected:0=OK				
GLOBAL ERRORS BIT 7	3x00240 4x00240 I:239	0,0x0000 B:00 00			UINT16 R/O	
		BIT 7:Watchdog error detected:0=OK				
The global error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
DIGITAL OUTPUTS: GLOBAL ERRORS						
CHIP #4:DO19-DO24						
GLOBAL ERRORS BIT 0	3x00241 4x00241 I:240	0,0x0000 B:00 00			UINT16 R/O	
		BIT 0:Internal under voltage detected:0=OK				
GLOBAL ERRORS BIT 1	3x00242 4x00242 I:241	0,0x0000 B:00 00			UINT16 R/O	
		BIT 1:VA under voltage detected (<2.3V):0=OK				
GLOBAL ERRORS BIT 2	3x00243 4x00243 I:242	1,0x0001 B:00 01			UINT16 R/O	
		BIT 2:VDD not good detected (<17V):1=FAULT				
GLOBAL ERRORS BIT 3	3x00244 4x00244 I:243	1,0x0001 B:00 01			UINT16 R/O	
		BIT 3:VDD warning detected (<12V):1=FAULT				
GLOBAL ERRORS BIT 4	3x00245 4x00245 I:244	1,0x0001 B:00 01			UINT16 R/O	
		BIT 4:VDD under voltage detected (<8V):1=FAULT				
GLOBAL ERRORS BIT 5	3x00246 4x00246 I:245	0,0x0000 B:00 00			UINT16 R/O	
		BIT 5:Thermal shutdown:0=OK				
GLOBAL ERRORS BIT 6	3x00247 4x00247 I:246	0,0x0000 B:00 00			UINT16 R/O	
		BIT 6:Synchronisation error detected:0=OK				
GLOBAL ERRORS BIT 7	3x00248 4x00248 I:247	0,0x0000 B:00 00			UINT16 R/O	
		BIT 7:Watchdog error detected:0=OK				
The global error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						

DIGITAL OUTPUTS: INTERRUPT STATUS**CHIP #1:DO1-DO6**

INTERRUPT STATUS BIT 0	3x00249 4x00249 I:248	0,0x0000 B:00 00			UINT16 R/O	
		BIT 0:Overload detected:0=OK				
INTERRUPT STATUS BIT 1	3x00250 4x00250 I:249	0,0x0000 B:00 00			UINT16 R/O	
		BIT 1:Current limit detected:0=OK				
INTERRUPT STATUS BIT 2	3x00251 4x00251 I:250	0,0x0000 B:00 00			UINT16 R/O	
		BIT 2:Open wire while OFF detected:0=OK				
INTERRUPT STATUS BIT 3	3x00252 4x00252 I:251	0,0x0000 B:00 00			UINT16 R/O	
		BIT 3:Open wire while ON detected:0=OK				
INTERRUPT STATUS BIT 4	3x00253 4x00253 I:252	0,0x0000 B:00 00			UINT16 R/O	
		BIT 4:Short to VDD while ON detected:0=OK				
INTERRUPT STATUS BIT 5	3x00254 4x00254 I:253	0,0x0000 B:00 00			UINT16 R/O	
		BIT 5:Thermal error detected-shutdown:0=OK				
INTERRUPT STATUS BIT 6	3x00255 4x00255 I:254	1,0x0001 B:00 01			UINT16 R/O	
		BIT 6:Supply error detected:1=FAULT				
INTERRUPT STATUS BIT 7	3x00256 4x00256 I:255	0,0x0000 B:00 00			UINT16 R/O	
		BIT 7:Communication error detected:0=OK				

The global interrupt error state for the output group. Each bit stands for a different error
 =0:No fault, =1:Fault

DIGITAL OUTPUTS: INTERRUPT STATUS**CHIP #2:DO7-DO12**

INTERRUPT STATUS BIT 0	3x00257 4x00257 I:256	0,0x0000 B:00 00			UINT16 R/O	
		BIT 0:Overload detected:0=OK				
INTERRUPT STATUS BIT 1	3x00258 4x00258 I:257	0,0x0000 B:00 00			UINT16 R/O	
		BIT 1:Current limit detected:0=OK				

INTERRUPT STATUS BIT 2	3x00259 4x00259 I:258	0,0x0000 B:00 00			UINT16 R/O	
		BIT 2:Open wire while OFF detected:0=OK				
INTERRUPT STATUS BIT 3	3x00260 4x00260 I:259	0,0x0000 B:00 00			UINT16 R/O	
		BIT 3:Open wire while ON detected:0=OK				
INTERRUPT STATUS BIT 4	3x00261 4x00261 I:260	0,0x0000 B:00 00			UINT16 R/O	
		BIT 4:Short to VDD while ON detected:0=OK				
INTERRUPT STATUS BIT 5	3x00262 4x00262 I:261	0,0x0000 B:00 00			UINT16 R/O	
		BIT 5:Thermal error detected-shutdown:0=OK				
INTERRUPT STATUS BIT 6	3x00263 4x00263 I:262	1,0x0001 B:00 01			UINT16 R/O	
		BIT 6:Supply error detected:1=FAULT				
INTERRUPT STATUS BIT 7	3x00264 4x00264 I:263	0,0x0000 B:00 00			UINT16 R/O	
		BIT 7:Communication error detected:0=OK				

The global interrupt error state for the output group. Each bit stands for a different error
=0:No fault, =1:Fault

DIGITAL OUTPUTS: INTERRUPT STATUS**CHIP #3:DO13-DO18**

INTERRUPT STATUS BIT 0	3x00265 4x00265 I:264	0,0x0000 B:00 00			UINT16 R/O	
		BIT 0:Overload detected:0=OK				
INTERRUPT STATUS BIT 1	3x00266 4x00266 I:265	0,0x0000 B:00 00			UINT16 R/O	
		BIT 1:Current limit detected:0=OK				
INTERRUPT STATUS BIT 2	3x00267 4x00267 I:266	0,0x0000 B:00 00			UINT16 R/O	
		BIT 2:Open wire while OFF detected:0=OK				
INTERRUPT STATUS BIT 3	3x00268 4x00268 I:267	0,0x0000 B:00 00			UINT16 R/O	
		BIT 3:Open wire while ON detected:0=OK				

INTERRUPT STATUS BIT 4	3x00269 4x00269 I:268	0,0x0000 B:00 00			UINT16 R/O	
		BIT 4:Short to VDD while ON detected:0=OK				
INTERRUPT STATUS BIT 5	3x00270 4x00270 I:269	0,0x0000 B:00 00			UINT16 R/O	
		BIT 5:Thermal error detected-shutdown:0=OK				
INTERRUPT STATUS BIT 6	3x00271 4x00271 I:270	1,0x0001 B:00 01			UINT16 R/O	
		BIT 6:Supply error detected:1=FAULT				
INTERRUPT STATUS BIT 7	3x00272 4x00272 I:271	0,0x0000 B:00 00			UINT16 R/O	
		BIT 7:Communication error detected:0=OK				
The global interrupt error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
DIGITAL OUTPUTS: INTERRUPT STATUS						
CHIP #4:DO19-DO24						
INTERRUPT STATUS BIT 0	3x00273 4x00273 I:272	0,0x0000 B:00 00			UINT16 R/O	
		BIT 0:Overload detected:0=OK				
INTERRUPT STATUS BIT 1	3x00274 4x00274 I:273	0,0x0000 B:00 00			UINT16 R/O	
		BIT 1:Current limit detected:0=OK				
INTERRUPT STATUS BIT 2	3x00275 4x00275 I:274	0,0x0000 B:00 00			UINT16 R/O	
		BIT 2:Open wire while OFF detected:0=OK				
INTERRUPT STATUS BIT 3	3x00276 4x00276 I:275	0,0x0000 B:00 00			UINT16 R/O	
		BIT 3:Open wire while ON detected:0=OK				
INTERRUPT STATUS BIT 4	3x00277 4x00277 I:276	0,0x0000 B:00 00			UINT16 R/O	
		BIT 4:Short to VDD while ON detected:0=OK				
INTERRUPT STATUS BIT 5	3x00278 4x00278 I:277	0,0x0000 B:00 00			UINT16 R/O	
		BIT 5:Thermal error detected-shutdown:0=OK				

INTERRUPT STATUS BIT 6	3x00279 4x00279 I:278	1,0x0001 B:00 01			UINT16 R/O	
		BIT 6:Supply error detected:1=FAULT				
INTERRUPT STATUS BIT 7	3x00280 4x00280 I:279	0,0x0000 B:00 00			UINT16 R/O	
		BIT 7:Communication error detected:0=OK				
The global interrupt error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
SPI COMMUNICATION DIGITAL OUTPUTS						
SPI COMMUNICATION CHIP #1: DO1-DO6	3x00281 4x00281 I:280	0,0x0000 B:00 00			UINT16 R/O	
		Actual SPI communication state:0=NO FAULT				
The current monitoring state of the SPI communication for the digital output group =0:No fault, =1:Fault						
SPI COMMUNICATION CHIP #2: DO7-DO12	3x00282 4x00282 I:281	0,0x0000 B:00 00			UINT16 R/O	
		Actual SPI communication state:0=NO FAULT				
The current monitoring state of the SPI communication for the digital output group =0:No fault, =1:Fault						
SPI COMMUNICATION CHIP #3: DO13-DO18	3x00283 4x00283 I:282	0,0x0000 B:00 00			UINT16 R/O	
		Actual SPI communication state:0=NO FAULT				
The current monitoring state of the SPI communication for the digital output group =0:No fault, =1:Fault						
SPI COMMUNICATION CHIP #4: DO19-DO24	3x00284 4x00284 I:283	0,0x0000 B:00 00			UINT16 R/O	
		Actual SPI communication state:0=NO FAULT				
STATUS OF DIGITAL OUTPUTS						
STATUS OF ALL DOS DO1-DO16	3x10001 4x10001 I:10000	0,0x0000 B:00 00		0xFFFF	UINT16 R/W	NO
		Actual state of DO1:0=OFF		1		
		Actual state of DO2:0=OFF		1		
		Actual state of DO3:0=OFF		1		
		Actual state of DO4:0=OFF		1		
		Actual state of DO5:0=OFF		1		
		Actual state of DO6:0=OFF		1		
		Actual state of DO7:0=OFF		1		
		Actual state of DO8:0=OFF		1		
		Actual state of DO9:0=OFF		1		

		Actual state of DO10:0=OFF	1		
		Actual state of DO11:0=OFF	1		
		Actual state of DO12:0=OFF	1		
		Actual state of DO13:0=OFF	1		
		Actual state of DO14:0=OFF	1		
		Actual state of DO15:0=OFF	1		
		Actual state of DO16:0=OFF	1		
Actual state of all digital outputs Bit 0: =0:DO1 is OFF, =1:DO1 is ON Bit 1: =0:DO2 is OFF, =1:DO2 is ON ... Bit 14: =0:DO15 is OFF, =1:DO15 is ON Bit 15: =0:DO16 is OFF, =1:DO16 is ON Write on this register sets all digital outputs to a new state					
STATUS OF ALL DOS DO17-DO24	3x10002 4x10002 l:10001	0,0x0000 B:00 00		0x00FF	UINT16 R/W NO
		Actual state of DO17:0=OFF	1		
		Actual state of DO18:0=OFF	1		
		Actual state of DO19:0=OFF	1		
		Actual state of DO20:0=OFF	1		
		Actual state of DO21:0=OFF	1		
		Actual state of DO22:0=OFF	1		
		Actual state of DO23:0=OFF	1		
		Actual state of DO24:0=OFF	1		
Actual state of all digital outputs Bit 0: =0:DO17 is OFF, =1:DO17 is ON Bit 1: =0:DO28 is OFF, =1:DO18 is ON ... Bit 6: =0:DO23 is OFF, =1:DO23 is ON Bit 7: =0:DO24 is OFF, =1:DO24 is ON Write on this register sets all digital outputs to a new state					
DIGITAL OUTPUTS:ENABLE OPEN WIRE DETECTION WHILE ON					
ENABLE OPEN WIRE DETECTION WHILE ON DO1-DO16	3x10003 4x10003 l:10002	0,0x0000 B:00 00		0xFFFF	UINT16 R/W NO
		Actual setup of open wire detection while ON for DO1:0=DISABLED	1		
		Actual setup of open wire detection while ON for DO2:0=DISABLED	1		
		Actual setup of open wire detection while ON for DO3:0=DISABLED	1		
		Actual setup of open wire detection while ON for DO4:0=DISABLED	1		
		Actual setup of open wire detection while ON for DO5:0=DISABLED	1		

		Actual setup of open wire detection while ON for DO6:0=DISABLED	1		
		Actual setup of open wire detection while ON for DO7:0=DISABLED	1		
		Actual setup of open wire detection while ON for DO8:0=DISABLED	1		
		Actual setup of open wire detection while ON for DO9:0=DISABLED	1		
		Actual setup of open wire detection while ON for DO10:0=DISABLED	1		
		Actual setup of open wire detection while ON for DO11:0=DISABLED	1		
		Actual setup of open wire detection while ON for DO12:0=DISABLED	1		
		Actual setup of open wire detection while ON for DO13:0=DISABLED	1		
		Actual setup of open wire detection while ON for DO14:0=DISABLED	1		
		Actual setup of open wire detection while ON for DO15:0=DISABLED	1		
		Actual setup of open wire detection while ON for DO16:0=DISABLED	1		

Actual setup state for open wire detection while ON for digital output DOx

Bit 0: =0:Open wire detection for DO1 is DISABLED, =1:Open wire detection for DO1 is ENABLED

Bit 1: =0:Open wire detection for DO2 is DISABLED, =1:Open wire detection for DO2 is ENABLED

...

Bit 14: =0:Open wire detection for DO15 is DISABLED, =1:Open wire detection for DO15 is ENABLED

Bit 15: =0:Open wire detection for DO16 is DISABLED, =1:Open wire detection for DO16 is ENABLED

Write on this register sets for all digital outputs a new setup state

ENABLE OPEN WIRE DETECTION WHILE ON DO17-DO24	3x10004 4x10004 I:10003	0,0x0000 B:00 00		0x00FF	UINT16 R/W	NO
		Actual setup of open wire detection while ON for DO17:0=DISABLED	1			
		Actual setup of open wire detection while ON for DO18:0=DISABLED	1			
		Actual setup of open wire detection while ON for DO19:0=DISABLED	1			
		Actual setup of open wire detection while ON for DO20:0=DISABLED	1			
		Actual setup of open wire detection while ON for DO21:0=DISABLED	1			
		Actual setup of open wire detection while ON for DO22:0=DISABLED	1			
		Actual setup of open wire detection while ON for DO23:0=DISABLED	1			

		Actual setup of open wire detection while ON for DO24:0=DISABLED	1		
Actual setup state for open wire detection while ON for digital output DOx Bit 0: =0:Open wire detection for DO17 is DISABLED, =1:Open wire detection for DO17 is ENABLED Bit 1: =0:Open wire detection for DO18 is DISABLED, =1:Open wire detection for DO18 is ENABLED ... Bit 6: =0:Open wire detection for DO23 is DISABLED, =1:Open wire detection for DO23 is ENABLED Bit 7: =0:Open wire detection for DO24 is DISABLED, =1:Open wire detection for DO24 is ENABLED					
Write on this register sets for all digital outputs a new setup state					
DIGITAL OUTPUTS:ENABLE OPEN WIRE DETECTION WHILE OFF					
ENABLE OPEN WIRE DETECTION WHILE OFF DO1-DO16	3x10005 4x10005 1:10004	0,0x0000 B:00 00		0xFFFF	UINT16 R/W NO
		Actual setup of open wire detection while OFF for DO1:0=DISABLED	1		
		Actual setup of open wire detection while OFF for DO2:0=DISABLED	1		
		Actual setup of open wire detection while OFF for DO3:0=DISABLED	1		
		Actual setup of open wire detection while OFF for DO4:0=DISABLED	1		
		Actual setup of open wire detection while OFF for DO5:0=DISABLED	1		
		Actual setup of open wire detection while OFF for DO6:0=DISABLED	1		
		Actual setup of open wire detection while OFF for DO7:0=DISABLED	1		
		Actual setup of open wire detection while OFF for DO8:0=DISABLED	1		
		Actual setup of open wire detection while OFF for DO9:0=DISABLED	1		
		Actual setup of open wire detection while OFF for DO10:0=DISABLED	1		
		Actual setup of open wire detection while OFF for DO11:0=DISABLED	1		
		Actual setup of open wire detection while OFF for DO12:0=DISABLED	1		
		Actual setup of open wire detection while OFF for DO13:0=DISABLED	1		
		Actual setup of open wire detection while OFF for DO14:0=DISABLED	1		
		Actual setup of open wire detection while OFF for DO15:0=DISABLED	1		
		Actual setup of open wire detection while OFF for DO16:0=DISABLED	1		

Actual setup state for open wire detection while OFF for digital output DOx

Bit 0: =0:Open wire detection for DO1 is DISABLED, =1:Open wire detection for DO1 is ENABLED

Bit 1: =0:Open wire detection for DO2 is DISABLED, =1:Open wire detection for DO2 is ENABLED

...

Bit 14: =0:Open wire detection for DO15 is DISABLED, =1:Open wire detection for DO15 is ENABLED

Bit 15: =0:Open wire detection for DO16 is DISABLED, =1:Open wire detection for DO16 is ENABLED

Write on this register sets for all digital outputs a new setup state

ENABLE OPEN WIRE DETECTION WHILE OFF DO17-DO24	3x10006 4x10006 I:10005	0,0x0000 B:00 00		0x00FF	UINT16 R/W	NO
		Actual setup of open wire detection while OFF for DO17:0=DISABLED	1			
		Actual setup of open wire detection while OFF for DO18:0=DISABLED	1			
		Actual setup of open wire detection while OFF for DO19:0=DISABLED	1			
		Actual setup of open wire detection while OFF for DO20:0=DISABLED	1			
		Actual setup of open wire detection while OFF for DO21:0=DISABLED	1			
		Actual setup of open wire detection while OFF for DO22:0=DISABLED	1			
		Actual setup of open wire detection while OFF for DO23:0=DISABLED	1			
		Actual setup of open wire detection while OFF for DO24:0=DISABLED	1			

Actual setup state for open wire detection while OFF for digital output DOx

Bit 0: =0:Open wire detection for DO17 is DISABLED, =1:Open wire detection for DO17 is ENABLED

Bit 1: =0:Open wire detection for DO18 is DISABLED, =1:Open wire detection for DO18 is ENABLED

...

Bit 6: =0:Open wire detection for DO23 is DISABLED, =1:Open wire detection for DO23 is ENABLED

Bit 7: =0:Open wire detection for DO24 is DISABLED, =1:Open wire detection for DO24 is ENABLED

Write on this register sets for all digital outputs a new setup state

DIGITAL OUTPUTS:ENABLE SHORTCUT TO VDD DETECTION WHILE OFF

ENABLE SHORTCUT TO VDD DETECTION WHILE OFF DO1-DO16	3x10007 4x10007 I:10006	0,0x0000 B:00 00		0xFFFF	UINT16 R/W	NO
		Actual setup of shortcut detection to VDD while OFF for DO1:0=DISABLED	1			
		Actual setup of shortcut detection to VDD while OFF for DO2:0=DISABLED	1			
		Actual setup of shortcut detection to VDD while OFF for DO3:0=DISABLED	1			
		Actual setup of shortcut detection to VDD while OFF for DO4:0=DISABLED	1			

		Actual setup of shortcut detection to VDD while OFF for DO5:0=DISABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO6:0=DISABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO7:0=DISABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO8:0=DISABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO9:0=DISABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO10:0=DISABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO11:0=DISABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO12:0=DISABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO13:0=DISABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO14:0=DISABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO15:0=DISABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO16:0=DISABLED	1		

Actual setup state for shortcut to VDD detection while OFF for digital output DOx

Bit 0: =0:Shortcut to VDD detection for DO1 is DISABLED, =1:Shortcut to VDD detection for DO1 is ENABLED

Bit 1: =0:Shortcut to VDD detection for DO2 is DISABLED, =1:Shortcut to VDD detection for DO2 is ENABLED

...

Bit 14: =0:Shortcut to VDD detection for DO15 is DISABLED, =1:Shortcut to VDD detection for DO15 is ENABLED

Bit 15: =0:Shortcut to VDD detection for DO16 is DISABLED, =1:Shortcut to VDD detection for DO16 is ENABLED

Write on this register sets for all digital outputs a new setup state

ENABLE SHORTCUT TO VDD DETECTION WHILE OFF DO17-DO24	3x10008 4x10008 1:10007	0,0x0000 B:00 00		0x00FF	UINT16 R/W	NO
		Actual setup of shortcut detection to VDD while OFF for DO17:0=DISABLED	1			
		Actual setup of shortcut detection to VDD while OFF for DO18:0=DISABLED	1			
		Actual setup of shortcut detection to VDD while OFF for DO19:0=DISABLED	1			
		Actual setup of shortcut detection to VDD while OFF for DO20:0=DISABLED	1			
		Actual setup of shortcut detection to VDD while OFF for DO21:0=DISABLED	1			
		Actual setup of shortcut detection to VDD while OFF for DO22:0=DISABLED	1			

		Actual setup of shortcut detection to VDD while OFF for DO23:0=DISABLED	1		
		Actual setup of shortcut detection to VDD while OFF for DO24:0=DISABLED	1		
Actual setup state for shortcut to VDD detection while OFF for digital output DOx Bit 17: =0:Shortcut to VDD detection for DO17 is DISABLED, =1:Shortcut to VDD detection for DO17 is ENABLED Bit 18: =0:Shortcut to VDD detection for DO18 is DISABLED, =1:Shortcut to VDD detection for DO18 is ENABLED ... Bit 6: =0:Shortcut to VDD detection for DO23 is DISABLED, =1:Shortcut to VDD detection for DO23 is ENABLED Bit 7: =0:Shortcut to VDD detection for DO24 is DISABLED, =1:Shortcut to VDD detection for DO24 is ENABLED					
Write on this register sets for all digital outputs a new setup state					
DIGITAL OUTPUTS:OPEN WIRE WHILE ON DETECTION STATE					
OPEN WIRE DETECTION STATE WHILE ON DO1-DO16	3x10009 4x10009 1:10008	0,0x0000 B:00 00			UINT16 R/O
		Actual state of open wire detection while ON for DO1:0=OFF			
		Actual state of open wire detection while ON for DO2:0=OFF			
		Actual state of open wire detection while ON for DO3:0=OFF			
		Actual state of open wire detection while ON for DO4:0=OFF			
		Actual state of open wire detection while ON for DO5:0=OFF			
		Actual state of open wire detection while ON for DO6:0=OFF			
		Actual state of open wire detection while ON for DO7:0=OFF			
		Actual state of open wire detection while ON for DO8:0=OFF			
		Actual state of open wire detection while ON for DO9:0=OFF			
		Actual state of open wire detection while ON for DO10:0=OFF			
		Actual state of open wire detection while ON for DO11:0=OFF			
		Actual state of open wire detection while ON for DO12:0=OFF			
		Actual state of open wire detection while ON for DO13:0=OFF			
		Actual state of open wire detection while ON for DO14:0=OFF			
		Actual state of open wire detection while ON for DO15:0=OFF			

		Actual state of open wire detection while ON for DO16:0=OFF			
Actual diagnostic state for open wire detection while ON for digital output DOx Bit 0: =0:Output DO1 is OK, =1:Fault-Open wire detected on DO1 Bit 1: =0:Output DO2 is OK, =1:Fault-Open wire detected on DO2 ... Bit 14: =0:Output DO15 is OK, =1:Fault-Open wire detected on DO15 Bit 15: =0:Output DO16 is OK, =1:Fault-Open wire detected on DO16					
OPEN WIRE DETECTION STATE WHILE ON DO17-DO24	3x10010 4x10010 I:10009	0,0x0000 B:00 00			UINT16 R/O
		Actual state of open wire detection while ON for DO17:0=OFF			
		Actual state of open wire detection while ON for DO18:0=OFF			
		Actual state of open wire detection while ON for DO19:0=OFF			
		Actual state of open wire detection while ON for DO20:0=OFF			
		Actual state of open wire detection while ON for DO21:0=OFF			
		Actual state of open wire detection while ON for DO22:0=OFF			
		Actual state of open wire detection while ON for DO23:0=OFF			
		Actual state of open wire detection while ON for DO24:0=OFF			
Actual diagnostic state for open wire detection while ON for digital output DOx Bit 0: =0:Output DO17 is OK, =1:Fault-Open wire detected on DO17 Bit 1: =0:Output DO18 is OK, =1:Fault-Open wire detected on DO18 ... Bit 6: =0:Output DO23 is OK, =1:Fault-Open wire detected on DO23 Bit 7: =0:Output DO24 is OK, =1:Fault-Open wire detected on DO24					
DIGITAL OUTPUTS:OPEN WIRE WHILE OFF DETECTION STATE					
OPEN WIRE DETECTION STATE WHILE OFF DO1-DO16	3x10011 4x10011 I:10010	0,0x0000 B:00 00			UINT16 R/O
		Actual state of open wire detection while OFF for DO1:0=OFF			
		Actual state of open wire detection while OFF for DO2:0=OFF			
		Actual state of open wire detection while OFF for DO3:0=OFF			
		Actual state of open wire detection while OFF for DO4:0=OFF			
		Actual state of open wire detection while OFF for DO5:0=OFF			

		Actual state of open wire detection while OFF for DO6:0=OFF			
		Actual state of open wire detection while OFF for DO7:0=OFF			
		Actual state of open wire detection while OFF for DO8:0=OFF			
		Actual state of open wire detection while OFF for DO9:0=OFF			
		Actual state of open wire detection while OFF for DO10:0=OFF			
		Actual state of open wire detection while OFF for DO11:0=OFF			
		Actual state of open wire detection while OFF for DO12:0=OFF			
		Actual state of open wire detection while OFF for DO13:0=OFF			
		Actual state of open wire detection while OFF for DO14:0=OFF			
		Actual state of open wire detection while OFF for DO15:0=OFF			
		Actual state of open wire detection while OFF for DO16:0=OFF			

Actual diagnostic state for open wire detection while OFF for digital output DOx

Bit 0: =0:Output DO1 is OK, =1:Fault-Open wire detected on DO1

Bit 1: =0:Output DO2 is OK, =1:Fault-Open wire detected on DO2

...

Bit 14: =0:Output DO15 is OK, =1:Fault-Open wire detected on DO15

Bit 15: =0:Output DO16 is OK, =1:Fault-Open wire detected on DO16

OPEN WIRE DETECTION STATE WHILE OFF DO17-DO24	3x10012 4x10012 1:10011	0,0x0000 B:00 00			UINT16 R/O
		Actual state of open wire detection while OFF for DO17:0=OFF			
		Actual state of open wire detection while OFF for DO18:0=OFF			
		Actual state of open wire detection while OFF for DO19:0=OFF			
		Actual state of open wire detection while OFF for DO20:0=OFF			
		Actual state of open wire detection while OFF for DO21:0=OFF			
		Actual state of open wire detection while OFF for DO22:0=OFF			
		Actual state of open wire detection while OFF for DO23:0=OFF			

		Actual state of open wire detection while OFF for DO24:0=OFF			
Actual diagnostic state for open wire detection while OFF for digital output DOx Bit 0: =0:Output DO17 is OK, =1:Fault-Open wire detected on DO17 Bit 1: =0:Output DO18 is OK, =1:Fault-Open wire detected on DO18 ... Bit 6: =0:Output DO23 is OK, =1:Fault-Open wire detected on DO23 Bit 7: =0:Output DO24 is OK, =1:Fault-Open wire detected on DO24					
DIGITAL OUTPUTS:SHORTCUT TO VDD WHILE OFF DETECTION STATE					
SHORTCUT TO VDD WHILE OFF DETECTION STATE DO1-DO16	3x10013 4x10013 1:10012	0,0x0000 B:00 00			UINT16 R/O
		Actual state of shortcut to VDD detection while OFF for DO1:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO2:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO3:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO4:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO5:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO6:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO7:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO8:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO9:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO10:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO11:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO12:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO13:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO14:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO15:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO16:0=OFF			

Actual diagnostic state for shortcut to VDD detection while OFF for digital output DOx Bit 0: =0:Output DO1 is OK, =1:Fault-shortcut detected on DO1 Bit 1: =0:Output DO2 is OK, =1:Fault-shortcut detected on DO2 ... Bit 14: =0:Output DO15 is OK, =1:Fault-shortcut detected on DO15 Bit 15: =0:Output DO16 is OK, =1:Fault-shortcut detected on DO16					
SHORTCUT TO VDD WHILE OFF DETECTION STATE DO17-DO24	3x10014 4x10014 l:10013	0,0x0000 B:00 00			UINT16 R/O
		Actual state of shortcut to VDD detection while OFF for DO17:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO18:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO19:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO20:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO21:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO22:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO23:0=OFF			
		Actual state of shortcut to VDD detection while OFF for DO24:0=OFF			
Actual diagnostic state for shortcut to VDD detection while OFF for digital output DOx Bit 0: =0:Output DO17 is OK, =1:Fault-shortcut detected on DO17 Bit 1: =0:Output DO18 is OK, =1:Fault-shortcut detected on DO18 ... Bit 6: =0:Output DO23 is OK, =1:Fault-shortcut detected on DO23 Bit 7: =0:Output DO24 is OK, =1:Fault-shortcut detected on DO24					
DIGITAL OUTPUTS:THERMAL OVERLOAD DETECTION STATE					
THERMAL OVERLOAD DETECTION STATE DO1-DO16	3x10015 4x10015 l:10014	0,0x0000 B:00 00			UINT16 R/O
		Actual thermal overload detection state for DO1:0=OFF			
		Actual thermal overload detection state for DO2:0=OFF			
		Actual thermal overload detection state for DO3:0=OFF			
		Actual thermal overload detection state for DO4:0=OFF			
		Actual thermal overload detection state for DO5:0=OFF			
		Actual thermal overload detection state for DO6:0=OFF			

		Actual thermal overload detection state for DO7:0=OFF			
		Actual thermal overload detection state for DO8:0=OFF			
		Actual thermal overload detection state for DO9:0=OFF			
		Actual thermal overload detection state for DO10:0=OFF			
		Actual thermal overload detection state for DO11:0=OFF			
		Actual thermal overload detection state for DO12:0=OFF			
		Actual thermal overload detection state for DO13:0=OFF			
		Actual thermal overload detection state for DO14:0=OFF			
		Actual thermal overload detection state for DO15:0=OFF			
		Actual thermal overload detection state for DO16:0=OFF			
Actual thermal overload detection state for digital output DOx Bit 0: =0:Output DO1 is OK, =1:Fault-Thermal overload on DO1 Bit 1: =0:Output DO2 is OK, =1:Fault-Thermal overload on DO2 ... Bit 14: =0:Output DO15 is OK, =1:Fault-Thermal overload on DO15 Bit 15: =0:Output DO16 is OK, =1:Fault-Thermal overload on DO16					
THERMAL OVERLOAD DETECTION STATE DO17-DO24	3x10016 4x10016 1:10015	0,0x0000 B:00 00		UINT16 R/O	
		Actual thermal overload detection state for DO17:0=OFF			
		Actual thermal overload detection state for DO18:0=OFF			
		Actual thermal overload detection state for DO19:0=OFF			
		Actual thermal overload detection state for DO20:0=OFF			
		Actual thermal overload detection state for DO21:0=OFF			
		Actual thermal overload detection state for DO22:0=OFF			
		Actual thermal overload detection state for DO23:0=OFF			
		Actual thermal overload detection state for DO24:0=OFF			

Actual thermal overload detection state for digital output DOx
Bit 0: =0:Output DO17 is OK, =1:Fault-Thermal overload on DO17
Bit 1: =0:Output DO18 is OK, =1:Fault-Thermal overload on DO18
...
Bit 6: =0:Output DO23 is OK, =1:Fault-Thermal overload on DO23
Bit 7: =0:Output DO24 is OK, =1:Fault-Thermal overload on DO24

DIGITAL OUTPUTS:CURRENT LIMIT DETECTION STATE

CURRENT LIMIT DETECTION STATE DO1-DO16	3x10017 4x10017 l:10016	0,0x0000 B:00 00			UINT16 R/O	
		Actual current limit detection state for DO1:0=OFF				
		Actual current limit detection state for DO2:0=OFF				
		Actual current limit detection state for DO3:0=OFF				
		Actual current limit detection state for DO4:0=OFF				
		Actual current limit detection state for DO5:0=OFF				
		Actual current limit detection state for DO6:0=OFF				
		Actual current limit detection state for DO7:0=OFF				
		Actual current limit detection state for DO8:0=OFF				
		Actual current limit detection state for DO9:0=OFF				
		Actual current limit detection state for DO10:0=OFF				
		Actual current limit detection state for DO11:0=OFF				
		Actual current limit detection state for DO12:0=OFF				
		Actual current limit detection state for DO13:0=OFF				
		Actual current limit detection state for DO14:0=OFF				
		Actual current limit detection state for DO15:0=OFF				
		Actual current limit detection state for DO16:0=OFF				

Actual current limit detection state for digital output DOx Bit 0: =0:Output DO1 is OK, =1:Fault-Current limit on DO1 Bit 1: =0:Output DO2 is OK, =1:Fault-Current limit on DO2 ... Bit 14: =0:Output DO15 is OK, =1:Fault-Current limit on DO15 Bit 15: =0:Output DO16 is OK, =1:Fault-Current limit on DO16						
CURRENT LIMIT DETECTION STATE DO17-DO24	3x10018 4x10018 l:10017	0,0x0000 B:00 00			UINT16 R/O	
		Actual current limit detection state for DO17:0=OFF				
		Actual current limit detection state for DO18:0=OFF				
		Actual current limit detection state for DO19:0=OFF				
		Actual current limit detection state for DO20:0=OFF				
		Actual current limit detection state for DO21:0=OFF				
		Actual current limit detection state for DO22:0=OFF				
		Actual current limit detection state for DO23:0=OFF				
		Actual current limit detection state for DO24:0=OFF				
Actual current limit detection state for digital output DOx Bit 0: =0:Output DO17 is OK, =1:Fault-Current limit on DO17 Bit 1: =0:Output DO18 is OK, =1:Fault-Current limit on DO18 ... Bit 6: =0:Output DO23 is OK, =1:Fault-Current limit on DO23 Bit 7: =0:Output DO24 is OK, =1:Fault-Current limit on DO24						
DIGITAL OUTPUTS: GLOBAL ERRORS						
CHIP #1:DO1-DO6						
CHIP #2:DO7-DO12						
GLOBAL ERRORS FOR CHIP #1+#2	3x10019 4x10019 l:10018	0,0x0000 B:00 00			UINT16 R/O	
		BIT 0:CHIP#1:Internal under voltage detected:0=OK				
		BIT 1:CHIP#1:VA under voltage detected (<2.3V):0=OK				
		BIT 2:CHIP#1:VDD not good detected (<17V):0=OK				
		BIT 3:CHIP#1:VDD warning detected (<12V):0=OK				
		BIT 4:CHIP#1:VDD under voltage detected (<8V):0=OK				
		BIT 5:CHIP#1:Thermal shutdown:0=OK				
		BIT 6:CHIP#1:Synchronisation error detected:0=OK				
		BIT 7:CHIP#1:Watchdog error detected:0=OK				
		BIT 8:CHIP#2:Internal under voltage detected:0=OK				
		BIT 9:CHIP#2:VA under voltage detected (<2.3V):0=OK				

		BIT 10:CHIP#2:VDD not good detected (<17V):0=OK		
		BIT 11:CHIP#2:VDD warning detected (<12V):0=OK		
		BIT 12:CHIP#2:VDD under voltage detected (<8V):0=OK		
		BIT 13:CHIP#2:Thermal shutdown:0=OK		
		BIT 14:CHIP#2:Synchronisation error detected:0=OK		
		BIT 15:CHIP#2:Watchdog error detected:0=OK		

The global error state for the output group. Each bit stands for a different error

=0:No fault, =1:Fault

CHIP #3:DO13-DO18

CHIP #4:DO19-DO24

GLOBAL ERRORS FOR CHIP #3+#4	3x10020 4x10020 I:10019	7196,0x1C1C B:1C 1C		UINT16 R/O	
		BIT 0:CHIP#3:Internal under voltage detected:0=OK			
		BIT 1:CHIP#3:VA under voltage detected (<2.3V):0=OK			
		BIT 2:CHIP#3:VDD not good detected (<17V):1=FAULT			
		BIT 3:CHIP#3:VDD warning detected (<12V):1=FAULT			
		BIT 4:CHIP#3:VDD under voltage detected (<8V):1=FAULT			
		BIT 5:CHIP#3:Thermal shutdown:0=OK			
		BIT 6:CHIP#3:Synchronisation error detected:0=OK			
		BIT 7:CHIP#3:Watchdog error detected:0=OK			
		BIT 8:CHIP#4:Internal under voltage detected:0=OK			
		BIT 9:CHIP#4:VA under voltage detected (<2.3V):0=OK			
		BIT 10:CHIP#4:VDD not good detected (<17V):1=FAULT			
		BIT 11:CHIP#4:VDD warning detected (<12V):1=FAULT			
		BIT 12:CHIP#4:VDD under voltage detected (<8V):1=FAULT			
		BIT 13:CHIP#4:Thermal shutdown:0=OK			
		BIT 14:CHIP#4:Synchronisation error detected:0=OK			
		BIT 15:CHIP#4:Watchdog error detected:0=OK			

The global error state for the output group. Each bit stands for a different error

=0:No fault, =1:Fault

DIGITAL OUTPUTS: INTERRUPT STATUS

CHIP #1:DO1-DO6

CHIP #2:DO7-DO12

INTERRUPT STATUS FOR CHIP#1+#2	3x10021 4x10021 I:10020	7196,0x1C1C B:1C 1C		UINT16 R/O	
		BIT 0:CHIP#1:Overload detected:0=OK			
		BIT 1:CHIP#1:Current limit detected:0=OK			
		BIT 2:CHIP#1:Open wire while OFF detected:1=FAULT			
		BIT 3:CHIP#1:Open wire while ON detected:1=FAULT			
		BIT 4:CHIP#1:Shortcut to VDD detected:1=FAULT			
		BIT 5:CHIP#1:Thermal shutdown:0=OK			
		BIT 6:CHIP#1:Supply error detected:0=OK			
		BIT 7:CHIP#1:Communication error detected:0=OK			
		BIT 8:CHIP#2:Overload detected:0=OK			

		BIT 9:CHIP#2:Current limit detected:0=OK		
		BIT 10:CHIP#2:Open wire while OFF detected:1=FAULT		
		BIT 11:CHIP#2:Open wire while ON detected:1=FAULT		
		BIT 12:CHIP#2:Shortcut to VDD detected:1=FAULT		
		BIT 13:CHIP#2:Thermal shutdown:0=OK		
		BIT 14:CHIP#2:Supply error detected:0=OK		
		BIT 15:CHIP#2:Communication error detected:0=OK		
The interrupt state for the output group. Each bit stands for a different error =0:No fault, =1:Fault				
CHIP #3:DO13-DO18				
CHIP #4:DO19-DO24				
INTERRUPT STATUS FOR CHIP#3+#4	3x10022 4x10022 I:10021	16448,0x4040 B:40 40		UINT16 R/O
		BIT 0:CHIP#3:Overload detected:0=OK		
		BIT 1:CHIP#3:Current limit detected:0=OK		
		BIT 2:CHIP#3:Open wire while OFF detected:0=OK		
		BIT 3:CHIP#3:Open wire while ON detected:0=OK		
		BIT 4:CHIP#3:Shortcut to VDD detected:0=OK		
		BIT 5:CHIP#3:Thermal shutdown:0=OK		
		BIT 6:CHIP#3:Supply error detected:1=FAULT		
		BIT 7:CHIP#3:Communication error detected:0=OK		
		BIT 8:CHIP#4:Overload detected:0=OK		
		BIT 9:CHIP#4:Current limit detected:0=OK		
		BIT 10:CHIP#4:Open wire while OFF detected:0=OK		
		BIT 11:CHIP#4:Open wire while ON detected:0=OK		
		BIT 12:CHIP#4:Shortcut to VDD detected:0=OK		
		BIT 13:CHIP#4:Thermal shutdown:0=OK		
		BIT 14:CHIP#4:Supply error detected:1=FAULT		
		BIT 15:CHIP#4:Communication error detected:0=OK		
The interrupt state for the output group. Each bit stands for a different error =0:No fault, =1:Fault				
SPI COMMUNICATION DIGITAL OUTPUTS				
CHIP #1:DO1-DO6				
CHIP #2:DO7-DO12				
CHIP #3:DO13-DO18				
CHIP #4:DO19-DO24				
SPI COMMUNICATION DIGITAL OUTPUTS	3x10023 4x10023 I:10022	16448,0x4040 B:40 40		UINT16 R/O
		Actual SPI communcation state of CHIP#1:0=OK		
		Actual SPI communcation state of CHIP#2:0=OK		
		Actual SPI communcation state of CHIP#3:0=OK		
		Actual SPI communcation state of CHIP#4:0=OK		

The current monitoring state of the SPI communication for the digital output group =0:No fault, =1:Fault						
Current SPI communication state of all digital output groups Bit x: =0:CHIP x has no fault, =1:CHIP x SPI Fault						
DIGITAL OUTPUTS: NUMBER OF CHIPSET						
NUMBER OF DIGITAL OUTPUT CHIPS	3x10099 4x10099 I:10098	4,0x0004 B:00 04			UINT16 R/O	
	Actual number of installed DO CHIPS:4					
The actual number of used output chips						
DIGITAL OUTPUTS: CHIPSET TYPE						
DIGITAL OUTPUTS CHIPSET TYPE	3x10100 4x10100 I:10099	1,0x0001 B:00 01			UINT16 R/O	
	Actual chipset for DOs:1=MAX14915					
The current chipset for the digital outputs: =0: NCV7608 =1: MAX14915						
STATUS OF DIGITAL OUTPUTS						
REAL STATUS OF ALL DOS DO1-DO16	3x10501 4x10501 I:10500	0,0x0000 B:00 00			UINT16 R/O	
		Real state of DO1:0=OFF				
		Real state of DO2:0=OFF				
		Real state of DO3:0=OFF				
		Real state of DO4:0=OFF				
		Real state of DO5:0=OFF				
		Real state of DO6:0=OFF				
		Real state of DO7:0=OFF				
		Real state of DO8:0=OFF				
		Real state of DO9:0=OFF				
		Real state of DO10:0=OFF				
		Real state of DO11:0=OFF				
		Real state of DO12:0=OFF				
		Real state of DO13:0=OFF				
		Real state of DO14:0=OFF				
		Real state of DO15:0=OFF				
		Real state of DO16:0=OFF				
Actual state of all digital outputs in the DO chips Bit 0: =0:DO1 is OFF, =1:DO1 is ON Bit 1: =0:DO2 is OFF, =1:DO2 is ON ... Bit 14: =0:DO15 is OFF, =1:DO15 is ON Bit 15: =0:DO16 is OFF, =1:DO16 is ON						
Write on this register sets all digital outputs to a new state						

REAL STATUS OF ALL DOS DO17-DO24	3x10502 4x10502 I:10501	0,0x0000 B:00 00			UINT16 R/O	
		Real state of DO17:0=OFF				
		Real state of DO18:0=OFF				
		Real state of DO19:0=OFF				
		Real state of DO20:0=OFF				
		Real state of DO21:0=OFF				
		Real state of DO22:0=OFF				
		Real state of DO23:0=OFF				
		Real state of DO24:0=OFF				
Actual state of all digital outputs in the DO chips Bit 0: =0:DO17 is OFF, =1:DO17 is ON Bit 1: =0:DO28 is OFF, =1:DO18 is ON ... Bit 6: =0:DO23 is OFF, =1:DO23 is ON Bit 7: =0:DO24 is OFF, =1:DO24 is ON Write on this register sets all digital outputs to a new state						
STATUS DIGITAL OUTPUTS						
DO1	3x16001 4x16001 I:16000	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Actual state of DO1:0=OFF		ENTER NEW STATE (0 or 1)		
Current state of the digital output DOx =0:DO is OFF, =1:DO is ON Writing on this register changes the state of the digital output						
DO2	3x16002 4x16002 I:16001	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO2:0=OFF		ENTER NEW STATE (0 or 1)		
DO3	3x16003 4x16003 I:16002	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO3:0=OFF		ENTER NEW STATE (0 or 1)		
DO4	3x16004 4x16004 I:16003	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO4:0=OFF		ENTER NEW STATE (0 or 1)		
DO5	3x16005 4x16005 I:16004	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO5:0=OFF		ENTER NEW STATE (0 or 1)		
DO6	3x16006 4x16006 I:16005	0,0x0000 B:00 00		0	UINT16 R/W	NO

		Actual state of DO6:0=OFF		ENTER NEW STATE (0 or 1)		
DO7	3x16007 4x16007 I:16006	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO7:0=OFF		ENTER NEW STATE (0 or 1)		
DO8	3x16008 4x16008 I:16007	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO8:0=OFF		ENTER NEW STATE (0 or 1)		
DO9	3x16009 4x16009 I:16008	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO9:0=OFF		ENTER NEW STATE (0 or 1)		
DO10	3x16010 4x16010 I:16009	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO10:0=OFF		ENTER NEW STATE (0 or 1)		
DO11	3x16011 4x16011 I:16010	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO11:0=OFF		ENTER NEW STATE (0 or 1)		
DO12	3x16012 4x16012 I:16011	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO12:0=OFF		ENTER NEW STATE (0 or 1)		
DO13	3x16013 4x16013 I:16012	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO13:0=OFF		ENTER NEW STATE (0 or 1)		
DO14	3x16014 4x16014 I:16013	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO14:0=OFF		ENTER NEW STATE (0 or 1)		
DO15	3x16015 4x16015 I:16014	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO15:0=OFF		ENTER NEW STATE (0 or 1)		
DO16	3x16016 4x16016 I:16015	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO16:0=OFF		ENTER NEW STATE (0 or 1)		
DO17	3x16017 4x16017 I:16016	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO17:0=OFF		ENTER NEW STATE (0 or 1)		

DO18	3x16018 4x16018 I:16017	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO18:0=OFF		ENTER NEW STATE (0 or 1)		
DO19	3x16019 4x16019 I:16018	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO19:0=OFF		ENTER NEW STATE (0 or 1)		
DO20	3x16020 4x16020 I:16019	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO20:0=OFF		ENTER NEW STATE (0 or 1)		
DO21	3x16021 4x16021 I:16020	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO21:0=OFF		ENTER NEW STATE (0 or 1)		
DO22	3x16022 4x16022 I:16021	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO22:0=OFF		ENTER NEW STATE (0 or 1)		
DO23	3x16023 4x16023 I:16022	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO23:0=OFF		ENTER NEW STATE (0 or 1)		
DO24	3x16024 4x16024 I:16023	0,0x0000 B:00 00		0	UINT16 R/W	NO
		Actual state of DO24:0=OFF		ENTER NEW STATE (0 or 1)		

Register NAME Command NAME	MODBUS Register ASCII Command	Register VALUE ASCII Command	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
PULSE TIME FOR DIGITAL OUTPUTS						
PULSE TIME DO1	3x20001 4x20001 I:20000	0,0x0000 B:00 00	200	20,0	UINT16 R/W	YES
Generate a pulse on digital output x in 100ms units (0,1 to 6553,5 Seconds selectable) If you write onto this register, the digital output will be switched on for the desired time in 100ms units.						
PULSE TIME DO2	3x20002 4x20002 I:20001	0,0x0000 B:00 00	300	30,0	UINT16 R/W	NO
PULSE TIME DO3	3x20003 4x20003 I:20002	0,0x0000 B:00 00	400	40,0	UINT16 R/W	NO
PULSE TIME DO4	3x20004 4x20004 I:20003	0,0x0000 B:00 00	500	50,0	UINT16 R/W	NO
PULSE TIME DO5	3x20005 4x20005 I:20004	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO6	3x20006 4x20006 I:20005	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO7	3x20007 4x20007 I:20006	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO8	3x20008 4x20008 I:20007	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO9	3x20009 4x20009 I:20008	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO10	3x20010 4x20010 I:20009	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO11	3x20011 4x20011 I:20010	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO12	3x20012 4x20012 I:20011	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO

PULSE TIME DO13	3x20013 4x20013 I:20012	0,0x0000 B:00 00	400	40,0	UINT16 R/W	NO
PULSE TIME DO14	3x20014 4x20014 I:20013	0,0x0000 B:00 00	500	50,0	UINT16 R/W	NO
PULSE TIME DO15	3x20015 4x20015 I:20014	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO16	3x20016 4x20016 I:20015	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO17	3x20017 4x20017 I:20016	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO18	3x20018 4x20018 I:20017	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO19	3x20019 4x20019 I:20018	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO20	3x20020 4x20020 I:20019	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO21	3x20021 4x20021 I:20020	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO22	3x20022 4x20022 I:20021	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO23	3x20023 4x20023 I:20022	0,0x0000 B:00 00	400	40,0	UINT16 R/W	NO
PULSE TIME DO24	3x20024 4x20024 I:20023	0,0x0000 B:00 00	500	50,0	UINT16 R/W	NO
PULSE STATUS FOR DIGITAL OUTPUTS						
PULSE TIMER DO1	3x21001 4x21001 I:21000	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
Remaining time of the pulse on digital output x in Milliseconds.						
PULSE TIMER DO2	3x21003 4x21003 I:21002	0,0x00000000 B:00 00 00 00			UINT32 R/O	

		0,0 seconds			
PULSE TIMER DO3	3x21005 4x21005 I:21004	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO4	3x21007 4x21007 I:21006	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO5	3x21009 4x21009 I:21008	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO6	3x21011 4x21011 I:21010	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO7	3x21013 4x21013 I:21012	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO8	3x21015 4x21015 I:21014	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO9	3x21017 4x21017 I:21016	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO10	3x21019 4x21019 I:21018	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO11	3x21021 4x21021 I:21020	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO12	3x21023 4x21023 I:21022	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO13	3x21025 4x21025 I:21024	0,0x00000000 B:00 00 00 00			UINT32 R/O

		0,0 seconds			
PULSE TIMER DO14	3x21027 4x21027 I:21026	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO15	3x21029 4x21029 I:21028	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO16	3x21031 4x21031 I:21030	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO17	3x21033 4x21033 I:21032	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO18	3x21035 4x21035 I:21034	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO19	3x21037 4x21037 I:21036	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO20	3x21039 4x21039 I:21038	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO21	3x21041 4x21041 I:21040	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO22	3x21043 4x21043 I:21042	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO23	3x21045 4x21045 I:21044	0,0x00000000 B:00 00 00 00			UINT32 R/O
		0,0 seconds			
PULSE TIMER DO24	3x21047 4x21047 I:21046	0,0x00000000 B:00 00 00 00			UINT32 R/O

		0,0 seconds			
PULSE STATUS FOR DIGITAL OUTPUTS					
PULSE TIMER DO1	3x21049 4x21049 I:21048	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
Remaining time of the pulse on digital output x in Milliseconds.					
PULSE TIMER DO2	3x21051 4x21051 I:21050	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO3	3x21053 4x21053 I:21052	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO4	3x21055 4x21055 I:21054	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO5	3x21057 4x21057 I:21056	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO6	3x21059 4x21059 I:21058	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO7	3x21061 4x21061 I:21060	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO8	3x21063 4x21063 I:21062	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO9	3x21065 4x21065 I:21064	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			
PULSE TIMER DO10	3x21067 4x21067 I:21066	0,0x00000000 B:00 00 00 00			UINT32R R/O
		0,0 seconds			

PULSE TIMER DO11	3x21069 4x21069 I:21068	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO12	3x21071 4x21071 I:21070	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO13	3x21073 4x21073 I:21072	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO14	3x21075 4x21075 I:21074	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO15	3x21077 4x21077 I:21076	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO16	3x21079 4x21079 I:21078	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO17	3x21081 4x21081 I:21080	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO18	3x21083 4x21083 I:21082	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO19	3x21085 4x21085 I:21084	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO20	3x21087 4x21087 I:21086	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO21	3x21089 4x21089 I:21088	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				

PULSE TIMER DO22	3x21091 4x21091 I:21090	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO23	3x21093 4x21093 I:21092	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO24	3x21095 4x21095 I:21094	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				

Register NAME Command NAME	MODBUS Register ASCII Command	Register VALUE ASCII Command	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
INITIAL & WATCHDOG STATUS FOR ALL DIGITAL OUTPUTS						
FRAM INTIAL & WATCHDOG STATUS OF DO1-DO16	3x59001 4x59001 l:59000	0,0x0000 B:00 00		0xFFFF	UINT16 R/W	NO
		Actual init & watchdog state of DO1:0=OFF	1			
		Actual init & watchdog state of DO2:0=OFF	1			
		Actual init & watchdog state of DO3:0=OFF	1			
		Actual init & watchdog state of DO4:0=OFF	1			
		Actual init & watchdog state of DO5:0=OFF	1			
		Actual init & watchdog state of DO6:0=OFF	1			
		Actual init & watchdog state of DO7:0=OFF	1			
		Actual init & watchdog state of DO8:0=OFF	1			
		Actual init & watchdog state of DO9:0=OFF	1			
		Actual init & watchdog state of DO10:0=OFF	1			
		Actual init & watchdog state of DO11:0=OFF	1			
		Actual init & watchdog state of DO12:0=OFF	1			
		Actual init & watchdog state of DO13:0=OFF	1			
		Actual init & watchdog state of DO14:0=OFF	1			
		Actual init & watchdog state of DO15:0=OFF	1			
		Actual init & watchdog state of DO16:0=OFF	1			
Current FRAM setting of initial and watchdog state of all digital outputs. This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured Bit 0: =0:DO1 is OFF, =1:DO1 is ON Bit 1: =0:DO2 is OFF, =1:DO2 is ON ... Bit 14: =0:DO15 is OFF, =1:DO15 is ON Bit 15: =0:DO16 is OFF, =1:DO16 is ON Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FRAM						
FRAM INTIAL & WATCHDOG STATUS OF DO17-DO24	3x59002 4x59002 l:59001	0,0x0000 B:00 00		0x00FF	UINT16 R/W	NO
		Actual init & watchdog state of DO17:0=OFF	1			
		Actual init & watchdog state of DO18:0=OFF	1			
		Actual init & watchdog state of DO19:0=OFF	1			
		Actual init & watchdog state of DO20:0=OFF	1			
		Actual init & watchdog state of DO21:0=OFF	1			
		Actual init & watchdog state of DO22:0=OFF	1			
		Actual init & watchdog state of DO23:0=OFF	1			
		Actual init & watchdog state of DO24:0=OFF	1			

Current FRAM setting of initial and watchdog state of all digital outputs. This state is used after power on and after a communcation watchdog timeout, if a watchdog time is configured
Bit 0: =0:DO17 is OFF, =1:DO17 is ON
Bit 1: =0:DO18 is OFF, =1:DO18 is ON
...
Bit 6: =0:DO23 is OFF, =1:DO23 is ON
Bit 7: =0:DO24 is OFF, =1:DO24 is ON

Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FRAM

DIGITAL OUTPUTS:ENABLE OPEN WIRE DETECTION WHILE ON						
INITIAL & WATCHDOG SETUP	3x59003	0,0x0000		0xFFFF	UINT16 R/W	NO
ENABLE OPEN WIRE DETECTION	4x59003	B:00 00				
WHILE ON DO1-DO16	I:59002					
		Initial setup of open wire detection while ON for DO1:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO2:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO3:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO4:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO5:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO6:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO7:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO8:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO9:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO10:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO11:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO12:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO13:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO14:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO15:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO16:0=DISABLED	1			

Current FRAM setting for intial and watchdog state for open wire detection while ON for digital output DOx. This state is used after power on and after a communcation watchdog timeout, if a watchdog time is configured

Bit 0: =0:Open wire detection for DO1 is DISABLED, =1:Open wire detection for DO1 is ENABLED

Bit 1: =0:Open wire detection for DO2 is DISABLED, =1:Open wire detection for DO2 is ENABLED

...

Bit 14: =0:Open wire detection for DO15 is DISABLED, =1:Open wire detection for DO15 is ENABLED

Bit 15: =0:Open wire detection for DO16 is DISABLED, =1:Open wire detection for DO16 is ENABLED

Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FRAM

INITIAL & WATCHDOG SETUP	3x59004	0,0x0000		0x00FF	UINT16 R/W	NO
ENABLE OPEN WIRE DETECTION	4x59004	B:00 00				
WHILE ON DO17-DO24	I:59003					
		Initial setup of open wire detection while ON for DO17:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO18:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO19:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO20:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO21:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO22:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO23:0=DISABLED	1			
		Initial setup of open wire detection while ON for DO24:0=DISABLED	1			

Current FRAM setting for intial and watchdog state for open wire detection while ON for digital output DOx. This state is used after power on and after a communcation watchdog timeout, if a watchdog time is configured

Bit 0: =0:Open wire detection for DO17 is DISABLED, =1:Open wire detection for DO17 is ENABLED

Bit 1: =0:Open wire detection for DO18 is DISABLED, =1:Open wire detection for DO18 is ENABLED

...

Bit 6: =0:Open wire detection for DO23 is DISABLED, =1:Open wire detection for DO23 is ENABLED

Bit 7: =0:Open wire detection for DO24 is DISABLED, =1:Open wire detection for DO24 is ENABLED

Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FRAM

DIGITAL OUTPUTS:ENABLE OPEN WIRE DETECTION WHILE OFF

INITIAL & WATCHDOG SETUP	3x59005	0,0x0000		0xFFFF	UINT16 R/W	NO
ENABLE OPEN WIRE DETECTION	4x59005	B:00 00				
WHILE OFF DO1-DO16	I:59004					
		Initial setup of open wire detection while OFF for DO1:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO2:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO3:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO4:0=DISABLED	1			

		Initial setup of open wire detection while OFF for DO5:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO6:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO7:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO8:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO9:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO10:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO11:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO12:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO13:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO14:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO15:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO16:0=DISABLED	1		

Current FRAM setting for initial and watchdog state for open wire detection while OFF for digital output DOx. This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured

Bit 0: =0:Open wire detection for DO1 is DISABLED, =1:Open wire detection for DO1 is ENABLED

Bit 1: =0:Open wire detection for DO2 is DISABLED, =1:Open wire detection for DO2 is ENABLED

...

Bit 14: =0:Open wire detection for DO15 is DISABLED, =1:Open wire detection for DO15 is ENABLED

Bit 15: =0:Open wire detection for DO16 is DISABLED, =1:Open wire detection for DO16 is ENABLED

Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FRAM

INITIAL & WATCHDOG SETUP ENABLE OPEN WIRE DETECTION WHILE OFF DO17-DO24	3x59006 4x59006 1:59005	0,0x0000 B:00 00		0x00FF	UINT16 R/W	NO
		Initial setup of open wire detection while OFF for DO17:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO18:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO19:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO20:0=DISABLED	1			
		Initial setup of open wire detection while OFF for DO21:0=DISABLED	1			

		Initial setup of open wire detection while OFF for DO22:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO23:0=DISABLED	1		
		Initial setup of open wire detection while OFF for DO24:0=DISABLED	1		
Current FRAM setting for intial and watchdog state for open wire detection while OFF for digital output DOx. This state is used after power on and after a communcation watchdog timeout, if a watchdog time is configured Bit 0: =0:Open wire detection for DO17 is DISABLED, =1:Open wire detection for DO17 is ENABLED Bit 1: =0:Open wire detection for DO18 is DISABLED, =1:Open wire detection for DO18 is ENABLED ... Bit 6: =0:Open wire detection for DO23 is DISABLED, =1:Open wire detection for DO23 is ENABLED Bit 7: =0:Open wire detection for DO24 is DISABLED, =1:Open wire detection for DO24 is ENABLED					
Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FRAM					
DIGITAL OUTPUTS:ENABLE SHORTCUT DETECTION WHILE OFF					
INITIAL & WATCHDOG SETUP ENABLE SHORTCUT TO VDD DETECTION WHILE OFF DO1-DO16	3x59007 4x59007 1:59006	0,0x0000 B:00 00		0xFFFF	UINT16 R/W NO
		Initial setup of shortcut to VDD detection while OFF for DO1:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO2:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO3:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO4:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO5:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO6:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO7:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO8:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO9:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO10:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO11:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO12:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO13:0=DISABLED	1		

		Initial setup of shortcut to VDD detection while OFF for DO14:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO15:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO16:0=DISABLED	1		
Current FRAM setting for intial and watchdog state for shortcut to VDD detection while OFF for digital output DOx. This state is used after power on and after a communcation watchdog timeout, if a watchdog time is configured Bit 0: =0:Shortcut detection for DO1 is DISABLED, =1:Shortcut detection for DO1 is ENABLED Bit 1: =0:Shortcut detection for DO2 is DISABLED, =1:Shortcut detection for DO2 is ENABLED ... Bit 14: =0:Shortcut detection for DO15 is DISABLED, =1:Shortcut detection for DO15 is ENABLED Bit 15: =0:Shortcut detection for DO16 is DISABLED, =1:Shortcut detection for DO16 is ENABLED Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FRAM					
INITIAL & WATCHDOG SETUP ENABLE SHORTCUT TO VDD DETECTION WHILE OFF DO17-DO24	3x59008 4x59008 1:59007	0,0x0000 B:00 00		0x00FF	UINT16 R/W
		Initial setup of shortcut to VDD detection while OFF for DO17:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO18:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO19:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO20:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO21:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO22:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO23:0=DISABLED	1		
		Initial setup of shortcut to VDD detection while OFF for DO24:0=DISABLED	1		
Current FRAM setting for intial and watchdog state for shortcut to VDD detection while OFF for digital output DOx. This state is used after power on and after a communcation watchdog timeout, if a watchdog time is configured Bit 0: =0:Shortcut detection for DO17 is DISABLED, =1:Shortcut detection for DO17 is ENABLED Bit 1: =0:Shortcut detection for DO18 is DISABLED, =1:Shortcut detection for DO18 is ENABLED ... Bit 6: =0:Shortcut detection for DO23 is DISABLED, =1:Shortcut detection for DO23 is ENABLED Bit 7: =0:Shortcut detection for DO24 is DISABLED, =1:Shortcut detection for DO24 is ENABLED Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FRAM					