

Register NAME	MODBUS Register	Register VALUE	DATA TYPE	DO WRITE
ASCII COMMANDS				
HEART BEAT	ASCII READ COMMAND	#HB<CR> Result: #HB<CR>	ASCII	
	TX	#255,HB<CR>		
	RX	#255,HB<CR>		
Sends an Heartbeat to test the communication				
GET VERSION	ASCII READ COMMAND	#VERSION<CR> Result: #VERSION:<VersionHi>,<VersionMed>,<VersionLo><CR>	ASCII	
	TX	#255,VERSION<CR>		
	RX	#255,VERSION:1.1.0<CR>		
		Actual SW version:1.1.0		
Returns the version number of the module VersionHi: Version number high (1..255) VersionMed: Version number medium (1..255) VersionLo: Version number low (1..255)				
GET TYPE	ASCII READ COMMAND	#TYPE<CR> Result: #TYPE:<Type><CR>	ASCII	
	TX	#255,TYPE<CR>		
	RX	#255,TYPE:RESI-C4-A-6DO<CR>		
		Actual module type:RESI-C4-A-6DO		
Returns the actual module type				
GET FEATURES	ASCII READ COMMAND	#FTRS<CR> Result: #FTRS:<Type><CR>	ASCII	
	TX	#255,FTRS<CR>		
	RX	#255,FTRS:RESI-C4-A-6DO,RS485,DO:6,DOIC:MAX14915<CR>		
		Actual module type:N/A		
		Number of digital inputs:N/A		
		Type of digital inputs:N/A		
Returns the actual module features				
GET OWNER	ASCII READ COMMAND	#OWNER<CR> Result: #OWNER:<Owner><CR>	ASCII	
	TX	#255,OWNER<CR>		
	RX	#255,OWNER:RESI<CR>		
		Actual owner:RESI		
Returns the actual owner of the module				

GET CREATOR	ASCII READ COMMAND	#CREATOR<CR> Result: #CREATOR:<Creator><CR>	ASCII	
	TX	#255,CREATOR<CR>		
	RX	#255,CREATOR:DI HC SIGL,MSC<CR>		
		Actual creator:DI HC SIGL,MSC		
Returns the actual creator of the module				
GET COPYRIGHT	ASCII READ COMMAND	#COPYRIGHT<CR> Result: #COPYRIGHT:<Copyright><CR>	ASCII	
	TX	#255,COPYRIGHT<CR>		
	RX	#255,COPYRIGHT:2015-24 BY RESI AND DI HC SIGL,MSC WWW.RESI.CC<CR>		
		Actual copyright:2015-24 BY RESI AND DI HC SIGL,MSC WWW.RESI.CC		
Returns the actual copyright of the module				
GET SERIAL NUMBER	ASCII READ COMMAND	#SN<CR> Result: #SN:<Serial><CR>	ASCII	
	TX	#255,SN<CR>		
	RX	#255,SN:270034000E57435535333920<CR>		
		Actual serial number:270034000E57435535333920		
Returns the actual serial number of the module				
SET BOX NAME	ASCII WRITE COMMAND	#SETBOXNAME:<BOXNAME><CR> Result: #OK<CR>	ASCII	YES
	BOXNAME	MYBOX		
	TX	#255,SETBOXNAME:MYBOX<CR>		
	RX	#255,OK<CR>		
Sets a new box name for the controller				
GET BOX NAME	ASCII READ COMMAND	#BOXNAME<CR> Result: #BOXNAME:<BoxName><CR>	ASCII	
	TX	#255,BOXNAME<CR>		
	RX	#255,BOXNAME:NONAME<CR>		
		Actual box name:NONAME		
Returns the actual box name of the module. If no box name is defined, the value NONAME is returned				
GET INTERNAL STATUS	ASCII READ COMMAND	#INTSTAT<CR> Result: #INTSTAT:<Status><CR>	ASCII	
	TX	#255,INTSTAT<CR>		
	RX	#255,INTSTAT:I2C1:0,I2C2:0<CR>		
		Actual internal status:I2C1		
Returns the device specific internal status				

GET DIP SWITCH	ASCII READ COMMAND	#GDIP<CR> Result: #GDIP:<DIPSwitchDec>,<DIPSwitchHex><CR>	ASCII	
	TX	#255,GDIP<CR>		
	RX	#255,GDIP:0,0x0<CR>		
		Actual DIP SWITCH settings:00000000		
Returns the actual setting of the Dip switches as decimal number and as hexadecimal number. DIPSwitchDec DIPSwitchHex The current value of the DIP switches: Bit 0: DIP Switch 1 (=0:OFF, =1:ON) Bit 1: DIP Switch 2 (=0:OFF, =1:ON) Bit 2: DIP Switch 3 (=0:OFF, =1:ON) Bit 3: DIP Switch 4 (=0:OFF, =1:ON) Bit 4: DIP Switch 5 (=0:OFF, =1:ON) Bit 5: DIP Switch 6 (=0:OFF, =1:ON) Bit 6: DIP Switch 7(=0:OFF, =1:ON) Bit 7: DIP Switch 8 (=0:OFF, =1:ON)				
SYSTEM COMMANDS				
RESET	ASCII WRITE COMMAND	#RST<CR> Result: #OK<CR>	ASCII	NO
	TX	#255,RST<CR>		
	RX	N/A		
Executes a software reset (Reboot) of the module. Be aware that you will losse all serial interfaces in USB!!!				
FACTORY RESET	ASCII WRITE COMMAND	#FRST<CR> Result: #OK<CR>	ASCII	NO
	TX	#255,FRST<CR>		
	RX	N/A		
Executes a factory reset of the module				
WATCHDOG TIMER	ASCII WRITE COMMAND	#WD:<WDTIME><CR> Result: #OK<CR>	ASCII	YES
	WDTIME	0		
	TX	#255,WD:0<CR>		
	RX	#255,OK<CR>		
Enables or disables the WATCHDOG Timer for the Raspberry Pi module. WDTIME: 1..3600000: Time for Watchdog in Milliseconds (Maximum 60 Minutes) =0: No Watchdog is generated HINT: The Watchdog is internally handled every 10ms, so every value below 10 will reset immediately the Raspberry Pi computer.				
SET IO WATCHDOG TIMER	ASCII WRITE COMMAND	#SIOWATCHDOG:<IOWDTIME><CR> Result: #OK<CR>	ASCII	YES
	IOWDTIME	0		
	TX	#255,SIOWATCHDOG:0<CR>		

	RX	#255,OK<CR>		
Sets a new time for the internal IO WATCHDOG Timer. <IOWDTIME> is a time in 100ms. =0: No IO Watchdog is used HINT: The Watchdog is internally handled every 100ms, if the Timer reaches 0, all internal IOS will be set to a preconfigured state. Every ASCII command or MODBUS request will reset this timer.				
GET IO WATCHDOG TIMER	ASCII READ COMMAND	#GIOWATCHDOG<CR> Result: #GIOWATCHDOG::<IOWDTIME> <CR>	ASCII	
	TX	#255,GIOWATCHDOG<CR>		
	RX	#255,GIOWATCHDOG:0,0x0<CR>		
Returns the actual time for the internal IO WATCHDOG Timer. <IOWDTIME> is a time in 100ms. =0: No IO Watchdog is used HINT: The Watchdog is internally handled every 100ms, if the Timer reaches 0, all internal IOS will be set to a preconfigured state. Every ASCII command or MODBUS request will reset this timer.				
CPU PARAMETERS				
GET CPU VOLTAGE	ASCII READ COMMAND	#GCPUTEMP<CR> Result: #GCPUTEMP:<CPUTemp> <CR>	ASCII	
	TX	#255,GCPUTEMP<CR>		
	RX	#255,GCPUTEMP:49.1242<CR>		
		Actual internal temperature of CPU:49.1242°C		
Current internal temperature of CPU in ° Celsius.				
GET CPU VOLTAGE	ASCII READ COMMAND	#GCPUVOLT<CR> Result: #GCPUVOLT:<CPUVoltage> <CR>	ASCII	
	TX	#255,GCPUVOLT<CR>		
	RX	#255,GCPUVOLT:3.3253<CR>		
		Actual supply voltage of CPU:3.3253V		
Current internal supply voltage of CPU in Volt.				
GET CPU BACKUP	ASCII READ COMMAND	#GCPUBACK<CR> Result: #GCPUBACK:<CPUBackupVoltage> <CR>	ASCII	
	TX	#255,GCPUBACK<CR>		
	RX	#255,GCPUBACK:3.1508<CR>		
		Actual backup voltage of CPU for RTC:3.1508V		
Current internal backup voltage of CPU for the RTC in Volt.				

Register NAME	MODBUS Register	Register VALUE	DATA TYPE	DO WRITE
ASCII COMMANDS:REAL TIME CLOCK				
GET REAL TIME CLOCK	ASCII READ COMMAND	#GRTC<CR> Result: #GRTC:YMD,<YEAR>,<MONTH>,<DAY>,HMS,<HOUR>,<MINUTE>,<SECOND>,<WEEKDAY> ,DOK,<DATEOK>,TOK,<TIMEOK><CR> #255,GRTC<CR>	ASCII	
	TX	#255,GRTC:YMD,25,3,3,HMS,9,52,29,MON,DOK,1,TOK,1<CR>		
	RX	Actual date DD.MM.YYYY:3.3.2025		
		Actual time HH.MM.SS (24h):09:52:29		
		Actual Weekday:MON		
		Battery buffered date is ok:YES		
		Battery buffered time is ok:YES		
Shows current RTC time of battery backup RTC on module				
ASCII COMMANDS:REAL TIME CLOCK				
SET REAL TIME CLOCK	ASCII WRITE COMMAND	#SRTC:YMD,<YEAR>,<MONTH>,<DAY>,HMS,<HOUR>,<MINUTE>,<SECOND>,<WEEKDAY><CR> Result: #OK<CR>	ASCII	YES
	YEAR	2025		
	MONTH	03		
	DAY	03		
	HOUR	11		
	MINUTE	34		
	SECOND	35		
	WEEKDAY	MON		
	TX	#255,SRTC:YMD,25,03,03,HMS,11,34,35,MON<CR>		
	RX	#255,OK<CR>		
Executes a software reset (Reboot) of the module.				

Register NAME	MODBUS Register	Register VALUE	DATA TYPE	DO WRITE
ASCII COMMANDS:FRAM				
GET FRAMSIZE	ASCII READ COMMAND	#GFRAMSIZE <CR> Result: #GFRAMSIZE:<FRAMType>,<FRAMSize>,<UsedSizeDEC>,<UsedSizeHEX> <CR>	ASCII	
	TX	#255,GFRAMSIZE <CR>		
	RX	#255,GFRAMSIZE:FM25L16B G,2kB,210,0xD2 <CR>		
Reads the actual type and size of the used FRAM. The <UsedSize> describes the internal used space in bytes of the FRAM				
ASCII COMMANDS:FRAM				
GET FRAM16	ASCII READ COMMAND	#GFRAM16:<INDEX> <CR> Result: #GFRAM16:<INDEXDEC>,<VALUEDEC>,<INDEXHEX>,<VALUEHEX> <CR> or #GFRAM16:<INDEXDEC>,ERR,<INDEXHEX>,ERR<CR>	ASCII	
	INDEX	350		
	TX	#255,GFRAM16:350<CR>		
	RX	#255,GFRAM16:350,0,0x15E,0x0<CR>		
		FRAM Index in bytes:350		
		FRAM Value in decimal:0		
Reads the actual UINT16 value (2 bytes) of FRAM memory <INDEX>. <INDEX> is a BYTE index in the FRAM strogae starting with 0.				
GET FRAM32	ASCII READ COMMAND	#GFRAM32:<INDEX> <CR> Result: #GFRAM32:<INDEXDEC>,<VALUEDEC>,<INDEXHEX>,<VALUEHEX> <CR> or #GFRAM32:<INDEXDEC>,ERR,<INDEXHEX>,ERR<CR>	ASCII	
	INDEX	350		
	TX	#255,GFRAM32:350<CR>		
	RX	#255,GFRAM32:350,0,0x15E,0x0<CR>		
		FRAM Index in bytes:350		
		FRAM Value in decimal:0		
Reads the actual UINT32 value 4 bytes) of FRAM memory <INDEX>. <INDEX> is a BYTE index in the FRAM strogae starting with 0.				
GET FRAMDBL	ASCII READ COMMAND	#GFRAMDBL:<INDEX> <CR> Result: #GFRAMDBL:<INDEXDEC>,<VALUEDBL>,<INDEXHEX>,<VALUEDBL> <CR> or #GFRAMDBL:<INDEXDEC>,ERR,<INDEXHEX>,ERR<CR>	ASCII	
	INDEX	400		
	TX	#255,GFRAMDBL:400<CR>		
	RX	#255,GFRAMDBL:400,0,0x190,0<CR>		
		FRAM Index in bytes:400		
		FRAM Value in decimal:0		
Reads the actual DOUBLE value 8 bytes) of FRAM memory <INDEX>. <INDEX> is a BYTE index in the FRAM strogae starting with 0.				
ASCII COMMANDS:FRAM				

SET FRAM16	ASCII WRITE COMMAND	#SFRAM16:<INDEX>,<VALUE> <CR> Result: #SFRAM16:OK<CR> or #SFRAM16:ERR<CR>	ASCII	YES
	INDEX	350		
	VALUE	1234		
	TX	#255,SFRAM16:350,1234<CR>		
	RX	#255,SFRAM16:OK<CR>		
Writes a new UINT16 value (2 byte) into FRAM memory <INDEX>. <INDEX> is a BYTE index in the FRAM strogae starting with 0.				
SET FRAM32	ASCII WRITE COMMAND	#SFRAM32:<INDEX>,<VALUE> <CR> Result: #SFRAM32:OK<CR> or #SFRAM32:ERR<CR>	ASCII	YES
	INDEX	350		
	VALUE	123456		
	TX	#255,SFRAM32:350,123456<CR>		
	RX	#255,SFRAM32:OK<CR>		
Writes a new UINT32 value (4 byte) into FRAM memory <INDEX>. <INDEX> is a BYTE index in the FRAM strogae starting with 0.				
SET FRAMDBL	ASCII WRITE COMMAND	#SFRAMDBL:<INDEX>,<DOUBLEVALUE> <CR> Result: #SFRAMDBL:OK<CR> or #SFRAMDBL:ERR<CR>	ASCII	YES
	INDEX	400		
	DOUBLEVALUE	3,1415926		
	TX	#255,SFRAMDBL:400,3.1415926<CR>		
	RX	#255,SFRAMDBL:OK<CR>		
Writes a new DOUBLE value (8 byte) into FRAM memory <INDEX>. <INDEX> is a BYTE index in the FRAM strogae starting with 0.				

Register NAME	MODBUS Register	Register VALUE	DATA TYPE	DO WRITE
LED STATUS:LED1:GREEN				
GET LED1	ASCII READ COMMAND	#GLED1<CR> Result: #GLED1:<LEDMode>,<LEDStateDec>,<LEDStateHex> <CR>	ASCII	
	TX	#255,GLED1<CR>		
	RX	#255,GLED1:OFF,0,0x0<CR>		
		Actual LED state:OFF LED ist currently 0		
Returns the actual state of the LED1:GREEN on the cover of module				
LED COMMANDS:LED1:GREEN				
SET LED1 OFF	ASCII WRITE COMMAND	#SL1OFF<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL1OFF<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED1:GREEN on the cover of module to OFF				
SET LED1 ON	ASCII WRITE COMMAND	#SL1ON<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL1ON<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED1:GREEN on the cover of module to ON				
SET LED1 INVERT	ASCII WRITE COMMAND	#SL1INV<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL1INV<CR>		
	RX	#255,OK<CR>		
Inverts the current state of the LED1:GREEN on the cover of module from ON to OFF or from OFF to ON				
SET LED1 PULSE	ASCII WRITE COMMAND	#SL1PULSE:<PULSETIME> <CR> Result: #OK<CR>	ASCII	YES
	PULSETIME	1000		
	TX	#255,SL1PULSE:1000<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED1:GREEN on the cover of module to PULSE and defines the one time pulse duration in Milliseconds between 1 and 60000				
SET LED1 BLINK	ASCII WRITE COMMAND	#SL1BLINK:<BLINKTIME> <CR> Result: #OK<CR>	ASCII	YES
	BLINKTIME	1000		
	TX	#255,SL1BLINK:1000<CR>		
	RX	#255,OK<CR>		

Sets the current state of the LED1:GREEN on the cover of module to PULSE and defines the one time pulse duration in Milliseconds between 1 and 60000				
SET LED1 FLASH	ASCII WRITE COMMAND	#SL1FLASH: <ONTIME>,<OFFTIME> <CR> Result: #OK<CR>	ASCII	YES
	ONTIME	200		
	OFFTIME	3000		
	TX	#255,SL1FLASH:200,3000<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED1:GREEN on the cover of module to FLASH and defines the on and off intervals in Milliseconds between 20 and 600000				

Register NAME	MODBUS Register	Register VALUE	DATA TYPE	DO WRITE
LED STATUS:LED2:WHITE				
GET LED2	ASCII READ COMMAND	#GLED2<CR> Result: #GLED2:<LEDMode>,<LEDStateDec>,<LEDStateHex><CR>	ASCII	
	TX	#255,GLED2<CR>		
	RX	#255,GLED2:BLINK,0,0x0<CR>		
		Actual LED state:BLINK LED ist currently 0		
Returns the actual state of the LED2:WHITE on the cover of module				
LED COMMANDS:LED2:WHITE				
SET LED2 OFF	ASCII WRITE COMMAND	#SL2OFF<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL2OFF<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED2:WHITE on the cover of module to OFF				
SET LED2 ON	ASCII WRITE COMMAND	#SL2ON<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL2ON<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED2:WHITE on the cover of module to ON				
SET LED2 INVERT	ASCII WRITE COMMAND	#SL2INV<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL2INV<CR>		
	RX	#255,OK<CR>		
Inverts the current state of the LED2:WHITE on the cover of module from ON to OFF or from OFF to ON				
SET LED2 PULSE	ASCII WRITE COMMAND	#SL2PULSE:<PULSETIME><CR> Result: #OK<CR>	ASCII	YES
	PULSETIME	1000		
	TX	#255,SL2PULSE:1000<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED2:WHITE on the cover of module to PULSE and defines the one time pulse duration in Milliseconds between 1 and 60000				
SET LED2 BLINK	ASCII WRITE COMMAND	#SL2BLINK:<BLINKTIME><CR> Result: #OK<CR>	ASCII	YES
	BLINKTIME	1000		
	TX	#255,SL2BLINK:1000<CR>		
	RX	#255,OK<CR>		

Sets the current state of the LED2:WHITE on the cover of module to PULSE and defines the one time pulse duration in Milliseconds between 1 and 60000				
SET LED2 FLASH	ASCII WRITE COMMAND	#SL2FLASH:<ONTIME>,<OFFTIME><CR> Result: #OK<CR>	ASCII	YES
	ONTIME	200		
	OFFTIME	3000		
	TX	#255,SL2FLASH:200,3000<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED2:WHITE on the cover of module to FLASH and defines the on and off intervals in Milliseconds between 20 and 600000				

Register NAME	MODBUS Register	Register VALUE	DATA TYPE	DO WRITE
LED STATUS:LED3:RED				
GET LED3	ASCII READ COMMAND	#GLED3<CR> Result: #GLED3:<LEDMode>,<LEDStateDec>,<LEDStateHex><CR>	ASCII	
	TX	#255,GLED3<CR>		
	RX	#255,GLED3:ON,1,0x1<CR>		
		Actual LED state:ON LED ist currently 1		
Returns the actual state of the LED3:RED on the cover of module				
LED COMMANDS:LED3:RED				
SET LED3 OFF	ASCII WRITE COMMAND	#SL3OFF<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL3OFF<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED3:RED on the cover of module to OFF				
SET LED3 ON	ASCII WRITE COMMAND	#SL3ON<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL3ON<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED3:RED on the cover of module to ON				
SET LED3 INVERT	ASCII WRITE COMMAND	#SL3INV<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL3INV<CR>		
	RX	#255,OK<CR>		
Inverts the current state of the LED3:RED on the cover of module from ON to OFF or from OFF to ON				
SET LED3 PULSE	ASCII WRITE COMMAND	#SL3PULSE:<PULSETIME><CR> Result: #OK<CR>	ASCII	YES
	PULSETIME	1000		
	TX	#255,SL3PULSE:1000<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED3:RED on the cover of module to PULSE and defines the one time pulse duration in Milliseconds between 1 and 60000				
SET LED3 BLINK	ASCII WRITE COMMAND	#SL3BLINK:<BLINKTIME><CR> Result: #OK<CR>	ASCII	YES
	BLINKTIME	1000		
	TX	#255,SL3BLINK:1000<CR>		
	RX	#255,OK<CR>		

Sets the current state of the LED3:RED on the cover of module to PULSE and defines the one time pulse duration in Milliseconds between 1 and 60000				
SET LED3 FLASH	ASCII WRITE COMMAND	#SL3FLASH:<ONTIME>,<OFFTIME><CR> Result: #OK<CR>	ASCII	YES
	ONTIME	200		
	OFFTIME	3000		
	TX	#255,SL3FLASH:200,3000<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED3:RED on the cover of module to FLASH and defines the on and off intervals in Milliseconds between 20 and 600000				

Register NAME	MODBUS Register	Register VALUE	DATA TYPE	DO WRITE
LED STATUS:LED4:YELLOW				
GET LED4	ASCII READ COMMAND	#GLED4<CR> Result: #GLED4:<LEDMode>,<LEDStateDec>,<LEDStateHex><CR>	ASCII	
	TX	#255,GLED4<CR>		
	RX	#255,GLED4:OFF,0,0x0<CR>		
		Actual LED state:OFF LED ist currently 0		
Returns the actual state of the LED4:YELLOW on the cover of module				
LED COMMANDS:LED4:YELLOW				
SET LED4 OFF	ASCII WRITE COMMAND	#SL4OFF<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL4OFF<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED4:YELLOW on the cover of module to OFF				
SET LED4 ON	ASCII WRITE COMMAND	#SL4ON<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL4ON<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED4:YELLOW on the cover of module to ON				
SET LED4 INVERT	ASCII WRITE COMMAND	#SL4INV<CR> Result: #OK<CR>	ASCII	YES
	TX	#255,SL4INV<CR>		
	RX	#255,OK<CR>		
Inverts the current state of the LED4:YELLOW on the cover of module from ON to OFF or from OFF to ON				
SET LED4 PULSE	ASCII WRITE COMMAND	#SL4PULSE:<PULSETIME><CR> Result: #OK<CR>	ASCII	YES
	PULSETIME	1000		
	TX	#255,SL4PULSE:1000<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED4:YELLOW on the cover of module to PULSE and defines the one time pulse duration in Milliseconds between 1 and 60000				
SET LED4 BLINK	ASCII WRITE COMMAND	#SL4BLINK:<BLINKTIME><CR> Result: #OK<CR>	ASCII	YES
	BLINKTIME	1000		
	TX	#255,SL4BLINK:1000<CR>		
	RX	#255,OK<CR>		

Sets the current state of the LED4:YELLOW on the cover of module to PULSE and defines the one time pulse duration in Milliseconds between 1 and 60000				
SET LED4 FLASH	ASCII WRITE COMMAND	#SL4FLASH:<ONTIME>,<OFFTIME><CR> Result: #OK<CR>	ASCII	YES
	ONTIME	200		
	OFFTIME	3000		
	TX	#255,SL4FLASH:200,3000<CR>		
	RX	#255,OK<CR>		
Sets the current state of the LED4:YELLOW on the cover of module to FLASH and defines the on and off intervals in Milliseconds between 20 and 600000				

Register NAME	MODBUS Register	Register VALUE	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
PRODUCT INFO						
HW_GROUP	3x65201 4x65201 I:65200	50176,0xC400 B:C4 00			UINT16 R/O	
This is the group of hardware of the current product						
HW_TYPE	3x65202 4x65202 I:65201	336,0x0150 B:01 50			UINT16 R/O	
This is the type of hardware of the current product						
SW_VERSION	3x65203 4x65203 I:65202	272,0x0110 B:01 10			UINT16 R/O	
		SW VERSION:0.1.0				
This is the current software version of the firmware						
SW_AUTHOR	3x65204 4x65204 I:65203	21321,0x5349 B:53 49			UINT16 R/O	
This is the current software author of the firmware						
MANUFACTURER	3x65205 4x65205 I:65204	1380275017,0x52455349 B:52 45 53 49			UINT32 R/O	
This is the current software author of the firmware						
NUMBER OF DIGITAL INPUTS	3x65207 4x65207 I:65206	0,0x0000 B:00 00			UINT16 R/O	
		Number of DIS:0				
This is the current software version of the firmware						
NUMBER OF DIGITAL OUTPUTS	3x65208 4x65208 I:65207	6,0x0006 B:00 06			UINT16 R/O	
		Number of DOS:6				
This is the current software version of the firmware						
NUMBER OF ANALOG INPUTS	3x65209 4x65209 I:65208	0,0x0000 B:00 00			UINT16 R/O	
		Number of AIS:0				
This is the current software version of the firmware						
NUMBER OF ANALOG OUTPUTS	3x65210 4x65210 I:65209	0,0x0000 B:00 00			UINT16 R/O	
		Number of AOS:0				

This is the current software version of the firmware						
NUMBER OF UNIVERSAL IN/OUTPUTS	3x65211 4x65211 I:65210	0,0x0000 B:00 00			UINT16 R/O	
		Number of AIOX:0				
This is the current software version of the firmware						
NUMBER OF SPECIAL INPUTS	3x65212 4x65212 I:65211	0,0x0000 B:00 00			UINT16 R/O	
		Number of special inputs:0				
This is the current software version of the firmware						
NUMBER OF SPECIAL OUTPUTS	3x65213 4x65213 I:65212	0,0x0000 B:00 00			UINT16 R/O	
		Number of special outputs:0				
This is the current software version of the firmware						
FEATURE1	3x65214 4x65214 I:65213	2,0x0002 B:00 02			UINT16 R/O	
		Feature:RS485				
This is the feature list of the controller: 0:NONE, 1:RS232, 2:RS485, 3:KNX, 4:DALI, 5:MBUS, 6:LORA, 7:LTE, 8:2xETHERNET						
FEATURE2	3x65215 4x65215 I:65214	0,0x0000 B:00 00			UINT16 R/O	
		Feature:NONE				
FEATURE3	3x65216 4x65216 I:65215	0,0x0000 B:00 00			UINT16 R/O	
		Feature:NONE				
FEATURE4	3x65217 4x65217 I:65216	0,0x0000 B:00 00			UINT16 R/O	
		Feature:NONE				
FEATURE5	3x65218 4x65218 I:65217	0,0x0000 B:00 00			UINT16 R/O	
		Feature:NONE				
FEATURE6	3x65219 4x65219 I:65218	0,0x0000 B:00 00			UINT16 R/O	
		Feature:NONE				
FEATURE7	3x65220 4x65220 I:65219	0,0x0000 B:00 00			UINT16 R/O	

		Feature:NONE				
FEATURE8	3x65221 4x65221 l:65220	0,0x0000 B:00 00			UINT16 R/O	
		Feature:NONE				
MODBUS WATCHDOG						
MODBUS WATCHDOG TIME	3x65222 4x65222 l:65221	0,0x0000 B:00 00		50	UINT16 R/W	NO
		Actual watchdog time in 1/100s:0 -> 0,0s				
Writing a value onto this register defines a new time for the internal communication watchdog timer. The value is a timespan in 1/100s. =0: The communication watchdog is disabled =1..65535: Communication watchdog will be trigegred after x 1/100s pause on communication line In case of an communication watchdog, the module sets all outputs to the states defined in the configuration output registers Reading this register will return the current stored time from the internal FRAM						
RASPBERRY PI WATCHDOG TIMER	3x65223 4x65223 l:65222	0,0x0000 B:00 00		50	UINT16 R/W	NO
		remaining watchdog time in 10ms:0 -> 0,000s				
Enables or disables the WATCHDOG Timer for the Raspberry Pi module. 1..65535: Time for Watchdog in x10 Milliseconds (Maximum 655,35 seconds) =0: No Watchdog is generated						
FRAM						
GET FRAM TYPE	3x65224 4x65224 l:65223	2,0x0002 B:00 02			UINT16 R/O	
		FRAM size & type:FM25L16 2kB				
Returns the current ytpе of the FRAM and its total size =2:FM25L16B_G, 2kB =64:FM25V05, 64kB =128:FM25V10, 128kB						
GET FRAM USED BYTES	3x65225 4x65225 l:65224	26,0x001A B:00 1A			UINT16 R/O	
		FRAM used bytes:26				
Returns the amount of used bytes from system in FRAM						
RTC REAL TIME CLOCK						
RTC YEAR	3x65231 4x65231 l:65230	25,0x0019 B:00 19		24	UINT16 R/W	NO
		Actual RTC year:25				
Returns the actual year of the internal real time clock in the range of 24 to 99. Writing to this register prepares the setting of a new time.						

RTC MONTH	3x65232 4x65232 I:65231	3,0x0003 B:00 03		1		UINT16 R/W	NO
		Actual RTC month:3					
Returns the actual month of the internal real time clock in the range of 1 to 12 Writing to this register prepares the setting of a new time.							
RTC DAY	3x65233 4x65233 I:65232	3,0x0003 B:00 03		1		UINT16 R/W	NO
		Actual RTC day:3					
Returns the actual day of the internal real time clock in the range of 1 to 31 Writing to this register prepares the setting of a new time.							
RTC HOUR	3x65234 4x65234 I:65233	9,0x0009 B:00 09		12		UINT16 R/W	NO
		Actual RTC month:9					
Returns the actual hour of the internal real time clock in the range of 0 to 23 Writing to this register prepares the setting of a new time.							
RTC MINUTE	3x65235 4x65235 I:65234	53,0x0035 B:00 35		45		UINT16 R/W	NO
		Actual RTC hour:53					
Returns the actual minute of the internal real time clock in the range of 0 to 59 Writing to this register prepares the setting of a new time.							
RTC SECOND	3x65236 4x65236 I:65235	28,0x001C B:00 1C		30		UINT16 R/W	NO
		Actual RTC second:28					
Returns the actual second of the internal real time clock in the range of 0 to 59 Writing to this register prepares the setting of a new time.							
RTC DAY OF WEEK	3x65237 4x65237 I:65236	1,0x0001 B:00 01		5:FRIDAY		UINT16 R/W	NO
		Actual RTC week day:MON		SELECT DAY OF WEEK			
Returns the actual day of week in the range 1 to 7 1:MON, 2:TUE, 3:WED, 4:THU, 5:FRI, 6:SAT, 7:SUN Writing to this register writes a new date and time and weekday to the RTC							
DIP SWITCH STATUS							
DIP SWITCH	3x65501 4x65501 I:65500	0,0x0000 B:00 00				UINT16 R/O	

Returns the actual setting of the Dip switches.
Bit 0: DIP Switch 1 (=0:OFF, =1:ON)
Bit 1: DIP Switch 2 (=0:OFF, =1:ON)
Bit 2: DIP Switch 3 (=0:OFF, =1:ON)
Bit 3: DIP Switch 4 (=0:OFF, =1:ON)
Bit 4: DIP Switch 5 (=0:OFF, =1:ON)
Bit 5: DIP Switch 6 (=0:OFF, =1:ON)
Bit 6: DIP Switch 7 (=0:OFF, =1:ON)
Bit 7: DIP Switch 8 (=0:OFF, =1:ON)

CPU DATA

SERIAL1	3x65521 4x65521 I:65520	9984,0x2700 B:27 00			UINT16 R/O	
Serial number of module as 96 bit unsigned integer number						
SERIAL2	3x65522 4x65522 I:65521	13312,0x3400 B:34 00			UINT16 R/O	
SERIAL3	3x65523 4x65523 I:65522	3671,0x0E57 B:0E 57			UINT16 R/O	
SERIAL4	3x65524 4x65524 I:65523	17237,0x4355 B:43 55			UINT16 R/O	
SERIAL5	3x65525 4x65525 I:65524	13619,0x3533 B:35 33			UINT16 R/O	
SERIAL6	3x65526 4x65526 I:65525	14624,0x3920 B:39 20			UINT16 R/O	
		SERIAL:00270034570E554333352039				
Serial number of module as 96 bit unsigned integer number						
CPU TEMPERATURE	3x65527 4x65527 I:65526	5021,0x139D B:13 9D			UINT16 R/O	
		Actual internal temperature of CPU:50,21°C				
Current internal temperature of CPU in ° Celsius multiplied by 100.						
CPU VOLTAGE	3x65528 4x65528 I:65527	332,0x014C B:01 4C			UINT16 R/O	
		Actual supply voltage of CPU:3,32V				
Current internal supply voltage of CPU in Volt multiplied by 100.						
CPU BACKUP	3x65529 4x65529 I:65528	315,0x013B B:01 3B			UINT16 R/O	
		Actual backup voltage of CPU for RTC:3,15V				

RESETs

RESI Configurator RESI-C4-6DO,6DOxAIOX-V1008					RESI-C4-xxx MB Basics	
RASPBERRY PI RESET	3x65534 4x65534 I:65533	0,0x00 B:00 00		1:PERFORM RASPBERRY PI RESET	BIT R/W	NO
Resets the Raspberry Pi						
RASPBERRY PI RESET	3x65534 4x65534 I:65533	0,0x0000 B:00 00		1:PERFORM RASPBERRY PI RESET	UINT16 R/W	NO
Resets the Raspberry Pi						
FACTORY RESET	1x65535 2x65535 I:65534	0,0x00 B:00		1:PERFORM FACTORY RESET	BIT R/W	NO
Performs a factory reset of all internal saved parameters						
FACTORY RESET	3x65535 4x65535 I:65534	0,0x0000 B:00 00		1:PERFORM FACTORY RESET	UINT16 R/W	NO
Performs a factory reset of all internal saved parameters						
RESET	1x65536 2x65536 I:65535	0,0x00 B:00		1:PERFORM ARM RESET	BIT R/W	NO
Performs a software reset, whenever 1 is written to this register. If the host writes to this register 1, the module executes a soft reset (reboot). Be aware that you will lose all serial interfaces in USB!!!						
RESET	3x65536 4x65535 I:65535	0,0x0000 B:00 00		1:PERFORM ARM RESET	UINT16 R/W	NO
Performs a software reset, whenever 1 is written to this register. If the host writes to this register 1, the module executes a soft reset (reboot). Be aware that you will lose all serial interfaces in USB!!!						

Register NAME	MODBUS Register	Register VALUE	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
DIP SWITCH STATUS						
DIP SWITCH DIP1	1x65001 2x65001 I:65000	0,0x00 B:00			BIT R/O	
Returns the actual setting of the Dip switches. =0: DIP is OFF =1: DIP is ON						
DIP SWITCH DIP2	1x65002 2x65002 I:65001	0,0x00 B:00			BIT R/O	
DIP SWITCH DIP3	1x65003 2x65003 I:65002	0,0x00 B:00			BIT R/O	
DIP SWITCH DIP4	1x65004 2x65004 I:65003	0,0x00 B:00			BIT R/O	
DIP SWITCH DIP5	1x65005 2x65005 I:65004	0,0x00 B:00			BIT R/O	
DIP SWITCH DIP6	1x65006 2x65006 I:65005	0,0x00 B:00			BIT R/O	
DIP SWITCH DIP7	1x65007 2x65007 I:65006	0,0x00 B:00			BIT R/O	
DIP SWITCH DIP8	1x65008 2x65008 I:65007	0,0x00 B:00			BIT R/O	
LED1:GREEN						
LED1:GREEN SET TO OFF	1x65009 2x65009 I:65008	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil sets the LED to OFF						
LED1:GREEN SET TO ON	1x65010 2x65010 I:65009	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil sets the LED to ON						
LED1:GREEN INVERT LED STATE	1x65011 2x65011 I:65010	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil inverts the actual LED state						
LED1:GREEN BLINK	1x65012 2x65012 I:65011	????		N/A:DO NOTHING	BIT W/O	NO

Writing 1 to this coil start symmetrical blinking of LED with last defined time						
LED1:GREEN FLASH	1x65013 2x65013 I:65012	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with last defined times						
LED1:GREEN PULSE	1x65014 2x65014 I:65013	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with last defined time						
LED1:GREEN BLINK 5s	1x65015 2x65015 I:65014	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 5s ON-5s OFF cycle						
LED1:GREEN BLINK 1s	1x65016 2x65016 I:65015	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 1s ON-1s OFF cycle						
LED1:GREEN BLINK 250ms	1x65017 2x65017 I:65016	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 250ms ON-250ms OFF cycle						
LED1:GREEN BLINK 50ms	1x65018 2x65018 I:65017	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 50ms ON-50ms OFF cycle						
LED1:GREEN FLASH 5s-1s	1x65019 2x65019 I:65018	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 5s ON-1s OFF cycle						
LED1:GREEN FLASH 1s-250ms	1x65020 2x65020 I:65019	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 1s ON-250ms OFF cycle						
LED1:GREEN FLASH 500ms-100ms	1x65021 2x65021 I:65020	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 500ms ON-100ms OFF cycle						
LED1:GREEN FLASH 300ms-50ms	1x65022 2x65022 I:65021	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 300ms ON-50ms OFF cycle						
LED1:GREEN PULSE 1s	1x65023 2x65023 I:65022	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 1s ON						

LED1:GREEN PULSE 500ms	1x65024 2x65024 I:65023	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 500ms ON						
LED1:GREEN PULSE 250ms	1x65025 2x65025 I:65024	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 250ms ON						
LED1:GREEN PULSE 100ms	1x65026 2x65026 I:65025	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 100ms ON						
LED1:GREEN PULSE 20ms	1x65027 2x65027 I:65026	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 20ms ON						
LED2:WHITE						
LED2:WHITE SET TO OFF	1x65029 2x65029 I:65028	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil sets the LED to OFF						
LED2:WHITE SET TO ON	1x65030 2x65030 I:65029	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil sets the LED to ON						
LED2:WHITE INVERT LED STATE	1x65031 2x65031 I:65030	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil inverts the actual LED state						
LED2:WHITE BLINK	1x65032 2x65032 I:65031	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with last defined time						
LED2:WHITE FLASH	1x65033 2x65033 I:65032	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with last defined times						
LED2:WHITE PULSE	1x65034 2x65034 I:65033	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with last defined time						
LED2:WHITE BLINK 5s	1x65035 2x65035 I:65034	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 5s ON-5s OFF cycle						

LED2:WHITE BLINK 1s	1x65036 2x65036 I:65035	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 1s ON-1s OFF cycle						
LED2:WHITE BLINK 250ms	1x65037 2x65037 I:65036	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 250ms ON-250ms OFF cycle						
LED2:WHITE BLINK 50ms	1x65038 2x65038 I:65037	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 50ms ON-50ms OFF cycle						
LED2:WHITE FLASH 5s-1s	1x65039 2x65039 I:65038	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 5s ON-1s OFF cycle						
LED2:WHITE FLASH 1s-250ms	1x65040 2x65040 I:65039	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 1s ON-250ms OFF cycle						
LED2:WHITE FLASH 500ms-100ms	1x65041 2x65041 I:65040	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 500ms ON-100ms OFF cycle						
LED2:WHITE FLASH 300ms-50ms	1x65042 2x65042 I:65041	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 300ms ON-50ms OFF cycle						
LED2:WHITE PULSE 1s	1x65043 2x65043 I:65042	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 1s ON						
LED2:WHITE PULSE 500ms	1x65044 2x65044 I:65043	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 500ms ON						
LED2:WHITE PULSE 250ms	1x65045 2x65045 I:65044	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 250ms ON						
LED2:WHITE PULSE 100ms	1x65046 2x65046 I:65045	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 100ms ON						

LED2:WHITE PULSE 20ms	1x65047 2x65047 I:65046	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 20ms ON						
LED3:RED						
LED3:RED SET TO OFF	1x65049 2x65049 I:65048	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil sets the LED to OFF						
LED3:RED SET TO ON	1x65050 2x65050 I:65049	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil sets the LED to ON						
LED3:RED INVERT LED STATE	1x65051 2x65051 I:65050	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil inverts the actual LED state						
LED3:RED BLINK	1x65052 2x65052 I:65051	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with last defined time						
LED3:RED FLASH	1x65053 2x65053 I:65052	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with last defined times						
LED3:RED PULSE	1x65054 2x65054 I:65053	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with last defined time						
LED3:RED BLINK 5s	1x65055 2x65055 I:65054	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 5s ON-5s OFF cycle						
LED3:RED BLINK 1s	1x65056 2x65056 I:65055	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 1s ON-1s OFF cycle						
LED3:RED BLINK 250ms	1x65057 2x65057 I:65056	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 250ms ON-250ms OFF cycle						
LED3:RED BLINK 50ms	1x65058 2x65058 I:65057	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 50ms ON-50ms OFF cycle						

LED3:RED FLASH 5s-1s	1x65059 2x65059 I:65058	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 5s ON-1s OFF cycle						
LED3:RED FLASH 1s-250ms	1x65060 2x65060 I:65059	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 1s ON-250ms OFF cycle						
LED3:RED FLASH 500ms-100ms	1x65061 2x65061 I:65060	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 500ms ON-100ms OFF cycle						
LED3:RED FLASH 300ms-50ms	1x65062 2x65062 I:65061	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 300ms ON-50ms OFF cycle						
LED3:RED PULSE 1s	1x65063 2x65063 I:65062	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 1s ON						
LED3:RED PULSE 500ms	1x65064 2x65064 I:65063	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 500ms ON						
LED3:RED PULSE 250ms	1x65065 2x65065 I:65064	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 250ms ON						
LED3:RED PULSE 100ms	1x65066 2x65066 I:65065	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 100ms ON						
LED3:RED PULSE 20ms	1x65067 2x65067 I:65066	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 20ms ON						
LED4:YELLOW						
LED4:YELLOW SET TO OFF	1x65069 2x65069 I:65068	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil sets the LED to OFF						
LED4:YELLOW SET TO ON	1x65070 2x65070 I:65069	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil sets the LED to ON						

LED4:YELLOW INVERT LED STATE	1x65071 2x65071 I:65070	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil inverts the actual LED state						
LED4:YELLOW BLINK	1x65072 2x65072 I:65071	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with last defined time						
LED4:YELLOW FLASH	1x65073 2x65073 I:65072	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with last defined times						
LED4:YELLOW PULSE	1x65074 2x65074 I:65073	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with last defined time						
LED4:YELLOW BLINK 5s	1x65075 2x65075 I:65074	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 5s ON-5s OFF cycle						
LED4:YELLOW BLINK 1s	1x65076 2x65076 I:65075	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 1s ON-1s OFF cycle						
LED4:YELLOW BLINK 250ms	1x65077 2x65077 I:65076	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 250ms ON-250ms OFF cycle						
LED4:YELLOW BLINK 50ms	1x65078 2x65078 I:65077	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start symmetrical blinking of LED with 50ms ON-50ms OFF cycle						
LED4:YELLOW FLASH 5s-1s	1x65079 2x65079 I:65078	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 5s ON-1s OFF cycle						
LED4:YELLOW FLASH 1s-250ms	1x65080 2x65080 I:65079	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 1s ON-250ms OFF cycle						
LED4:YELLOW FLASH 500ms-100ms	1x65081 2x65081 I:65080	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 500ms ON-100ms OFF cycle						

LED4:YELLOW FLASH 300ms-50ms	1x65082 2x65082 I:65081	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start asymmetrical flashing of LED with 300ms ON-50ms OFF cycle						
LED4:YELLOW PULSE 1s	1x65083 2x65083 I:65082	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 1s ON						
LED4:YELLOW PULSE 500ms	1x65084 2x65084 I:65083	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 500ms ON						
LED4:YELLOW PULSE 250ms	1x65085 2x65085 I:65084	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 250ms ON						
LED4:YELLOW PULSE 100ms	1x65086 2x65086 I:65085	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 100ms ON						
LED4:YELLOW PULSE 20ms	1x65087 2x65087 I:65086	????		N/A:DO NOTHING	BIT W/O	NO
Writing 1 to this coil start one time pulse of LED with 20ms ON						
DIP SWITCH STATUS						
DIP SWITCH	3x65501 4x65501 I:65500	0,0x0000 B:00 00			UINT16 R/O	
Returns the actual setting of the Dip switches. Bit 0: DIP Switch 1 (=0:OFF, =1:ON) Bit 1: DIP Switch 2 (=0:OFF, =1:ON) Bit 2: DIP Switch 3 (=0:OFF, =1:ON) Bit 3: DIP Switch 4 (=0:OFF, =1:ON) Bit 4: DIP Switch 5 (=0:OFF, =1:ON) Bit 5: DIP Switch 6 (=0:OFF, =1:ON) Bit 6: DIP Switch 7 (=0:OFF, =1:ON) Bit 7: DIP Switch 8 (=0:OFF, =1:ON)						
LED1:GREEN						
LED1:GREEN STATE	3x65502 4x65502 I:65501	3,0x0003 B:00 03		1:SET TO ON	UINT16 R/W	NO
		State of LED:BLINK				

Returns the actual state of the LED Writing to this register will set a new state for the LED 0: Switch LED permanent OFF 1: Switch LED permanent ON 2: Invert last state of LED 3: Start symmetrical blinking of LED with TIME1 ON and TIME1 OFF 4: Start asymmetrical flashing of LED with TIME1 ON and TIME2 OFF 5: Start one time pulse of LED with TIME1 ON and infinite OFF						
LED1:GREEN TIME1	3x65503 4x65503 I:65502	1000,0x03E8 B:03 E8		1000	UINT16 R/W	YES
		Actual time 1 in ms:1000				
Returns the actual time1 for blink,flash and pulse ON time in Milliseconds Writing to this register sets a new time in the range 20-65534ms						
LED1:GREEN TIME2	3x65504 4x65504 I:65503	2000,0x07D0 B:07 D0		2000	UINT16 R/W	YES
		Actual time 2 in ms:2000				
Returns the actual time2 for blink and flash OFF time in Milliseconds Writing to this register sets a new time in the range 20-65534ms						
LED2:WHITE						
LED2:WHITE STATE	3x65505 4x65505 I:65504	1,0x0001 B:00 01		1:SET TO ON	UINT16 R/W	NO
		State of LED:ON				
Returns the actual state of the LED Writing to this register will set a new state for the LED 0: Switch LED permanent OFF 1: Switch LED permanent ON 2: Invert last state of LED 3: Start symmetrical blinking of LED with TIME1 ON and TIME1 OFF 4: Start asymmetrical flashing of LED with TIME1 ON and TIME2 OFF 5: Start one time pulse of LED with TIME1 ON and infinite OFF						
LED2:WHITE TIME1	3x65506 4x65506 I:65505	1000,0x03E8 B:03 E8		1000	UINT16 R/W	YES
		Actual time 1 in ms:1000				
Returns the actual time1 for blink,flash and pulse ON time in Milliseconds Writing to this register sets a new time in the range 20-65534ms						
LED2:WHITE TIME2	3x65507 4x65507 I:65506	2000,0x07D0 B:07 D0		2000	UINT16 R/W	YES
		Actual time 2 in ms:2000				
Returns the actual time2 for blink and flash OFF time in Milliseconds Writing to this register sets a new time in the range 20-65534ms						
LED3:RED						
LED3:RED STATE	3x65508 4x65508 I:65507	0,0x0000 B:00 00		1:SET TO ON	UINT16 R/W	NO

		State of LED:OFF				
Returns the actual state of the LED Writing to this register will set a new state for the LED 0: Switch LED permanent OFF 1: Switch LED permanent ON 2: Invert last state of LED 3: Start symmetrical blinking of LED with TIME1 ON and TIME1 OFF 4: Start asymmetrical flashing of LED with TIME1 ON and TIME2 OFF 5: Start one time pulse of LED with TIME1 ON and infinite OFF						
LED3:RED TIME1	3x65509 4x65509 I:65508	1000,0x03E8 B:03 E8		1000	UINT16 R/W	YES
		Actual time 1 in ms:1000				
Returns the actual time1 for blink,flash and pulse ON time in Milliseconds Writing to this register sets a new time in the range 20-65534ms						
LED3:RED TIME2	3x65510 4x65510 I:65509	2000,0x07D0 B:07 D0		2000	UINT16 R/W	YES
		Actual time 2 in ms:2000				
Returns the actual time2 for blink and flash OFF time in Milliseconds Writing to this register sets a new time in the range 20-65534ms						
LED4:YELLOW						
LED4:YELLOW STATE	3x65511 4x65511 I:65510	0,0x0000 B:00 00		1:SET TO ON	UINT16 R/W	NO
		State of LED:OFF				
Returns the actual state of the LED Writing to this register will set a new state for the LED 0: Switch LED permanent OFF 1: Switch LED permanent ON 2: Invert last state of LED 3: Start symmetrical blinking of LED with TIME1 ON and TIME1 OFF 4: Start asymmetrical flashing of LED with TIME1 ON and TIME2 OFF 5: Start one time pulse of LED with TIME1 ON and infinite OFF						
LED4:YELLOW TIME1	3x65512 4x65512 I:65511	1000,0x03E8 B:03 E8		1000	UINT16 R/W	YES
		Actual time 1 in ms:1000				
Returns the actual time1 for blink,flash and pulse ON time in Milliseconds Writing to this register sets a new time in the range 20-65534ms						
LED4:YELLOW TIME2	3x65513 4x65513 I:65512	2000,0x07D0 B:07 D0		2000	UINT16 R/W	YES
		Actual time 2 in ms:2000				
Returns the actual time2 for blink and flash OFF time in Milliseconds Writing to this register sets a new time in the range 20-65534ms						

Register NAME Command NAME	MODBUS Register ASCII Command	Register VALUE ASCII Command	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
STATUS DIGITAL OUTPUTS						
DO1	1x00001 2x00001 I:0	0,0x00 B:00		1	BIT R/W	YES
		Current state of DO1:0=OFF		ENTER NEW STATE (0 or 1)		
Current state of the digital output DOx =0:DO is OFF, =1:DO is ON						
Writing on this register changes the state of the digital output						
DO2	1x00002 2x00002 I:1	0,0x00 B:00		0	BIT R/W	NO
		Current state of DO2:0=OFF		ENTER NEW STATE (0 or 1)		
DO3	1x00003 2x00003 I:2	0,0x00 B:00		0	BIT R/W	NO
		Current state of DO3:0=OFF		ENTER NEW STATE (0 or 1)		
DO4	1x00004 2x00004 I:3	0,0x00 B:00		1	BIT R/W	YES
		Current state of DO4:0=OFF		ENTER NEW STATE (0 or 1)		
DO5	1x00005 2x00005 I:4	0,0x00 B:00		0	BIT R/W	NO
		Current state of DO5:0=OFF		ENTER NEW STATE (0 or 1)		
DO6	1x00006 2x00006 I:5	0,0x00 B:00		0	BIT R/W	NO
		Current state of DO6:0=OFF		ENTER NEW STATE (0 or 1)		
DIGITAL OUTPUTS: ENABLE OPEN WIRE DETECTION ON						
ENABLE OPEN WIRE DETECTION ON DO1	1x00007 2x00007 I:6	0,0x00 B:00		1	BIT R/W	YES
		Current setup of open wire detection for state ON of DO1:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
Enables/disabled detection of an open wire in DO state ON for the digital output DOx =0:Open wire detection is OFF, =1:Open wire detection is ON						
Writing on this register changes the state of the open wire detection for this output						
ENABLE OPEN WIRE DETECTION ON DO2	1x00008 2x00008 I:7	0,0x00 B:00		1	BIT R/W	YES

		Current setup of open wire detection for state ON of DO2:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO3	1x00009 2x00009 I:8	0,0x00 B:00		1	BIT R/W	YES
		Current setup of open wire detection for state ON of DO3:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO4	1x00010 2x00010 I:9	0,0x00 B:00		1	BIT R/W	YES
		Current setup of open wire detection for state ON of DO4:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO5	1x00011 2x00011 I:10	0,0x00 B:00		1	BIT R/W	YES
		Current setup of open wire detection for state ON of DO5:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO6	1x00012 2x00012 I:11	0,0x00 B:00		1	BIT R/W	YES
		Current setup of open wire detection for state ON of DO6:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
DIGITAL OUTPUTS: ENABLE OPEN WIRE DETECTION OFF						
ENABLE OPEN WIRE DETECTION OFF DO1	1x00013 2x00013 I:12	0,0x00 B:00		1	BIT R/W	NO
		Current setup of open wire detection for state OFF of DO1:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
Enables/disabled detection of an open wire in DO state OFF for the digital output DOx =0:Open wire detection is OFF, =1:Open wire detection is ON						
Writing on this register changes the state of the open wire detection for this output						
ENABLE OPEN WIRE DETECTION OFF DO2	1x00014 2x00014 I:13	0,0x00 B:00		1	BIT R/W	NO
		Current setup of open wire detection for state OFF of DO2:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO3	1x00015 2x00015 I:14	0,0x00 B:00		1	BIT R/W	NO
		Current setup of open wire detection for state OFF of DO3:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO4	1x00016 2x00016 I:15	0,0x00 B:00		1	BIT R/W	NO
		Current setup of open wire detection for state OFF of DO4:0=OFF		ENTER NEW SETUP MODE (0 or 1)		

ENABLE OPEN WIRE DETECTION OFF DO5	1x00017 2x00017 I:16	0,0x00 B:00		1		BIT R/W	NO
		Current setup of open wire detection for state ON of DO5:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE OPEN WIRE DETECTION OFF DO6	1x00018 2x00018 I:17	0,0x00 B:00		1		BIT R/W	NO
		Current setup of open wire detection for state OFF of DO6:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
DIGITAL OUTPUTS: ENABLE SHORT TO VDD DETECTION							
ENABLE SHORT TO VDD DETECTION DO1	1x00019 2x00019 I:18	0,0x00 B:00		1		BIT R/W	YES
		Current setup of open wire detection for state OFF of DO1:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
Enables/disabled detection of a shortcut to VDD in DO state OFF for the digital output DOx =0:Shortcut to VDD detection is OFF, =1:Shortcut to VDD detection is ON							
Writing on this register changes the state of the shortcut detection for this output							
ENABLE SHORT TO VDD DETECTION DO2	1x00020 2x00020 I:19	0,0x00 B:00		1		BIT R/W	YES
		Current setup of open wire detection for state OFF of DO2:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO3	1x00021 2x00021 I:20	0,0x00 B:00		1		BIT R/W	YES
		Current setup of open wire detection for state OFF of DO3:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO4	1x00022 2x00022 I:21	0,0x00 B:00		1		BIT R/W	YES
		Current setup of open wire detection for state OFF of DO4:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO5	1x00023 2x00023 I:22	0,0x00 B:00		1		BIT R/W	YES
		Current setup of open wire detection for state ON of DO5:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
ENABLE SHORT TO VDD DETECTION DO6	1x00024 2x00024 I:23	0,0x00 B:00		1		BIT R/W	YES
		Current setup of open wire detection for state OFF of DO6:0=OFF		ENTER NEW SETUP MODE (0 or 1)			
DIGITAL OUTPUTS: OPEN WIRE DETECTION STATUS WHILE ON							

OPEN WIRE WHILE ON DO1	1x00025 2x00025 I:24	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state ON for DO1:0=OK				
The current detection state of an open wire in the output state ON for the digital output DOx =0:No fault, =1:Fault-open wire detected						
OPEN WIRE WHILE ON DO2	1x00026 2x00026 I:25	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state ON for DO2:0=OK				
OPEN WIRE WHILE ON DO3	1x00027 2x00027 I:26	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state ON for DO3:0=OK				
OPEN WIRE WHILE ON DO4	1x00028 2x00028 I:27	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state ON for DO4:0=OK				
OPEN WIRE WHILE ON DO5	1x00029 2x00029 I:28	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state ON for DO5:0=OK				
OPEN WIRE WHILE ON DO6	1x00030 2x00030 I:29	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state ON for DO6:0=OK				
DIGITAL OUTPUTS: OPEN WIRE DETECTION STATUS WHILE OFF						
OPEN WIRE WHILE OFF DO1	1x00031 2x00031 I:30	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state OFF for DO1:0=OK				
The current detection state of an open wire in the output state OFF for the digital output DOx =0:No fault, =1:Fault-open wire detected						
OPEN WIRE WHILE OFF DO2	1x00032 2x00032 I:31	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state OFF for DO2:0=OK				

OPEN WIRE WHILE OFF DO3	1x00033 2x00033 I:32	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state OFF for DO3:0=OK				
OPEN WIRE WHILE OFF DO4	1x00034 2x00034 I:33	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state OFF for DO4:0=OK				
OPEN WIRE WHILE OFF DO5	1x00035 2x00035 I:34	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state OFF for DO5:0=OK				
OPEN WIRE WHILE OFF DO6	1x00036 2x00036 I:35	0,0x00 B:00			BIT R/O	
		Current detection state of an open wire in state OFF for DO6:0=OK				
DIGITAL OUTPUTS: SHORTCUT DETECTION STATUS TO VDD WHILE OFF						
OPEN WIRE SHORTCUT TO VDD DO1	1x00037 2x00037 I:36	0,0x00 B:00			BIT R/O	
		Current detection state of a shortcut to VDD in state OFF for DO1:0=OK				
The current detection state of a shortcut to VDD in the output state OFF for the digital output DOx =0:No fault, =1:Fault-shortcut to VDD detected						
OPEN WIRE SHORTCUT TO VDD DO2	1x00038 2x00038 I:37	0,0x00 B:00			BIT R/O	
		Current detection state of a shortcut to VDD in state OFF for DO2:0=OK				
OPEN WIRE SHORTCUT TO VDD DO3	1x00039 2x00039 I:38	0,0x00 B:00			BIT R/O	
		Current detection state of a shortcut to VDD in state OFF for DO3:0=OK				
OPEN WIRE SHORTCUT TO VDD DO4	1x00040 2x00040 I:39	0,0x00 B:00			BIT R/O	
		Current detection state of a shortcut to VDD in state OFF for DO4:0=OK				
OPEN WIRE SHORTCUT TO VDD DO5	1x00041 2x00041 I:40	0,0x00 B:00			BIT R/O	

		Current detection state of a shortcut to VDD in state OFF for DO5:0=OK			
OPEN WIRE SHORTCUT TO VDD DO6	1x00042 2x00042 I:41	0,0x00 B:00			BIT R/O
		Current detection state of a shortcut to VDD in state OFF for DO6:0=OK			
DIGITAL OUTPUTS: THERMAL OVERLOAD DETECTION STATUS					
THERMAL OVERLOAD DETECTION STATUS DO1	1x00043 2x00043 I:42	0,0x00 B:00			BIT R/O
		Current detection state of a thermal overload for DO1:0=OK			
The current detection state of a thermal overload for the digital output DOx =0:No fault, =1:Fault-thermal overload detected					
THERMAL OVERLOAD DETECTION STATUS DO2	1x00044 2x00044 I:43	0,0x00 B:00			BIT R/O
		Current detection state of a thermal overload for DO2:0=OK			
THERMAL OVERLOAD DETECTION STATUS DO3	1x00045 2x00045 I:44	0,0x00 B:00			BIT R/O
		Current detection state of a thermal overload for DO3:0=OK			
THERMAL OVERLOAD DETECTION STATUS DO4	1x00046 2x00046 I:45	0,0x00 B:00			BIT R/O
		Current detection state of a thermal overload for DO4:0=OK			
THERMAL OVERLOAD DETECTION STATUS DO5	1x00047 2x00047 I:46	0,0x00 B:00			BIT R/O
		Current detection state of a thermal overload for DO5:0=OK			
THERMAL OVERLOAD DETECTION STATUS DO6	1x00048 2x00048 I:47	0,0x00 B:00			BIT R/O
		Current detection state of a thermal overload for DO6:0=OK			
DIGITAL OUTPUTS: CURRENT LIMIT DETECTION STATUS WHILE ON					
CURRENT LIMIT DETECTION STATUS WHILE ON DO1	1x00049 2x00049 I:48	0,0x00 B:00			BIT R/O
		Current detection state of a current limit while ON for DO1:0=OK			

The current detection state of a current limit while output is ON for the digital output DOx

=0:No fault, =1:Fault-current limit error

CURRENT LIMIT DETECTION STATUS WHILE ON DO2	1x00050 2x00050 I:49	0,0x00 B:00			BIT R/O	
		Current detection state of a current limit while ON for DO2:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO3	1x00051 2x00051 I:50	0,0x00 B:00			BIT R/O	
		Current detection state of a current limit while ON for DO3:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO4	1x00052 2x00052 I:51	0,0x00 B:00			BIT R/O	
		Current detection state of a current limit while ON for DO4:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO5	1x00053 2x00053 I:52	0,0x00 B:00			BIT R/O	
		Current detection state of a current limit while ON for DO5:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO6	1x00054 2x00054 I:53	0,0x00 B:00			BIT R/O	
		Current detection state of a current limit while ON for DO6:0=OK				
DIGITAL OUTPUTS: GLOBAL ERRORS DO1-DO6						
GLOBAL ERRORS FOR DO1-DO6 BIT 0	1x00055 2x00055 I:54	0,0x00 B:00			BIT R/O	
		BIT 0:Internal under voltage detected:0=OK				
GLOBAL ERRORS FOR DO1-DO6 BIT 1	1x00056 2x00056 I:55	0,0x00 B:00			BIT R/O	
		BIT 1:VA under voltage detected (<2.3V):0=OK				
GLOBAL ERRORS FOR DO1-DO6 BIT 2	1x00057 2x00057 I:56	0,0x00 B:00			BIT R/O	
		BIT 2:VDD not good detected (<17V):0=OK				
GLOBAL ERRORS FOR DO1-DO6 BIT 3	1x00058 2x00058 I:57	0,0x00 B:00			BIT R/O	
		BIT 3:VDD warning detected (<12V):0=OK				
GLOBAL ERRORS FOR DO1-DO6 BIT 4	1x00059 2x00059 I:58	0,0x00 B:00			BIT R/O	

		BIT 4:VDD under voltage detected (<8V):0=OK				
GLOBAL ERRORS FOR DO1-DO6 BIT 5	1x00060 2x00060 I:59	0,0x00 B:00			BIT R/O	
		BIT 5:Thermal shutdown:0=OK				
GLOBAL ERRORS FOR DO1-DO6 BIT 6	1x00061 2x00061 I:60	0,0x00 B:00			BIT R/O	
		BIT 6:Synchronisation error detected:0=OK				
GLOBAL ERRORS FOR DO1-DO6 BIT 7	1x00062 2x00062 I:61	0,0x00 B:00			BIT R/O	
		BIT 7:Watchdog error detected:0=OK				
The global error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
DIGITAL OUTPUTS: INTERRUPT STATUS DO1-DO6						
INTERRUPT STATUS FOR DO1-DO6 BIT 0	1x00063 2x00063 I:62	0,0x00 B:00			BIT R/O	
		BIT 0:Overload detected:0=OK				
INTERRUPT STATUS FOR DO1-DO6 BIT 1	1x00064 2x00064 I:63	0,0x00 B:00			BIT R/O	
		BIT 1:Current limit detected:0=OK				
INTERRUPT STATUS FOR DO1-DO6 BIT 2	1x00065 2x00065 I:64	0,0x00 B:00			BIT R/O	
		BIT 2:Open wire while OFF detected:0=OK				
INTERRUPT STATUS FOR DO1-DO6 BIT 3	1x00066 2x00066 I:65	0,0x00 B:00			BIT R/O	
		BIT 3:Open wire while ON detected:0=OK				
INTERRUPT STATUS FOR DO1-DO6 BIT 4	1x00067 2x00067 I:66	0,0x00 B:00			BIT R/O	
		BIT 4:Short to VDD while ON detected:0=OK				
INTERRUPT STATUS FOR DO1-DO6 BIT 5	1x00068 2x00068 I:67	0,0x00 B:00			BIT R/O	
		BIT 5:Thermal error detected-shutdown:0=OK				
INTERRUPT STATUS FOR DO1-DO6 BIT 6	1x00069 2x00069 I:68	0,0x00 B:00			BIT R/O	
		BIT 6:Supply error detected:0=OK				
INTERRUPT STATUS FOR DO1-DO6 BIT 7	1x00070 2x00070 I:69	0,0x00 B:00			BIT R/O	

		BIT 7:Communication error detected:0=OK				
The global interrupt error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
SPI COMMUNICATION DIGITAL OUTPUTS						
SPI COMMUNICATION DO1-DO6	1x00071 2x00071 I:70	0,0x00 B:00			BIT R/O	
		Current SPI communication for DO1-DO6:0=NO FAULT				
The current monitoring state of the SPI communication for the digital output group =0:No fault, =1:Fault						
DIGITAL INPUTS: RESET						
RESET COUNTERS	1x10000 2x10000 I:9999	0,0x00 B:00		1:PERFORM RESET	BIT R/W	NO
If this register is written to 1, all internal edge counters and event counters are set to 0. 0 is always returned when reading.						
STATUS DIGITAL INPUTS						
DI1	3x00001 4x00001 I:0	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI1:0=OFF				
Current state of the digital input DIx =0:DI is OFF, =1:DI is ON						
DI2	3x00002 4x00002 I:1	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI2:0=OFF				
DI3	3x00003 4x00003 I:2	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI3:0=OFF				
DI4	3x00004 4x00004 I:3	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI4:0=OFF				
DI5	3x00005 4x00005 I:4	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI5:0=OFF				
DI6	3x00006 4x00006 I:5	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI6:0=OFF				
STATUS DIGITAL OUTPUTS						
DO1	3x00007 4x00007 I:6	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Current state of DO1:0=OFF		ENTER NEW STATE (0 or 1)		

Current state of the digital output DOx
=0:DO is OFF, =1:DO is ON

Writing on this register changes the state of the digital output

DO2	3x00008 4x00008 I:7	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Current state of DO2:0=OFF		ENTER NEW STATE (0 or 1)		
DO3	3x00009 4x00009 I:8	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Current state of DO3:0=OFF		ENTER NEW STATE (0 or 1)		
DO4	3x00010 4x00010 I:9	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Current state of DO4:0=OFF		ENTER NEW STATE (0 or 1)		
DO5	3x00011 4x00011 I:10	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Current state of DO5:0=OFF		ENTER NEW STATE (0 or 1)		
DO6	3x00012 4x00012 I:11	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Current state of DO6:0=OFF		ENTER NEW STATE (0 or 1)		

DIGITAL OUTPUTS: ENABLE OPEN WIRE DETECTION ON

ENABLE OPEN WIRE DETECTION ON DO1	3x00013 4x00013 I:12	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Current setup of open wire detection for state ON of DO1:0=OFF		ENTER NEW SETUP MODE (0 or 1)		

Enables/disabled detection of an open wire in DO state ON for the digital output DOx
=0:Open wire detection is OFF, =1:Open wire detection is ON

Writing on this register changes the state of the open wire detection for this output

ENABLE OPEN WIRE DETECTION ON DO2	3x00014 4x00014 I:13	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Current setup of open wire detection for state ON of DO2:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO3	3x00015 4x00015 I:14	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Current setup of open wire detection for state ON of DO3:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO4	3x00016 4x00016 I:15	0,0x0000 B:00 00		1	UINT16 R/W	NO

		Current setup of open wire detection for state ON of DO4:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO5	3x00017 4x00017 I:16	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state ON of DO5:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION ON DO6	3x00018 4x00018 I:17	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state ON of DO6:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
DIGITAL OUTPUTS: ENABLE OPEN WIRE DETECTION OFF					
ENABLE OPEN WIRE DETECTION OFF DO1	3x00019 4x00019 I:18	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO1:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
Enables/disabled detection of an open wire in DO state OFF for the digital output DOx =0:Open wire detection is OFF, =1:Open wire detection is ON					
Writing on this register changes the state of the open wire detection for this output					
ENABLE OPEN WIRE DETECTION OFF DO2	3x00020 4x00020 I:19	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO2:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO3	3x00021 4x00021 I:20	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO3:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO4	3x00022 4x00022 I:21	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO4:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO5	3x00023 4x00023 I:22	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state ON of DO5:0=OFF	ENTER NEW SETUP MODE (0 or 1)		
ENABLE OPEN WIRE DETECTION OFF DO6	3x00024 4x00024 I:23	0,0x0000 B:00 00	1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO6:0=OFF	ENTER NEW SETUP MODE (0 or 1)		

DIGITAL OUTPUTS: ENABLE SHORT TO VDD DETECTION

ENABLE SHORT TO VDD DETECTION DO1	3x00025 4x00025 I:24	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO1:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
Enables/disabled detection of a shortcut to VDD in DO state OFF for the digital output DOx =0:Shortcut to VDD detection is OFF, =1:Shortcut to VDD detection is ON						
Writing on this register changes the state of the shortcut detection for this output						
ENABLE SHORT TO VDD DETECTION DO2	3x00026 4x00026 I:25	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO2:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO3	3x00027 4x00027 I:26	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO3:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO4	3x00028 4x00028 I:27	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO4:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO5	3x00029 4x00029 I:28	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Current setup of open wire detection for state ON of DO5:0=OFF		ENTER NEW SETUP MODE (0 or 1)		
ENABLE SHORT TO VDD DETECTION DO6	3x00030 4x00030 I:29	0,0x0000 B:00 00		1	UINT16 R/W	NO
		Current setup of open wire detection for state OFF of DO6:0=OFF		ENTER NEW SETUP MODE (0 or 1)		

DIGITAL OUTPUTS: OPEN WIRE DETECTION STATUS WHILE ON

OPEN WIRE WHILE ON DO1	3x00031 4x00031 I:30	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of an open wire in state ON for DO1:0=OK				
The current detection state of an open wire in the output state ON for the digital output DOx =0:No fault, =1:Fault-open wire detected						
OPEN WIRE WHILE ON DO2	3x00032 4x00032 I:31	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of an open wire in state ON for DO2:0=OK				

OPEN WIRE WHILE ON DO3	3x00033 4x00033 I:32	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of an open wire in state ON for DO3:0=OK				
OPEN WIRE WHILE ON DO4	3x00034 4x00034 I:33	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of an open wire in state ON for DO4:0=OK				
OPEN WIRE WHILE ON DO5	3x00035 4x00035 I:34	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of an open wire in state ON for DO5:0=OK				
OPEN WIRE WHILE ON DO6	3x00036 4x00036 I:35	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of an open wire in state ON for DO6:0=OK				
DIGITAL OUTPUTS: OPEN WIRE DETECTION STATUS WHILE OFF						
OPEN WIRE WHILE OFF DO1	3x00037 4x00037 I:36	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of an open wire in state OFF for DO1:0=OK				
The current detection state of an open wire in the output state OFF for the digital output DOx =0:No fault, =1:Fault-open wire detected						
OPEN WIRE WHILE OFF DO2	3x00038 4x00038 I:37	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of an open wire in state OFF for DO2:0=OK				
OPEN WIRE WHILE OFF DO3	3x00039 4x00039 I:38	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of an open wire in state OFF for DO3:0=OK				
OPEN WIRE WHILE OFF DO4	3x00040 4x00040 I:39	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of an open wire in state OFF for DO4:0=OK				
OPEN WIRE WHILE OFF DO5	3x00041 4x00041 I:40	0,0x0000 B:00 00			UINT16 R/O	

		Current detection state of an open wire in state OFF for DO5:0=OK			
OPEN WIRE WHILE OFF DO6	3x00042 4x00042 I:41	0,0x0000 B:00 00		UINT16 R/O	
		Current detection state of an open wire in state OFF for DO6:0=OK			
DIGITAL OUTPUTS: SHORTCUT DETECTION STATUS TO VDD WHILE OFF					
OPEN WIRE SHORTCUT TO VDD DO1	3x00043 4x00043 I:42	0,0x0000 B:00 00		UINT16 R/O	
		Current detection state of a shortcut to VDD in state OFF for DO1:0=OK			
The current detection state of a shortcut to VDD in the output state OFF for the digital output DOx =0:No fault, =1:Fault-shortcut to VDD detected					
OPEN WIRE SHORTCUT TO VDD DO2	3x00044 4x00044 I:43	0,0x0000 B:00 00		UINT16 R/O	
		Current detection state of a shortcut to VDD in state OFF for DO2:0=OK			
OPEN WIRE SHORTCUT TO VDD DO3	3x00045 4x00045 I:44	0,0x0000 B:00 00		UINT16 R/O	
		Current detection state of a shortcut to VDD in state OFF for DO3:0=OK			
OPEN WIRE SHORTCUT TO VDD DO4	3x00046 4x00046 I:45	0,0x0000 B:00 00		UINT16 R/O	
		Current detection state of a shortcut to VDD in state OFF for DO4:0=OK			
OPEN WIRE SHORTCUT TO VDD DO5	3x00047 4x00047 I:46	0,0x0000 B:00 00		UINT16 R/O	
		Current detection state of a shortcut to VDD in state OFF for DO5:0=OK			
OPEN WIRE SHORTCUT TO VDD DO6	3x00048 4x00048 I:47	0,0x0000 B:00 00		UINT16 R/O	
		Current detection state of a shortcut to VDD in state OFF for DO6:0=OK			
DIGITAL OUTPUTS: THERMAL OVERLOAD DETECTION STATUS					
THERMAL OVERLOAD DETECTION STATUS DO1	3x00049 4x00049 I:48	0,0x0000 B:00 00		UINT16 R/O	
		Current detection state of a thermal overload for DO1:0=OK			

The current detection state of a thermal overload for the digital output DOx
=0:No fault, =1:Fault-thermal overload detected

THERMAL OVERLOAD DETECTION STATUS DO2	3x00050 4x00050 I:49	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a thermal overload for DO2:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO3	3x00051 4x00051 I:50	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a thermal overload for DO3:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO4	3x00052 4x00052 I:51	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a thermal overload for DO4:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO5	3x00053 4x00053 I:52	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a thermal overload for DO5:0=OK				
THERMAL OVERLOAD DETECTION STATUS DO6	3x00054 4x00054 I:53	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a thermal overload for DO6:0=OK				

DIGITAL OUTPUTS: CURRENT LIMIT DETECTION STATUS WHILE ON

CURRENT LIMIT DETECTION STATUS WHILE ON DO1	3x00055 4x00055 I:54	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a current limit while ON for DO1:0=OK				

The current detection state of a current limit while output is ON for the digital output DOx
=0:No fault, =1:Fault-current limit error

CURRENT LIMIT DETECTION STATUS WHILE ON DO2	3x00056 4x00056 I:55	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a current limit while ON for DO2:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO3	3x00057 4x00057 I:56	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a current limit while ON for DO3:0=OK				

CURRENT LIMIT DETECTION STATUS WHILE ON DO4	3x00058 4x00058 I:57	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a current limit while ON for DO4:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO5	3x00059 4x00059 I:58	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a current limit while ON for DO5:0=OK				
CURRENT LIMIT DETECTION STATUS WHILE ON DO6	3x00060 4x00060 I:59	0,0x0000 B:00 00			UINT16 R/O	
		Current detection state of a current limit while ON for DO6:0=OK				
DIGITAL OUTPUTS: GLOBAL ERRORS DO1-DO6						
GLOBAL ERRORS FOR DO1-DO6 BIT 0	3x00061 4x00061 I:60	0,0x0000 B:00 00			UINT16 R/O	
		BIT 0:Internal under voltage detected:0=OK				
GLOBAL ERRORS FOR DO1-DO6 BIT 1	3x00062 4x00062 I:61	0,0x0000 B:00 00			UINT16 R/O	
		BIT 1:VA under voltage detected (<2.3V):0=OK				
GLOBAL ERRORS FOR DO1-DO6 BIT 2	3x00063 4x00063 I:62	0,0x0000 B:00 00			UINT16 R/O	
		BIT 2:VDD not good detected (<17V):0=OK				
GLOBAL ERRORS FOR DO1-DO6 BIT 3	3x00064 4x00064 I:63	0,0x0000 B:00 00			UINT16 R/O	
		BIT 3:VDD warning detected (<12V):0=OK				
GLOBAL ERRORS FOR DO1-DO6 BIT 4	3x00065 4x00065 I:64	0,0x0000 B:00 00			UINT16 R/O	
		BIT 4:VDD under voltage detected (<8V):0=OK				
GLOBAL ERRORS FOR DO1-DO6 BIT 5	3x00066 4x00066 I:65	0,0x0000 B:00 00			UINT16 R/O	
		BIT 5:Thermal shutdown:0=OK				
GLOBAL ERRORS FOR DO1-DO6 BIT 6	3x00067 4x00067 I:66	0,0x0000 B:00 00			UINT16 R/O	
		BIT 6:Synchronisation error detected:0=OK				
GLOBAL ERRORS FOR DO1-DO6 BIT 7	3x00068 4x00068 I:67	0,0x0000 B:00 00			UINT16 R/O	

		BIT 7:Watchdog error detected:0=OK				
The global error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
DIGITAL OUTPUTS: INTERRUPT STATUS DO1-DO6						
INTERRUPT STATUS FOR DO1-DO6 BIT 0	3x00069 4x00069 I:68	0,0x0000 B:00 00			UINT16 R/O	
		BIT 0:Overload detected:0=OK				
INTERRUPT STATUS FOR DO1-DO6 BIT 1	3x00070 4x00070 I:69	0,0x0000 B:00 00			UINT16 R/O	
		BIT 1:Current limit detected:0=OK				
INTERRUPT STATUS FOR DO1-DO6 BIT 2	3x00071 4x00071 I:70	0,0x0000 B:00 00			UINT16 R/O	
		BIT 2:Open wire while OFF detected:0=OK				
INTERRUPT STATUS FOR DO1-DO6 BIT 3	3x00072 4x00072 I:71	1,0x0001 B:00 01			UINT16 R/O	
		BIT 3:Open wire while ON detected:1=FAULT				
INTERRUPT STATUS FOR DO1-DO6 BIT 4	3x00073 4x00073 I:72	1,0x0001 B:00 01			UINT16 R/O	
		BIT 4:Short to VDD while ON detected:1=FAULT				
INTERRUPT STATUS FOR DO1-DO6 BIT 5	3x00074 4x00074 I:73	0,0x0000 B:00 00			UINT16 R/O	
		BIT 5:Thermal error detected-shutdown:0=OK				
INTERRUPT STATUS FOR DO1-DO6 BIT 6	3x00075 4x00075 I:74	0,0x0000 B:00 00			UINT16 R/O	
		BIT 6:Supply error detected:0=OK				
INTERRUPT STATUS FOR DO1-DO6 BIT 7	3x00076 4x00076 I:75	1,0x0001 B:00 01			UINT16 R/O	
		BIT 7:Communication error detected:1=FAULT				
The global interrupt error state for the output group. Each bit stands for a different error =0:No fault, =1:Fault						
SPI COMMUNICATION DIGITAL OUTPUTS						
SPI COMMUNICATION DO1-DO6	3x00077 4x00077 I:76	1,0x0001 B:00 01			UINT16 R/O	
		Current SPI communication for DO1-DO6:1=FAULT				
The current monitoring state of the SPI communication for the digital output group =0:No fault, =1:Fault						
DIGITAL INPUTS						

STATUS DI1 A	3x00094 4x00094 I:93	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
Status for the digital input Dlx Bit 0-4: Lower 5 bits of CHANGE COUNTER Bit 5-9: Lower 5 bits of RISING EDGE COUNTER Bit 10-14: Lower 5 bits of FALLING EDGE COUNTER Bit 15: Current Status of Dlx =0: Dlx si OFF, =1: Dlx is ON						
STATUS DI1 B	3x00095 4x00095 I:94	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
Status for the digital input Dlx Bit 0-4: Lower 5 bits of SHORT KEYPRESS EVENTS Bit 5-9: Lower 5 bits of LONG KEYPRESS START EVENTS Bit 10-14: Lower 5 bits of LONG KEYPRESS END EVENTS Bit 15: Current Status of Dlx =0: Dlx si OFF, =1: Dlx is ON						
STATUS DI2 A	3x00096 4x00096 I:95	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI2 B	3x00097 4x00097 I:96	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI3 A	3x00098 4x00098 I:97	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI3 B	3x00099 4x00099 I:98	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI4 A	3x00100 4x00100 I:99	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI4 B	3x00101 4x00101 I:100	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI5 A	3x00102 4x00102 I:101	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				

STATUS DI5 B	3x00103 4x00103 I:102	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS DI6 A	3x00104 4x00104 I:103	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,CC:0,REC:0,FEC:0				
STATUS DI6 B	3x00105 4x00105 I:104	0,0x0000 B:00 00			UINT16 R/O	
		DI:0,SKE:0,LKSE:0,LKEE:0				
STATUS						
FILTER PATTERN DI1	3x00106 4x00106 I:105	0,0x00000000 B:00 00 00 00			UINT32 R/O	
The internal pattern for corresponding digital input for AC/DC filtering. The internal used state is created out of this internal pattern via oversampling.						
FILTER PATTERN DI2	3x00108 4x00108 I:107	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI3	3x00110 4x00110 I:109	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI4	3x00112 4x00112 I:111	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI5	3x00114 4x00114 I:113	0,0x00000000 B:00 00 00 00			UINT32 R/O	
FILTER PATTERN DI6	3x00116 4x00116 I:115	0,0x00000000 B:00 00 00 00			UINT32 R/O	
PULSE TIME FOR DIGITAL OUTPUTS						
PULSE TIME DO1	3x00201 4x00201 I:200	0,0x0000 B:00 00	200	20,0	UINT16 R/W	NO
Generate a pulse on digital output x in 100ms units (0,1 to 6553,5 Seconds selectable) If you write onto this register, the digital output will be switched on for the desired time in 100ms units.						
PULSE TIME DO2	3x00202 4x00202 I:201	0,0x0000 B:00 00	300	30,0	UINT16 R/W	NO
PULSE TIME DO3	3x00203 4x00203 I:202	0,0x0000 B:00 00	400	40,0	UINT16 R/W	NO
PULSE TIME DO4	3x00204 4x00204 I:203	0,0x0000 B:00 00	500	50,0	UINT16 R/W	NO

PULSE TIME DO5	3x00205 4x00205 I:204	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE TIME DO6	3x00206 4x00206 I:205	0,0x0000 B:00 00	20	2,0	UINT16 R/W	NO
PULSE STATUS FOR DIGITAL OUTPUTS						
PULSE TIMER DO1	3x00301 4x00301 I:300	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
Remaining time of the pulse on digital output x in Milliseconds.						
PULSE TIMER DO2	3x00303 4x00303 I:302	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO3	3x00305 4x00305 I:304	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO4	3x00307 4x00307 I:306	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO5	3x00309 4x00309 I:308	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE TIMER DO6	3x00311 4x00311 I:310	0,0x00000000 B:00 00 00 00			UINT32 R/O	
		0,0 seconds				
PULSE STATUS FOR DIGITAL OUTPUTS						
PULSE TIMER DO1	3x00313 4x00313 I:312	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
Remaining time of the pulse on digital output x in Milliseconds.						
PULSE TIMER DO2	3x00315 4x00315 I:314	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO3	3x00317 4x00317 I:316	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				

PULSE TIMER DO4	3x00319 4x00319 I:318	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO5	3x00321 4x00321 I:320	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
PULSE TIMER DO6	3x00323 4x00323 I:322	0,0x00000000 B:00 00 00 00			UINT32R R/O	
		0,0 seconds				
GENERAL STATUS OF DIS						
RESET COUNTERS	3x10000 4x10000 I:9999	0,0x0000 B:00 00		1:PERFORM RESET	UINT16 R/W	NO
If this register is written to 1, all internal edge counters and event counters are set to 0. 0 is always returned when reading.						
HAS DIS CHANGED	3x10001 4x10001 I:10000	0,0x0000 B:00 00			UINT16 R/O	
		0 event(s)				
As soon as the module registrates an event on one of the available digital inputs, this global event counter is incremented by 1. Possible events are: Detection of a short keypress Detection of the start of a long keypress Detection of the end of a long keypress						
STATUS OF ALL DIS DI1..DI6	3x10002 4x10002 I:10001	0,0x0000 B:00 00			UINT16 R/O	
		Current state of DI1:0=OFF				
		Current state of DI2:0=OFF				
		Current state of DI3:0=OFF				
		Current state of DI4:0=OFF				
		Current state of DI5:0=OFF				
		Current state of DI6:0=OFF				
Current state of all digital inputs DI1..DI6 Bit 0: =0:DI1 is OFF, =1:DI1 is ON Bit 1: =0:DI2 is OFF, =1:DI2 is ON ... Bit 4: =0:DI5 is OFF, =1:DI5 is ON Bit 5: =0:DI6 is OFF, =1:DI6 is ON						
STATUS OF DIGITAL OUTPUTS						
STATUS OF DO1-DO6	3x10003 4x10003 I:10002	0,0x0000 B:00 00		0x003F	UINT16 R/W	NO
		Current state of DO1:0=OFF	1			
		Current state of DO2:0=OFF	1			
		Current state of DO3:0=OFF	1			

		Current state of DO4:0=OFF	1		
		Current state of DO5:0=OFF	1		
		Current state of DO6:0=OFF	1		
Current state of all digital outputs Bit 0: =0:DO1 is OFF, =1:DO1 is ON Bit 1: =0:DO2 is OFF, =1:DO2 is ON ... Bit 4: =0:DO5 is OFF, =1:DO5 is ON Bit 5: =0:DO6 is OFF, =1:DO6 is ON					
Write on this register sets all digital outputs to a new state					
DIGITAL OUTPUTS:ENABLE OPEN WIRE DETECTION WHILE ON					
ENABLE OPEN WIRE DETECTION WHILE ON DO1-DO6	3x10004 4x10004 I:10003	0,0x0000 B:00 00		0x003F	UINT16 R/W
		Current setup of open wire detection while ON for DO1:0=OFF	1		
		Current setup of open wire detection while ON for DO2:0=OFF	1		
		Current setup of open wire detection while ON for DO3:0=OFF	1		
		Current setup of open wire detection while ON for DO4:0=OFF	1		
		Current setup of open wire detection while ON for DO5:0=OFF	1		
		Current setup of open wire detection while ON for DO6:0=OFF	1		
Current setup state for open wire detection while ON for digital output DOx Bit 0: =0:Open wire detection for DO1 is OFF, =1:Open wire detection for DO1 is ON Bit 1: =0:Open wire detection for DO2 is OFF, =1:Open wire detection for DO2 is ON ... Bit 4: =0:Open wire detection for DO5 is OFF, =1:Open wire detection for DO5 is ON Bit 5: =0:Open wire detection for DO6 is OFF, =1:Open wire detection for DO6 is ON					
Write on this register sets for all digital outputs a new setup state					
DIGITAL OUTPUTS:ENABLE OPEN WIRE DETECTION WHILE OFF					
ENABLE OPEN WIRE DETECTION WHILE OFF DO1-DO6	3x10005 4x10005 I:10004	0,0x0000 B:00 00		0x003F	UINT16 R/W
		Current setup of open wire detection while OFF for DO1:0=OFF	1		
		Current setup of open wire detection while OFF for DO2:0=OFF	1		
		Current setup of open wire detection while OFF for DO3:0=OFF	1		
		Current setup of open wire detection while OFF for DO4:0=OFF	1		
		Current setup of open wire detection while OFF for DO5:0=OFF	1		

		Current setup of open wire detection while OFF for DO6:0=OFF	1		
Current setup state for open wire detection while OFF for digital output DOx Bit 0: =0:Open wire detection for DO1 is OFF, =1:Open wire detection for DO1 is ON Bit 1: =0:Open wire detection for DO2 is OFF, =1:Open wire detection for DO2 is ON ... Bit 4: =0:Open wire detection for DO5 is OFF, =1:Open wire detection for DO5 is ON Bit 5: =0:Open wire detection for DO6 is OFF, =1:Open wire detection for DO6 is ON Write on this register sets for all digital outputs a new setup state					
DIGITAL OUTPUTS:ENABLE SHORTCUT TO VDD DETECTION WHILE OFF					
ENABLE SHORTCUT TO VDD DETECTION WHILE OFF DO1-DO6	3x10006 4x10006 I:10005	0,0x0000 B:00 00		0x003F	UINT16 R/W
		Current setup of shortcut detection to VDD while OFF for DO1:0=OFF	1		
		Current setup of shortcut detection to VDD while OFF for DO2:0=OFF	1		
		Current setup of shortcut detection to VDD while OFF for DO3:0=OFF	1		
		Current setup of shortcut detection to VDD while OFF for DO4:0=OFF	1		
		Current setup of shortcut detection to VDD while OFF for DO5:0=OFF	1		
		Current setup of shortcut detection to VDD while OFF for DO6:0=OFF	1		
Current setup state for shortcut to VDD detection while OFF for digital output DOx Bit 0: =0:Shortcut to VDD detection for DO1 is OFF, =1:Shortcut to VDD detection for DO1 is ON Bit 1: =0:Shortcut to VDD detection for DO2 is OFF, =1:Shortcut to VDD detection for DO2 is ON ... Bit 4: =0:Shortcut to VDD detection for DO5 is OFF, =1:Shortcut to VDD detection for DO5 is ON Bit 5: =0:Shortcut to VDD detection for DO6 is OFF, =1:Shortcut to VDD detection for DO6 is ON Write on this register sets for all digital outputs a new setup state					
DIGITAL OUTPUTS:OPEN WIRE WHILE ON DETECTION STATE					
OPEN WIRE DETECTION STATE WHILE ON DO1-DO6	3x10007 4x10007 I:10006	0,0x0000 B:00 00			UINT16 R/O
		Current state of open wire detection while ON for DO1:0=OK			
		Current state of open wire detection while ON for DO2:0=OK			
		Current state of open wire detection while ON for DO3:0=OK			
		Current state of open wire detection while ON for DO4:0=OK			
		Current state of open wire detection while ON for DO5:0=OK			

		Current state of open wire detection while ON for DO6:0=OK			
Current diagnostic state for open wire detection while ON for digital output DOx Bit 0: =0:Output DO1 is OK, =1:Fault-Open wire detected on DO1 Bit 1: =0:Output DO2 is OK, =1:Fault-Open wire detected on DO2 ... Bit 4: =0:Output DO5 is OK, =1:Fault-Open wire detected on DO5 Bit 5: =0:Output DO6 is OK, =1:Fault-Open wire detected on DO6					
DIGITAL OUTPUTS:OPEN WIRE WHILE OFF DETECTION STATE					
OPEN WIRE DETECTION STATE WHILE OFF DO1-DO6	3x10008 4x10008 l:10007	0,0x0000 B:00 00			UINT16 R/O
		Current state of open wire detection while OFF for DO1:0=OK			
		Current state of open wire detection while OFF for DO2:0=OK			
		Current state of open wire detection while OFF for DO3:0=OK			
		Current state of open wire detection while OFF for DO4:0=OK			
		Current state of open wire detection while OFF for DO5:0=OK			
		Current state of open wire detection while OFF for DO6:0=OK			
Current diagnostic state for open wire detection while OFF for digital output DOx Bit 0: =0:Output DO1 is OK, =1:Fault-Open wire detected on DO1 Bit 1: =0:Output DO2 is OK, =1:Fault-Open wire detected on DO2 ... Bit 4: =0:Output DO5 is OK, =1:Fault-Open wire detected on DO5 Bit 5: =0:Output DO6 is OK, =1:Fault-Open wire detected on DO6					
DIGITAL OUTPUTS:SHORTCUT TO VDD WHILE OFF DETECTION STATE					
SHORTCUT TO VDD WHILE OFF DETECTION STATE DO1-DO6	3x10009 4x10009 l:10008	0,0x0000 B:00 00			UINT16 R/O
		Current state of shortcut to VDD detection while OFF for DO1:0=OK			
		Current state of shortcut to VDD detection while OFF for DO2:0=OK			
		Current state of shortcut to VDD detection while OFF for DO3:0=OK			
		Current state of shortcut to VDD detection while OFF for DO4:0=OK			
		Current state of shortcut to VDD detection while OFF for DO5:0=OK			
		Current state of shortcut to VDD detection while OFF for DO6:0=OK			

Current diagnostic state for shortcut to VDD detection while OFF for digital output DOx
Bit 0: =0:Output DO1 is OK, =1:Fault-shortcut detected on DO1
Bit 1: =0:Output DO2 is OK, =1:Fault-shortcut detected on DO2
...
Bit 4: =0:Output DO5 is OK, =1:Fault-shortcut detected on DO5
Bit 5: =0:Output DO6 is OK, =1:Fault-shortcut detected on DO6

DIGITAL OUTPUTS:THERMAL OVERLOAD DETECTION STATE

THERMAL OVERLOAD DETECTION STATE DO1-DO6	3x10010 4x10010 I:10009	0,0x0000 B:00 00			UINT16 R/O	
		Current thermal overload detection state for DO1:0=OK				
		Current thermal overload detection state for DO2:0=OK				
		Current thermal overload detection state for DO3:0=OK				
		Current thermal overload detection state for DO4:0=OK				
		Current thermal overload detection state for DO5:0=OK				
		Current thermal overload detection state for DO6:0=OK				

Current thermal overload detection state for digital output DOx
Bit 0: =0:Output DO1 is OK, =1:Fault-Thermal overload on DO1
Bit 1: =0:Output DO2 is OK, =1:Fault-Thermal overload on DO2
...
Bit 4: =0:Output DO5 is OK, =1:Fault-Thermal overload on DO5
Bit 5: =0:Output DO6 is OK, =1:Fault-Thermal overload on DO6

DIGITAL OUTPUTS:CURRENT LIMIT DETECTION STATE

CURRENT LIMIT DETECTION STATE DO1-DO6	3x10011 4x10011 I:10010	0,0x0000 B:00 00			UINT16 R/O	
		Current current limit detection state for DO1:0=OK				
		Current current limit detection state for DO2:0=OK				
		Current current limit detection state for DO3:0=OK				
		Current current limit detection state for DO4:0=OK				
		Current current limit detection state for DO5:0=OK				
		Current current limit detection state for DO6:0=OK				

Current current limit detection state for digital output DOx
Bit 0: =0:Output DO1 is OK, =1:Fault-Current limit on DO1
Bit 1: =0:Output DO2 is OK, =1:Fault-Current limit on DO2
...
Bit 4: =0:Output DO5 is OK, =1:Fault-Current limit on DO5
Bit 5: =0:Output DO6 is OK, =1:Fault-Current limit on DO6

DIGITAL OUTPUTS: GLOBAL ERRORS

GLOBAL ERRORS FOR DO1-DO6	3x10012 4x10012 l:10011	0,0x00 B:00 00			BIT R/O	
		BIT 0:Internal under voltage detected:0=FAULT				
		BIT 1:VA under voltage detected (<2.3V):0=FAULT				
		BIT 2:VDD not good detected (<17V):0=FAULT				
		BIT 3:VDD warning detected (<12V):0=FAULT				
		BIT 4:VDD under voltage detected (<8V):0=FAULT				
		BIT 5:Thermal shutdown:0=FAULT				
		BIT 6:Synchronisation error detected:0=FAULT				
		BIT 7:Watchdog error detected:0=FAULT				

The global error state for the output group. Each bit stands for a different error
=0:No fault, =1:Fault

DIGITAL OUTPUTS: INTERRUPT STATUS

INTERRUPT STATUS FOR DO1-DO6	3x10013 4x10013 l:10012	0,0x00 B:00 00			BIT R/O	
		BIT 0:Overload detected:0=FAULT				
		BIT 1:Current limit detected:0=FAULT				
		BIT 2:Open wire while OFF detected:0=FAULT				
		BIT 3:Open wire while ON detected:0=FAULT				
		BIT 4:Shortcut to VDD detected:0=FAULT				
		BIT 5:Thermal shutdown:0=FAULT				
		BIT 6:Supply error detected:0=FAULT				
		BIT 7:Communication error detected:0=FAULT				

The interrupt state for the output group. Each bit stands for a different error
=0:No fault, =1:Fault

SPI COMMUNICATION DIGITAL OUTPUTS

SPI COMMUNICATION DIGITAL OUTPUTS	3x10014 4x10014 l:10013	115,0x0073 B:00 73			UINT16 R/O	
		Current SPI communcation state of DO1-DO6:1=FAULT				

The current monitoring state of the SPI communication for the digital output group
=0:No fault, =1:Fault

Current SPI communication state of all digital output groups
Bit 0: =0:DO1-DO6 No fault, =1:DO1-DO6 Fault

DIGITAL OUTPUTS: CHIPSET TYPE

DIGITAL OUTPUTS CHIPSET TYPE	3x10100 4x10100 l:10099	1,0x0001 B:00 01			UINT16 R/O	
		Current chipset for DOs:1=MAX14915				

The current chipset for the digital outputs: =0: NCV7608 =1: MAX14915					
INITIAL & WATCHDOG STATUS FOR ALL DIGITAL OUTPUTS					
INITIAL & WATCHDOG STATUS OF DO1-DO6	3x59001 4x59001 I:59000	0,0x0000 B:00 00		0x003F	UINT16 R/W
		Current init & watchdog state of DO1:0=OFF	1		
		Current init & watchdog state of DO2:0=OFF	1		
		Current init & watchdog state of DO3:0=OFF	1		
		Current init & watchdog state of DO4:0=OFF	1		
		Current init & watchdog state of DO5:0=OFF	1		
		Current init & watchdog state of DO6:0=OFF	1		
Current initial and watchdog state of all digital outputs. This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured Bit 0: =0:DO1 is OFF, =1:DO1 is ON Bit 1: =0:DO2 is OFF, =1:DO2 is ON ... Bit 4: =0:DO5 is OFF, =1:DO5 is ON Bit 5: =0:DO6 is OFF, =1:DO6 is ON					
Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FLASH					
DIGITAL OUTPUTS:ENABLE OPEN WIRE DETECTION WHILE ON					
INITIAL & WATCHDOG SETUP ENABLE OPEN WIRE DETECTION WHILE ON DO1-DO6	3x59002 4x59002 I:59001	0,0x0000 B:00 00		0x003F	UINT16 R/W
		Initial setup of open wire detection while ON for DO1:0=OFF	1		
		Initial setup of open wire detection while ON for DO2:0=OFF	1		
		Initial setup of open wire detection while ON for DO3:0=OFF	1		
		Initial setup of open wire detection while ON for DO4:0=OFF	1		
		Initial setup of open wire detection while ON for DO5:0=OFF	1		
		Initial setup of open wire detection while ON for DO6:0=OFF	1		
Initial setup state for open wire detection while ON for digital output DOx. Used after power on and watchdog event Bit 0: =0:Open wire detection for DO1 is OFF, =1:Open wire detection for DO1 is ON Bit 1: =0:Open wire detection for DO2 is OFF, =1:Open wire detection for DO2 is ON ... Bit 4: =0:Open wire detection for DO5 is OFF, =1:Open wire detection for DO5 is ON Bit 5: =0:Open wire detection for DO6 is OFF, =1:Open wire detection for DO6 is ON					
Write on this register sets for all digital outputs a new setup state					
DIGITAL OUTPUTS:ENABLE OPEN WIRE DETECTION WHILE OFF					
INITIAL & WATCHDOG SETUP ENABLE OPEN WIRE DETECTION WHILE OFF DO1-DO6	3x59003 4x59003 I:59002	0,0x0000 B:00 00		0x003F	UINT16 R/W

		Initial setup of open wire detection while OFF for DO1:0=OFF	1		
		Initial setup of open wire detection while OFF for DO2:0=OFF	1		
		Initial setup of open wire detection while OFF for DO3:0=OFF	1		
		Initial setup of open wire detection while OFF for DO4:0=OFF	1		
		Initial setup of open wire detection while OFF for DO5:0=OFF	1		
		Initial setup of open wire detection while OFF for DO6:0=OFF	1		

Initial setup state for open wire detection while OFF for digital output DOx. Used after power on and watchdog event
Bit 0: =0:Open wire detection for DO1 is OFF, =1:Open wire detection for DO1 is ON
Bit 1: =0:Open wire detection for DO2 is OFF, =1:Open wire detection for DO2 is ON
...
Bit 4: =0:Open wire detection for DO5 is OFF, =1:Open wire detection for DO5 is ON
Bit 5: =0:Open wire detection for DO6 is OFF, =1:Open wire detection for DO6 is ON

Write on this register sets for all digital outputs a new setup state

DIGITAL OUTPUTS:ENABLE OPEN WIRE DETECTION WHILE OFF

INITIAL & WATCHDOG SETUP ENABLE SHOTCUT TO VDD DETECTION WHILE OFF DO1-DO6	3x59004 4x59004 1:59003	0,0x0000 B:00 00		0x003F	UINT16 R/W	NO
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		Initial setup of shortcut to VDD detection while OFF for DO1:0=OFF	1		
		Initial setup of shortcut to VDD detection while OFF for DO2:0=OFF	1		
		Initial setup of shortcut to VDD detection while OFF for DO3:0=OFF	1		
		Initial setup of shortcut to VDD detection while OFF for DO4:0=OFF	1		
		Initial setup of shortcut to VDD detection while OFF for DO5:0=OFF	1		
		Initial setup of shortcut to VDD detection while OFF for DO6:0=OFF	1		

Initial setup state for shortcut to VDD detection while OFF for digital output DOx. Used after power on and watchdog event
Bit 0: =0:Shortcut detection for DO1 is OFF, =1:Shortcut detection for DO1 is ON
Bit 1: =0:Shortcut detection for DO2 is OFF, =1:Shortcut detection for DO2 is ON
...
Bit 4: =0:Shortcut detection for DO5 is OFF, =1:Shortcut detection for DO5 is ON
Bit 5: =0:Shortcut detection for DO6 is OFF, =1:Shortcut detection for DO6 is ON

Write on this register sets for all digital outputs a new setup state

Command NAME	ASCII command type	ASCII command structure	NEW REAL VALUE	NEW VALUE	DATA TYPE	DO WRITE
ASCII COMMANDS						
DIGITAL OUTPUTS						
SET DIGITAL OUTPUTS	ASCII WRITE COMMAND	#SDOS:<OutAllDOS> <CR> Result: #OK<CR>			ASCII	YES
	DO1	1:ON				
	DO2	0:OFF				
	DO3	1:ON				
	DO4	0:OFF				
	DO5	0:OFF				
	DO6	0:OFF				
	TX	#255,SDOS:5<CR>				
	RX	N/A				
Sets all digital outputs to the new state OutAllDOS The new state for all digital outputs Bit 0: State of DO1 (=0:OFF, =1:ON) Bit 1: State of DO2 (=0:OFF, =1:ON) ... Bit 4: State of DO5 (=0:OFF, =1:ON) Bit 5: State of DO6 (=0:OFF, =1:ON)						
SET DIGITAL OUTPUT DOx	ASCII WRITE COMMAND	#SDO<DONR>:<Out> <CR> Result: #OK<CR>			ASCII	YES
	DONR	1				
	DO1	1:ON				
	TX	#255,SDO1:1<CR>				
	RX	N/A				
Sets the new state for digital output DOx. The state is defined with <Out>. Out The new state of the digital output DOx: =0: digital output is OFF =1: digital output is ON						
GET DIGITAL OUTPUTS	ASCII READ COMMAND	#GDOS<CR> Result: #GDOS:<DOSDec>,<DOSHex> <CR>			ASCII	
	TX	#255,GDOS<CR>				
	RX	#255,GDOS:0,0x0<CR>				
		Current status of digital outputs:00.0000				

Returns the current state of the digital outputs as decimal number and as hexadecimal number.
DOSDec, DOSHex
The current state of the digital outputs:
Bit 0: State of DO1 (=0:OFF, =1:ON)
Bit 1: State of DO2 (=0:OFF, =1:ON)
...
Bit 4: State of DO5 (=0:OFF, =1:ON)
Bit 5: State of DO6 (=0:OFF, =1:ON)

GET DIGITAL OUTPUT DOx	ASCII READ COMMAND	#GDO<DONR> <CR> Result: #GDO<DONR>:<DOxDec>,<DOxHex> <CR>	ASCII	
	DONR	1		
	TX	#255,GDO1<CR>		
	RX	#255,GDO1:0,0x0<CR>		
		Current status of digital output DO1:0=OFF		

Returns the current state of the digital output DOx as decimal number and as hexadecimal number.
DOxDec, DOxHex
The current state of the digital output DOx:
=0: digital output is OFF
=1: digital output is ON

DIGITAL OUTPUTS: ENABLE OPEN WIRE DETECTION WHILE ON

SET DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE ON	ASCII WRITE COMMAND	#SDOEOWDONS:<OpenWireDOS> <CR> Result: #OK<CR>	ASCII	YES
	DO1	1:ENABLE		
	DO2	1:ENABLE		
	DO3	1:ENABLE		
	DO4	1:ENABLE		
	DO5	1:ENABLE		
	DO6	1:ENABLE		
	TX	#255,SDOEOWDONS:63<CR>		
	RX	N/A		

Sets the open wire mode for all digital outputs to the new mode OpenWireDOS. This enables the diagnostic of open wire while the digital output is ON.
The new state for all digital outputs
Bit 0: New mode for DO1 (=0:DISABLED, =1:ENABLED)
Bit 1: New mode for DO2 (=0:DISABLED, =1:ENABLED)
...
Bit 4: New mode for DO5 (=0:DISABLED, =1:ENABLED)
Bit 5: New mode for DO6 (=0:DISABLED, =1:ENABLED)

SET DIGITAL OUTPUT ENABLE OPEN WIRE DETECTION WHILE ON	ASCII WRITE COMMAND	#SDOEOWDON<DONR>:<OpenWireDOx> <CR> Result: #OK<CR>	ASCII	YES
	DONR	1		
	DO1	0:DISABLE		
	TX	#255,SDOEOWDON1:0<CR>		
	RX	N/A		

Sets the open wire mode for digital output DOx to the new mode OpenWireDOx. This enables the diagnostic of open wire while the digital output is ON.

The new mode of the digital output DOx:

=0: diagnostic mode for digital output is DISABLED

=1: diagnostic mode for digital output is ENABLED

GET DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE ON	ASCII READ COMMAND	#GDOEOWDONS<CR> Result: #GDOEOWDONS:<OpenWireDOSDec>,<OpenWireDOSHex> <CR>	ASCII	
	TX	#255,GDOEOWDONS<CR>		
	RX	#255,GDOEOWDONS:0,0x0<CR>		
		Current mode for open wire diagnostic while ON of digital outputs:00.0000		

Returns the current mode for open wire diagnostic while digital output is ON as decimal number and as hexadecimal number.

OpenWireDOSDec, OpenWireDOSHex

The current mode for open wire diagnostic while ON of the digital outputs:

Bit 0: Open wire diagnostic mode of DO1 (=0:DISABLED, =1:ENABLED)

Bit 1: Open wire diagnostic mode of DO2 (=0:DISABLED, =1:ENABLED)

...

Bit 4: Open wire diagnostic mode of DO5 (=0:DISABLED, =1:ENABLED)

Bit 5: Open wire diagnostic mode of DO6 (=0:DISABLED, =1:ENABLED)

GET DIGITAL OUTPUT ENABLE OPEN WIRE DETECTION WHILE ON	ASCII READ COMMAND	#GDOEOWDON<DONR> <CR> Result: #GDOEOWDON<DONR>:<OpenWireDOxDec>,<OpenWireDOxHex> <CR>	ASCII	
	DONR	1		
	TX	#255,GDOEOWDON1<CR>		
	RX	#255,GDOEOWDON1:0,0x0<CR>		
		Current open wire diagnostic mode while ON of digital output DO1:0=DISABLED		

Returns the current open wire diagnostic mode while ON of the digital output DOx as decimal number and as hexadecimal number. X stands for the number of the digital output.

OpenWireDOxDec, OpenWireDOxHex

The current diagnostic mode of the digital output DOx:

=0: open wire diagnostic mode for digital output is DISABLED

=1: open wire diagnostic mode for digital output is ENABLED

DIGITAL OUTPUTS: ENABLE OPEN WIRE DETECTION WHILE OFF

SET DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE OFF	ASCII WRITE COMMAND	#SDOEOWDOFFS:<OpenWireDOS> <CR> Result: #OK<CR>	ASCII	YES
	DO1	1:ENABLE		
	DO2	1:ENABLE		
	DO3	1:ENABLE		
	DO4	1:ENABLE		
	DO5	1:ENABLE		
	DO6	1:ENABLE		
	TX	#255,SDOEOWDOFFS:63<CR>		
	RX	N/A		

Sets the open wire mode for all digital outputs to the new mode OpenWireDOS. This enables the diagnostic of open wire while the digital output is OFF.

The new state for all digital outputs

Bit 0: New mode for DO1 (=0:DISABLED, =1:ENABLED)

Bit 1: New mode for DO2 (=0:DISABLED, =1:ENABLED)

...

Bit 4: New mode for DO5 (=0:DISABLED, =1:ENABLED)

Bit 5: New mode for DO6 (=0:DISABLED, =1:ENABLED)

SET DIGITAL OUTPUT ENABLE OPEN WIRE DETECTION WHILE OFF	ASCII WRITE COMMAND	#SDOEOWDOFF<DONR>:<OpenWireDOx> <CR> Result: #OK<CR>	ASCII	YES
	DONR	1		
	DO1	0:DISABLE		
	TX	#255,SDOEOWDOFF1:0<CR>		
	RX	N/A		

Sets the open wire mode for digital output DOx to the new mode OpenWireDOx. This enables the diagnostic of open wire while the digital output is OFF.

The new mode of the digital output DOx:

=0: diagnostic mode for digital output is DISABLED

=1: diagnostic mode for digital output is ENABLED

GET DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE OFF	ASCII READ COMMAND	#GDOEOWDOFFS<CR> Result: #GDOEOWDOFFS:<OpenWireDOSDec>,<OpenWireDOSHex> <CR>	ASCII	
	TX	#255,GDOEOWDOFFS<CR>		
	RX	#255,GDOEOWDOFFS:0,0x0<CR>		
		Current mode for open wire diagnostic while OFF of digital outputs:00.0000		

Returns the current mode for open wire diagnostic while digital output is OFF as decimal number and as hexadecimal number.

OpenWireDOSDec, OpenWireDOSHex

The current mode for open wire diagnostic while OFF of the digital outputs:

Bit 0: Open wire diagnostic mode of DO1 (=0:DISABLED, =1:ENABLED)

Bit 1: Open wire diagnostic mode of DO2 (=0:DISABLED, =1:ENABLED)

...

Bit 4: Open wire diagnostic mode of DO5 (=0:DISABLED, =1:ENABLED)

Bit 5: Open wire diagnostic mode of DO6 (=0:DISABLED, =1:ENABLED)

GET DIGITAL OUTPUT ENABLE OPEN WIRE DETECTION WHILE OFF	ASCII READ COMMAND	#GDOEOWDOFF<DONR> <CR> Result: #GDOEOWDOFF<DONR>:<OpenWireDOxDec>,<OpenWireDOxHex> <CR>	ASCII	
	DONR	1		
	TX	#255,GDOEOWDOFF1<CR>		
	RX	#255,GDOEOWDOFF1:0,0x0<CR>		
		Current open wire diagnostic mode while OFF of digital output DO1:0=DISABLED		

Returns the current open wire diagnostic mode while OFF of the digital output DOx as decimal number and as hexadecimal number. X stands for the number of the digital output.

OpenWireDOxDec, OpenWireDOxHex

The current diagnostic mode of the digital output DOx:

=0: open wire diagnostic mode for digital output is DISABLED

=1: open wire diagnostic mode for digital output is ENABLED

DIGITAL OUTPUTS: ENABLE SHORTCUT TO VDD DETECTION WHILE OFF

SET DIGITAL OUTPUTS ENABLE SHORTCUT TO VDD DETECTION WHILE OFF	ASCII WRITE COMMAND	#SDOESVDDS:<ShortCutDOS> <CR> Result: #OK<CR>	ASCII	YES
	DO1	1:ENABLE		
	DO2	1:ENABLE		
	DO3	1:ENABLE		
	DO4	1:ENABLE		
	DO5	1:ENABLE		
	DO6	1:ENABLE		
	TX	#255,SDOESVDDS:63<CR>		

	RX	N/A		
Sets the shortcut to VDD detection mode for all digital outputs to the new mode ShortCutDOS. This enables the diagnostic of shortcut to VDD while the digital output is OFF. The new state for all digital outputs Bit 0: New mode for DO1 (=0:DISABLED, =1:ENABLED) Bit 1: New mode for DO2 (=0:DISABLED, =1:ENABLED) ... Bit 4: New mode for DO5 (=0:DISABLED, =1:ENABLED) Bit 5: New mode for DO6 (=0:DISABLED, =1:ENABLED)				
SET DIGITAL OUTPUT ENABLE SHORTCUT TO VDD DETECTION WHILE OFF	ASCII WRITE COMMAND	#SDOESVDD<DONR>:<ShortCutDOx> <CR> Result: #OK<CR>	ASCII	YES
	DONR	1		
	DO1	0:DISABLE		
	TX	#255,SDOESVDD1:0<CR>		
	RX	N/A		
Sets the shortcut to VDD mode for digital output DOx to the new mode ShortCutDOx. This enables the diagnostic of shortcut to VDD while the digital output is OFF. The new mode of the digital output DOx: =0: diagnostic mode for digital output is DISABLED =1: diagnostic mode for digital output is ENABLED				
GET DIGITAL OUTPUTS ENABLE SHORT CUT TO VDD DETECTION WHILE OFF	ASCII READ COMMAND	#GDOESVDDS<CR> Result: #GDOESDDS:<ShortCutDOSDec>,<ShortCutDOSHex> <CR>	ASCII	
	TX	#255,GDOESVDDS<CR>		
	RX	#255,GDOESVDDS:0,0x0<CR>		
		Current mode for shortcut to VDD diagnostic while OFF of digital outputs:00.0000		
Returns the current mode for shortcut to VDD diagnostic while digital output is OFF as decimal number and as hexadecimal number. ShortCutDOSDec, ShortCutDOSHex The current mode for shortcut diagnostic while OFF of the digital outputs: Bit 0: Open wire diagnostic mode of DO1 (=0:DISABLED, =1:ENABLED) Bit 1: Open wire diagnostic mode of DO2 (=0:DISABLED, =1:ENABLED) ... Bit 4: Open wire diagnostic mode of DO5 (=0:DISABLED, =1:ENABLED) Bit 5: Open wire diagnostic mode of DO6 (=0:DISABLED, =1:ENABLED)				
GET DIGITAL OUTPUT ENABLE SHORT CUT TO VDD DETECTION WHILE OFF	ASCII READ COMMAND	#GDOESVDD<DONR> <CR> Result: #GDOESVDD<DONR>:<ShortCutDOxDec>,<ShortCutDOxHex> <CR>	ASCII	
	DONR	1		
	TX	#255,GDOESVDD1<CR>		
	RX	#255,GDOESVDD1:0,0x0<CR>		
		Current shortcut to VDD diagnostic mode while OFF of digital output DO1:0=DISABLED		
Returns the current short cut to VDD diagnostic mode while OFF of the digital output DOx as decimal number and as hexadecimal number. X stands for the number of the digital output. ShortCutDOxDec, ShortCutDOxHex The current diagnostic mode of the digital output DOx: =0: open wire diagnostic mode for digital output is DISABLED =1: open wire diagnostic mode for digital output is ENABLED				
DIGITAL OUTPUTS: SPI STATUS				
GET SPI STATUS DIGITAL OUTPUT GROUPS	ASCII READ COMMAND	#GSSDOGS<CR> Result: #GSSDOGS:<SPIDOGSDec>,<SPIDOGSHex> <CR>	ASCII	

	TX	#255,GSSDOGS<CR>		
	RX	#255,GSSDOGS:0,0x0<CR>		
		Current SPI status of digital output groups:0		

Returns the current SPI communication state of the corresponding output group as decimal number and as hexadecimal number.

SPIDOGSDec,SPIDOGSHex

The current SPI communication state of the digital output group:

Bit 0: SPI communication state for digital output group #1 (DO1-DO6) (=0:NO FAULT, =1:FAULT)

GET SPI STATUS DIGITAL OUTPUT GROUPx	ASCII READ COMMAND	#GSSDOG<DOGRP> <CR> Result: #GSSDOG<DOGRP>:<SPIDOGxDec>,<SPIDOGxHex> <CR>	ASCII	
	DOGRP	1		
	TX	#255,GSSDOG1<CR>		
	RX	#255,GSSDOG1:0,0x0<CR>		
		Current SPI status of digital output group DOG1:0=NO FAULT		

Returns the current SPI communication state of the digital output group DOGRP as decimal number and as hexadecimal number. X stands for the number of the digital output group.

SPIDOGxDec, SPIDOGxHex

The current SPI communication state of the digital output group DOGRP:

=0: SPI communication state for output group is OK (NO FAULT)

=1: SPI communication state for output group is FAULT

Digital output group #1 is DO1-DO6

DIGITAL OUTPUTS: INTERRUPT STATUS

GET DIGITAL OUTPUTS INTERRUPT STATUS	ASCII READ COMMAND	#GDOINTS<CR> Result: #GDOINTS:<InterruptStatusDec>,<InterruptStatusHex> <CR>	ASCII	
	TX	#255,GDOINTS<CR>		
	RX	#255,GDOINTS:0,0x0<CR>		
		Current interrupt status of all digital output groups:0000.0000		

Returns the current interrupt state state of all output groups as decimal number and as hexadecimal number.

InterruptStatusDec,InterruptStatusHex

The current interrupt state of all digital output groups

Bit 0:DO1-DO6:Overload detected (0=OK,1=FAULT)

Bit 1:DO1-DO6:Current limit detected(0=OK,1=FAULT)

Bit 2:DO1-DO6:Open wire while OFF detected (0=OK,1=FAULT)

Bit 3:DO1-DO6:Open wire while ON detected (0=OK,1=FAULT)

Bit 4:DO1-DO6:Short to VDD while ON detected (0=OK,1=FAULT)

Bit 5:DO1-DO6:Thermal error detected-shutdown (0=OK,1=FAULT)

Bit 6:DO1-DO6:Supply error detected (0=OK,1=FAULT)

Bit 7:DO1-DO6:Communication error detected (0=OK,1=FAULT)

GET DIGITAL OUTPUT GROUPx INTERRUPT STATUS	ASCII READ COMMAND	#GDOINT<DOGRP> <CR> Result: #GDOINT<DOGRP>:<InterruptStatusDec>,<InterruptStatusHex> <CR>	ASCII	
	DOGRP	1		
	TX	#255,GDOINT1<CR>		
	RX	#255,GDOINT1:0,0x0<CR>		
		Current interrupt status of digital output group 1:0000.0000		

Returns the current interrupt state of the digital output group DOGRP as decimal number and as hexadecimal number. X stands for the number of the digital output group.

InterruptStatusDec, InterruptStatusHex
Group 1: DO1-DO6
Bit 0:Overload detected (0=OK,1=FAULT)
Bit 1:Current limit detected(0=OK,1=FAULT)
Bit 2:Open wire while OFF detected (0=OK,1=FAULT)
Bit 3:Open wire while ON detected (0=OK,1=FAULT)
Bit 4:Short to VDD while ON detected (0=OK,1=FAULT)
Bit 5:Thermal error detected-shutdown (0=OK,1=FAULT)
Bit 6:Supply error detected (0=OK,1=FAULT)
Bit 7:Communication error detected (0=OK,1=FAULT)

DIGITAL OUTPUTS: GLOBAL ERRORS

GET DIGITAL OUTPUTS GLOBAL ERRORS	ASCII READ COMMAND	#GDOERRS<CR> Result: #GDOERRS:<GlobalErrorsDec>,<GlobalErrorsHex><CR>	ASCII	
	TX	#255,GDOERRS<CR>		
	RX	#255,GDOERRS:0,0x0<CR>		
		Current global errors of all digital output groups:0000.0000		

Returns the current interrupt state state of all output groups as decimal number and as hexadecimal number.

InterruptStatusDec,InterruptStatusHex
The current interrupt state of all digital output groups
Bit 0:DO1-DO6:Internal under voltage detected (0=OK,1=FAULT)
Bit 1:DO1-DO6:VA under voltage detected (<2.3V) (0=OK,1=FAULT)
Bit 2:DO1-DO6:VDD not good detected (<17V) (0=OK,1=FAULT)
Bit 3:DO1-DO6:VDD warning detected (<12V) (0=OK,1=FAULT)
Bit 4:DO1-DO6:VDD under voltage detected (<8V) (0=OK,1=FAULT)
Bit 5:DO1-DO6:Thermal shutdown (0=OK,1=FAULT)
Bit 6:DO1-DO6:Synchronisation error detected (0=OK,1=FAULT)
Bit 7:DO1-DO6:Watchdog error detected (0=OK,1=FAULT)

GET DIGITAL OUTPUT GROUPx GLOBAL ERRORS	ASCII READ COMMAND	#GDOERR<DOGRP><CR> Result: #GDOERR<DOGRP>:<GlobalErrorsDec>,<GlobalErrorsHex><CR>	ASCII	
	DOGRP	1		
	TX	#255,GDOERR1<CR>		
	RX	#255,GDOERR1:0,0x0<CR>		
		Current global errors of digital output group 1:0000.0000		

Returns the current interrupt state of the digital output group DOGRP as decimal number and as hexadecimal number. X stands for the number of the digital output group.

InterruptStatusDec, InterruptStatusHex
Group 1: DO1-DO6
Bit 0:Internal under voltage detected (0=OK,1=FAULT)
Bit 1:VA under voltage detected (<2.3V) (0=OK,1=FAULT)
Bit 2:VDD not good detected (<17V) (0=OK,1=FAULT)
Bit 3:VDD warning detected (<12V) (0=OK,1=FAULT)
Bit 4:VDD under voltage detected (<8V) (0=OK,1=FAULT)
Bit 5:Thermal shutdown (0=OK,1=FAULT)
Bit 6:Synchronisation error detected (0=OK,1=FAULT)
Bit 7:Watchdog error detected (0=OK,1=FAULT)

DIGITAL OUTPUTS: THERMAL OVERLOAD DETECTION

GET DIGITAL OUTPUTS THERMAL OVERLOAD DETECTION	ASCII READ COMMAND	#GDOTOS<CR> Result: #GDOTOS:<StatusDOSDec>,<StatusDOSHex><CR>	ASCII	
	TX	#255,GDOTOS<CR>		
	RX	#255,GDOTOS:0,0x0<CR>		
		Current thermal overload detection status of digital outputs:00.0000		
Returns the current state of the thermal overload detection for all digital outputs as decimal number and as hexadecimal number. StatusDOSDec, StatusDOSHex The current detection state of the digital outputs: Bit 0: State of DO1 (=0:OFF, =1:ON) Bit 1: State of DO2 (=0:OFF, =1:ON) ... Bit 4: State of DO5 (=0:OFF, =1:ON) Bit 5: State of DO6 (=0:OFF, =1:ON)				
GET DIGITAL OUTPUT DOx THERMAL OVERLOAD DETECTION	ASCII READ COMMAND	#GDOTO<DONR><CR> Result: #GDOTO<DONR>:<StatusDOxDec>,<StatusDOxHex><CR>	ASCII	
	DONR	1		
	TX	#255,GDOTO1<CR>		
	RX	#255,GDOTO1:0,0x0<CR>		
		Current thermal overload detection status of DO1:0=OFF		
Returns the current state of the thermal overload detection for digital output DOx as decimal number and as hexadecimal number. StatusDOxDec, StatusDOxHex The current detection state for digital output DOx: =0: digital output is OK =1: FAULT detected on digital output				
DIGITAL OUTPUTS: CURRENT LIMIT DETECTION				
GET DIGITAL OUTPUTS CURRENT LIMIT DETECTION	ASCII READ COMMAND	#GDOCLS<CR> Result: #GDOCLS:<StatusDOSDec>,<StatusDOSHex><CR>	ASCII	
	TX	#255,GDOCLS<CR>		
	RX	#255,GDOCLS:0,0x0<CR>		
		Current current limit detection status of digital outputs:00.0000		
Returns the current state of the current limit detection for all digital outputs as decimal number and as hexadecimal number. StatusDOSDec, StatusDOSHex The current detection state of the digital outputs: Bit 0: State of DO1 (=0:OFF, =1:ON) Bit 1: State of DO2 (=0:OFF, =1:ON) ... Bit 4: State of DO5 (=0:OFF, =1:ON) Bit 5: State of DO6 (=0:OFF, =1:ON)				
GET DIGITAL OUTPUT DOx CURRENT LIMIT DETECTION	ASCII READ COMMAND	#GDOCL<DONR><CR> Result: #GDOCL<DONR>:<StatusDOxDec>,<StatusDOxHex><CR>	ASCII	
	DONR	1		
	TX	#255,GDOCL1<CR>		
	RX	#255,GDOCL1:0,0x0<CR>		
		Current current limit detection status of DO1:0=OFF		

Returns the current state of the current limit detection for digital output DOx as decimal number and as hexadecimal number.
StatusDOxDec, StatusDOxHex
The current detection state for digital output DOx:
=0: digital output is OK
=1: FAULT detected on digital output

DIGITAL OUTPUTS: OPEN WIRE DETECTION WHILE ON

GET DIGITAL OUTPUTS OPEN WIRE FAULT DETECTION WHILE ON	ASCII READ COMMAND	#GDOOWFONS<CR> Result: #GDOOWFONS:<StatusDOSDec>,<StatusDOSHex> <CR>	ASCII	
	TX	#255,GDOOWFONS<CR>		
	RX	#255,GDOOWFONS:0,0x0<CR>		
		Current open wire fault detection status while ON of digital outputs:00.0000		

Returns the current state of the open wire fault detection while ON for all digital outputs as decimal number and as hexadecimal number.
StatusDOSDec, StatusDOSHex
The current detection state of the digital outputs:
Bit 0: State of DO1 (=0:OFF, =1:ON)
Bit 1: State of DO2 (=0:OFF, =1:ON)
...
Bit 4: State of DO5 (=0:OFF, =1:ON)
Bit 5: State of DO6 (=0:OFF, =1:ON)

GET DIGITAL OUTPUT DOx OPEN WIRE FAULT DETECTION WHILE ON	ASCII READ COMMAND	#GDOOWFON<DONR> <CR> Result: #GDOOWFON<DONR>:<StatusDOxDec>,<StatusDOxHex> <CR>	ASCII	
	DONR	1		
	TX	#255,GDOOWFON1<CR>		
	RX	#255,GDOOWFON1:0,0x0<CR>		
		Current open wire fault detection status while ON of DO1:0=OFF		

Returns the current state of the open wire fault detection while ON for digital output DOx as decimal number and as hexadecimal number.
StatusDOxDec, StatusDOxHex
The current detection state for digital output DOx:
=0: digital output is OK
=1: FAULT detected on digital output

DIGITAL OUTPUTS: OPEN WIRE DETECTION WHILE OFF

GET DIGITAL OUTPUTS OPEN WIRE FAULT DETECTION WHILE OFF	ASCII READ COMMAND	#GDOOWFOFFS<CR> Result: #GDOOWFOFFS:<StatusDOSDec>,<StatusDOSHex> <CR>	ASCII	
	TX	#255,GDOOWFOFFS<CR>		
	RX	#255,GDOOWFOFFS:0,0x0<CR>		
		Current open wire fault detection status while OFF of digital outputs:00.0000		

Returns the current state of the open wire fault detection while OFF for all digital outputs as decimal number and as hexadecimal number.
StatusDOSDec, StatusDOSHex
The current detection state of the digital outputs:
Bit 0: State of DO1 (=0:OFF, =1:ON)
Bit 1: State of DO2 (=0:OFF, =1:ON)
...
Bit 4: State of DO5 (=0:OFF, =1:ON)
Bit 5: State of DO6 (=0:OFF, =1:ON)

GET DIGITAL OUTPUT DOx OPEN WIRE FAULT DETECTION WHILE OFF	ASCII READ COMMAND	#GDOOWFOFF<DONR><CR> Result: #GDOOWFOFF<DONR>:<StatusDOxDec>,<StatusDOxHex><CR>	ASCII	
	DONR	1		
	TX	#255,GDOOWFOFF1<CR>		
	RX	#255,GDOOWFOFF1:0,0x0<CR>		
		Current open wire fault detection status while OFF of DO1:0=OFF		

Returns the current state of the open wire fault detection while OFF for digital output DOx as decimal number and as hexadecimal number.

StatusDOxDec, StatusDOxHex

The current detection state for digital output DOx:

=0: digital output is OK

=1: FAULT detected on digital output

DIGITAL OUTPUTS: SHORTCUT TO VDD DETECTION WHILE OFF

GET DIGITAL OUTPUTS SHORTCUT TO VDD FAULT DETECTION WHILE OFF	ASCII READ COMMAND	#GDOSVDDS<CR> Result: #GDOSVDDS:<StatusDOSDec>,<StatusDOSHex><CR>	ASCII	
	TX	#255,GDOSVDDS<CR>		
	RX	#255,GDOSVDDS:0,0x0<CR>		
		Current shortcut to VDD fault detection status while OFF of digital outputs:00.0000		

Returns the current state of the shortcut to VDD fault detection while OFF for all digital outputs as decimal number and as hexadecimal number.

StatusDOSDec, StatusDOSHex

The current detection state of the digital outputs:

Bit 0: State of DO1 (=0:OFF, =1:ON)

Bit 1: State of DO2 (=0:OFF, =1:ON)

...

Bit 4: State of DO5 (=0:OFF, =1:ON)

Bit 5: State of DO6 (=0:OFF, =1:ON)

GET DIGITAL OUTPUT DOx SHORTCUT TO VDD FAULT DETECTION WHILE OFF	ASCII READ COMMAND	#GDOSVDD<DONR><CR> Result: #GDOSVDD<DONR>:<StatusDOxDec>,<StatusDOxHex><CR>	ASCII	
	DONR	1		
	TX	#255,GDOSVDD1<CR>		
	RX	#255,GDOSVDD1:0,0x0<CR>		
		Current shortcut to VDD fault detection status while OFF of DO1:0=OFF		

Returns the current state of the shortcut to VDD fault detection while OFF for digital output DOx as decimal number and as hexadecimal number.

StatusDOxDec, StatusDOxHex

The current detection state for digital output DOx:

=0: digital output is OK

=1: FAULT detected on digital output

DIGITAL OUTPUTS: PULSE OUTPUT

PULSE DOx	ASCII WRITE COMMAND	#PDO<DONR>:<Time><CR> Result: #OK<CR>	ASCII	YES
	DONR	1		
	TIME	200		
	TX	#255,PDO1:200<CR>		
	RX	N/A		

This command switches the digital output DOx on for the pulse duration <PulseTimeIn100ms>*100ms. PulseTimeIn100ms: A duration in 100ms units. The corresponding digital output is switched on for this time period.				
GET PULSE TIMER DOx	ASCII READ COMMAND	#GPT<DONR> <CR> Result: #GPT:<TimeDec>,<TimeHex> <CR>	ASCII	
	DONR	1		
	TX	#255,GPT1<CR>		
	RX	#255,GPT1:0,0x0<CR>		
		Current pulse time for DO1:0,0s		
Returns the remaining timer value of the pulse for digital output DOx in ms. PulseTimeInMSDec, PulseTimeInMSHex The remaining time of the pulse in Milliseconds				
INITIAL & WATCHDOG STATE FOR DIGITAL OUTPUTS				
SET INITIAL & WATCHDOG STATE FOR DIGITAL OUTPUTS	ASCII WRITE COMMAND	#SCDOS:<OutAllDOS> <CR> Result: #OK<CR>	ASCII	YES
	DO1	0:OFF		
	DO2	0:OFF		
	DO3	0:OFF		
	DO4	0:OFF		
	DO5	0:OFF		
	DO6	0:OFF		
	TX	#255,SCDOS:0<CR>		
	RX	N/A		
Write on this register sets all digital outputs to a new state for module restart and watchdog function. The state is saved in FLASH. This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured. OutAllDOS The new state for the digital outputs: Bit 0: New state of DO1 (=0:OFF, =1:ON) Bit 1: New state of DO2 (=0:OFF, =1:ON) ... Bit 4: New state of DO5 (=0:OFF, =1:ON) Bit 5: New state of DO6 (=0:OFF, =1:ON)				
GET INITIAL & WATCHDOG STATE FOR DIGITAL OUTPUTS	ASCII READ COMMAND	#GDOS<CR> Result: #GDOS:<DOSDec>,<DOSHex> <CR>	ASCII	
	TX	#255,GDOS<CR>		
	RX	#255,GDOS:0,0x0<CR>		
		Current status of digital outputs:00.0000		

Returns the current initial and watchdog state of the digital outputs as decimal number and as hexadecimal number. This state is used after power on and after a communcation watchdog timeout, if a watchdog time is configured

DOSDec, DOSHex
The current state of the digital outputs:
Bit 0: State of DO1 (=0:OFF, =1:ON)
Bit 1: State of DO2 (=0:OFF, =1:ON)
...
Bit 4: State of DO5 (=0:OFF, =1:ON)
Bit 5: State of DO6 (=0:OFF, =1:ON)

DIGITAL OUTPUTS: INIT & WATCHDOG ENABLE OPEN WIRE DETECTION WHILE ON

SET CONFIG DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE ON	ASCII WRITE COMMAND	#SCDOEOWDONS:<OpenWireDOS> <CR> Result: #OK<CR>	ASCII	YES
	DO1	1:ENABLE		
	DO2	1:ENABLE		
	DO3	1:ENABLE		
	DO4	1:ENABLE		
	DO5	1:ENABLE		
	DO6	1:ENABLE		
	TX	#255,SCDOEOWDONS:63 <CR>		
	RX	N/A		

Sets the open wire mode for all digital outputs to the new mode OpenWireDOS for init & watchdog. This enables the diagnostic of open wire while the digital output is ON.
This state is used after power on and after a communcation watchdog timeout, if a watchdog time is configured
The new state for all digital outputs
Bit 0: New mode for DO1 (=0:DISABLED, =1:ENABLED)
Bit 1: New mode for DO2 (=0:DISABLED, =1:ENABLED)
...
Bit 4: New mode for DO5 (=0:DISABLED, =1:ENABLED)
Bit 5: New mode for DO6 (=0:DISABLED, =1:ENABLED)

GET CONFIG DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE ON	ASCII READ COMMAND	#GCDOEOWDONS <CR> Result: #GCDOEOWDONS:<OpenWireDOSDec>,<OpenWireDOSHex> <CR>	ASCII	
	TX	#255,GCDOEOWDONS<CR>		
	RX	#255,GCDOEOWDONS:0,0x0<CR>		
		Init & watchdog mode for open wire diagnostic while ON of digital outputs:00.0000		

Returns the current mode for open wire diagnostic while digital output is ON as decimal number and as hexadecimal number.
This values are used after power on of the module an after a watchdog event.
OpenWireDOSDec, OpenWireDOSHex
The current mode for open wire diagnostic while ON of the digital outputs:
Bit 0: Open wire diagnostic mode of DO1 (=0:DISABLED, =1:ENABLED)
Bit 1: Open wire diagnostic mode of DO2 (=0:DISABLED, =1:ENABLED)
...
Bit 4: Open wire diagnostic mode of DO5 (=0:DISABLED, =1:ENABLED)
Bit 5: Open wire diagnostic mode of DO6 (=0:DISABLED, =1:ENABLED)

DIGITAL OUTPUTS: INIT & WATCHDOG ENABLE OPEN WIRE DETECTION WHILE OFF

SET CONFIG DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE OFF	ASCII WRITE COMMAND	#SCDOEOWDOFFS:<OpenWireDOS> <CR> Result: #OK<CR>	ASCII	YES
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	DO1	1:ENABLE		
	DO2	1:ENABLE		
	DO3	1:ENABLE		
	DO4	1:ENABLE		
	DO5	1:ENABLE		
	DO6	1:ENABLE		
	TX	#255,SCDOEOWDOFFS:63<CR>		
	RX	N/A		
Sets the open wire mode for all digital outputs to the new mode OpenWireDOS for init & watchdog. This enables the diagnostic of open wire while the digital output is OFF. This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured The new state for all digital outputs Bit 0: New mode for DO1 (=0:DISABLED, =1:ENABLED) Bit 1: New mode for DO2 (=0:DISABLED, =1:ENABLED) ... Bit 4: New mode for DO5 (=0:DISABLED, =1:ENABLED) Bit 5: New mode for DO6 (=0:DISABLED, =1:ENABLED)				
GET CONFIG DIGITAL OUTPUTS ENABLE OPEN WIRE DETECTION WHILE OFF	ASCII READ COMMAND	#GCDOEOWDOFFS<CR> Result: #GCDOEOWDOFFS:<OpenWireDOSDec>,<OpenWireDOSHex><CR>	ASCII	
	TX	#255,GCDOEOWDOFFS<CR>		
	RX	#255,GCDOEOWDOFFS:0,0x0<CR>		
		Current mode for open wire diagnostic while OFF of digital outputs:00.0000		
Returns the current mode for open wire diagnostic while digital output is OFF as decimal number and as hexadecimal number. This values are used after power on of the module an after a watchdog event. OpenWireDOSDec, OpenWireDOSHex The current mode for open wire diagnostic while OFF of the digital outputs: Bit 0: Open wire diagnostic mode of DO1 (=0:DISABLED, =1:ENABLED) Bit 1: Open wire diagnostic mode of DO2 (=0:DISABLED, =1:ENABLED) ... Bit 4: Open wire diagnostic mode of DO5 (=0:DISABLED, =1:ENABLED) Bit 5: Open wire diagnostic mode of DO6 (=0:DISABLED, =1:ENABLED)				
DIGITAL OUTPUTS: INIT & WATCHDOG ENABLE SHORTCUT TO VDD DETECTION WHILE OFF				
SET CONFIG DIGITAL OUTPUTS ENABLE SHORTCUT TO VDD DETECTION WHILE OFF	ASCII WRITE COMMAND	#SCDOESVDDS:<ShortCutDOS><CR> Result: #OK<CR>	ASCII	YES
	DO1	1:ENABLE		
	DO2	1:ENABLE		
	DO3	1:ENABLE		
	DO4	1:ENABLE		
	DO5	1:ENABLE		
	DO6	1:ENABLE		
	TX	#255,SCDOESVDDS:63<CR>		
	RX	N/A		

Sets the shortcut to VDD detection mode for all digital outputs to the new mode ShortCutDOS for init & watchdog. This enables the diagnostic of shortcut to VDD while the digital output is OFF. This state is used after power on and after a communication watchdog timeout, if a watchdog time is configured The new state for all digital outputs Bit 0: New mode for DO1 (=0:DISABLED, =1:ENABLED) Bit 1: New mode for DO2 (=0:DISABLED, =1:ENABLED) ... Bit 4: New mode for DO5 (=0:DISABLED, =1:ENABLED) Bit 5: New mode for DO6 (=0:DISABLED, =1:ENABLED)				
GET CONFIG DIGITAL OUTPUTS ENABLE SHORT CUT TO VDD DETECTION WHILE OFF	ASCII READ COMMAND	#GCDOESVDDS<CR> Result: #GCDOESDDS:<ShortCutDOSDec>,<ShortCutDOSHex> <CR>	ASCII	
	TX	#255,GCDOESVDDS<CR>		
	RX	#255,GCDOESVDDS:0,0x0<CR>		
		Current mode for shortcut to VDD diagnostic while OFF of digital outputs:00.0000		
Returns the current mode for shortcut to VDD diagnostic while digital output is OFF as decimal number and as hexadecimal number. This values are used after power on of the module an after a watchdog event. ShortCutDOSDec, ShortCutDOSHex The current mode for shortcut diagnostic while OFF of the digital outputs: Bit 0: Open wire diagnostic mode of DO1 (=0:DISABLED, =1:ENABLED) Bit 1: Open wire diagnostic mode of DO2 (=0:DISABLED, =1:ENABLED) ... Bit 4: Open wire diagnostic mode of DO51 (=0:DISABLED, =1:ENABLED) Bit 5: Open wire diagnostic mode of DO6 (=0:DISABLED, =1:ENABLED)				
GET DIGITAL OUTPUTS CHIPSET NAME	ASCII READ COMMAND	#GDOCHIPSET<CR> Result: #GDOCHIPSET:<ChipSetName>	ASCII	
	TX	#255,GDOCHIPSET<CR>		
	RX	#255,GDOCHIPSET:MAX14915<CR>		
		Current name of chipset for digital outputs:MAX14915		
Returns the current name of the chipset of the digital outputs				