CONVERTER STATUS	3x05051 4x05051 I:5050	0,0x0000 B:00 00		UINT16 R/O	
DIP SWITCH	3x10010 4x10010 I:10009	15,0x000F B:00 0F		UINT16 R/O	
Returns the current setting of the Dip For ULTRA SLIM IOS The current value of the DIP switche Bit 0: DIP Switch 1 (=0:OFF, =1:ON) Bit 1: DIP Switch 2 (=0:OFF, =1:ON) Bit 2: DIP Switch 3 (=0:OFF, =1:ON) Bit 3: DIP Switch 4 (=0:OFF, =1:ON)	o switches.				
SOFTWARE RESET					
RESET	1x06001 2x06001 I:6000	0,0x00 B:00	N/A:NO CHANGE	BIT R/W	YES
Performs a software reset, whenever	r 1 is written to this register. If the he	ost writes to this register 1, the module executes a se	ft reset (reboot).	1	
RESET	3x06001 4x06001 I:6000	0,0x0000 B:00 00	N/A:NO CHANGE	UINT16 R/W	YES
Performs a software reset, whenever	r 1 is written to this register. If the he	ost writes to this register 1, the module executes a secure a se	ft reset (reboot).		
PRODUCT DATA					
HW_GROUP	3x65201 4x65201 I:65200	8528,0x2150 B:21 50		UINT16 R/O	
This is the group of hardware of the o		I			
SW_GROUP	3x65202 4x65202 I:65201	4096,0x1000 B:10 00		UINT16 R/O	
This is the group of software of the c		I			
SW_VERSION	3x65203 4x65203 I:65202	16384,0x4000 B:40 00		UINT16 R/O	
		SW VERSION:4.0.0			
This is the cuirrent software version of	of the firmware				
SW_AUTHOR	3x65204 4x65204 I:65203	18771,0x4953 B:49 53		UINT16 R/O	
This is the cuirrent software author o	of the firmware				
MODBUS SETTINGS					
UNIT_ID	3x65222 4x65222 I:65221	65535,0xFFFF B:FF FF	N/A:NO CHANGE	UINT16 R/W	NO
		UNIT ID:255			

If the host reads this register, the current pro If the host write a new value into this registe The host can execute a reboot in writing to t NOTE:DIP switch 4 must set to OFF to activ	r, the new value will be store	1	55. activated after a	power off/power on cycle or a software reboot of the mod	lule.	
HINT:This settings will be active after you	a repower or reset your de	vice !!				
BAUD_RATE	3x65223 4x65223 I:65222	4294967295,0xFFFFFFFF B:FF FF FF FF	38400	38400	UINT32 R/W	NO
		57600Bd		ENTER BAUD RATE		
This is the current configured baud rate for I DIP switch settings: DIP1-DIP2 OFF-OFF:9600bd ON-OFF:19200bd OFF-ON:38400bd ON-ON:default 57600bd or the defined baud		DIP2=ON (default is 57600bd)				
Valid baud rates are: 300bd 600bd 1200bd 2400bd 4800bd 9600bd 19200bd 38400bd all other:57600bd						
HINT:This settings will be active after you	a repower or reset your de	vice !!				
PARITY	3x65225 4x65225 I:65224	65535,0xFFFF B:FF FF		N/A:NO CHANGE	UINT16 R/W	NO
		NO PARITY		SELECT PARITY		
If the register is read out, the currently set p Writing a value to this register will change th Parity values are 0: no parity 1: even parity 2: odd parity	arity of the serial interface is le new parity in FLASH. This	returned. will only take effect after a restart of the module. This	can be triggered	by writing to the RESET SYSTEM register.		
STOP BITS	3x65226 4x65226 I:65225	65535,0xFFFF B:FF FF		N/A:NO CHANGE	UINT16 R/W	NO
		ONE STOPBIT		SELECT STOPBITS		
f the register is read out, the currently set nu Writing a value to this register will change th Values for stop bits are 1: one stop bit 2: two stop bits	Imber of stop bits of the seri	al interface is returned.	f the module. This	can be triggered by writing to the RESET SYSTEM regis	ster.	

GET VERSION	ASCII READ COMMAND	#VERSION <cr> #VER<cr> Result: #VERSION:<versionhi>,<versionmed>,<versionlo><cr></cr></versionlo></versionmed></versionhi></cr></cr>	ASCII	
	тх	#VERSION <cr></cr>		
	RX	#255,VERSION:4.0.0 <cr></cr>		
		Current SW version:4.0.0		
Returns the version number of the /ersionHi: Version number high (1. /ersionMed: Version number mediu /ersionLo: Version number low (1	.255) Jm (1255)			
GET TYPE	ASCII	#TYPE <cr></cr>	ASCII	
	READ	#TYP <cr></cr>		
	COMMAND	Result:		
		#TYPE: <type><cr></cr></type>		
	ТХ	#TYPE <cr></cr>		
	RX	#255,TYPE:RESI-DALI-SIO <cr></cr>		
		Current module type:RESI-DALI-SIO		
Returns the current module type				
GET OWNER	ASCII	#OWNER <cr></cr>	ASCII	
	READ	#OWN <cr></cr>		
	COMMAND	Result:		
		#OWNER: <owner><cr></cr></owner>		
	тх	#OWNER <cr></cr>		
	RX	#255,OWNER:RESI <cr></cr>		
		Current owner:RESI		
Returns the current owner of the m	odule			
GET CREATOR	ASCII	#CREATOR <cr></cr>	ASCII	
	READ	#CRE <cr></cr>		
	COMMAND	Result:		
		#CREATOR: <creator><cr></cr></creator>		
	тх	#CREATOR <cr></cr>		
	RX	#255,CREATOR:DI HC SIGL,MSC <cr></cr>		
		Current creator:DI HC SIGL,MSC		
Returns the current creator of the n	nodule			
GET COPYRIGHT	ASCII	#COPYRIGHT <cr></cr>	ASCII	
	READ	#COPY <cr></cr>		
	COMMAND	Result:		
		#COPYRIGHT: <copyright><cr></cr></copyright>		
	ТХ	#COPYRIGHT <cr></cr>		
	RX	#255,COPYRIGHT:2016,2020 BY RESI AND DI HC SIGL,MSC WWW.RESI.CC <cr></cr>		

GET DIP SWITCH	ASCII READ COMMAND	#GET DIP <cr> #GDIP<cr> Result: #GDIP:<dipswitchdec>,<dipswitchhex><cr></cr></dipswitchhex></dipswitchdec></cr></cr>	ASCII	
	ТХ	#GET DIP <cr></cr>		
	RX	#255,GDIP:15,0xF <cr></cr>		
		Current DIP SWITCH settings:1111		
Returns the current setting of the Dip sv DIPSwitchDec DIPSwitchHex The current value of the DIP switches: Bit 0: DIP Switch 1 (=0:OFF, =1:ON) Bit 1: DIP Switch 2 (=0:OFF, =1:ON) Bit 2: DIP Switch 3 (=0:OFF, =1:ON) Bit 3: DIP Switch 4 (=0:OFF, =1:ON)	vitches as decimal number	and as hexadecimal number.		
ASCII COMMANDS				
SET MODBUS ADDRESS	ASCII WRITE COMMAND	#SET MODBUS ADDRESS: <unitid><cr> #SETMBADR:<unitid><cr> Result: #OK<cr></cr></cr></unitid></cr></unitid>	ASCII	NO
	UNITID	1		
	ТХ	#SET MODBUS ADDRESS:1 <cr></cr>		
	RX	N/A		
Redefines the unit ID of the module. Th HINT: The new settings are activated af SET MODBUS BAUDRATE				NO
SET MODBUS BAUDRATE	WRITE COMMAND	#SET MODBUS BAUDRATE: <baud><cr> #SETMBBAUD:<baud><cr> Result: #OK<cr></cr></cr></baud></cr></baud>	ASCII	NO
	BAUD	57600:57600BD		
	ТХ	#SET MODBUS BAUDRATE:57600 <cr></cr>		
	RX	N/A		
Sets a new baudrate for the serial interf The following baudrates are allowed: 300bd 600bd 1200bd 2400bd 4800bd 9600bd 19200bd 38400bd all others are interpreted as 57600bd				
HINT: The new setup parameters will be	a active after a reatert of the	madula		

SET MODBUS PARITY	ASCII	#SET MODBUS PARITY: <parity><cr></cr></parity>	ASCII	NO
	WRITE	#SETMBPAR: <parity><cr></cr></parity>		
	COMMAND	Result:		
		#OK <cr></cr>		
	PARITY	NONE:NO PARITY		
	ТХ	#SET MODBUS PARITY:NONE <cr></cr>		
	RX	N/A		
Sets a new parity for the serial interface MBParity: NONE: no parity EVEN: even parity DDD: odd parity	e.			
HINT: The new setup parameters will b	e active after a restart of the	e module.		
SET MODBUS STOPS	ASCII	#SET MODBUS STOP: <stopbit><cr></cr></stopbit>	ASCII	NO
	WRITE	#SETMBSTOP: <stopbit><cr></cr></stopbit>		
	COMMAND	Result:		
		#OK <cr></cr>		
	STOPBIT			
	TX	#SET MODBUS STOP:ONE <cr></cr>		
	RX	N/A		
MBStops ONE: one stop bit				
Sets a new amount of stop bits for the MBStops ONE: one stop bit TWO: two stop bits HINT: The new setup parameters will b	serial interface.			
MBStops ONE: one stop bit TWO: two stop bits HINT: The new setup parameters will b	serial interface. be active after a restart of the	e module.	ASCII	NO
MBStops ONE: one stop bit TWO: two stop bits	serial interface. be active after a restart of the ASCII	e module. #SET MODBUS PARAMS: <unitid>,<baud>,<parity>,<stopbit><cr></cr></stopbit></parity></baud></unitid>	ASCII	NO
MBStops DNE: one stop bit TWO: two stop bits HINT: The new setup parameters will b	serial interface. be active after a restart of the	e module.	ASCII	NO
MBStops DNE: one stop bit FWO: two stop bits HINT: The new setup parameters will b	serial interface. be active after a restart of the ASCII WRITE	e module. #SET MODBUS PARAMS: <unitid>,<baud>,<parity>,<stopbit><cr> #SETMBPARAMS:<unitid>,<baud>,<parity>,<stopbit><cr></cr></stopbit></parity></baud></unitid></cr></stopbit></parity></baud></unitid>	ASCII	NO
MBStops DNE: one stop bit TWO: two stop bits HINT: The new setup parameters will b	serial interface. be active after a restart of the ASCII WRITE	e module. #SET MODBUS PARAMS: <unitid>,<baud>,<parity>,<stopbit><cr> #SETMBPARAMS:<unitid>,<baud>,<parity>,<stopbit><cr> Result:</cr></stopbit></parity></baud></unitid></cr></stopbit></parity></baud></unitid>	ASCII	NO
MBStops DNE: one stop bit FWO: two stop bits HINT: The new setup parameters will b	serial interface. he active after a restart of the ASCII WRITE COMMAND	e module. #SET MODBUS PARAMS: <unitid>,<baud>,<parity>,<stopbit><cr> #SETMBPARAMS:<unitid>,<baud>,<parity>,<stopbit><cr> Result:</cr></stopbit></parity></baud></unitid></cr></stopbit></parity></baud></unitid>	ASCII	NO
MBStops DNE: one stop bit TWO: two stop bits HINT: The new setup parameters will b	serial interface. e active after a restart of the ASCII WRITE COMMAND UNITID	e module. #SET MODBUS PARAMS: <unitid>,<baud>,<parity>,<stopbit><cr> #SETMBPARAMS:<unitid>,<baud>,<parity>,<stopbit><cr> Result: #OK<cr> 1</cr></cr></stopbit></parity></baud></unitid></cr></stopbit></parity></baud></unitid>	ASCII	NO
MBStops ONE: one stop bit TWO: two stop bits HINT: The new setup parameters will b	serial interface.	e module. #SET MODBUS PARAMS: <unitid>,<baud>,<parity>,<stopbit><cr> #SETMBPARAMS:<unitid>,<baud>,<parity>,<stopbit><cr> Result: #OK<cr> 1 57600:57600BD</cr></cr></stopbit></parity></baud></unitid></cr></stopbit></parity></baud></unitid>	ASCII	NO
MBStops DNE: one stop bit FWO: two stop bits HINT: The new setup parameters will b	serial interface. e active after a restart of the ASCII WRITE COMMAND UNITID BAUD PARITY STOPBIT TX	e module. #SET MODBUS PARAMS: <unitid>,<baud>,<parity>,<stopbit><cr> #SETMBPARAMS:<unitid>,<baud>,<parity>,<stopbit><cr> Result: #OK<cr> 1 57600:57600BD NONE:NO PARITY</cr></cr></stopbit></parity></baud></unitid></cr></stopbit></parity></baud></unitid>	ASCII	NO
MBStops DNE: one stop bit TWO: two stop bits HINT: The new setup parameters will b SET MODBUS PARAMS	serial interface. e active after a restart of the ASCII WRITE COMMAND UNITID BAUD PARITY STOPBIT TX RX	e module. #SET MODBUS PARAMS: <unitid>,<baud>,<parity>,<stopbit><cr> #SETMBPARAMS:<unitid>,<baud>,<parity>,<stopbit><cr> Result: #OK<cr> 1 57600:57600BD NONE:NO PARITY ONE:ONE STOPBIT</cr></cr></stopbit></parity></baud></unitid></cr></stopbit></parity></baud></unitid>	ASCII	NO
MBStops DNE: one stop bit TWO: two stop bits HINT: The new setup parameters will b	serial interface. e active after a restart of the ASCII WRITE COMMAND UNITID BAUD PARITY STOPBIT TX RX	e module. #SET MODBUS PARAMS: <unitid>,<baud>,<parity>,<stopbit><cr> #SETMBPARAMS:<unitid>,<baud>,<parity>,<stopbit><cr> Result: #OK<cr> 1 57600:57600BD NONE:NO PARITY ONE:ONE STOPBIT #SET MODBUS PARAMS:1,57600,NONE,ONE<cr></cr></cr></cr></stopbit></parity></baud></unitid></cr></stopbit></parity></baud></unitid>	ASCII	NO
MBStops DNE: one stop bit TWO: two stop bits HINT: The new setup parameters will b SET MODBUS PARAMS	serial interface. e active after a restart of the ASCII WRITE COMMAND UNITID BAUD PARITY STOPBIT TX RX e ASCII	e module. #SET MODBUS PARAMS: <unitid>,<baud>,<parity>,<stopbit><cr> #SETMBPARAMS:<unitid>,<baud>,<parity>,<stopbit><cr> Result: #OK<cr> 1 57600:57600BD NONE:NO PARITY ONE:ONE STOPBIT #SET MODBUS PARAMS:1,57600,NONE,ONE<cr> N/A #GET MODBUS ADDRESS<cr></cr></cr></cr></cr></stopbit></parity></baud></unitid></cr></stopbit></parity></baud></unitid>	ASCII	NO
MBStops ONE: one stop bit TWO: two stop bits HINT: The new setup parameters will b SET MODBUS PARAMS	serial interface. e active after a restart of the ASCII WRITE COMMAND UNITID BAUD PARITY STOPBIT TX RX C ASCII READ	e module. #SET MODBUS PARAMS: <unitid>,<baud>,<parity>,<stopbit><cr> #SETMBPARAMS:<unitid>,<baud>,<parity>,<stopbit><cr> Result: #OK<cr> 1 57600:57600BD NONE:NO PARITY ONE:NO PARITY ONE:ONE STOPBIT #SET MODBUS PARAMS:1,57600,NONE,ONE<cr> N/A #GET MODBUS ADDRESS<cr> #GMBADR<cr></cr></cr></cr></cr></cr></stopbit></parity></baud></unitid></cr></stopbit></parity></baud></unitid>		NO
MBStops ONE: one stop bit TWO: two stop bits HINT: The new setup parameters will b SET MODBUS PARAMS	serial interface. e active after a restart of the ASCII WRITE COMMAND UNITID BAUD PARITY STOPBIT TX RX e ASCII	e module. #SET MODBUS PARAMS: <unitid>,<baud>,<parity>,<stopbit><cr> #SETMBPARAMS:<unitid>,<baud>,<parity>,<stopbit><cr> Result: #OK<cr> 1 57600:57600BD NONE:NO PARITY ONE:NO PARITY ONE:ONE STOPBIT #SET MODBUS PARAMS:1,57600,NONE,ONE<cr> N/A #GET MODBUS ADDRESS<cr> #GMBADR<cr> Result:</cr></cr></cr></cr></cr></stopbit></parity></baud></unitid></cr></stopbit></parity></baud></unitid>		NO
MBStops ONE: one stop bit TWO: two stop bits HINT: The new setup parameters will b SET MODBUS PARAMS	serial interface. e active after a restart of the ASCII WRITE COMMAND UNITID BAUD PARITY STOPBIT TX RX S ASCII READ COMMAND	e module. #SET MODBUS PARAMS: <unitid>,<baud>,<parity>,<stopbit><cr> #SETMBPARAMS:<unitid>,<baud>,<parity>,<stopbit><cr> Result: #OK<cr> 1 57600:57600BD NONE:NO PARITY ONE:NO PARITY ONE:ONE STOPBIT #SET MODBUS PARAMS:1,57600,NONE,ONE<cr> N/A #GET MODBUS ADDRESS<cr> #GMBADR<cr> Result: #GMBADR:<mbunitdec>,<mbflashdec>,<mbunithex>,<mbflashhex><cr></cr></mbflashhex></mbunithex></mbflashdec></mbunitdec></cr></cr></cr></cr></cr></stopbit></parity></baud></unitid></cr></stopbit></parity></baud></unitid>		NO
MBStops ONE: one stop bit TWO: two stop bits HINT: The new setup parameters will b SET MODBUS PARAMS	serial interface. e active after a restart of the ASCII WRITE COMMAND UNITID BAUD PARITY STOPBIT TX RX ASCII READ COMMAND TX	e module. #SET MODBUS PARAMS: <unitid>,<baud>,<parity>,<stopbit><cr> #SETMBPARAMS:<unitid>,<baud>,<parity>,<stopbit><cr> Result: #OK<cr> 1 57600:57600BD NONE:NO PARITY ONE:NO PARITY ONE:ONE STOPBIT #SET MODBUS PARAMS:1,57600,NONE,ONE<cr> N/A #GET MODBUS ADDRESS<cr> #GMBADR<cr> Result: #GMBADR:<mbunitdec>,<mbflashdec>,<mbunithex>,<mbflashhex><cr> #GET MODBUS ADDRESS<cr></cr></cr></mbflashhex></mbunithex></mbflashdec></mbunitdec></cr></cr></cr></cr></cr></stopbit></parity></baud></unitid></cr></stopbit></parity></baud></unitid>		NO
MBStops ONE: one stop bit TWO: two stop bits HINT: The new setup parameters will b SET MODBUS PARAMS	serial interface. e active after a restart of the ASCII WRITE COMMAND UNITID BAUD PARITY STOPBIT TX RX S ASCII READ COMMAND	e module. #SET MODBUS PARAMS: <unitid>,<baud>,<parity>,<stopbit><cr> #SETMBPARAMS:<unitid>,<baud>,<parity>,<stopbit><cr> Result: #OK<cr> 1 57600:57600BD NONE:NO PARITY ONE:NO PARITY ONE:ONE STOPBIT #SET MODBUS PARAMS:1,57600,NONE,ONE<cr> N/A #GET MODBUS ADDRESS<cr> #GMBADR<cr> Result: #GMBADR:<mbunitdec>,<mbflashdec>,<mbunithex>,<mbflashhex><cr></cr></mbflashhex></mbunithex></mbflashdec></mbunitdec></cr></cr></cr></cr></cr></stopbit></parity></baud></unitid></cr></stopbit></parity></baud></unitid>		NO

Shows the current used MODBUS/RTU or ASCII unit address and shows also the stored unit address in the FLASH memory, which is only used if the DIP switch for the bus address is set to 0.

MBUnitDec,MBUnitHex The current used MODBUS/RTU unit or ASCII address for communication

MBFLASHDec,MBFLASHHex The internal stored MODBUS/RTU unit address or ASCII address from the FLASH memory, if the DIP switch DIP3 is OFF.

GET MODBUS BAUDRATE	ASCII READ COMMAND	#GET MODBUS BAUDRATE <cr> #GMBBAUD<cr> Result:</cr></cr>	ASCII
		#GMBBAUD: <baudrate><cr></cr></baudrate>	
	TX	#GET MODBUS BAUDRATE <cr></cr>	
	RX	#255,GMBBAUD:57600 <cr></cr>	
Returns the current defined baud rate for		Current baudrate for DIP1+2=ON:57600	
The following baudrates are allowed: 300bd 600bd 1200bd 2400bd 4800bd 9600bd 19200bd 38400bd all others are interpreted as 57600bd			
GET MODBUS PARITY	ASCII READ COMMAND	#GET MODBUS PARITY <cr> #GMBPAR<cr> Result: #GMBPAR:<mbparity><cr></cr></mbparity></cr></cr>	ASCII
	ТХ	#GET MODBUS PARITY <cr></cr>	
	RX	#255,GMBPAR:NONE <cr></cr>	
		Current parity:NONE	
Shows the current configured parity of the MBParity NONE: no parity EVEN: even parity ODD: odd parity			
GET MODBUS STOP	ASCII READ COMMAND	#GET MODBUS STOP <cr> #GMBSTOP<cr> Result: #GMBSTOP:<mbstop><cr></cr></mbstop></cr></cr>	ASCII
	ТХ	#GET MODBUS STOP <cr></cr>	
	RX	#255,GMBSTOP:ONE <cr></cr>	
		Current stopbit(s):ONE	
Returns the current configured amount of MBStops ONE: one stop bit TWO: two stop bits	of stop bits for the serial inte		

GET MODBUS PARAMS	ASCII	#GET MODBUS PARAMS <cr></cr>	ASCII	
	READ	#GMBPARAMS <cr></cr>		
	COMMAND	Result:		
		#GMBPARAMS: <mbunitdec>,<mbflashdec>,<mbunithex>,<mbflashhex>,</mbflashhex></mbunithex></mbflashdec></mbunitdec>		
		<mbbaudratedec>,<mbbaudratehex>,<mbparity>,<mbstops><cr></cr></mbstops></mbparity></mbbaudratehex></mbbaudratedec>		
	ТХ	#GET MODBUS PARAMS <cr></cr>		
	RX	#255,GMBPARAMS:255,0xFF,65535,0xFFFF,57600,0xE100,NONE,ONE <cr></cr>		
		Current MODBUS unit ID used:255		
		Current MODBUS unit ID in FLASH:65535		
		Current baudrate in FLASH:57600		
		Current parity in FLASH:NONE		
		Current stopbit(s) in FLASH:ONE		
Returns the complete settings for seri	ial interface		· · · · ·	
ASCII COMMANDS				
RESET	ASCII	#RESET <cr></cr>	ASCII	NO
	WRITE	#RST <cr></cr>		
	COMMAND	Result:		
		#OK <cr></cr>		
	ТХ	#RESET <cr></cr>		
	RX	N/A		
Executes a software reset (Reboot) o	f the module			
		#FACTORY RESET <cr></cr>		NO
FACTORY RESET	ASCII WRITE	#FACTORY RESETCER>	ASCII	NO
	COMMAND	Result:		
	COMMAND	#OK <cr></cr>		
	тх	#FACTORY RESET <cr></cr>		
	RX	N/A		

						-
DALI BUS ERROR	1x00001	0,0x00			BIT	
	2x00001	B:00			R/O	
	1:0					
	(BUS IS OK				
Shows the status of the DALI bus.						
=0:DALI bus is OK and fully function	nal DALI line, overload on DALI line or no	DALL nower supply				
DALI PRIORITY SLOT	3x00101	2.0x0002	1	1	UINT16	NO
	4x00101	B:00 02	-	-	R/W	
	1:100	8.00 02			10,00	
		Current priority slot:2->14ms				
Sate or returns the ourrent defined I						
0. 12ms. Highest priority used for n	DALI phones shat must be repeated	allable.				
1: 13ms: User initiated arc power co	ontrol commands and commands which	ailable: ch have an impact on manual adjustment of arc	nower (e.a. dim un wit	h a special fade time)		
2: 14ms: Configuration in general		si nave an impact of manual adjustment of are	power (e.g., ann ap wit			
3: 15ms: Automatic arc power contr	oi commands					
4: 16ms: Lowest priority, used for q	uery messages					
LAMP LEVEL	3x00511	0,0x0000	639	50,0	UINT16	NO
	4x00511	B:00 00	000	30,0	R/W	NO
	1:510	B.00 00				
	1.510		0.0075			
			0x027F			
	v brightness level between 0 and 254.		LAMP	2:LAMP SHORT ADDRESS 3		
Upper 8 bits:8-15:SHORTADRESS Lower 8 bits: 0-7:VALUE:brightness	number of the lamp to be controlled (s value that should be send (0254 for	063) r 0100% and 255 for MASK)				
GROUP LEVEL	3x00521	0,0x0000	383	50,0	UINT16	NO
	4x00521	B:00 00			R/W	
	1:520	2.00 00				
			0x017F			
			GROUP	1:LAMP GROUP 2		
Sate for a lamp group a pow bright	acc lovel between 0 and 254, 255 m		GROUP	I.LAWP GROUP 2		
Upper 8 bits:8-15:GROUP:number Lower 8 bits:0-7:VALUE:brightness	tess level between 0 and 254. 255 me of the group to be controlled (015) value that should be send (0254 for	0100% and 255 for MASK)				
ALL LEVEL	3x00531	0.0x0000	127	50,0	UINT16	NO
	4x00531	B:00 00			R/W	
	1:530	D.00 00			10,00	
	1.550		0.0075			
Coto for all lampa a navy brighter	lovel between 0 and 054 055 million	MACK	0x007F			
Upper 8 bits:8-15:Unused, always 0	level between 0 and 254. 255 means value that should be send (0254 for					
LAMP COMMAND	3x00512	????	919	AO	UINT16	NO
DALI 1.0	4x00512		010		W/O	
	1:511				W/O	
			0,0007			
			0x0397			
			0x0397 COMMAND LAMP	97:QUERY VERSION NUMBER 3:LAMP SHORT ADDRESS 4		

Sends a command to a specific lamp shor Upper 8 bits:15-8: Short address of lamp (Lower 8 bits:7-0: Command value between	t address. The 16 bit value is)63 for lamp 1 to 64 n 0 and 255 or 0x00 and 0xFF	divided into two parts: =				
If you read this register, you can poll the 8 0x8000: No answer received from the DAL 0x20FF: A collision was detected on the D 0x000xFF: The 8 bit result of the last con HINT: After you have read out the DALI re	ALI bus nmand.	value is defined with: but will again result in 0x8000 for no further 8 bit data	! So you can only re	eadout the result one time		
LAMP COMMAND+REPEAT DALI 1.0	3x00513 4x00513 I:512	????	768	A0	UINT16 W/O	NO
			0x0300			
			COMMAND	00:OFF		
			LAMP	3:LAMP SHORT ADDRESS 4		
Sends a command twice within 100ms to a Upper 8 bits:15-8: Short address of lamp (Lower 8 bits:7-0: Command value between If you read this register, you can poll the 8 0x8000: No answer received from the DAL 0x20FF: A collision was detected on the D 0x000xFF: The 8 bit result of the last con HINT: After you have read out the DALI re	D.63 for lamp 1 to 64 n 0 and 255 or 0x00 and 0xFF bit DALI answer. The return v I bus up to now ALI bus nmand.	:	! So you can only re	adout the result one time		
GROUP COMMAND DALI 1.0	3x00522 4x00522 I:521	????	151	A0	UINT16 W/O	NO
			0x0097			
			COMMAND	97:QUERY VERSION NUMBER		
			GROUP	0:LAMP GROUP 1		
	lamp group 1 to 16 n 0 and 255 or 0x00 and 0xFF bit DALI answer. The return v I bus up to now ALI bus mand. sult, the next MODBUS reado	zalue is defined with: but will again result in 0x8000 for no further 8 bit data				
GROUP COMMAND+REPEAT DALI 1.0	3x00523 4x00523 I:522	????	0	A0	UINT16 W/O	NO
			0x0000			
			COMMAND	00:OFF		
			GROUP	0:LAMP GROUP 1		
Sends a command twice within 100ms to a Upper 8 bits:15-8: Group of lamp 015 for Lower 8 bits:7-0: Command value between If you read this register, you can poll the 8 0x8000: No answer received from the DAL 0x20FF: A collision was detected on the D	lamp group 1 to 16 n 0 and 255 or 0x00 and 0xFF bit DALI answer. The return v L bus up to now	:				
0x000xFF: The 8 bit result of the last con	nmand.	out will again result in 0x8000 for no further 8 bit data	! So you can only re	eadout the result one time		

ALL COMMAND	0.00500	0000				
DALI 1.0	3x00532 4x00532	????	151	AO	UINT16 W/O	NO
	1:531				W/O	
			0x0097			
			COMMAND	97:QUERY VERSION NUMBER		
Upper 8 bits:15-8: Always 0	y the lower 8 bits of the 16 bit value is u etween 0 and 255 or 0x00 and 0xFF	sed:			·	
0x20FF: A collision was detected on 0x000xFF: The 8 bit result of the la	st command.	e is defined with: vill again result in 0x8000 for no further 8 bit data!	So you can only re	eadout the result one time		
ALL COMMAND+REPEAT	3x00533	????	0	AO	UINT16	NO
DALI 1.0	4x00533 I:532				W/O	No
			0x0000			
			COMMAND	00:OFF		
0x000xFF: The 8 bit result of the la HINT: After you have read out the D	st command.	vill again requit in 0.4000 for no further 0 hit data	Co you con only r			
DIRECT 16 BIT COMMAND	3x00542 4x00542	????	43264	A100	UINT16 W/O	NO
DIRECT 16 BIT COMMAND	3x00542		43264			NO
DIRECT 16 BIT COMMAND	3x00542 4x00542		43264 0xA900	A100		NO
DIRECT 16 BIT COMMAND	3x00542 4x00542		43264	A100 A900:COMPARE		NO
DIRECT 16 BIT	3x00542 4x00542 I:541		43264 0xA900	A100		NO
DIRECT 16 BIT COMMAND DALI 1.0 Sends a 16 bit DALI 1.0 frame to the f you read this register, you can poll Dx8000: No answer received from th Dx20FF: A collision was detected on Dx20FF: The 8 bit result of the la	3x00542 4x00542 1:541 e DALI bus line I the 8 bit DALI answer. The return valu the DALI bus up to now the DALI bus st command.	????	43264 0xA900 COMMAND V	A100 A900:COMPARE A0		NO
DIRECT 16 BIT COMMAND DALI 1.0 Sends a 16 bit DALI 1.0 frame to the f you read this register, you can poll 0x8000: No answer received from th 0x20FF: A collision was detected on 0x00.0xFF: The 8 bit result of the la HINT: After you have read out the D DIRECT 16 BIT	3x00542 4x00542 1:541 e DALI bus line I the 8 bit DALI answer. The return value e DALI bus up to now the DALI bus st command. ALI result, the next MODBUS readout v 3x00543	???? e is defined with:	43264 0xA900 COMMAND V	A100 A900:COMPARE A0		NO
DIRECT 16 BIT COMMAND DALI 1.0 Sends a 16 bit DALI 1.0 frame to the f you read this register, you can poll 0x8000: No answer received from th 0x20FF: A collision was detected on 0x000xFF: The 8 bit result of the la HINT: After you have read out the D DIRECT 16 BIT COMMAND+REPEAT	3x00542 4x00542 1:541 e DALI bus line I the 8 bit DALI answer. The return value e DALI bus up to now the DALI bus st command. ALI result, the next MODBUS readout v 3x00543 4x00543	???? e is defined with: vill again result in 0x8000 for no further 8 bit data!	43264 0xA900 COMMAND V So you can only re	A100 A900:COMPARE A0 eadout the result one time	W/O	
DIRECT 16 BIT COMMAND DALI 1.0 Sends a 16 bit DALI 1.0 frame to the f you read this register, you can poll 0x8000: No answer received from th 0x20FF: A collision was detected on 0x000xFF: The 8 bit result of the la HINT: After you have read out the D DIRECT 16 BIT COMMAND+REPEAT	3x00542 4x00542 1:541 e DALI bus line I the 8 bit DALI answer. The return value e DALI bus up to now the DALI bus st command. ALI result, the next MODBUS readout v 3x00543	???? e is defined with: vill again result in 0x8000 for no further 8 bit data!	43264 0xA900 COMMAND V So you can only re 41216	A100 A900:COMPARE A0 eadout the result one time	UINT16	
DIRECT 16 BIT COMMAND DALI 1.0 Sends a 16 bit DALI 1.0 frame to the f you read this register, you can poll 0x8000: No answer received from th 0x20FF: A collision was detected on 0x000xFF: The 8 bit result of the la HINT: After you have read out the D DIRECT 16 BIT COMMAND+REPEAT	3x00542 4x00542 1:541 e DALI bus line I the 8 bit DALI answer. The return value e DALI bus up to now the DALI bus st command. ALI result, the next MODBUS readout v 3x00543 4x00543	???? e is defined with: vill again result in 0x8000 for no further 8 bit data!	43264 0xA900 COMMAND V So you can only re 41216 0xA100	A100 A900:COMPARE A0 eadout the result one time A100	UINT16	
DIRECT 16 BIT COMMAND DALI 1.0 Sends a 16 bit DALI 1.0 frame to the f you read this register, you can poll 0x8000: No answer received from th 0x20FF: A collision was detected on 0x20FF: The 8 bit result of the la	3x00542 4x00542 1:541 e DALI bus line I the 8 bit DALI answer. The return value e DALI bus up to now the DALI bus st command. ALI result, the next MODBUS readout v 3x00543 4x00543	???? e is defined with: vill again result in 0x8000 for no further 8 bit data!	43264 0xA900 COMMAND V So you can only re 41216	A100 A900:COMPARE A0 eadout the result one time	UINT16	

Sends a 16 bit DALI 1.0 frame twice within 10	0ms to to the DALI bus lir	ne				
If you read this register, you can poll the 8 bit 0x8000: No answer received from the DALI bi 0x20FF: A collision was detected on the DALI	DALI answer. The return	value is defined with:				
0x000xFF: The 8 bit result of the last comma	and.		, ,			
		out will again result in 0x8000 for no further 8 bit data! \$	So you can only rea	dout the result one time	т	I
DALI STATUS	3x00551 4x00551 I:550	3,0x0003 B:00 03			UINT16 R/O	
		Bits 0-2:Number of received bytes:3	1			
		Bit 4:DALI receive is active:0:NO				
		Bit 5:DALI receive error:0:NO				
		Bit 6:DALI transmit is active:0:NO				
Converter status indication: indicates the curre		Bit 7:DALI transmit error:0:NO				
Bits 0-2:RXState: number of received bytes C Bit 3: reserved:reserved Bit 4:RXBusy: =0: RX inactive, =1: RX active Bit 5:RXError: =0: no receiving error, =1: rece Bit 6:TXBusy: =0: TX inactive, =1:TX active Bit 7:TXError: =0: no sending error, =1: sendin Bits 8-15: always 0) as value indicates that n	o value is received. Under normal DALI conditions a va	lue of 1 indicates a	8 bit feedback frame from the slave		
DALI BUS ERROR	3x00552 4x00552	0,0x0000 B:00 00			UINT16 R/O	
	1:551	0:BUS IS OK			i	
Shows the status of the DALI bus.		U.BUS IS OK				
=0:DALI bus is OK and fully functional =1:DALI bus error, short circuit on DALI line, of		o DALI power supply				
DALI RESULT DALI 1.0	3x00512 4x00512 I:511	32768,0x8000 B:80 00	1	1	UINT16 R/O	
		Last DALI answer:32768,0x8000				
Whenever you write on the modbus registers 4x00512,3x00512,1:511 4x00513,3x00513,1:512 4x00522,3x00522,1:521 4x00523,3x00523,1:522 4x00532,3x00532,1:531 4x00533,3x00533,1:532 the 8 bit DALI answer is internally stored until 0x8000: No answer received from the DALI bi 0x20FF: A collision was detected on the DALI bi 0x000xFF: The 8 bit result of the last comma HINT: After you have read out the DALI result	you read one of this regis us up to now bus nd.	ters. The return value is defined with: out will again result in 0x8000 for no further 8 bit data! S	So you can only rea	dout the result one time		
DSI VALUE	3x00501	????	161	A1	UINT16	NO
	4x00501 I:500				W/O	
Condo o voluo in DCI modo (ODitu no o datas	aina)		0x00A1			
Sends a value in DSI mode (8Bit – no address	sing)					

MB RESI-DALI-SIO, ETH Registers 2

DALI PRIORITY SLOT	3x00101	2,0x0002	1	1	UINT16	NO
DAEIT MONTH SECT	4x00101	B:00 02	-	±	R/W	NO
	1:100					
		Current priority slot:2->14ms	1			
Sets or returns the current defined DALI prio 0: 12ms: Highest priority, used for messages 1: 13ms: User initiated arc power control con 2: 14ms: Configuration in general 3: 15ms: Automatic arc power control comma 4: 16ms: Lowest priority, used for query mes	s that must be repeated nmands and commands wł ands	available: nich have an impact on manual adjustment of arc powe	er (e.g., dim up with	n a special fade time)		
DALI 8 BIT FRAME						
DALI10 SEND 8 BIT DALI FRAME	3x10001 4x10001 I:10000	????	255	FF	UINT32 W/O	YES
Writing a 8 bit value to this register generate			1	1		
DALI10 SEND 8 BIT DALI FRAME AND REPEAT WITHIN 100ms	3x10003 4x10003 I:10002	????	160	A0	UINT32 W/O	YES
Writing a 8 bit value to this register generate						
DALI10 SEND 8 BIT DALI FRAME	3x10005 4x10005 I:10004	????	255	FF	UINT32R W/O	YES
Writing a 8 bit value to this register generate	s a 8 bit DALI/DSI value or	n the DALI bus			•	
DALI10 SEND 8 BIT DALI FRAME AND REPEAT WITHIN 100ms	3x10007 4x10007 I:10006	????	160	AO	UINT32R W/O	YES
Writing a 8 bit value to this register generate	s a 8 bit DALI/DSI value or	n the DALI bus				
DALI 16 BIT FRAME						
DALI10 SEND 16 BIT DALI FRAME	4x10011 I:10010	????	65278	FEFE	UINT32 W/O	YES
Writing a 16 bit value to this register generat	es a 16 bit DALI1.0 comm	and on the DALI bus				
DALI10 SEND 16 BIT DALI FRAME AND REPEAT WITHIN 100ms	3x10013 4x10013 I:10012	????	65024	FE00	UINT32 W/O	YES
Writing a 16 bit value to this register generat						
DALI10 SEND 16 BIT DALI FRAME	3x10015 4x10015 I:10014	????	65278	FEFE	UINT32R W/O	YES
Writing a 16 bit value to this register generat		and on the DALI bus				
DALI10 SEND 16 BIT DALI FRAME AND REPEAT WITHIN 100ms	3x10017 4x10017 I:10016	????	65024	FE00	UINT32R W/O	YES
Writing a 16 bit value to this register generat		and on the DALI bus				
DALI 24 BIT FRAME						
DALI20 SEND 24 BIT DALI FRAME	3x10021 4x10021 I:10020	????	8946700	88840C	UINT32 W/O	YES

Writing a 24 bit value to this register generates a	24 bit DALI2.0 frame o	n the DALI bus				
AND REPEAT WITHIN 100ms 43	x10023 x10023 10022	????	8946701	88840D	UINT32 W/O	YES
Writing a 24 bit value to this register generates a	24 bit DALI2.0 frame o	n the DALI bus				
43	x10025 x10025 10024	????	8946700	88840C	UINT32R W/O	YES
Writing a 24 bit value to this register generates a	24 bit DALI2.0 frame o	n the DALI bus				
AND REPEAT WITHIN 100ms 43	x10027 x10027 10026	????	8946701	88840D	UINT32R W/O	YES
Writing a 24 bit value to this register generates a	24 bit DALI2.0 frame o	n the DALI bus				
DALI 25 BIT eDALI FRAME – automat	ic redundand bit					
1:	x10031 10030	????	272742	42966	UINT32 W/O	YES
Writing a 24 bit value to this register generates o	out of a 24 bit eDALI con	nmand a valid 25 bit eDALI frame on the DALI bus				
1:	x10033 10032	????	272743	42967	UINT32 W/O	YES
		nmand a valid 25 bit eDALI frame on the DALI bus				
1:	x10035 10034	????	272742	42966	UINT32R W/O	YES
		nmand a valid 25 bit eDALI frame on the DALI bus				
AND REPEAT WITHIN 100ms 4:	x10037 x10037 10036	????	272743	42967	UINT32R W/O	YES
		nmand a valid 25 bit eDALI frame on the DALI bus				
DALI 25 BIT eDALI FRAME – user def	ined redundand bi	t				
1:	x10041 10040	????	545382	85266	UINT32 W/O	YES
Writing a 25 bit value to this register generates a		1				
::	x10043 10042	????	545383	85267	UINT32 W/O	YES
Writing a 25 bit value to this register generates a						
1:	x10045 10044	????	545382	85266	UINT32R W/O	YES
Writing a 25 bit value to this register generates a	25 bit eDALI frame on					
AND REPEAT WITHIN 100ms 4:	x10047 x10047 10046	????	545383	85267	UINT32R W/O	YES
Writing a 25 bit value to this register generates a	25 bit eDALI frame on	the DALI bus			·	

DALI 28 BIT FRAME						
DALI20 SEND 28 BIT DALI FRAME	3x10051 4x10051 I:10050	????	19088743	1234567	UINT32 W/O	YES
Writing a 28 bit value to this register generate	es a 28 bit DALI2.0 frame	on the DALI bus				
DALI20 SEND 28 BIT DALI FRAME AND REPEAT WITHIN 100ms	3x10053 4x10053 I:10052	????	19088744	1234568	UINT32 W/O	YES
Writing a 28 bit value to this register generate		on the DALI bus	J		1	
DALI20 SEND 28 BIT DALI FRAME	3x10055 4x10055 I:10054	????	19088743	1234567	UINT32R W/O	YES
Writing a 28 bit value to this register generate	es a 28 bit DALI2.0 frame	on the DALI bus				
DALI20 SEND 28 BIT DALI FRAME AND REPEAT WITHIN 100ms	3x10057 4x10057 I:10056	????	19088744	1234568	UINT32R W/O	YES
Writing a 28 bit value to this register generate	es a 28 bit DALI2.0 frame	on the DALI bus				
DALI 32 BIT FRAME						
DALI20 SEND 32 BIT DALI FRAME	3x10061 4x10061 I:10060	????	305419896	12345678	UINT32 W/O	YES
Writing a 32 bit value to this register generate	es a 32 bit DALI2.0 frame	on the DALI bus	-			
DALI20 SEND 32 BIT DALI FRAME AND REPEAT WITHIN 100ms	3x10063 4x10063 I:10062	????	305419896	12345678	UINT32 W/O	YES
Writing a 32 bit value to this register generate	es a 32 bit DALI2.0 frame	on the DALI bus				
DALI20 SEND 32 BIT DALI FRAME	3x10065 4x10065 I:10064	????	305419896	12345678	UINT32R W/O	YES
Writing a 32 bit value to this register generate	es a 32 bit DALI2.0 frame	on the DALI bus				
DALI20 SEND 32 BIT DALI FRAME AND REPEAT WITHIN 100ms	3x10067 4x10067 I:10066	????	305419896	12345678	UINT32R W/O	YES
Writing a 32 bit value to this register generate	es a 32 bit DALI2.0 frame	on the DALI bus				
DALI n BIT FRAME						
DALI20 HIGH 32 BITS DALI BUFFER FORMAT:UINT64	3x10091 4x10091 I:10090	0,0x00000000 B:00 00 00 00	0	0	UINT32 R/W	NO
This register in combination with the next one HINT: You must write first into this register (3	e builds an 64 Bit DALI frar x10091,4x10091,I:10090)	ne buffer. This buffer is used by the DALI commands b and imediately afterwards into the register (3x10092,4	elow. x10092,I:10091) to	setup the 64 Bit value.		
DALI20 LOW 32 BITS DALI BUFFER FORMAT:UINT64	3x10093 4x10093 I:10092	0,0x00000000 B:00 00 00 00	272742	42966	UINT32 R/W	NO
HINT: You must write first into the previous re	one builds an 64 Bit DAL egister (3x10091,4x10091,	frame buffer. This buffer is used by the DALI commar I:10090) and imediately afterwards into this register (3:	ids below. x10092,4x10092,I::	10091) to setup the 64 Bit value.		

MB RESI-DALI-SIO, ETH Registers 2

DALI20 LOW 32 BITS	3x10095	0.0×0000000	272742	42966	UINT32R	NO
DALI BUFFER FORMAT:UINT64R	4x10095	B:00 00 00 00		+2000	R/W	NO
DALI BOFFER FORMAT.OINT04R	1:10094	B.00 00 00 00			17/ 77	
This register in combination with the next one		⊔ ne buffer. This buffer is used by the DALI commands b	elow			
HINT: You must write first into this register (3	x10091,4x10091,I:10090)	and imediately afterwards into the register (3x10092,4)	(10092,1:10091) to :	setup the 64 Bit value.		
	· · · · · ·		,			
DALI20 HIGH 32 BITS	3x10097	0,0×0000000	0	0	UINT32R	NO
DALI BUFFER FORMAT:UINT64R	4x10097	B:00 00 00 00			R/W	
	I:10096					
This register in combination with the previous	s one builds an 64 Bit DALI	frame buffer. This buffer is used by the DALI comman I:10090) and imediately afterwards into this register (3>	ds below.	•		
HINT: You must write first into the previous re	egister (3x10091,4x10091,	I:10090) and imediately afterwards into this register (3)	(10092,4x10092,I:1	0091) to setup the 64 Bit value.		
DALI20 SEND DALI FRAME	3x10099	0,0x0000	125	24	UINT16	NO
WITH N BITS	4x10099	B:00 00			R/W	
	I:10098					
			0x007D			
			COMMAND	125:eDALI FRAME user generation of redundand b	bit	
This command sends an individual DALI fran	ne with x bits to the DALI b		register, the stored	64 bit DALI buffer will be transmitted as a DALI frame with you	r defined bit length.	
			-	-		
DALI20 SEND DALI FRAME	3x10100	0,0x0000	125	24	UINT16	NO
WITH N BITS AND	4x10100	B:00 00			R/W	
REPEAT COMMAND	I:10099					
WITHIN 100ms						
			0x007D			
			COMMAND	125:eDALI FRAME user generation of redundand b	it	
This command sends an individual DALI fram	ne with x bits twice within 1			register, the stored 64 bit DALI buffer will be transmitted as a D		r defined bit
length twice.						

MB RESI-DALI-SIO, ETH Registers 3

DALI LAST ANSWER COUNTER	3x10101	5,0x0005	1	1	UINT16
	4x10101	B:00 05			R/O
	I:10100				
		Current receive counter:5			
Potures a 16 bit counter for indicating a cha	ngo in the received data:	Every time the gateway receives a new DALI frame this	countor will be incr	amonted by one	
Returns a 10 bit counter for indicating a cha	inge in the received data. E	very time the gateway receives a new DALI frame this		entented by one.	
DALI LAST ANSWER BITLENGTH	3x10102	24,0x0018	1	1	UINT16
Briefer of Anover Brieferonn	4x10102	B:00 18	-	-	R/O
	1:10101	D.00 10			100
	1.10101				
Indicates the DALI frame length in bits of the	a last received DALL frame	Bit size of last received DALI frame:24 bits			
=			-	1.	
DALI LAST ANSWER	3x10103	0,0x0000000	1	1	UINT32
64 BIT BUFFER	4x10103	B:00 00 00 00			R/O
HIGH 32 BITS	I:10102				
Contains the upper 32 bit value of the last re	eceived 64-Bit DALI buffer				
DALI LAST ANSWER	3x10105	8946702,0x0088840E	1	1	UINT32
64 BIT BUFFER	4x10105	B:00 88 84 0E	-	-	R/O
LOW 32 BITS	1:10104	D.00 00 04 0E			100
LOW 32 BI13	1.10104	Last received 64 bit DALI frame:000000000			
Contains the lower 32 bit value of the last re	animod 64 Dit DALL buffor	Last received 64 bit DALI frame:000000000	J88840E		
	1				
DALI LAST ANSWER	3x10107	8946702,0x0088840E	1	1	UINT32R
64 BIT BUFFER	4x10107	B:84 0E 00 88			R/O
LOW 32 BITS	I:10106				
Contains the lower 32 bit value of the last re	eceived 64-Bit DALI buffer				1
DALI LAST ANSWER	3x10109	0,0x0000000	1	1	UINT32R
64 BIT BUFFER	4x10109	B:00 00 00 00	-	-	R/O
HIGH 32 BITS	1:10108	D.00 00 00 00			100
	1.10100				
Contains the upper 32 bit value of the last re		Last received 64 bit DALI frame:000000000	J88840E		
	eceiveu 64-bil DALI buller				
DALI ANSWER RING PUFFER					
DALI RING PUFFER COUNTER	3x20001	1,0x0001	1	1	UINT16
	4x20001	B:00 01			R/O
	1:20000	2.00 02			
		Current receive counter:1			
Returns a 16 bit counter for indicating a cha	nge in the received data. E	Every time the gateway receives a new DALI frame this	counter will be incr	emented by one	
	inge in the received dala. L	and the galeway receives a new DALI fiame (ins		emented by one.	
DALI RING PUFFER FLAGS	3x20002	2,0x0002	1	1	UINT16
	4x20002	B:00 02	-		R/O
	1:20001	B.00 02			100
	1.20001				
Indicates additional information for the DALL	frama	Flags of the received DALI frame:2 -> 2:RX			
Indicates additional information for the DALI =1:DALI frame was transmitted by the gatew =2:DALI frame was received by the gateway =3:DALI collision on the bus detected	vay				
	000000	04.0-0010	1	1	
DALI RING PUFFER BITLENGTH	3x20003	24,0x0018	1	1	UINT16
	4x20003	B:00 18			R/O
	1:20002				
		Bit size of last received DALI frame:24 bits			
	1				I

Indicates the DALI frame length in bits of th	e last received DALI frame				
DALI RING PUFFER DATA 32 BIT	3x20004 4x20004 I:20003	8946700,0x0088840C B:00 88 84 0C	1	1	UINT32 R/O
		32 bit DALI frame from ring puffer:0088840C	•		
Contains the 32 bit value of the last receive	d DALI frame				
DALI RING PUFFER TICKS	3x20006 4x20006 I:20005	24449,0x00005F81 B:00 00 5F 81	1	1	UINT32 R/O
		Gap to previous DALI frame:24449ms -> 24,	449s		
Contains a value in Milliseconds, which des	cribes the gap to the previo	bus DALI frame on the DALI bus.			
DALI RING PUFFER DATA 32 BIT	3x20008 4x20008 I:20007	8946700,0x0088840C B:84 0C 00 88	1	1	UINT32R R/O
		32 bit DALI frame from ring puffer:0088840C	•		
Contains the 32 bit value of the last receive	d DALI frame	· · · ·			
DALI RING PUFFER TICKS	3x20010 4x20010 I:20009	24449,0x00005F81 B:5F 81 00 00	1	1	UINT32R R/O
		Gap to previous DALI frame:24449ms -> 24,	449s		
Contains a value in Milliseconds, which des	cribes the gap to the previo	bus DALI frame on the DALI bus.			·

MB RESI-DALI-SIO, ETH DT8 XY Registers

4x01101 1:1100 This register shows the actual status of the last triggered DT8 command was finished. The following values are returned: 0: No operation happened in the past 1: Last operation was successfully finished. The values in the 9999: Last operation was finished, but had errors (Timeout of 32768,0x8000: Last command is currently processed and is in DALI DT8:REGISTER 1 3x01102 4x01102 1:1101 This is an universal register for DALI device type 8 functions. DALI DT8:REGISTER 2 3x01103 4x01103 1:1101 This is an universal register for DALI device type 8 functions. DALI DT8:REGISTER 3 3x01104 4x01104 1:1103 This is an universal register for DALI device type 8 functions. DALI DT8:REGISTER 3 3x01104 4x01104 1:1103 This is an universal register for DALI device type 8 functions. DALI DT8:REGISTER 4 3x01105 4x01105 1:1104	e registers are valid. r no answer). not finished yet. Current value of register:0 Current value of register:0 Usually the Arc Power is stored here as a value betweer 0,0x0000 B:00 00 Current value of register:0 Depending on the DT8 function, this register stores diffe 0,0x0000 B:00 00 Current value of register:0	127 10 and 255.	I commands are generated. Therefore you ha	R/O R/O R/O Nave to poll this registers to find out, if and he UINT16 R/W UINT16 R/W UINT16 R/W UINT16 R/W	ow the last DTA YES YES
command was finished. The following values are returned: D: No operation happened in the past 1: 1: Last operation was successfully finished. The values in the 2999: Last operation was finished, but had errors (Timeout of 32768,0x8000: Last command is currently processed and is in DALI DT8:REGISTER 1 3x01102 4x01102 1:1101 This is an universal register for DALI device type 8 functions. 3x01103 4x01103 1:1102 This is an universal register for DALI device type 8 functions. 3x01104 4x01103 1:1102 This is an universal register for DALI device type 8 functions. 3x01104 4x01104 1:1103 DALI DT8:REGISTER 3 3x01104 4x01104 1:1103 This is an universal register for DALI device type 8 functions. DALI DT8:REGISTER 3 DALI DT8:REGISTER 4 3x01104 4x01104 1:1103	8 command. Wevery time you trigger a new DT8 commar e registers are valid. r no answer). not finished yet. Current value of register:0 Usually the Arc Power is stored here as a value betweer 0,0x0000 B:00 00 Current value of register:0 Depending on the DT8 function, this register stores diffe 0,0x0000 B:00 00 Current value of register:0	127 1 0 and 255. 1 rent colour values.	127 1	UINT16 R/W UINT16 R/W	YES
command was finished. Command was finished. The following values are returned: Command was finished in the past 1: Last operation was successfully finished. The values in the jage: Command was finished, but had errors (Timeout or 32768,0x8000: Last command is currently processed and is in 2000; Last command is currently processed and is in 2000; Last command is currently processed and is in 2000; Last command is currently processed and is in 2000; Last command is currently processed and is in 2000; Last command is currently processed and is in 2000; Last command is currently processed and is in 2000; Last command is currently processed and is in 2000; Last command is currently processed and is in 2000; Last command is currently processed and is in 2000; Last command is currently processed and is in 2000; Last command is currently processed and is in 2000; Last command is currently processed and is in 2000; Last command is currently processed and is in 2000; Last command is currently processed and is in 2000; Last command is currently processed and is in 2000; Last command is currently processed and is in 2000; Lin	e registers are valid. r no answer). not finished yet. Current value of register:0 Current value of register:0 Usually the Arc Power is stored here as a value betweer 0,0x0000 B:00 00 Current value of register:0 Depending on the DT8 function, this register stores diffe 0,0x0000 B:00 00 Current value of register:0	127 1 0 and 255. 1 rent colour values.	127 1	UINT16 R/W UINT16 R/W	YES
4x01102 1:1101 This is an universal register for DALI device type 8 functions. DALI DT8:REGISTER 2 3x01103 4x01102 1:1102 This is an universal register for DALI device type 8 functions. DALI DT8:REGISTER 3 3x01104 4x01104 1:1103 This is an universal register for DALI device type 8 functions. DALI DT8:REGISTER 3 3x01104 4x01104 1:1103 This is an universal register for DALI device type 8 functions. DALI DT8:REGISTER 4 3x01105 1:1104 1:1104	B:00 00 Current value of register:0 Usually the Arc Power is stored here as a value betweer 0,0x0000 B:00 00 Current value of register:0 Depending on the DT8 function, this register stores diffe 0,0x0000 B:00 00 Current value of register:0	n 0 and 255. 1 rent colour values.	1	UINT16 R/W UINT16 R/W UINT16	YES
DALI DT8:REGISTER 2 3x01103 4x01103 1:1102 This is an universal register for DALI device type 8 functions. DALI DT8:REGISTER 3 3x01104 4x01104 1:1103 This is an universal register for DALI device type 8 functions. DALI DT8:REGISTER 4 3x01105 4x01105 1:1104	Usually the Arc Power is stored here as a value betweer 0,0x0000 B:00 00 Current value of register:0 Depending on the DT8 function, this register stores diffe 0,0x0000 B:00 00 Current value of register:0 Depending on the DT8 function, this register stores diffe 0,0x0000 B:00 00 Current value of register:0	rent colour values.	2	UINT16	
DALI DT8:REGISTER 2 3x01103 4x01103 1:1102 This is an universal register for DALI device type 8 functions. DALI DT8:REGISTER 3 3x01104 4x01104 1:1103 This is an universal register for DALI device type 8 functions. DALI DT8:REGISTER 4 3x01105 4x01105 1:1104	0,0x0000 B:00 00 Current value of register:0 Depending on the DT8 function, this register stores diffe 0,0x0000 B:00 00 Current value of register:0	rent colour values.	2	UINT16	
4x01103 I:1102 This is an universal register for DALI device type 8 functions. DALI DT8:REGISTER 3 3x01104 4x01104 1:1103 This is an universal register for DALI device type 8 functions. DALI DT8:REGISTER 4 3x01105 4x01105 1:1104	B:00 00 Current value of register:0 Depending on the DT8 function, this register stores diffe 0,0x0000 B:00 00 Current value of register:0	rent colour values.	2	UINT16	
DALI DT8:REGISTER 3 This is an universal register for DALI device type 8 functions. DALI DT8:REGISTER 4 3x01105 4x01105 1:1104	Depending on the DT8 function, this register stores diffe 0,0x0000 B:00 00 Current value of register:0		2		YES
DALI DT8:REGISTER 3 This is an universal register for DALI device type 8 functions. DALI DT8:REGISTER 4 3x01105 4x01105 1:1104	0,0x0000 B:00 00 Current value of register:0		2		YES
4x01104 I:1103 This is an universal register for DALI device type 8 functions. DALI DT8:REGISTER 4 3x01105 4x01105 1:1104	B:00 00 Current value of register:0	2	2		YES
This is an universal register for DALI device type 8 functions. DALI DT8:REGISTER 4 3x01105 4x01105 I:1104					
DALI DT8:REGISTER 4 4x01105 1:1104	Depending on the DTO function, this register at the diffe				
4x01105 I:1104	. Depending on the עומו או מומומו או האופן inction, this register stores diffe	rent colour values.		<u> </u>	
	0,0x0000 B:00 00	3	3	UINT16 R/W	NO
	Current value of register:0				
This is an universal register for DALI device type 8 functions.	Depending on the DT8 function, this register stores diffe	rent colour values.			
DALI DT8:REGISTER 5 3x01106 4x01106 1:1105	0,0x0000 B:00 00	4	4	UINT16 R/W	NO
	Current value of register:0				
This is an universal register for DALI device type 8 functions.	Depending on the DT8 function, this register stores diffe	rent colour values.			
DALI DT8:REGISTER 6 3x01107 4x01107 l:1106	0,0x0000 B:00 00	5	5	UINT16 R/W	NO
	Current value of register:0				
This is an universal register for DALI device type 8 functions.	Depending on the DT8 function, this register stores diffe	rent colour values.			
DALI DT8:REGISTER 7 3x01108 4x01108 1:1107	0,0x0000 B:00 00	6	6	UINT16 R/W	NO
	Current value of register:0	1			

MB RESI-DALI-SIO, ETH DT8 XY Registers

DALI DT8:REGISTER 8	3x01109	0,0x0000	65535	65.535	UINT16	NO
	4x01109	B:00 00			R/W	
	I:1108					
		Current value of register:0	·			
his is an universal register for DALI de	evice type 8 functions. Dep	ending on the DT8 function, this register stores diffe	erent colour values.		I I	
DALI DT8:REGISTER 9	3x01110	0,0x0000	65535	65.535	UINT16	NO
	4x01110	B:00 00	00000	00.000	R/W	NO
	1:1109	B.00 00				
	1.1103	Current value of register/0				
his is an universal register for DALL d	evice type 8 functions. Den	Current value of register:0 ending on the DT8 function, this register stores diffe	erent colour values			
DALI DT8:REGISTER 10	3x01111	0,0x0000	65535	65.535	UINT16	NO
	4x01111	B:00 00			R/W	
	I:1110					
		Current value of register:0				
his is an universal register for DALI de	evice type 8 functions. Dep	ending on the DT8 function, this register stores diffe	erent colour values.		· · · ·	
DALI DT8:REGISTER 11	3x01112	0,0x0000	65535	65.535	UINT16	NO
ALI DIO.REGISTER II	4x01112	B:00 00	00000	05.555	R/W	NO
		B:00 00			R/W	
	l:1111					
		Current value of register:0				
his is an universal register for DALI de	evice type 8 functions. Dep	ending on the DT8 function, this register stores diffe	erent colour values.			
DALI DT8:REGISTER 12	3x01113	0,0x0000	65535	65.535	UINT16	NO
	4x01113	B:00 00			R/W	
	1:1112	2.00 00				
		Current value of register:0				
This is an universal register for DALI de	evice type 8 functions. Dep	ending on the DT8 function, this register stores diffe	erent colour values.			
	001111	0.0.0000	0000			NO
DALI DT8:REGISTER 13	3x01114	0,0x0000	65535	65.535	UINT16	NO
	4x01114	B:00 00			R/W	
	I:1113					
		Current value of register:0				
his is an universal register for DALI de	evice type 8 functions. Dep	ending on the DT8 function, this register stores diffe	erent colour values.			
DALI DT8:REGISTER 14	3x01115	0,0x0000	65535	65.535	UINT16	NO
	4x01115	B:00 00	00000	00.000	R/W	
	1:1114	В.00 00			R/ W	
	1.1114	Our mante a chur a fun ai sta mO				
bis is an universal register for DALL d	ovice type 8 functions. Den	Current value of register:0 ending on the DT8 function, this register stores diffe	prent colour values			
The is an universal register for DALI ut	evice type o functions. Dep	כוימויוש טוי נויב ביוס ומויכנוטוו, נוווא ובטואניו אנווא מווא	הכות נטוטעו אמועבט.			
DALI DT8:REGISTER 15	3x01116	0,0x0000	65535	65.535	UINT16	NO
	4x01116	B:00 00			R/W	
	1:1115	2.00 00				
		Current value of register:0				
his is an universal register for DALI de	evice type 8 functions. Dep	ending on the DT8 function, this register stores diffe	erent colour values.			
			07505		110.000	
DALI DT8:REGISTER 16	3x01117	0,0x0000	65535	65.535	UINT16	NO
	4x01117	B:00 00			R/W	
	I:1116					
		Current value of register:0				

This is an universal register for DALI device	type 8 functions. Depending	g on the DT8 function, this register stores different colo	our values.			
DALI DT8:XY-COORDINATE						
DALI DT8:SET XY-COORDINATE AND ARC LEVEL N DIGITS	3x01151 4x01151 I:1150	????	110	110:LAMP GROUP 11	UINT16 W/O	NO
This functions sets the arc power level and the Writing 0 to 63 activates this command for a Writing 100 to 115 activates this command for Writing 65535 activates a broadcast command	or a specific lamp group	between 0 and 65535. Iamp				
Reğister 4x01104,3x01104,I:1103: New y co	bordinate in the format 0 to 6 bordinate in the format 0 to 6	65535. Calculation: DIGITS=X/65536. 65535 means M 65535. Calculation: DIGITS=Y/65536. 65535 means M	IASK. IASK.			
HNT:Check the status register 4x01101,3x0 DALI DT8:SET XY-COORDINATE AND ARC LEVEL	01101,I:1100 for the current 3x01152 4x01152 I:1151	status of the internal command processing ????	110	110:LAMP GROUP 11	UINT16 W/O	NO
This functions sets the arc power level and the Vriting 0 to 63 activates this command for a Vriting 100 to 115 activates this command for Vriting 65535 activates a broadcast comma Register 4x01102,3x01102,1:1101: New arc Register 4x01103,3x01103,1:1102: New x co Register 4x01104,3x01104,1:1103: New y co	specific short address of a or a specific lamp group nd for all lamps power level between 0 and pordinate in the format 0 to 9 pordinate in the format 0 to 9	254, 255 means MASK. 9999. Calculation: VALUE=X*10000. 9999. Calculation: VALUE=X*10000.				
AND ARC LEVEL	4x01161	status of the internal command processing	10	10:LAMP SHORT ADDRESS 11	UINT16 W/O	NO
N DIGITS This functions reads from a specific lamp the Writing 0 to 63 activates this command for a	I:1160 e arc power level and the X specific short address of a	Y coordinates as digits between 0 and 65535 . Iamp				
f the command was successful, the register Register 4x01102,3x01102,1:1101: Current a Register 4x01103,3x01103,1:1102: Current x Register 4x01104,3x01104,1:1103: Current y	s hold the following results: arc power level between 0 a < coordinate in the format 0 / coordinate in the format 0	nd 254, 255 means MASK. to 65535. Calculation: X=VALUE/65536. 65535 means to 65535. Calculation: Y=VALUE/65536. 65535 means	s MASK. s MASK.			
IINT:Check the status register 4x01101,3x0 ALI DT8:GET XY-COORDINATE ND ARC LEVEL	01101,I:1100 for the current 3x01162 4x01162 I:1161	status of the internal command processing ????	10	10:LAMP SHORT ADDRESS 11	UINT16 W/O	NO
his functions reads from a specific lamp the Vriting 0 to 63 activates this command for a	e arc power level and the X specific short address of a	Y coordinates as digits between 0 and 65535 . Iamp			I	
f the command was successful, the register: Register 4x01102,3x01102,1:1101: Current a Register 4x01103,3x01103,1:1102: Current x Register 4x01104,3x01104,1:1103: Current y	arc power level between 0 a	nd 254, 255 means MASK. to 65535. Calculation: X=VALUE/10000. 65535 means to 65535. Calculation: Y=VALUE/10000. 65535 means	s MASK. s MASK.			
INT:Check the status register 4x01101,3x0 Contains the lower 32 bit value of the last re)1101,I:1100 for the current ceived 64-Bit DALI buffer	status of the internal command processing				

MB RESI-DALI-SIO, ETH DT8 Tc Registers

DALI DT8:STATUS REGISTER	3x01101	1,0×0001	0		UINT16	
DAEI DT0.STAT05 REDISTER	4x01101	B:00 01	0		R/O	
	1:1100	D.00 01			100	
	1.1100	Current value of register:1				
This register shows the actual status of the	a lact triggered DT9 com	Current value of register:1	internally many DALLS	ommands are generated. Therefore you have to poll this re	vaictors to find out, if and l	ow the last DT9
Command was finished. The following values are returned: 0: No operation happened in the past 1: Last operation was successfully fiinishe 9999: Last operation was finished, but hac 32768,0x8000: Last command is currently						
DALI DT8:REGISTER 1	3x01102	116,0x0074	127	127	UINT16	YES
	4x01102	B:00 74	121		R/W	120
	1:1101	D.00 74				
	1.1101	Current value of register:116				
This is an universal register for DALL device	a tuna 0 functiona Lloua	Current value of register:116) and 2EE			
	e type a functions. Usua	lly the Arc Power is stored here as a value between (J anu 255.			
DALI DT8:REGISTER 2	3x01103	0,0x0000	1	1	UINT16	YES
	4x01103	B:00 00			R/W	
	1:1102	2.00 00				
		Current value of register:0				
This is an universal register for DALL devic	e type 8 functions. Dene	ending on the DT8 function, this register stores difference	nt colour values			
DALI DT8:REGISTER 3	3x01104	254,0x00FE	2	2	UINT16	YES
	4x01104	B:00 FE			R/W	
	I:1103					
		Current value of register:254				
This is an universal register for DALI device	ce type 8 functions. Depe	nding on the DT8 function, this register stores differe	nt colour values.			1
	0.01105	0.0.0000	0	3		NO
DALI DT8:REGISTER 4	3x01105 4x01105	0,0x0000 B:00 00	3	3	UINT16	NO
		B:00 00			R/W	
	I:1104					
This is a second s		Current value of register:0				
I his is an universal register for DALI devic	ce type 8 functions. Depe	nding on the DT8 function, this register stores differe	nt colour values.			
DALI DT8:REGISTER 5	3x01106	4,0x0004	4	4	UINT16	NO
	4x01106	B:00 04			R/W	
	I:1105					
		Current value of register:4				
This is an universal register for DALI devic	ce type 8 functions. Depe	ending on the DT8 function, this register stores differe	nt colour values.			
DALI DT8:REGISTER 6	3x01107	5,0x0005	5	5	UINT16	NO
	4x01107	B:00 05			R/W	
	I:1106					
		Current value of register:5				
This is an universal register for DALI device	ce type 8 functions. Depe	nding on the DT8 function, this register stores differe	nt colour values.			
	0.01100					
DALI DT8:REGISTER 7	3x01108	6,0x0006	6	6	UINT16	NO
	4x01108	B:00 06			R/W	
	I:1107					
		Current value of register:6				

		ending on the DT8 function, this register stores diffe				
DALI DT8:REGISTER 8	3x01109 4x01109 I:1108	0,0x0000 B:00 00	65535	65.535	UINT16 R/W	NO
		Current value of register:0				
his is an universal register for DALI d	evice type 8 functions. Dep	ending on the DT8 function, this register stores diffe	rent colour values.			
	0.01110	0.0.0000	05505	05 505		
OALI DT8:REGISTER 9	3x01110 4x01110 I:1109	0,0×0000 B:00 00	65535	65.535	UINT16 R/W	NO
		Current value of register:0				
his is an universal register for DALI d	evice type 8 functions. Dep	ending on the DT8 function, this register stores diffe	rent colour values.		I I	
OALI DT8:REGISTER 10	3x01111	0,0x0000	65535	65.535	UINT16	NO
	4x01111 I:1110	B:00 00			R/W	ne
		Current value of register:0				
his is an universal register for DALI d	evice type 8 functions. Dep	ending on the DT8 function, this register stores diffe	rent colour values.			
DALI DT8:REGISTER 11	3x01112	0,0x0000	65535	65.535	UINT16	NO
	4x01112 I:1111	B:00 00			R/W	
		Current value of register:0				
his is an universal register for DALI d	evice type 8 functions. Dep	ending on the DT8 function, this register stores diffe	rent colour values.			
DALI DT8:REGISTER 12	3x01113	0,0x0000	65535	65.535	UINT16	NO
	4x01113 I:1112	B:00 00			R/W	
		Current value of register:0				
his is an universal register for DALI d	evice type 8 functions. Dep	ending on the DT8 function, this register stores diffe	rent colour values.			
DALI DT8:REGISTER 13	3x01114	0,0x0000	65535	65.535	UINT16	NO
	4x01114 I:1113	B:00 00			R/W	
		Current value of register:0				
his is an universal register for DALI d	evice type 8 functions. Dep	ending on the DT8 function, this register stores diffe	rent colour values.			
DALI DT8:REGISTER 14	3x01115	0,0x0000	65535	65.535	UINT16	NO
	4x01115 I:1114	B:00 00			R/W	
		Current value of register:0				
his is an universal register for DALI d	evice type 8 functions. Dep	ending on the DT8 function, this register stores diffe	rent colour values.			
DALI DT8:REGISTER 15	3x01116	0,0x0000	65535	65.535	UINT16	NO
	4x01116 I:1115	B:00 00			R/W	
		Current value of register:0				

MB RESI-DALI-SIO, ETH DT8 Tc Registers

DALI DT8:REGISTER 16	3x01117 4x01117 I:1116	0,0x0000 B:00 00	65535	65.535	UINT16 R/W	NO
		Current value of register:0				
This is an universal register for DALI device	type 8 functions. Dependin	g on the DT8 function, this register stores different colo	our values.			
DALI DT8:COLOUR TEMPERATUR	RETC	1		1	1 1	
DALI DT8:SET ARC LEVEL AND	3x01171	????	3	3:LAMP SHORT ADDRESS 4	UINT16	NO
COLOUR TEMPERATURE TC	4x01171				W/O	
IN MIREK	1:1170					
Writing 0 to 63 activates this command for a Writing 100 to 115 activates this command for Writing 65535 activates a broadcast comman Register 4x01102,3x01102,I:1101: New arc j	specific short address of a or a specific lamp group nd for all lamps					
Register 4x01102,3x01102,1:1101: New Arc J Register 4x01103,3x01103,1:1102: New MIR	EK value for the colour tem	pperature between 0 and 65535. Calculation: MIREK=1	.000.000/KELVIN.	1 Mirek=1.000.000Kelvin, 65534 Mirek=15.25K 0 is invalid and	65535 means MASI	۲.
HINT:Check the status register 4x01101,3x0	1101,I:1100 for the current	status of the internal command processing				
DALI DT8:SET ARC LEVEL AND	3x01172	????	3	3:LAMP SHORT ADDRESS 4	UINT16	NO
COLOUR TEMPERATURE TC	4x01172				W/O	
IN KELVIN	I:1171	CW/WW/ dimmoro in KELVIN between 0 and CEE2E				
Writing 0 to 63 activates this command for a Writing 100 to 115 activates this command fo Writing 65535 activates a broadcast comman	or a specific lamp group	or CW-WW dimmers in KELVIN between 0 and 65535. lamp				
Register 4x01102,3x01102,I:1101: New arc Register 4x01103,3x01103,I:1102: New KEL		-				
HINT:Check the status register 4x01101,3x0 DALI DT8:GET ARC LEVEL AND	1101,I:1100 for the current	status of the internal command processing	10			NO
COLOUR TEMPERATURE TC	3x01181 4x01181	????	10	10:LAMP SHORT ADDRESS 11	UINT16 W/O	NO
IN MIREK	1:1180				VV/O	
This functions returns the current power leve	and the current colour ten	pperature Tc for CW-WW dimmers in MIREK between () and 65535.			
Writing 0 to 63 activates this command for a	specific short address of a	lamp				
Register 4x01102,3x01102,I:1101: Current a Register 4x01103,3x01103,I:1102: Current M	rc power level between 0 a AIREK value for the colour	and 254, 255 means MASK. temperature between 0 and 65535. Calculation: MIREk	(=1.000.000/KELV	IN. 1 Mirek=1.000.000Kelvin, 65534 Mirek=15.25K 0 is invalid a	and 65535 means M	ASK.
HINT:Check the status register 4x01101,3x0	1101,I:1100 for the current	status of the internal command processing				
DALI DT8:GET ARC LEVEL AND	3x01182	????	3	3:LAMP SHORT ADDRESS 4	UINT16	NO
COLOUR TEMPERATURE TC	4x01182				W/O	
IN KELVIN	I:1181					
This functions returns the current arc power I Writing 0 to 63 activates this command for a	level and the current coloui specific short address of a	r temperature Tc for CW-WW dimmers in KELVIN betw lamp	een 0 and 65535.			
Register 4x01102,3x01102,1:1101: Current a Register 4x01103,3x01103,1:1102: Current K	rc power level between 0 a ELVIN value for the colour	and 254, 255 means MASK. • temperature between 0 and 65535.				
HINT:Check the status register 4x01101,3x0	1101,I:1100 for the current	status of the internal command processing				

MB RESI-DALI-SIO, ETH DT8 Primary N Registers

DALI DT8:STATUS REGISTER	3x01101	1,0x0001	0		UINT16	
	4x01101 I:1100	B:00 01			R/O	
		Current value of register:1				
This register shows the actual status of the command was finished. The following values are returned: D: No operation happened in the past L: Last operation was successfully finished 1999: Last operation was finished, but had 12768,0x8000: Last command is currently	I. The values in the registe errors (Timeout or no ans)	rs are valid. ver).	ernally many DALI co	mmands are generated. Therefore you have to poll this reg	jisters to find out, if and h	ow the last DT
DALI DT8:REGISTER 1	3x01102 4x01102 l:1101	116,0x0074 B:00 74	127	127	UINT16 R/W	YES
		Current value of register:116				
This is an universal register for DALI device	e type 8 functions. Usually	the Arc Power is stored here as a value between 0 an	d 255.			
DALI DT8:REGISTER 2	3x01103	0.0x0000	1	1	UINT16	YES
	4x01103 I:1102	B:00 00	1		R/W	TES
		Current value of register:0				
This is an universal register for DALI device	e type 8 functions. Depend	ing on the DT8 function, this register stores different c	olour values.			
DALI DT8:REGISTER 3	3x01104	254.0x00FE	2	2	UINT16	YES
	4x01104 I:1103	B:00 FE			R/W	
		Current value of register:254				
This is an universal register for DALI device	e type 8 functions. Depend	ing on the DT8 function, this register stores different c	olour values.			
DALI DT8:REGISTER 4	3x01105 4x01105 l:1104	0,0x0000 B:00 00	3	3	UINT16 R/W	NO
		Current value of register:0				
This is an universal register for DALI device	e type 8 functions. Depend	ing on the DT8 function, this register stores different c	olour values.			
DALI DT8:REGISTER 5	3x01106 4x01106 l:1105	4,0x0004 B:00 04	4	4	UINT16 R/W	NO
		Current value of register:4				
This is an universal register for DALI device	e type 8 functions. Depend	ing on the DT8 function, this register stores different c	olour values.			
DALI DT8:REGISTER 6	3x01107 4x01107 I:1106	5,0x0005 B:00 05	5	5	UINT16 R/W	NO
		Current value of register:5				
This is an universal register for DALI device	e type 8 functions. Depend	ing on the DT8 function, this register stores different co	olour values.			
DALI DT8:REGISTER 7	3x01108 4x01108 I:1107	6,0x0006 B:00 06	6	6	UINT16 R/W	NO
	1.1101	Current value of register:6				
		ing on the DT8 function, this register stores different co				

DALI DT8:REGISTER 8	3x01109 4x01109	0,0x0000 B:00 00	65535	65.535	UINT16 R/W	NO
	1:1108	2.00 00				
		Current value of register:0				
This is an universal register for DALI d	evice type 8 functions. Dependi	ng on the DT8 function, this register stores differe	nt colour values.			
DALI DT8:REGISTER 9	3x01110	0,0x0000	65535	65.535	UINT16	NO
	4x01110	B:00 00			R/W	
	I:1109					
		Current value of register:0				
This is an universal register for DALI d	evice type 8 functions. Dependi	ng on the DT8 function, this register stores differe	nt colour values.			
	0.01111	0.0.0000	05505			NO
DALI DT8:REGISTER 10	3x01111	0,0x0000	65535	65.535	UINT16	NO
	4x01111 I:1110	B:00 00			R/W	
	1.1110	Current value of registery0				
This is an universal register for DALL	ovice type 9 functions Dependi	Current value of register:0 ng on the DT8 function, this register stores differe	nt colour values			
This is an universal register 101 DALT u	evice type o iunctions. Dependi		The COLOUT VALUES.			
DALI DT8:REGISTER 11	3x01112	0,0x0000	65535	65.535	UINT16	NO
	4x01112	B:00 00	00000		R/W	
	1:1111	2.00 00				
		Current value of register:0				
This is an universal register for DALI d	evice type 8 functions. Dependi	ng on the DT8 function, this register stores differe	nt colour values.			
DALI DT8:REGISTER 12	3x01113	0,0×0000	65535	65.535	UINT16	NO
	4x01113	B:00 00			R/W	
	I:1112					
		Current value of register:0				
This is an universal register for DALI d	evice type 8 functions. Dependi	ng on the DT8 function, this register stores differe	nt colour values.			
DALI DT8:REGISTER 13	3x01114	0,0x0000	65535	65.535	UINT16	NO
DALI DI 8. REGISTER 13	4x01114	B:00 00	00000	05.535	R/W	NO
	1:1113	Б.00 00			FK/ VV	
	1.1115	Current value of register:0				
This is an universal register for DALL	evice type 8 functions Dependi	ng on the DT8 function, this register stores differe	nt colour values			
	levice type o functions. Dependi					
DALI DT8:REGISTER 14	3x01115	0,0x0000	65535	65.535	UINT16	NO
	4x01115	B:00 00			R/W	
	1:1114					
		Current value of register:0				
This is an universal register for DALI d	evice type 8 functions. Dependi	ng on the DT8 function, this register stores differe	nt colour values.			
-						
DALI DT8:REGISTER 15	3x01116	0,0×0000	65535	65.535	UINT16	NO
	4x01116	B:00 00			R/W	
	l:1115					
		Current value of register:0				
This is an universal register for DALI d	evice type 8 functions. Dependi	ng on the DT8 function, this register stores differe	nt colour values.			
DALI DT8:REGISTER 16	3x01117	0,0x0000	65535	65.535	UINT16	NO
DALI DIO.REGISTER 10	4x01117	B:00 00	00035	05.535		NO
	4x01117 I:1116	B.00 00			R/W	
	1.1110	Current value of registered				
		Current value of register:0				

This is an universal register for DALI device ty	pe 8 functions. Depending	g on the DT8 function, this register stores different colour	values.			
DALI DT8:COLOUR PRIMARY N CH		0000	10			NO
DALI DT8:SET ARC LEVEL AND PRIMARY N CHANNEL 1	3x01191 4x01191 I:1190	????	10	10:LAMP SHORT ADDRESS 11	UINT16 W/O	NO
This functions sets the arc power level and the Writing 0 to 63 activates this command for a s Writing 100 to 115 activates this command for Writing 65535 activates a broadcast command	pecific short address of a r a specific lamp group d for all lamps	lamp				
		254, 255 means MASK. and 65535. 0 -> 0%, 65534 -> 100%, 65535 means MAS	К.			
HINT:Check the status register 4x01101,3x01 DALI DT8:SET ARC LEVEL AND PRIMARY N CHANNELS 1-2	101,I:1100 for the current 3x01192 4x01192 I:1191	status of the internal command processing ????	10	10:LAMP SHORT ADDRESS 11	UINT16 W/O	NO
This functions sets the arc power level and the Writing 0 to 63 activates this command for a s Writing 100 to 115 activates this command for Writing 65535 activates a broadcast command	pecific short address of a	lamp				
Reĝister 4x01104,3x01104,I:1103: New value	for channel 1 between 0 for channel 2 between 0	and 65535. 0 -> 0%, 65534 -> 100%, 65535 means MAS and 65535. 0 -> 0%, 65534 -> 100%, 65535 means MAS	K. K.			
HINT:Check the status register 4x01101,3x01 DALI DT8:SET ARC LEVEL AND PRIMARY N CHANNELS 1-3	4x01193 I:1192		10	10:LAMP SHORT ADDRESS 11	UINT16 W/O	NO
This functions sets the arc power level and the Writing 0 to 63 activates this command for a s Writing 100 to 115 activates this command for Writing 65535 activates a broadcast command	r a specific lamp group	nels 1 to 3 between 0 and 65535. Iamp				
Register 4x01102,3x01102,I:1101: New arc p Register 4x01103,3x01103,I:1102: New value Register 4x01104,3x01104,I:1103: New value Register 4x01105,3x01105,I:1104: New value	for channel 1 between 0	254, 255 means MASK. and 65535. 0 -> 0%, 65534 -> 100%, 65535 means MAS and 65535. 0 -> 0%, 65534 -> 100%, 65535 means MAS and 65535. 0 -> 0%, 65534 -> 100%, 65535 means MAS	K. K. K.			
HINT:Check the status register 4x01101,3x01 DALI DT8:SET ARC LEVEL AND PRIMARY N CHANNELS 1-4	101,I:1100 for the current 3x01194 4x01194 I:1193	status of the internal command processing ????	10	10:LAMP SHORT ADDRESS 11	UINT16 W/O	NO
This functions sets the arc power level and the Writing 0 to 63 activates this command for a s Writing 100 to 115 activates this command for Writing 65535 activates a broadcast command	pecific short address of a	lamp				
Register 4x01104,3x01104,I:1103: New value Register 4x01105,3x01105,I:1104: New value	e for channel 1 between 0 e for channel 2 between 0 e for channel 3 between 0	254, 255 means MASK. and 65535. 0 -> 0%, 65534 -> 100%, 65535 means MAS and 65535. 0 -> 0%, 65534 -> 100%, 65535 means MAS and 65535. 0 -> 0%, 65534 -> 100%, 65535 means MAS and 65535. 0 -> 0%, 65534 -> 100%, 65535 means MAS	K. K.			
HINT:Check the status register 4x01101,3x01	101,I:1100 for the current	status of the internal command processing				

DALI DT8:SET ARC LEVEL AND PRIMARY N CHANNELS 1-5	3x01195 4x01195 I:1194	????	10	10:LAMP SHORT ADDRESS 11	UINT16 W/O	NO
This functions sets the arc power level and the Writing 0 to 63 activates this command for a s Writing 100 to 115 activates this command for Writing 65535 activates a broadcast comman	pecific short address of a r a specific lamp group	lamp				
Register 4x01104,3x01104,I:1103: New value Register 4x01105,3x01105,I:1104: New value Register 4x01106,3x01106,I:1105: New value	e for channel 1 between 0 e for channel 2 between 0 e for channel 3 between 0 e for channel 4 between 0	254, 255 means MASK. and 65535. 0 -> 0%, 65534 -> 100%, 65535 means MA and 65535. 0 -> 0%, 65534 -> 100%, 65535 means MA and 65535. 0 -> 0%, 65534 -> 100%, 65535 means MA and 65535. 0 -> 0%, 65534 -> 100%, 65535 means MA and 65535. 0 -> 0%, 65534 -> 100%, 65535 means MA	.SK. .SK. .SK.			
HINT: Check the status register 4x01101, 3x01 DALI DT8: SET ARC LEVEL AND	101.1:1100 for the current 3x01196	status of the internal command processing	10	10:LAMP SHORT ADDRESS 11	UINT16	NO
PRIMARY N CHANNELS 1-6	4x01196 I:1195		-		W/O	
This functions sets the arc power level and the Writing 0 to 63 activates this command for a s Writing 100 to 115 activates this command for Writing 65535 activates a broadcast command	pecific short address of a r a specific lamp group	inels 1 to 6 between 0 and 65535. Iamp				
Register 4x01104,3x01104,1:1103: New value Register 4x01105,3x01105,1:1104: New value Register 4x01106,3x01106,1:1105: New value Register 4x01107,3x01107,1:1106: New value Register 4x01108,3x01108,1:1107: New value	e for channel 2 between 0 e for channel 3 between 0 e for channel 4 between 0 e for channel 5 between 0 e for channel 6 between 0 e for channel 6 between 0	254, 255 means MASK. and 65535. 0 -> 0%, 65534 -> 100%, 65535 means MA and 65535. 0 -> 0%, 65534 -> 100%, 65535 means MA and 65535. 0 -> 0%, 65534 -> 100%, 65535 means MA and 65535. 0 -> 0%, 65534 -> 100%, 65535 means MA and 65535. 0 -> 0%, 65534 -> 100%, 65535 means MA and 65535. 0 -> 0%, 65534 -> 100%, 65535 means MA	.SK. SK. SK. SK.			
HINT Check the status register 4x01101 3x01 DALI DT8:GET ARC LEVEL AND PRIMARY N CHANNEL 1	101 I:1100 for the current 3X01201 4x01201 I:1200	status of the internal command processing	10	10:LAMP SHORT ADDRESS 11	UINT16 W/O	NO
This functions returns the current arc power le Writing 0 to 63 activates this command for a s		f the primary N dimming channel 1 between 0 and 6553 lamp	35.		I	
	lue of channel 1 between	0 and 65535. 0 -> 0%, 65534 -> 100%, 65535 means N	IASK.			
HINT: Check the status register 4x01101,3x01 DALI DT8: GET ARC LEVEL AND	3x01202	status of the internal command processing ????	10	10:LAMP SHORT ADDRESS 11	UINT16	NO
PRIMARY N CHANNELS 1-2	4x01202 I:1201				W/O	
Writing 0 to 63 activates this command for a s	evel and the current level of specific short address of a	'	1 65535.			
Register 4x01102,3x01102,1:1101: Current ar Register 4x01103,3x01103,1:1102: Current va Register 4x01104,3x01104,1:1103: Current va	lue of channel 1 between	nd 254, 255 means MASK. 0 and 65535. 0 -> 0%, 65534 -> 100%, 65535 means N 0 and 65535. 0 -> 0%, 65534 -> 100%, 65535 means N	IASK. IASK.			
HINT:Check the status register 4x01101,3x01	.101,I:1100 for the current	status of the internal command processing				
DALI DT8:GET ARC LEVEL AND PRIMARY N CHANNELS 1-3	3x01203 4x01203 I:1202	????	10	10:LAMP SHORT ADDRESS 11	UINT16 W/O	NO

This functions returns the current arc power le Writing 0 to 63 activates this command for a s Register 4x01102,3x01102,I:1101: Current al	specific short address of a	•	d 65535.			
Register 4x01103,3x01103,I:1102: Current va Register 4x01104.3x01104.I:1103: Current va	alue of channel 1 between alue of channel 2 between	0 and 65535. 0 -> 0%, 65534 -> 100%, 65535 means l 0 and 65535. 0 -> 0%, 65534 -> 100%, 65535 means l 0 and 65535. 0 -> 0%, 65534 -> 100%, 65535 means l	MASK.			
HINT:Check the status register 4x01101,3x02	1101,I:1100 for the current	status of the internal command processing				
DALI DT8:GET ARC LEVEL AND PRIMARY N CHANNELS 1-4	3x01204 4x01204 I:1203	????	10	10:LAMP SHORT ADDRESS 11	UINT16 W/O	NO
This functions returns the current arc power le Writing 0 to 63 activates this command for a	evel and the current level of a specific short address of a	of the primary N dimming channels 1 to 4 between 0 an lamp	d 65535.			
Register 4x01104,3x01104,I:1103: Current va Register 4x01105.3x01105.I:1104: Current va	alue of channel 1 between alue of channel 2 between alue of channel 3 between	nd 254, 255 means MASK. 0 and 65535. 0 -> 0%, 65534 -> 100%, 65535 means l 0 and 65535. 0 -> 0%, 65534 -> 100%, 65535 means l 0 and 65535. 0 -> 0%, 65534 -> 100%, 65535 means l 0 and 65535. 0 -> 0%, 65534 -> 100%, 65535 means l	MASK. MASK.			
HINT:Check the status register 4x01101,3x02	1101,I:1100 for the current	status of the internal command processing				
DALI DT8:GET ARC LEVEL AND PRIMARY N CHANNELS 1-5	3x01205 4x01205 I:1204	????	10	10:LAMP SHORT ADDRESS 11	UINT16 W/O	NO
This functions returns the current arc power low Writing 0 to 63 activates this command for a structure the comman	evel and the current level of specific short address of a	f the primary N dimming channels 1 to 5 between 0 an lamp	d 65535.			
Register 4x01105,3x01105,1:1104: Current va Register 4x01106,3x01106,1:1105: Current va	alue of channel 3 between alue of channel 4 between	nd 254, 255 means MASK. 0 and 65535. 0 -> 0%, 65534 -> 100%, 65535 means l 0 and 65535. 0 -> 0%, 65534 -> 100%, 65535 means l 0 and 65535. 0 -> 0%, 65534 -> 100%, 65535 means l 0 and 65535. 0 -> 0%, 65534 -> 100%, 65535 means l 0 and 65535. 0 -> 0%, 65534 -> 100%, 65535 means l	MASK. MASK.			
HINT:Check the status register 4x01101,3x02	1101,I:1100 for the current	status of the internal command processing				
DALI DT8:GET ARC LEVEL AND PRIMARY N CHANNELS 1-6	3x01206 4x01206 I:1205	????	10	10:LAMP SHORT ADDRESS 11	UINT16 W/O	NO
This functions returns the current arc power le Writing 0 to 63 activates this command for a s	evel and the current level o specific short address of a	f the primary N dimming channels 1 to 6 between 0 an lamp	d 65535.			
Register 4x01104,3x01104,I:1103: Current va Register 4x01105,3x01105,I:1104: Current va Register 4x01106.3x01106,I:1105: Current va	alue of channel 1 between alue of channel 2 between alue of channel 3 between alue of channel 4 between	nd 254, 255 means MASK. 0 and 65535. 0 -> 0%, 65534 -> 100%, 65535 means 0 and 65535. 0 -> 0%, 65534 -> 100%, 65535 means 0 and 65535. 0 -> 0%, 65534 -> 100%, 65535 means 0 and 65535. 0 -> 0%, 65534 -> 100%, 65535 means 0 and 65535. 0 -> 0%, 65534 -> 100%, 65535 means 0 and 65535. 0 -> 0%, 65534 -> 100%, 65535 means	MASK. MASK. MASK.			
HINT: Check the status register 4x01101,3x02	1101,I:1100 for the current	status of the internal command processing				

MB RESI-DALI-SIO, ETH DT8 RGBWAF Registers

DALI DT8:STATUS REGISTER	3x01101	1,0x0001	0		UINT16	
	4x01101 I:1100	B:00 01	Ŭ		R/O	
		Current value of register:1				
This register shows the actual status of the command was finished. The following values are returned: 0: No operation happened in the past 1: Last operation was successfully fiinishe 9999: Last operation was finished, but hat 32768,0x8000: Last command is currently		nand. Wevery time you trigger a new DT8 command, in ters are valid. swer). shed yet.	nternally many DALI	commands are generated. Therefore you	have to poll this registers to find out, if and h	how the last DT8
DALI DT8:REGISTER 1	3x01102 4x01102 I:1101	116,0x0074 B:00 74	127	127	UINT16 R/W	YES
		Current value of register:116				
This is an universal register for DALI device	ce type 8 functions. Usual	ly the Arc Power is stored here as a value between 0 a	and 255.			
DALI DT8:REGISTER 2	3x01103 4x01103 I:1102	0,0x0000 B:00 00	1	1	UINT16 R/W	YES
		Current value of register:0				
This is an universal register for DALI device	ce type 8 functions. Deper	nding on the DT8 function, this register stores different	t colour values.			
DALI DT8:REGISTER 3	3x01104 4x01104 I:1103	254,0x00FE B:00 FE	2	2	UINT16 R/W	YES
		Current value of register:254				
This is an universal register for DALI device	ce type 8 functions. Deper	nding on the DT8 function, this register stores different	t colour values.			
DALI DT8:REGISTER 4	3x01105 4x01105 I:1104	0,0x0000 B:00 00	3	3	UINT16 R/W	NO
		Current value of register:0				
This is an universal register for DALI device	ce type 8 functions. Deper	nding on the DT8 function, this register stores different	t colour values.			
DALI DT8:REGISTER 5	3x01106 4x01106 I:1105	4,0x0004 B:00 04	4	4	UINT16 R/W	NO
		Current value of register:4				
This is an universal register for DALI device	ce type 8 functions. Deper	nding on the DT8 function, this register stores different	t colour values.			
DALI DT8:REGISTER 6	3x01107 4x01107 I:1106	5,0x0005 B:00 05	5	5	UINT16 R/W	NO
		Current value of register:5				
This is an universal register for DALI device	ce type 8 functions. Deper	nding on the DT8 function, this register stores different	t colour values.		·	
DALI DT8:REGISTER 7	3x01108 4x01108 I:1107	6,0x0006 B:00 06	6	6	UINT16 R/W	NO
		Current value of register:6				

This is an universal register for DALI d	evice type 8 functions. Depen	iding on the DT8 function, this register stores differe	ent colour values.			
DALI DT8:REGISTER 8	3x01109 4x01109 I:1108	0,0x0000 B:00 00	65535	65.535	UINT16 R/W	NO
		Current value of register:0				
This is an universal register for DALI d	evice type 8 functions. Depen	iding on the DT8 function, this register stores differe	ent colour values.			
DALI DT8:REGISTER 9	3x01110 4x01110 I:1109	0,0x0000 B:00 00	65535	65.535	UINT16 R/W	NO
		Current value of register:0				
This is an universal register for DALI d	evice type 8 functions. Depen	nding on the DT8 function, this register stores differe	ent colour values.			
DALI DT8:REGISTER 10	3x01111 4x01111 I:1110	0,0x0000 B:00 00	65535	65.535	UINT16 R/W	NO
		Current value of register:0				
This is an universal register for DALI d	evice type 8 functions. Depen	iding on the DT8 function, this register stores differe	ent colour values.			
DALI DT8:REGISTER 11	3x01112 4x01112 I:1111	0,0x0000 B:00 00	65535	65.535	UINT16 R/W	NO
		Current value of register:0				
This is an universal register for DALI d	evice type 8 functions. Depen	iding on the DT8 function, this register stores differe	ent colour values.			
DALI DT8:REGISTER 12	3x01113 4x01113 I:1112	0,0x0000 B:00 00	65535	65.535	UINT16 R/W	NO
		Current value of register:0				
This is an universal register for DALI d	evice type 8 functions. Depen	nding on the DT8 function, this register stores differe	ent colour values.			
DALI DT8:REGISTER 13	3x01114 4x01114 I:1113	0,0x0000 B:00 00	65535	65.535	UINT16 R/W	NO
		Current value of register:0				
This is an universal register for DALI d	evice type 8 functions. Depen	iding on the DT8 function, this register stores differe	ent colour values.		·	
DALI DT8:REGISTER 14	3x01115 4x01115 I:1114	0,0x0000 B:00 00	65535	65.535	UINT16 R/W	NO
		Current value of register:0				
This is an universal register for DALI d	evice type 8 functions. Depen	nding on the DT8 function, this register stores differe	ent colour values.		·	
DALI DT8:REGISTER 15	3x01116 4x01116 I:1115	0,0x0000 B:00 00	65535	65.535	UINT16 R/W	NO
		Current value of register:0				
This is an universal register for DALI d	evice type 8 functions. Deper	nding on the DT8 function, this register stores differe	ent colour values.			

MB RESI-DALI-SIO, ETH DT8 RGBWAF Registers

DALI DT8:REGISTER 16	3x01117 4x01117	0,0x0000 B:00 00	65535	65.535	UINT16 R/W	NO
	I:1116	В.00 00			R/W	
	1.1110	Current value of register:0				
This is an universal register for DALI device	type 8 functions. Dependir	ig on the DT8 function, this register stores different co	lour values.			
						1
DALI DT8:RGBWAF DIMMERS			-			
DALI DT8:SET ARC LEVEL AND	3x01211	????	1	1:LAMP SHORT ADDRESS 2	UINT16	NO
RGB COLOUR	4x01211 I:1210				W/O	
This functions sets the arc power level and t		dimmer between 0 and 255				
Writing 0 to 63 activates this command for a	specific short address of a	lamp				
Writing 100 to 115 activates this command f	or a specific lamp group	•				
Writing 65535 activates a broadcast comma	nd for all lamps					
Register 4x01102,3x01102,I:1101: New arc	power level between 0 and	254, 255 means MASK.				
Register 4x01103,3x01103,I:1102: New value	ie for RED between 0 and 2	255. 0 -> 0%, 254 -> 100%, 255 means MASK.				
Register 4x01104,3x01104,1:1103: New valu	ue for GREEN between 0 a	nd 255. 0 -> 0%, 254 -> 100%, 255 means MASK. 255. 0 -> 0%, 254 -> 100%, 255 means MASK.				
		233. 0 -> 0%, 234 -> 100%, 233 IIIealis MASK.				
HINT:Check the status register 4x01101,3x0			-			
DALI DT8:SET ARC LEVEL AND	3x01212	????	3	3:LAMP SHORT ADDRESS 4	UINT16	NO
RGBW COLOUR	4x01212				W/O	
This functions sets the arc power level and t	I:1211	r a PCBW dimmer between 0 and 255				
Writing 0 to 63 activates this command for a	specific short address of a	lamp				
Writing 100 to 115 activates this command f	or a specific lamp group					
Writing 65535 activates a broadcast comma	nd for all lamps					
Register 4x01102,3x01102,I:1101: New arc	power level between 0 and	254, 255 means MASK.				
Register 4x01103,3x01103,I:1102: New value	ie for RED between 0 and 2	255. 0 -> 0%, 254 -> 100%, 255 means MASK.				
Register 4x01104,3x01104,I:1103: New valu	ue for GREEN between 0 a	nd 255. 0 -> 0%, 254 -> 100%, 255 means MASK.				
Register 4x01105,3x01105,1:1104: New Vall Register 4x01106 3x01106 1:1105: New Val	le for BLUE between 0 and ie for WHITE between 0 ar	255. 0 -> 0%, 254 -> 100%, 255 means MASK. d 255. 0 -> 0%, 254 -> 100%, 255 means MASK.				
HINT: Check the status register 4x01101,3x0			0			NO
DALI DT8:SET ARC LEVEL AND RGBWA COLOUR	3x01213 4x01213	????	3	3:LAMP SHORT ADDRESS 4	UINT16 W/O	NO
RGBWA COLOUR	4x01213 I:1212				VV/O	
This functions sets the arc power level and t		colours for a RGBWA dimmer between 0 and 255.				
Writing 0 to 63 activates this command for a	specific short address of a	lamp				
Writing 100 to 115 activates this command f	or a specific lamp group					
Writing 65535 activates a broadcast comma	nu ior an iamps					
Register 4x01102,3x01102,I:1101: New arc	power level between 0 and	254, 255 means MASK.				
Register 4x01103,3x01103,I:1102: New valu	ie for RED between 0 and 2	255. 0 -> 0%, 254 -> 100%, 255 means MASK.				
Register 4x01104,3x01104,1:1103: New Vall	Je for GREEN Detween 0 and	nd 255. 0 -> 0%, 254 -> 100%, 255 means MASK. 255. 0 -> 0%, 254 -> 100%, 255 means MASK.				
Register 4x01106,3x01106,1:1105: New valu	ue for WHITE between 0 ar	Id 255. 0 -> 0%, 254 -> 100%, 255 means MASK.				
Register 4x01107,3x01107,I:1106: New valu	ue for AMBER between 0 a	nd 255. 0 -> 0%, 254 -> 100%, 255 means MASK.				
HINT:Check the status register 4x01101,3x0	1101 1.1100 for the ourrow	status of the internal command processing				
DALI DT8:SET ARC LEVEL AND	3x01214		3	3:LAMP SHORT ADDRESS 4	UINT16	NO
RGBWAF COLOUR	4x01214				W/O	
	I:1213					

This functions sets the arc power level and the RGB+WHITE+AMBER+FREECOLOUR colours for a RGBWA dimmer between 0 and 255. Writing 0 to 63 activates this command for a specific short address of a lamp Writing 100 to 115 activates this command for a specific lamp group Writing 65535 activates a broadcast command for all lamps

Register 4x01102,3x01102,I:1101: New arc power level between 0 and 254, 255 means MASK. Register 4x01103,3x01103,I:1102: New value for RED between 0 and 255. 0 -> 0%, 254 -> 100%, 255 means MASK. Register 4x01105,3x01104,I:1103: New value for GREEN between 0 and 255. 0 -> 0%, 254 -> 100%, 255 means MASK. Register 4x01105,3x01105,I:1104: New value for BLUE between 0 and 255. 0 -> 0%, 254 -> 100%, 255 means MASK. Register 4x01106,3x01106,I:1105: New value for WHITE between 0 and 255. 0 -> 0%, 254 -> 100%, 255 means MASK. Register 4x01107,3x01106,I:1105: New value for AMBER between 0 and 255. 0 -> 0%, 254 -> 100%, 255 means MASK. Register 4x01107,3x01107,I:1106: New value for AMBER between 0 and 255. 0 -> 0%, 254 -> 100%, 255 means MASK. Register 4x01108,3x01108,I:1107: New value for FREECOLOUR between 0 and 255. 0 -> 0%, 254 -> 100%, 255 means MASK.

HINT:Check the status register 4x01101,3x01101,I:1100 for the current status of the internal command processing

DALI DT8:GET ARC LEVEL AND RGB COLOUR	3x01221 4x01221 I:1220	????	1	1:LAMP SHORT ADDRESS 2	UINT16 W/O	YES
This functions sets the current arc power leve Writing 0 to 63 activates this command for a	el and the current RGB colo specific short address of a	purs for a RGB dimmer between 0 and 255. Iamp				
Register 4x01104,3x01104,1:1103: Current v	alue of RED between 0 and alue of GREEN between 0	nd 254, 255 means MASK. d 255. 0 -> 0%, 254 -> 100%, 255 means MASK. and 255. 0 -> 0%, 254 -> 100%, 255 means MASK. nd 255. 0 -> 0%, 254 -> 100%, 255 means MASK.				
HINT: Check the status register 4x01101,3x0	1101.I:1100 for the current	status of the internal command processing				
DALI DT8:GET ARC LEVEL AND RGBW COLOUR	3x01222 4x01222 I:1221	????	1	1:LAMP SHORT ADDRESS 2	UINT16 W/O	YES
This functions sets the current arc power leve Writing 0 to 63 activates this command for a	el and the current RGB+W	HITE colours for a RGBW dimmer between 0 and 255. lamp	I			
Register 4x01104,3x01104,1:1103: Current v Register 4x01105,3x01105,1:1104: Current v	alue of RED between 0 and alue of GREEN between 0 alue of BLUE between 0 ar	nd 254, 255 means MASK. d 255. 0 -> 0%, 254 -> 100%, 255 means MASK. and 255. 0 -> 0%, 254 -> 100%, 255 means MASK. nd 255. 0 -> 0%, 254 -> 100%, 255 means MASK. and 255. 0 -> 0%, 254 -> 100%, 255 means MASK.				
HINT:Check the status register 4x01101,3x0	1101,I:1100 for the current	status of the internal command processing				
DALI DT8:GET ARC LEVEL AND RGBWA COLOUR	3x01223 4x01223 I:1222	????	1	1:LAMP SHORT ADDRESS 2	UINT16 W/O	YES
This functions sets the current arc power leve Writing 0 to 63 activates this command for a	el and the current RGB+WI specific short address of a	HITE+AMBER colours for a RGBW dimmer between 0 lamp	and 255.	·		
Register 4x01104,3x01104,I:1103: Current v Register 4x01105,3x01105,I:1104: Current v Register 4x01106,3x01106,I:1105: Current v	alue of RED between 0 and alue of GREEN between 0 alue of BLUE between 0 a alue of WHITE between 0 a	nd 254, 255 means MASK. d 255. 0 -> 0%, 254 -> 100%, 255 means MASK. and 255. 0 -> 0%, 254 -> 100%, 255 means MASK. nd 255. 0 -> 0%, 254 -> 100%, 255 means MASK. and 255. 0 -> 0%, 254 -> 100%, 255 means MASK. and 255. 0 -> 0%, 254 -> 100%, 255 means MASK.				
HINT:Check the status register 4x01101,3x0	1101,I:1100 for the current	status of the internal command processing				
DALI DT8:GET ARC LEVEL AND RGBWAF COLOUR	3x01224 4x01224 I:1223	????	1	1:LAMP SHORT ADDRESS 2	UINT16 W/O	YES

This functions sets the current arc power level and the current RGB+WHITE+AMBER+FREECOLOUR colours for a RGBW dimmer between 0 and 255. Writing 0 to 63 activates this command for a specific short address of a lamp

Register 4x01102,3x01102,I:1101: Current arc power level between 0 and 254, 255 means MASK. Register 4x01103,3x01103,I:1102: Current value of RED between 0 and 255. 0 -> 0%, 254 -> 100%, 255 means MASK. Register 4x01104,3x01104,I:1103: Current value of GREEN between 0 and 255. 0 -> 0%, 254 -> 100%, 255 means MASK. Register 4x01105,3x01105,I:1104: Current value of BLUE between 0 and 255. 0 -> 0%, 254 -> 100%, 255 means MASK. Register 4x01106,3x01106,I:1105: Current value of WHITE between 0 and 255. 0 -> 0%, 254 -> 100%, 255 means MASK. Register 4x01107,3x01107,I:1106: Current value of AMBER between 0 and 255. 0 -> 0%, 254 -> 100%, 255 means MASK. Register 4x01107,3x01107,I:1106: Current value of FREEECOLOUR between 0 and 255. 0 -> 0%, 254 -> 100%, 255 means MASK.

HINT: Check the status register 4x01101,3x01101,I:1100 for the current status of the internal command processing

DALI BUS ERROR	ASCII	#DALI BUS ERROR <cr></cr>	ASCII	
	READ	#DBERR <cr></cr>		
	COMMAND	Result:		
		#DBERR: <buserrdec>,<buserrhex><cr></cr></buserrhex></buserrdec>		
	ТХ	#DALI BUS ERROR <cr></cr>		
	RX	#255,DBERR:0,0x0 <cr></cr>		
		0:BUS IS OK		
Shows the status of the DALI bus. BusErrDec,BusErrHex: =0:DALI bus is OK and fully function	al			
=1:DALI bus error, short circuit on DA	ALT line, overload on DALT line ASCII	#SET PRIORITY SLOT: <slot><cr></cr></slot>	ASCII	NO
SET PRIORITY SLOT	WRITE	#SET PRIORITY SECT. <sect <br="" ccr="">#SPS:<slot><cr <="" td=""><td>ASCII</td><td>NO</td></cr></slot></sect>	ASCII	NO
	COMMAND	Result:		
	COMMAND	#OK <cr></cr>		
	SLOT	3		
	TX	#SET PRIORITY SLOT:3 <cr></cr>		
This command defines the DALI pric	RX	N/A		
3: 15ms: Automatic arc power contro	essages that must be repeated ntrol commands and commands of commands ery messages			
2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que	ery messages	#GET PRIORITY SLOT <cr></cr>	ASCII	
3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que	ery messages	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr></cr></priorityslothex></priorityslotdec></cr></cr>	ASCII	
3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que	ery messages ASCII READ	#GPS <cr> Result:</cr>	ASCII	
3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que	ASCII READ COMMAND	#GPS <cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr></cr></priorityslothex></priorityslotdec></cr>	ASCII	
3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT	ASCII READ COMMAND TX RX	#GPS <cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms</cr></cr></cr></priorityslothex></priorityslotdec></cr>	ASCII	
3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT This command shows the current def 0: 12ms: Highest priority, used for me	ASCII READ COMMAND TX RX fined DALI priority slot. There essages that must be repeated htrol commands and command	#GPS <cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available:</cr></cr></cr></priorityslothex></priorityslotdec></cr>	ASCII	
3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT This command shows the current det 0: 12ms: Highest priority, used for me 1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que	ASCII READ COMMAND TX RX fined DALI priority slot. There essages that must be repeated ntrol commands and command of commands ery messages ASCII	#GPS <cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available:</cr></cr></cr></priorityslothex></priorityslotdec></cr>	ASCII	NO
3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT This command shows the current det 0: 12ms: Highest priority, used for me 1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que	ASCII READ COMMAND TX RX fined DALI priority slot. There essages that must be repeated ntrol commands and command ery messages	#GPS <cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: 1 Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time)</cr></cr></cr></priorityslothex></priorityslotdec></cr>		NO
3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT This command shows the current def 0: 12ms: Highest priority, used for me 1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que	ASCII READ COMMAND TX RX fined DALI priority slot. There essages that must be repeated ntrol commands and command of commands ery messages ASCII	#GPS <cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: J Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP LEVEL:<lamp>=<level><cr></cr></level></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cr>		NO
3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT This command shows the current det 0: 12ms: Highest priority, used for me 1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que	ASCII READ COMMAND TX RX fined DALI priority slot. There essages that must be repeated ntrol commands and command ol commands ery messages ASCII WRITE	#GPS <cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d swhich have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP LEVEL:<lamp>=<level><cr> #LAMP LEVEL:<lamp>,<level><cr> #LAMP LEVEL:<lamp>,<level><cr> #LAMP LEVEL:<lamp>,<level><cr> #LCAMP>,<level><cr> Result: #OK<cr></cr></cr></level></cr></level></lamp></cr></level></lamp></cr></level></lamp></cr></level></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cr>		NO
3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT This command shows the current det 0: 12ms: Highest priority, used for me 1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que	ASCII READ COMMAND TX RX fined DALI priority slot. There essages that must be repeated ntrol commands and command ol commands ery messages ASCII WRITE	#GPS <cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d swhich have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP LEVEL:<lamp>=<level><cr> #LAMP LEVEL:<lamp>,<level><cr> #LAMP LEVEL:<lamp>,<level><cr> #LI:<lamp>,<level><cr> Result:</cr></level></lamp></cr></level></lamp></cr></level></lamp></cr></level></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cr>		NO
3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT This command shows the current det 0: 12ms: Highest priority, used for me 1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que	ASCII READ COMMAND TX RX fined DALI priority slot. There essages that must be repeated htrol commands and command of commands ery messages ASCII WRITE COMMAND	#GPS <cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d swhich have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP LEVEL:<lamp>=<level><cr> #LAMP LEVEL:<lamp>,<level><cr> #LAMP LEVEL:<lamp>,<level><cr> #LAMP LEVEL:<lamp>,<level><cr> #LCAMP>,<level><cr> Result: #OK<cr></cr></cr></level></cr></level></lamp></cr></level></lamp></cr></level></lamp></cr></level></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cr>		NO
3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT This command shows the current det 0: 12ms: Highest priority, used for me 1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power control	ASCII READ COMMAND TX RX fined DALI priority slot. There essages that must be repeated htrol commands and command ol commands ery messages ASCII WRITE COMMAND	#GPS <cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: J Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP LEVEL:<lamp>=<level><cr> #LAMP LEVEL:<lamp>,<level><cr> #LL:<lamp>,<level><cr> Result: #OK<cr> 2:LAMP SHORT ADDRESS 3</cr></cr></level></lamp></cr></level></lamp></cr></level></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cr>		NO
3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT This command shows the current det 0: 12ms: Highest priority, used for me 1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que	ASCII READ COMMAND TX RX fined DALI priority slot. There essages that must be repeated htrol commands and command ol commands ery messages ASCII WRITE COMMAND LAMP LEVEL	#GPS <cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: j is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP LEVEL:<lamp>=<level><cr> #LAMP LEVEL:<lamp>,<level><cr> #LL:<lamp>,<level><cr> #OK<cr> 2:LAMP SHORT ADDRESS 3 100,0</cr></cr></level></lamp></cr></level></lamp></cr></level></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cr>		NO

GROUP LEVEL	A.C.C.II		ASCII	YES
GROUP LEVEL	ASCII	#GROUP LEVEL: <group>=<level><cr></cr></level></group>	ASCII	YES
	WRITE	#GROUP LEVEL: <group>,<level><cr></cr></level></group>		
	COMMAND	#GL: <group>,<level><cr></cr></level></group>		
		Result:		
		#OK <cr></cr>		
	GROUP	8:LAMP GROUP 9		
	LEVEL	100,0		
	ТХ	#GROUP LEVEL:8=254 <cr></cr>		
	RX	#255,OK <cr></cr>		
Sets for a lamp group a new brightne	ess level between 0 and 254. 2	55 means MASK.		
ALL LEVEL	ASCII	#ALL LEVEL: <level><cr></cr></level>	ASCII	YES
	WRITE	#ALL LEVEL: <level><cr></cr></level>		
	COMMAND	#AL: <level><cr></cr></level>		
		Result:		
		#OK <cr></cr>		
	LEVEL	100,0		
	ТХ	#ALL LEVEL:254 <cr></cr>		
	RX	#255,OK <cr></cr>		
Sets for all lamps a new brightness le				
DALI 1.0 RESET	ASCII	#LAMP RESET": <lamp><cr></cr></lamp>	ASCII	YES
FOR LAMP	WRITE	#LRESET: <lamp><cr></cr></lamp>	7,0011	120
	COMMAND	Result		
	COMMAND	#OK <cr></cr>		
	LAMP	5:LAMP SHORT ADDRESS 6		
	TX	#LAMP RESET":5 <cr></cr>		
	RX	#255,OK <cr></cr>		
Sends the selected DALI1.0 commar				
DALI 1.0 RESET	ASCII	#GROUP RESET: <group><cr></cr></group>	ASCII	YES
FOR LAMP GROUP	WRITE	#GRESET: <group><cr></cr></group>	7,001	. 20
	COMMAND	Result		
		#OK <cr></cr>		
	GROUP	8:LAMP GROUP 9		
	TX	#GROUP RESET:8 <cr></cr>		
	RX	#255,OK <cr></cr>		
Sends the selected DALI1.0 commar	nd RESET twice between 100	ns to the selected lamp group		
DALI 1.0 RESET	ASCII	#ALL RESET <cr></cr>	ASCII	NO
FOR ALL LAMPS	WRITE	#ARESET <cr></cr>		
	COMMAND	Result		
	COMMAND	#OK <cr></cr>		
	тх	#ALL RESET <cr></cr>		
	RX	N/A		
Sends the selected DALI1.0 commar				
				VEC
DALI 1.0 COMMAND	ASCII	#LAMP OFF: <lamp><cr></cr></lamp>	ASCII	YES
FOR LAMP	WRITE	#LOF: <lamp><cr></cr></lamp>		
	COMMAND	Result		
	00111115	#OK <cr></cr>		
	COMMAND	OFF:OF		

	LAMP	5:LAMP SHORT ADDRESS 6		
	ТХ	#LAMP OFF:5 <cr></cr>		
	RX	#255,OK <cr></cr>		
ends the selected DALI1.0 comma	ind to the selected lamp			
		SUP-STEP DOWN,SDO-RECALL MAX LEVEL,RMAXL-RECALL MIN LEVEL,RMINL- E DAPC SEQUENCE,EDAPCS-GOTO LAST ACTIVE LEVEL,GLAL		
		RESET,RESET-STORE ACTUAL LEVEL IN DTR,SALD-STORE DTR AS MAX LEVEL,SDMAXL-STORE DTR AS MIN LE AS POWER ON LEVEL,SDPOL-STORE DTR AS FADE TIME,SDFT-STORE DTR AS FADE RATE,SDFR-STIORE DTR A		
QUERY MISSING SHORT ADDRES QUERY CONTENT DTR1,QCD1-QU QUERY SYSTEM FAILURE LEVEL	SS,QMSA-QUERY VERSION I UERY CONTENT DTR2,QCD2 .OSFL-OUERY FADE TIME F/	RY CONTROL GEAR,QCG-QUERY LAMP FAILURE,QLF-QUERY LAMP POWER ON,QLPO-QUERY LIMIT ERROR,QLE NUMBER,QVN-QUERY CONTENT DTR,QCD-QUERY DEVICE TYPE,QDT-QUERY PHYSICAL MINIMUM LEVEL,QPMINI -QUERY ACTUAL LEVEL,QAL-QUERY MAX LEVEL,QMAXL-QUERY MIN LEVEL,QMINL-QUERY POWER ON LEVEL,Q ADE RATE,QFTFR-QUERY GROUPS 0-7,QG07-QUERY GROUPS 8-15,QG815- ESS M,QRAM-QUERY RANDOM ADDRESS L,QRAL-READ MEMORY LOCATION,RML-QUERY EXTENDED VERSION N	L-QUERY POWER FAILURE, POL-	- QPF-
DALI 1.0 COMMAND	ASCII	#GROUP RESET: <group><cr></cr></group>	ASCII	YES
OR LAMP GROUP	WRITE COMMAND	#GRESET: <group><cr> Result #OK<cr></cr></cr></group>		
	COMMAND	RESET:RESET		
	GROUP	8:LAMP GROUP 9		
	ТХ	#GROUP RESET:8 <cr></cr>		
	RX	#255.OK <cr></cr>		
ends the selected DALI1.0 comma	and to the selected group of lan	nps		1
TEP DOWN AND OFF,SDOO-ON	AND STEP UP, OSUP-ENABL	SUP-STEP DOWN,SDO-RECALL MAX LEVEL,RMAXL-RECALL MIN LEVEL,RMINL- E DAPC SEQUENCE,EDAPCS-GOTO LAST ACTIVE LEVEL,GLAL RESET,RESET-STORE ACTUAL LEVEL IN DTR,SALD-STORE DTR AS MAX LEVEL,SDMAXL-STORE DTR AS MIN LE AS POWER ON LEVEL,SDPOL-STORE DTR AS FADE TIME,SDFT-STORE DTR AS FADE RATE,SDFR-STIORE DTR A	VEL,SDMINL- S SHORT ADDRESS,SDSA-	
vailable DALI commands for queri	SS.OMSA-OUERY VEŘSIÓN I	RY CONTROL GEAR,QCG-QUERY LAMP FAILURE,QLF-QUERY LAMP POWER ON,QLPO-QUERY LIMIT ERROR,QLE NUMBER,QVN-QUERY CONTENT DTR,QCD-QUERY DEVICE TYPE,QDT-QUERY PHYSICAL MINIMUM LEVEL,QPMINI -QUERY ACTUAL LEVEL,QAL-QUERY MAX LEVEL,QMAXL-QUERY MIN LEVEL,QMINL-QUERY POWER ON LEVEL,Q	L-OUERY POWER FAILURE.	-)PF-
UERY CONTENT DTR1,QCD1-Q	UERY CONTENT DTR2,QCD2 .OSFL-OUERY FADE TIME F/	-QUERY ACTOAL LEVEL,QUERY MAX LEVEL,QMAX-QUERY MIN LEVEL,QMINL-QUERY POWER ON LEVEL,Q ADE RATE,QFTFR-QUERY GROUPS 0-7,QG07-QUERY GROUPS 8-15,QG815- ESS M,QRAM-QUERY RANDOM ADDRESS L,QRAL-READ MEMORY LOCATION,RML-QUERY EXTENDED VERSION N		ξ' '
UERY CONTENT DTR1,QCD1-Q UERY SYSTEM FAILURE LEVEL	UERY CONTENT DTR2,QCD2 .OSFL-OUERY FADE TIME F/	ADE RATE.OFTFR-OUERY GROUPS 0-7.0G07-OUERY GROUPS 8-15.0G815-		NO
UERY CONTENT DTR1,QCD1-Q UERY SYSTEM FAILURE LEVEL UERY RANDOM ADDRESS H,QF ALI 1.0 COMMAND	UERY CONTENT DTR2,QCD2 ,QSFL-QUERY FADE TIME F/ RAH-QUERY RANDOM ADDR ASCII WRITE	ADE RATE,OFTFR-QUERY GROUPS 0-7,OG07-QUERY GROUPS 8-15,OG815- ESS M,QRAM-QUERY RANDOM ADDRESS L,QRAL-READ MEMORY LOCATION,RML-QUERY EXTENDED VERSION N #ALL RESET <cr> #ARESET<cr> Result</cr></cr>	NUMBER,QEVN	
UERY CONTENT DTR1,QCD1-Q UERY SYSTEM FAILURE LEVEL UERY RANDOM ADDRESS H,QF ALI 1.0 COMMAND	UERY CONTENT DTR2,QCD2 ,QSFL-QUERY FADE TIME F/ RAH-QUERY RANDOM ADDR ASCII WRITE COMMAND	ADE RATE,QFTFR-QUERY GROUPS 0-7,QG07-QUERY GROUPS 8-15,QG815- ESS M,QRAM-QUERY RANDOM ADDRESS L,QRAL-READ MEMORY LOCATION,RML-QUERY EXTENDED VERSION N #ALL RESET <cr> #ARESET<cr> Result #OK<cr></cr></cr></cr>	NUMBER,QEVN	
UERY CONTENT DTR1,QCD1-Q UERY SYSTEM FAILURE LEVEL UERY RANDOM ADDRESS H,QF ALI 1.0 COMMAND	UERY CONTENT DTR2,QCD2 ,QSFL-QUERY FADE TIME F, AH-QUERY RANDOM ADDR ASCII WRITE COMMAND COMMAND	ADE RATE,OFTFR-QUERY GROUPS 0-7,QG07-QUERY GROUPS 8-15,QG815- ESS M,QRAM-QUERY RANDOM ADDRESS L,QRAL-READ MEMORY LOCATION,RML-QUERY EXTENDED VERSION N #ALL RESET <cr> #ARESET<cr> Result #OK<cr> RESET:RESET</cr></cr></cr>	NUMBER,QEVN	

		SUP-STEP DOWN,SDO-RECALL MAX LEVEL,RMAXL-RECALL MIN LEVEL,RMINL- E DAPC SEQUENCE,EDAPCS-GOTO LAST ACTIVE LEVEL,GLAL		
STORE DTR AS SYSTEM FAILURE LÉVEL ENABLE WRITE MEMORY,EWM	SDSFL-STORE DTR A	RESET,RESET-STORE ACTUAL LEVEL IN DTR,SALD-STORE DTR AS MAX LEVEL,SDMAXL-STORE DTR AS MIN LEVEL,SDM AS POWER ON LEVEL,SDPOL-STORE DTR AS FADE TIME,SDFT-STORE DTR AS FADE RATE,SDFR-STIORE DTR AS SHORT	ADDRESS,SDSA-	
OUERY SYSTEM FAILURE LEVEL.OSFL-O	UERY FADE TIME FA	RY CONTROL GEAR,QCG-QUERY LAMP FAILURE,QLF-QUERY LAMP POWER ON,QLPO-QUERY LIMIT ERROR,QLE-QUERY F UMBER,QVN-QUERY CONTENT DTR,QCD-QUERY DEVICE TYPE,QDT-QUERY PHYSICAL MINIMUM LEVEL,QPMINL-QUERY QUERY ACTUAL LEVEL,QAL-QUERY MAX LEVEL,QMAXL-QUERY MIN LEVEL,QMINL-QUERY POWER ON LEVEL,QPOL- DE RATE,QFTFR-QUERY GROUPS 0-7,QG07-QUERY GROUPS 8-15,QG815- SS M,QRAM-QUERY RANDOM ADDRESS L,QRAL-READ MEMORY LOCATION,RML-QUERY EXTENDED VERSION NUMBER,(- QPF-
DALI 1.0 SCENE COMMAND FOR LAMP	ASCII WRITE COMMAND	#LAMP GOTO SCENE: <lamp>=<scene><cr> #LAMP GOTO SCENE:<lamp>,<scene><cr> #LGS:<lamp>,<scene><cr> Result: #OK<cr></cr></cr></scene></lamp></cr></scene></lamp></cr></scene></lamp>	ASCII	YES
	SCENE	7:SCENE 8(7)		
	LAMP	5:LAMP SHORT ADDRESS 6		
	ТХ	#LAMP GOTO SCENE:5=7 <cr></cr>		
	RX	#255,OK <cr></cr>		
Activates the choosen DALI 1.0 scene on the	selected lamp			1
DALI 1.0 SCENE COMMAND FOR LAMP GROUP	ASCII WRITE COMMAND	#GROUP GOTO SCENE: <group>=<scene><cr> #GROUP GOTO SCENE:<group>,<scene><cr> #GGS:<group>,<scene><cr> Result: #OK<cr></cr></cr></scene></group></cr></scene></group></cr></scene></group>	ASCII	YES
	SCENE	6:SCENE 7(6)		
	GROUP	8:LAMP GROUP 9		
	ТХ	#GROUP GOTO SCENE:8=6 <cr></cr>		
	RX	#255,OK <cr></cr>		
Activates the choosen DALI 1.0 scene on the	selected lamp group			
DALI 1.0 SCENE COMMAND FOR ALL LAMPS	ASCII WRITE COMMAND	#ALL GOTO SCENE: <scene><cr> #AGS:<scene><cr> Result: #OK<cr></cr></cr></scene></cr></scene>	ASCII	YES
	SCENE	1:SCENE 2(1)		
	ТХ	#ALL GOTO SCENE:1 <cr></cr>		
	RX	#255,OK <cr></cr>		
Activates the choosen DALI 1.0 scene on all	lamps			
DALI 1.0 STORE DTR AS SCENE FOR LAMP	ASCII WRITE COMMAND	#LAMP STORE DTR AS SCENE: <lamp>=<scene><cr> #LAMP STORE DTR AS SCENE:<lamp>,<scene><cr> #LSDS:<lamp>,<scene><cr> Result: #OK<cr></cr></cr></scene></lamp></cr></scene></lamp></cr></scene></lamp>	ASCII	YES
	SCENE	7:SCENE 8(7)		

	LAMP	5:LAMP SHORT ADDRESS 6		
	ТХ	#LAMP STORE DTR AS SCENE:5=7 <cr></cr>		
	RX	#255,OK <cr></cr>		
Saves the current value of the DTR for the	DALI 1.0 scene X in the	selected lamp		
DALI 1.0 STORE DTR AS SCENE FOR LAMP GROUP	ASCII WRITE COMMAND	#GROUP STORE DTR AS SCENE: <group>=<scene><cr> #GROUP STORE DTR AS SCENE:<group>,<scene><cr> #GSDS:<group>,<scene><cr> Result: #OK<cr></cr></cr></scene></group></cr></scene></group></cr></scene></group>	ASCII	YES
	SCENE	6:SCENE 7(6)		
	GROUP	8:LAMP GROUP 9		
	TX	#GROUP STORE DTR AS SCENE:8=6 <cr></cr>		
	RX	#255,OK <cr></cr>		
Saves the current value of the DTR for the				
DALI 1.0 STORE DTR AS SCENE FOR ALL LAMPS	ASCII WRITE COMMAND	#ALL STORE DTR AS SCENE: <scene><cr> #ASDS:<scene><cr> Result: #OK<cr></cr></cr></scene></cr></scene>	ASCII	YES
	SCENE	1:SCENE 2(1)		
	ТХ	#ALL STORE DTR AS SCENE:1 <cr></cr>		
	RX	#255,OK <cr></cr>		
Saves the current value of the DTR for the	DALI 1.0 scene X for all	lamps		
DALI 1.0 REMOVE FROM SCENE FOR LAMP	ASCII WRITE COMMAND	#LAMP REMOVE FROM SCENE: <lamp>=<scene><cr> #LAMP REMOVE FROM SCENE:<lamp>,<scene><cr> #LRFS:<lamp>,<scene><cr> Result: #OK<cr></cr></cr></scene></lamp></cr></scene></lamp></cr></scene></lamp>	ASCII	YES
	SCENE	7:SCENE 8(7)		
		5:LAMP SHORT ADDRESS 6		
	TX	#LAMP REMOVE FROM SCENE:5=7 <cr></cr>		
	RX	#255,OK <cr></cr>		
The selected DALL1.0 scene X is removed		e value MASK(255) ist stored in the scene.		
DALI 1.0 REMOVE FROM SCENE FOR LAMP GROUP	ASCII WRITE COMMAND	#GROUP REMOVE FROM SCENE: <group>=<scene><cr> #GROUP REMOVE FROM SCENE:<group>,<scene><cr> #GRFS:<group>,<scene><cr> Result: #OK<cr></cr></cr></scene></group></cr></scene></group></cr></scene></group>	ASCII	YES
	SCENE	6:SCENE 7(6)		
	GROUP	8:LAMP GROUP 9		
	TX	#GROUP REMOVE FROM SCENE:8=6 <cr></cr>		
	RX	#255,OK <cr></cr>		
The selected DALI 1.0 scene X is removed		up. The value MASK(255) ist stored in the scene.		
DALI 1.0 REMOVE FROM SCENE FOR ALL LAMPS	ASCII WRITE COMMAND	#ALL REMOVE FROM SCENE: <scene><cr> #ARFS:<scene><cr> Result: #OK<cr></cr></cr></scene></cr></scene>	ASCII	YES
	SCENE	1:SCENE 2(1)		

	ТХ	#ALL REMOVE FROM SCENE:1 <cr></cr>		
	RX	#255,OK <cr></cr>		
The selected DALI 1.0 scene X is removed	in all lamps. The value N	/ASK(255) ist stored in the scene.		4
DALI 1.0 QUERY SCENE LEVEL FOR LAMP	ASCII WRITE COMMAND	#LAMP QUERY SCENE LEVEL: <lamp>=<scene><cr> #LAMP QUERY SCENE LEVEL:<lamp>,<scene><cr> #LQSL:<lamp>,<scene><cr> Result: #OK<cr></cr></cr></scene></lamp></cr></scene></lamp></cr></scene></lamp>	ASCII	YES
	SCENE	7:SCENE 8(7)		
	LAMP	5:LAMP SHORT ADDRESS 6		
	ТХ	#LAMP QUERY SCENE LEVEL:5=7 <cr></cr>		
	RX	#255,OK:9,99,0x63 <cr></cr>		
The current arc power of the selected DALI	1.0 scene X is queried f	rom the selected lamp.		
DALI 1.0 QUERY SCENE LEVEL FOR LAMP GROUP	ASCII WRITE COMMAND	#GROUP QUERY SCENE LEVEL: <group>=<scene><cr> #GROUP QUERY SCENE LEVEL:<group>,<scene><cr> #GQSL:<group>,<scene><cr> Result: #OK<cr></cr></cr></scene></group></cr></scene></group></cr></scene></group>	ASCII	YES
	SCENE	6:SCENE 7(6)		
	GROUP	8:LAMP GROUP 9		
	ТХ	#GROUP QUERY SCENE LEVEL:8=6 <cr></cr>		
	RX	#255,OK:9,99,0x63 <cr></cr>		
The current arc power of the selected DALI	1.0 scene X is queried f	rom the selected lamp group.		
DALI 1.0 QUERY SCENE LEVEL FOR ALL LAMPS	ASCII WRITE COMMAND	#ALL QUERY SCENE LEVEL: <scene><cr> #AQSL:<scene><cr> Result: #OK<cr></cr></cr></scene></cr></scene>	ASCII	YES
	SCENE	1:SCENE 2(1)		
	ТХ	#ALL QUERY SCENE LEVEL:1 <cr></cr>		
	RX	#255,OK:9,99,0x63 <cr></cr>		
The current arc power of the selected DALI				
DALI 1.0 ADD TO GROUP FOR LAMP	ASCII WRITE COMMAND	#LAMP ADD TO GROUP: <lamp>=<newgroup><cr> #LAMP ADD TO GROUP:<lamp>,<newgroup><cr> #LATG:<lamp>,<newgroup><cr> Result: #OK<cr></cr></cr></newgroup></lamp></cr></newgroup></lamp></cr></newgroup></lamp>	ASCII	YES
	NEWGROUP	8:LAMP GROUP 9		
	LAMP	5:LAMP SHORT ADDRESS 6		
	ТХ	#LAMP ADD TO GROUP:5=8 <cr></cr>		
	RX	#255,OK <cr></cr>		
The selected lamp is added to the DALI 1.0	group x			
DALI 1.0 ADD TO GROUP FOR LAMP GROUP	ASCII WRITE COMMAND	#GROUP ADD TO GROUP: <group>=<newgroup><cr> #GROUP ADD TO GROUP:<group>,<newgroup><cr> #GATG:<group>,<newgroup><cr> Result:</cr></newgroup></group></cr></newgroup></group></cr></newgroup></group>	ASCII	YES
		#OK <cr></cr>		

	GROUP	8:LAMP GROUP 9		
	ТХ	#GROUP ADD TO GROUP:8=8 <cr></cr>		
	RX	#255,OK <cr></cr>		
The selected group of lamps are added to the	e DALI 1.0 group x			
DALI 1.0 ADD TO GROUP FOR ALL LAMPS	ASCII WRITE COMMAND	#ALL ADD TO GROUP: <newgroup><cr> #AATG:<newgroup><cr> Result: #OK<cr></cr></cr></newgroup></cr></newgroup>	ASCII	YES
	NEWGROUP	8:LAMP GROUP 9		
	ТХ	#ALL ADD TO GROUP:8 <cr></cr>		
	RX	#255,OK <cr></cr>		
All lamps are added to the DALI 1.0 group x	·			
DALI 1.0 REMOVE FROM GROUP FOR LAMP	ASCII WRITE COMMAND	#LAMP REMOVE FROM GROUP: <lamp>=<newgroup><cr> #LAMP REMOVE FROM GROUP:<lamp>,<newgroup><cr> #LRFG:<lamp>,<newgroup><cr> Result: #OK<cr></cr></cr></newgroup></lamp></cr></newgroup></lamp></cr></newgroup></lamp>	ASCII	YES
	NEWGROUP	8:LAMP GROUP 9		
	LAMP	5:LAMP SHORT ADDRESS 6		
	ТХ	#LAMP REMOVE FROM GROUP:5=8 <cr></cr>		
	RX	#255,OK <cr></cr>		
The selected lamp is deleted from the DALI 1	L.O group x			
DALI 1.0 REMOVE FROM GROUP FOR LAMP GROUP	ASCII WRITE COMMAND	#GROUP REMOVE FROM GROUP: <group>=<newgroup><cr> #GROUP REMOVE FROM GROUP:<group>,<newgroup><cr> #GRFG:<group>,<newgroup><cr> Result: #OK<cr></cr></cr></newgroup></group></cr></newgroup></group></cr></newgroup></group>	ASCII	YES
	NEWGROUP	8:LAMP GROUP 9		
	GROUP	8:LAMP GROUP 9		
	ТХ	#GROUP REMOVE FROM GROUP:8=8 <cr></cr>		
	RX	#255,OK <cr></cr>		
All lamps of the selected group are deleted fi				
DALI 1.0 REMOVE FROM GROUP FOR ALL LAMPS	ASCII WRITE COMMAND	#ALL REMOVE FROM GROUP: <newgroup><cr> #ARFG:<newgroup><cr> Result: #OK<cr></cr></cr></newgroup></cr></newgroup>	ASCII	YES
	NEWGROUP	8:LAMP GROUP 9		
	ТХ	#ALL REMOVE FROM GROUP:8 <cr></cr>		
	RX	#255,OK <cr></cr>		
All lamps are deleted from the DALI 1.0 grou	рх			
DALI 1.0 COMMAND FOR LAMP WITH ANSWER	ASCII WRITE COMMAND	#LAMP COMMAND ANSWER: <lamp>=<command/><cr> #LAMP COMMAND ANSWER:<lamp>,<command/><cr> #LCMDA:<lamp>,<command/><cr> Result: #OK:<flags>,<resultdec>,<resulthex><cr></cr></resulthex></resultdec></flags></cr></lamp></cr></lamp></cr></lamp>	ASCII	YES
	COMMAND	0x90		
	LAMP	5:LAMP SHORT ADDRESS 6		
	1			

	ТХ	#LAMP COMMAND ANSWER:5=0x90 <cr></cr>		
	RX	#255.OK:1,36.0x24 <cr></cr>		
Sends the entered DALI 1.0 command to the If there is an DALI answer, ReturnDec and Re 8-Bit DALI answer: 1 16-Bit DALI answer: 2 24-Bit DALI answer: 3 25-Bit DALI answer: 4 28-Bit DALI answer: 5 32-Bit DALI answer: 6 Bus collision: 1 and ResultHex is 0x20FF	selected lamp and waits			
If no DALL answer is received and a timeout o	ccurs: Flags will be 9, R	lesultDec is 99 and ResultHex is 0x63 #GROUP COMMAND ANSWER: <group>=<command/><cr></cr></group>	ASCII	YES
FOR LAMP GROUP	WRITE	#GROUP COMMAND ANSWER: <group>=<command/><cr> #GROUP COMMAND ANSWER:<group>,<command/><cr></cr></group></cr></group>	ASCII	YES
WITH ANSWER		#GROUP COMMAND ANSWER. <group>,<command/><cr> #GCMDA:<group>,<command/><cr></cr></group></cr></group>		
	CONINIAND	Result:		
		#OK: <flags>,<resultdec>,<resulthex><cr></cr></resulthex></resultdec></flags>		
	COMMAND	$\frac{1}{2}$		
	GROUP	8:LAMP GROUP 9		
	TX	#GROUP COMMAND ANSWER:8=0x90 <cr></cr>		
	RX	#355.0K:9.99.0x63 <cr></cr>		
24-Bit DALI answer: 3 25-Bit DALI answer: 4 28-Bit DALI answer: 5 32-Bit DALI answer: 6 Bus collision: 1 and ResultHex is 0x20FF				
If no DALI answer is received and a timeout o	ccurs: Flags will be 9, R	esultDec is 99 and ResultHex is 0x63 #ALL COMMAND ANSWER: <command/> <cr></cr>	ASCII	YES
FOR ALL LAMPS	WRITE	#ACMDA: <command/> <cr></cr>	ASCII	TE3
WITH ANSWER	COMMAND	Result:		
		#OK: <flags>,<resultdec>,<resulthex><cr></cr></resulthex></resultdec></flags>		
	COMMAND	0x01		
	TX	#ALL COMMAND ANSWER:0x01 <cr></cr>		
	RX	#255.OK:9.99.0x63 <cr></cr>		
Sends the entered DALI 1.0 command to all la	RX amps and waits for an D			

DALI 1.0 COMMAND+REPEAT	ASCII	#LAMP COMMAND REPEAT ANSWER: <lamp>=<command/><cr></cr></lamp>	ASCII	YES
FOR LAMP	WRITE	#LAMP COMMAND REPEAT ANSWER: <lamp>,<command/><cr></cr></lamp>		
WITH ANSWER	COMMAND	#LCMDRA: <lamp>,<command/><cr></cr></lamp>		
		Result:		
		#OK: <flags>,<resultdec>,<resulthex><cr></cr></resulthex></resultdec></flags>		
	COMMAND	0x01		
	LAMP	5:LAMP SHORT ADDRESS 6		
	тх	#LAMP COMMAND REPEAT ANSWER:5=0x01 <cr></cr>		
	RX	#255,OK:9,99,0x63 <cr></cr>		
8-Bit DALI answer: 1 16-Bit DALI answer: 2 24-Bit DALI answer: 3 25-Bit DALI answer: 4 28-Bit DALI answer: 5 32-Bit DALI answer: 6 Bus collision: 1 and ResultHex is 0x20FF		peats the command within 100ms and waits for an DALI answer. In DALI answer value andf Flags singnals the type of answer:		
If no DALI answer is received and a timeou	it occurs: Flags will be 9	, ResultDec is 99 and ResultHex is 0x63		
DALI 1.0 COMMAND+REPEAT	ASCII	#GROUP COMMAND REPEAT ANSWER: <group>=<command/><cr></cr></group>	ASCII	YES
FOR LAMP GROUP	WRITE	#GROUP COMMAND REPEAT ANSWER: <group>,<command/><cr></cr></group>		
WITH ANSWER	COMMAND	#GCMDRA: <group>,<command/><cr></cr></group>		
		Result:		
		#OK: <flags>,<resultdec>,<resulthex><cr></cr></resulthex></resultdec></flags>		
	COMMAND	0x01		
	GROUP	8:LAMP GROUP 9		
	тх	#GROUP COMMAND REPEAT ANSWER:8=0x01 <cr></cr>		
	RX	#255,OK:9,99,0x63 <cr></cr>		
If there is an DALI answer, ReturnDec and 8-Bit DALI answer: 1 16-Bit DALI answer: 2 24-Bit DALI answer: 3 25-Bit DALI answer: 4 28-Bit DALI answer: 5 32-Bit DALI answer: 6 Bus collision: 1 and ResultHex is 0x20FF	ReturnHex represent th	and repeats the command within 100ms and waits for an DALI answer. In DALI answer value andf Flags singnals the type of answer:		
If no DALI answer is received and a timeou	3	·		
DALI 1.0 COMMAND+REPEAT	ASCII	#ALL COMMAND REPEAT ANSWER: <command/> <cr></cr>	ASCII	YES
	WRITE	#ACMDRA: <command/> <cr></cr>		
WITH ANSWER	COMMAND			
		#OK: <flags>,<resultdec>,<resulthex><cr></cr></resulthex></resultdec></flags>		
	COMMAND			
	ТХ	#ALL COMMAND REPEAT ANSWER:0x01 <cr></cr>		
	RX	#255,OK:9,99,0x63 <cr></cr>		

Sends the entered DALI 1.0 command to all lamps and repeats the command within 100ms and waits for an DALI answer.
If there is an DALI answer, ReturnDec and ReturnHex represent the DALI answer value and f Flags singnals the type of answer.
8-Bit DALI answer: 1
16-Bit DALI answer: 2
24-Bit DALI answer: 3
25-Bit DALI answer: 4
28-Bit DALI answer: 5
32-Bit DALI answer: 6

Bus collision: 1 and ResultHex is 0x20FF

If no DALI answer is received and a timed	out occurs: Flags will be 9	ResultDec is 99 and ResultHex is 0x63 #LAMP COMMAND: <lamp>=<command/><cr></cr></lamp>		
			ASCII	YES
FOR LAMP	WRITE	#LAMP COMMAND: <lamp>,<command/><cr></cr></lamp>		
	COMMAND	#LCMD: <lamp>,<command/><cr></cr></lamp>		
		Result:		
		#OK <cr></cr>		
	COMMAND	0x01		
	LAMP	5:LAMP SHORT ADDRESS 6		
	ТХ	#LAMP COMMAND:5=0x01 <cr></cr>		
	RX	#255,OK <cr></cr>		
Sends the entered DALI 1.0 command to	the selected lamp.			
DALI 1.0 COMMAND	ASCII	#GROUP COMMAND: <group>=<command/><cr></cr></group>	ASCII	YES
FOR LAMP GROUP	WRITE	#GROUP COMMAND: <group>,<command/><cr></cr></group>		
	COMMAND	#GCMD: <group>,<command/><cr></cr></group>		
		Result:		
		#OK <cr></cr>		
	COMMAND	0x02		
	GROUP	8:LAMP GROUP 9		
	ТХ	#GROUP COMMAND:8=0x02 <cr></cr>		
	RX	#255,OK <cr></cr>		
Sends the entered DALI 1.0 command to	the selected lamp group.		· · · · · ·	
DALI 1.0 COMMAND	ASCII	#ALL COMMAND: <command/> <cr></cr>	ASCII	YES
FOR ALL LAMPS	WRITE	#ACMD: <command/> <cr></cr>		
	COMMAND	Result:		
		#OK <cr></cr>		
	COMMAND	0x03		
	ТХ	#ALL COMMAND:0x03 <cr></cr>		
	RX	#255,OK <cr></cr>		
Sends the entered DALI 1.0 command to	all lamps.			
DALI 1.0 COMMAND+REPEAT	ASCII	#LAMP COMMAND REPEAT: <lamp>=<command/><cr></cr></lamp>	ASCII	YES
FOR LAMP	WRITE	#LAMP COMMAND REPEAT: <lamp>,<command/><cr></cr></lamp>		
	COMMAND	#LCMDR: <lamp>,<command/><cr></cr></lamp>		
		Result:		
		#OK <cr></cr>		
	COMMAND	0x01		
	LAMP	5:LAMP SHORT ADDRESS 6		
	ТХ	#LAMP COMMAND REPEAT:5=0x01 <cr></cr>		
	RX	#255,OK <cr></cr>		
Sends the entered DALI 1.0 command tw	ice within 100ms to the s			

DALI 1.0 COMMAND+REPEAT	ASCII	#GROUP COMMAND REPEAT: <group>=<command/><cr></cr></group>	ASCII	YES
FOR LAMP GROUP	WRITE	#GROUP COMMAND REPEAT. <group>,<command/><cr></cr></group>	ASCII	TL5
FOR LAWF GROOF	COMMAND	#GCMDR: <group>,<command/><cr></cr></group>		
	COMINIAND	Result:		
		#OK <cr></cr>		
	COMMAND	0x02		
	GROUP	8:LAMP GROUP 9		
	TX	#GROUP COMMAND REPEAT:8=0x02 <cr></cr>		
	RX	#255,OK <cr></cr>		
Sends the entered DALI 1.0 command twi				
DALI 1.0 COMMAND+REPEAT	ASCII	#ALL COMMAND REPEAT: <command/> <cr></cr>	ASCII	YES
FOR ALL LAMPS	WRITE	#ACMDR: <command/> <cr></cr>	ASCII	TL5
FOR ALL LAMPS	COMMAND	Result:		
	COMMAND	#OK <cr></cr>		
	COMMAND	0x03		
	TX	#ALL COMMAND REPEAT:0x03 <cr></cr>		
	RX	#255,OK <cr></cr>		
Sends the entered DALI 1.0 command twi				
DALI 1.0 ARC POWER	ASCII	#LAMP ARC POWER: <lamp>=<power><cr></cr></power></lamp>	ASCII	YES
FOR LAMP	WRITE	#LAMP ARC POWER: <lamp>,<power><cr></cr></power></lamp>	ASCII	TES
	COMMAND	#LAMP ARC POWER. <lamp2,<power><cr> #LARC:<lamp>,<power><cr></cr></power></lamp></cr></lamp2,<power>		
	COMMAND	#LARC.SLAMP2,SPOWER2SCR2 Result:		
		#OK <cr></cr>		
	POWER	127 50,0		
		5:LAMP SHORT ADDRESS 6		
		#LAMP ARC POWER:5=127 <cr></cr>		
	TX RX	#LAMP ARC POWER.5-127 <cr> #255,OK<cr></cr></cr>		
Sets for a lamp short address a new brigh				
		· · · · · · · · · · · · · · · · · · ·		
DALI 1.0 ARC POWER	ASCII	#GROUP ARC POWER: <group>=<power><cr></cr></power></group>	ASCII	YES
FOR LAMP GROUP	WRITE	#GROUP ARC POWER: <group>,<power><cr></cr></power></group>		
	COMMAND	#GARC: <group>,<power><cr></cr></power></group>		
		Result: #OK <cr></cr>		
		127 50,0		
	POWER GROUP	8:LAMP GROUP 9		
	TX	#GROUP ARC POWER:8=127 <cr></cr>		
Sets for a lamp group a new brightness le	RX	#255,OK <cr></cr>		
DALI 1.0 ARC POWER	ASCII	#ALL ARC POWER: <power><cr></cr></power>	ASCII	YES
FOR ALL LAMPS	WRITE	#AARC: <power><cr></cr></power>		
	COMMAND	Result:		
	DOWED	#OK <cr></cr>		
	POWER	127 50,0		
	TX	#ALL ARC POWER:127 <cr></cr>		
	RX	#255,OK <cr></cr>		

DALI BUS ERROR	ASCII	#DALI BUS ERROR <cr></cr>	ASCII	
	READ	#DBERR <cr></cr>		
	COMMAND	Result:		
		#DBERR: <buserrdec>,<buserrhex><cr></cr></buserrhex></buserrdec>		
	ТХ	#DALI BUS ERROR <cr></cr>		
	RX	#255,DBERR:0,0x0 <cr></cr>		
		0:BUS IS OK		
Shows the status of the DALI bus. BusErrDec,BusErrHex: =0:DALI bus is OK and fully functiona =1:DALI bus error, short circuit on D/	<u>ALI line, overload on DALI line</u>	or no DALI power supply		
SET PRIORITY SLOT	ASCII	#SET PRIORITY SLOT: <slot><cr></cr></slot>	ASCII	NO
	WRITE	#SPS: <slot><cr></cr></slot>		
	COMMAND	Result:		
		#OK <cr></cr>		
	SLOT	3		
	ТХ	#SET PRIORITY SLOT:3 <cr></cr>		
	RX	N/A		
This command defines the DALI prior 0: 12ms: Highest priority, used for mo 1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for qu	ntrol commands and comman ol commands	d Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time)		
GET PRIORITY SLOT	ASCII	#GET PRIORITY SLOT <cr></cr>	ASCII	
	READ	#GPS <cr></cr>		
	COMMAND	Result:		
		#GPS: <priorityslotdec>,<priorityslothex><cr></cr></priorityslothex></priorityslotdec>		
	ТХ	#GET PRIORITY SLOT <cr></cr>		
	RX	#255,GPS:2,0x2 <cr></cr>		
í literatura de la construcción de				
This command shows the current do	finad DALL priority slat. There	Current priority slot:2->14ms		_
This command shows the current de 0: 12ms: Highest priority, used for me 1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for qu	essages that must be repeate ntrol commands and command ol commands	Current priority slot:2->14ms are 5 slots available: d Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time)		
0: 12ms: Highest priority, used for mo 1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for qu DALI:8-BIT COMMANDS	essages that must be repeate ntrol commands and comman ol commands lery messages	are 5 slots available:		
0: 12ms: Highest priority, used for mo 1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for qu DALI:8-BIT COMMANDS	essages that must be repeate ntrol commands and command ol commands lery messages ASCII	are 5 slots available: Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #DALI CMD8: <command/> <cr></cr>	ASCII	YES
0: 12ms: Highest priority, used for me 1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power control	essages that must be repeate ntrol commands and command lery messages ASCII WRITE	are 5 slots available: Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #DALI CMD8: <command/> <cr> #DCMD8:<command/><cr></cr></cr>	ASCII	YES
0: 12ms: Highest priority, used for mo 1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for qu DALI:8-BIT COMMANDS DALI COMMAND	essages that must be repeate ntrol commands and command ol commands lery messages ASCII	are 5 slots available: j ls which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #DALI CMD8: <command/> <cr> #DCMD8:<command/><cr> Result:</cr></cr>	ASCII	YES
0: 12ms: Highest priority, used for mo 1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for qu DALI:8-BIT COMMANDS DALI COMMAND	essages that must be repeate ntrol commands and command pl commands iery messages ASCII WRITE COMMAND	are 5 slots available: J Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #DALI CMD8: <command/> <cr> #DCMD8:<command/><cr> Result: #OK<cr></cr></cr></cr>	ASCII	YES
0: 12ms: Highest priority, used for mo 1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for qu DALI:8-BIT COMMANDS DALI COMMAND	essages that must be repeate ntrol commands and command lery messages ASCII WRITE	are 5 slots available: J Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #DALI CMD8: <command/> <cr> #DCMD8:<command/><cr> Result:</cr></cr>	ASCII	YES
0: 12ms: Highest priority, used for mo 1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for qu DALI:8-BIT COMMANDS DALI COMMAND	ASCII WRITE COMMAND COMMAND TX	#DALI CMD8: <command/> <cr> #DALI CMD8:<command/><cr> #DCMD8:<command/><cr> Result: #OK<cr></cr></cr></cr></cr>	ASCII	YES
0: 12ms: Highest priority, used for mo 1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for qu DALI:8-BIT COMMANDS DALI COMMAND	ASCII WRITE COMMAND	are 5 slots available: Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #DALI CMD8: <command/> <cr> #DCMD8:<command/><cr> Result: #OK<cr> 0xFF</cr></cr></cr>	ASCII	YES

			A.C.C.U	VEC
	ASCII	#DALI CMDR8: <command/> <cr></cr>	ASCII	YES
WITH 8 BITS	WRITE COMMAND	#DCMDR8: <command/> <cr></cr>		
REPEAT WITIHN 100ms	COMMAND	Result:		
		#OK <cr></cr>		
	ТХ	#DALI CMDR8:0xFF <cr></cr>		
	RX	#255,OK <cr></cr>		
Sends the DSI/DALI 8 bit command to the D	OALI bus twice within 10	Dms		
DALI COMMAND	ASCII	#DALI CMDA8: <command/> <cr></cr>	ASCII	YES
WITH 8 BITS	WRITE	#DCMDA8: <command/> <cr></cr>		
WAIT FOR ANSWER	COMMAND	Result:		
		#OK: <status>,<valuedec>,<valuehex><cr></cr></valuehex></valuedec></status>		
	COMMAND	0xFF		
	тх	#DALI CMDA8:0xFF <cr></cr>		
	RX	#255,OK:9,99,0x63 <cr></cr>		
Sends the DSI/DALI 8 bit command to the D				1
STATUS:				
9: timeout no answer, VALUEDec is 99, VA	LUEHex is 0x63			
1: Answer is ok, one byte (8 bit) was receive	ed VALUEDec, VALUEF	ex is the received byte		
2: Answer is ok, one word (16 bit) was rece	IVED VALUEDEC, VALUE	Hex is the received word		
3: Answer is ok, 24 bit frame was received 4: Answer is ok, 25 bit frame was received 4		Is the received 24 bit frame		
5: Answer is ok, 28 bit frame was received	VALUEDEC, VALUEHEX	is the received 25 bit frame		
6: Answer is ok, 32 bit frame was received	VALUEDec.VALUEHex	s the received 32 bit frame		
			4000	2/50
DALICOMMAND	ASCII	#DALI CMDRA8: <command/> <cr></cr>	ASCII	YES
WITH 8 BITS	WRITE	#DCMDRA8: <command/> <cr></cr>		
REPEAT WITHIN 100ms	COMMAND			
WAIT FOR ANSWER		#OK: <status>,<valuedec>,<valuehex><cr></cr></valuehex></valuedec></status>		
	COMMAND	0xFF		
	ТХ	#DALI CMDRA8:0xFF <cr></cr>		
	RX	#255,OK:9,99,0x63 <cr></cr>		
Sends the DSI/DALI 8 bit command to the D	OALL hus twice within 10	Oms and waits for an answer		
STATUS:				
STATUS: 9: timeout no answer, VALUEDec is 99, VA	LUEHex is 0x63			
STATUS: 9: timeout no answer, VALUEDec is 99, VA 1: Answer is ok, one byte (8 bit) was receive	LUEHex is 0x63 ed VALUEDec,VALUEH	ex is the received byte		
STATUS: 9: timeout no answer, VALUEDec is 99, VA 1: Answer is ok, one byte (8 bit) was receive 2: Answer is ok, one word (16 bit) was receive	LUEHex is 0x63 ed VALUEDec,VALUEH ived VALUEDec,VALUE	ex is the received byte Hex is the received word		
STATUS: 9: timeout no answer, VALUEDec is 99, VA 1: Answer is ok, one byte (8 bit) was receive 2: Answer is ok, one word (16 bit) was received 3: Answer is ok, 24 bit frame was received V	LUEHex is 0x63 ed VALUEDec,VALUEH ived VALUEDec,VALUE VALUEDec,VALUEHex	ex is the received byte Hex is the received word is the received 24 bit frame		
STATUS: 9: timeout no answer, VALUEDec is 99, VA 1: Answer is ok, one byte (8 bit) was receive 2: Answer is ok, one word (16 bit) was received 3: Answer is ok, 24 bit frame was received 4: Answer is ok, 25 bit frame was received 5: Answer is ok, 28 bit frame was received	LUEHex is 0x63 ed VALUEDec,VALUEH ived VALUEDec,VALUE VALUEDec,VALUEHex VALUEDec,VALUEHex VALUEDec,VALUEHex	ex is the received byte Hex is the received word is the received 24 bit frame is the received 25 bit frame is the received 28 bit frame		
STATUS: 9: timeout no answer, VALUEDec is 99, VA 1: Answer is ok, one byte (8 bit) was receive 2: Answer is ok, one word (16 bit) was received 3: Answer is ok, 24 bit frame was received 4: Answer is ok, 25 bit frame was received	LUEHex is 0x63 ed VALUEDec,VALUEH ived VALUEDec,VALUE VALUEDec,VALUEHex VALUEDec,VALUEHex VALUEDec,VALUEHex	ex is the received byte Hex is the received word is the received 24 bit frame is the received 25 bit frame is the received 28 bit frame		
STATUS: 9: timeout no answer, VALUEDec is 99, VA 1: Answer is ok, one byte (8 bit) was receive 2: Answer is ok, one word (16 bit) was receive 3: Answer is ok, 24 bit frame was received 4: Answer is ok, 25 bit frame was received 5: Answer is ok, 28 bit frame was received 6: Answer is ok, 32 bit frame was received	LUEHex is 0x63 ed VALUEDec,VALUEH ived VALUEDec,VALUE VALUEDec,VALUEHex VALUEDec,VALUEHex VALUEDec,VALUEHex	ex is the received byte Hex is the received word is the received 24 bit frame is the received 25 bit frame is the received 28 bit frame		
STATUS: 9: timeout no answer, VALUEDec is 99, VA 1: Answer is ok, one byte (8 bit) was receive 2: Answer is ok, one word (16 bit) was receive 3: Answer is ok, 24 bit frame was received V 4: Answer is ok, 25 bit frame was received V 5: Answer is ok, 28 bit frame was received V 6: Answer is ok, 32 bit frame was received V DALI:16-BIT COMMANDS	LUEHex is 0x63 ed VALUEDec,VALUEH ved VALUEDec,VALUE VALUEDec,VALUEHex VALUEDec,VALUEHex VALUEDec,VALUEHex VALUEDec,VALUEHex	ex is the received byte Hex is the received word is the received 24 bit frame is the received 25 bit frame is the received 28 bit frame is the received 32 bit frame		VEC
STATUS: 9: timeout no answer, VALUEDec is 99, VA 1: Answer is ok, one byte (8 bit) was receive 2: Answer is ok, one word (16 bit) was received 3: Answer is ok, 24 bit frame was received 4: Answer is ok, 25 bit frame was received 5: Answer is ok, 28 bit frame was received 6: Answer is ok, 32 bit frame was received DALI:16-BIT COMMANDS DALI COMMAND	LUEHex is 0x63 ed VALUEDec,VALUEH ived VALUEDec,VALUE VALUEDec,VALUEHex VALUEDec,VALUEHex VALUEDec,VALUEHex VALUEDec,VALUEHex	ex is the received byte Hex is the received word is the received 24 bit frame is the received 25 bit frame is the received 28 bit frame #DALI CMD16: <command/> <cr></cr>	ASCII	YES
STATUS: 9: timeout no answer, VALUEDec is 99, VA 1: Answer is ok, one byte (8 bit) was receive 2: Answer is ok, one word (16 bit) was receive 3: Answer is ok, 24 bit frame was received V 4: Answer is ok, 25 bit frame was received V 5: Answer is ok, 28 bit frame was received V 6: Answer is ok, 32 bit frame was received V DALI:16-BIT COMMANDS	LUEHex is 0x63 ed VALUEDec,VALUEH VALUEDec,VALUEDec,VALUE VALUEDec,VALUEHex VALUEDec,VALUEHex VALUEDec,VALUEHex VALUEDec,VALUEHex VALUEDec,VALUEHex	ex is the received byte Hex is the received word is the received 24 bit frame is the received 25 bit frame is the received 28 bit frame #DALI CMD16: <command/> <cr> #DCMD16:<command/><cr></cr></cr>	ASCII	YES
STATUS: 9: timeout no answer, VALUEDec is 99, VA 1: Answer is ok, one byte (8 bit) was receive 2: Answer is ok, one word (16 bit) was received 3: Answer is ok, 24 bit frame was received 4: Answer is ok, 25 bit frame was received 5: Answer is ok, 28 bit frame was received 6: Answer is ok, 32 bit frame was received DALI:16-BIT COMMANDS DALI COMMAND	LUEHex is 0x63 ed VALUEDec,VALUEH ived VALUEDec,VALUE VALUEDec,VALUEHex VALUEDec,VALUEHex VALUEDec,VALUEHex VALUEDec,VALUEHex	ex is the received byte Hex is the received word is the received 24 bit frame is the received 25 bit frame is the received 28 bit frame #DALI CMD16: <command/> <cr> #DCMD16:<command/><cr> Result:</cr></cr>	ASCII	YES
STATUS: 9: timeout no answer, VALUEDec is 99, VA 1: Answer is ok, one byte (8 bit) was receive 2: Answer is ok, one word (16 bit) was received 3: Answer is ok, 24 bit frame was received 4: Answer is ok, 25 bit frame was received 5: Answer is ok, 28 bit frame was received 6: Answer is ok, 32 bit frame was received DALI:16-BIT COMMANDS DALI COMMAND	LUEHex is 0x63 ed VALUEDec,VALUEH ived VALUEDec,VALUEHex,VALUEDec,VALUEHex VALUEDec,VALUEHex VALUEDec,VALUEHex VALUEDec,VALUEHex VALUEDec,VALUEHex VALUEDEC,VALUEHex	ex is the received byte Hex is the received word is the received 24 bit frame is the received 25 bit frame is the received 28 bit frame #DALI CMD16: <command/> <cr> #DCMD16:<command/><cr> Result: #OK<cr></cr></cr></cr>	ASCII	YES
STATUS: 9: timeout no answer, VALUEDec is 99, VA 1: Answer is ok, one byte (8 bit) was receive 2: Answer is ok, one word (16 bit) was received 3: Answer is ok, 24 bit frame was received 4: Answer is ok, 25 bit frame was received 5: Answer is ok, 32 bit frame was received 6: Answer is ok, 32 bit frame was received DALI:16-BIT COMMANDS DALI COMMAND	LUEHex is 0x63 ed VALUEDec,VALUEH ived VALUEDec,VALUE VALUEDec,VALUEHex,VALUEDec,VALUEHex VALUEDec,VALUEHex VALUEDec,VALUEHex VALUEDec,VALUEHex COMMAND COMMAND	ex is the received byte Hex is the received word is the received 24 bit frame is the received 25 bit frame is the received 32 bit frame #DALI CMD16: <command/> <cr> #DCMD16:<command/><cr> Result: #OK<cr> 0xFFFF</cr></cr></cr>	ASCII	YES
STATUS: 9: timeout no answer, VALUEDec is 99, VA 1: Answer is ok, one byte (8 bit) was receive 2: Answer is ok, one word (16 bit) was received 3: Answer is ok, 24 bit frame was received 4: Answer is ok, 25 bit frame was received 5: Answer is ok, 28 bit frame was received 6: Answer is ok, 32 bit frame was received DALI:16-BIT COMMANDS DALI COMMAND	LUEHex is 0x63 ed VALUEDec,VALUEH ived VALUEDec,VALUE VALUEDec,VALUEHex,VALUEDec,VALUEHex,VALUEDec,VALUEHex VALUEDec,VALUEHex VALUEDec,VALUEHex WRITE COMMAND COMMAND TX	ex is the received byte Hex is the received word is the received 24 bit frame is the received 25 bit frame is the received 32 bit frame #DALI CMD16: <command/> <cr> #DCMD16:<command/><cr> Result: #OK<cr> 0xFFFF #DALI CMD16:0xFFFF<cr></cr></cr></cr></cr>	ASCII	YES
STATUS: 9: timeout no answer, VALUEDec is 99, VA 1: Answer is ok, one byte (8 bit) was receive 2: Answer is ok, one word (16 bit) was received 3: Answer is ok, 24 bit frame was received 4: Answer is ok, 25 bit frame was received 5: Answer is ok, 28 bit frame was received 6: Answer is ok, 32 bit frame was received DALI:16-BIT COMMANDS DALI COMMAND	LUEHex is 0x63 ed VALUEDec,VALUEH ived VALUEDec,VALUE VALUEDec,VALUEHex,VALUEDec,VALUEHex VALUEDec,VALUEHex VALUEDec,VALUEHex VALUEDec,VALUEHex COMMAND COMMAND	ex is the received byte Hex is the received word is the received 24 bit frame is the received 25 bit frame is the received 32 bit frame #DALI CMD16: <command/> <cr> #DCMD16:<command/><cr> Result: #OK<cr> 0xFFFF</cr></cr></cr>	ASCII	YES

DALI COMMAND	ASCII	#DALI CMDR16: <command/> <cr></cr>	ASCII	YES
WITH 16 BITS	WRITE	#DCMDR16: <command/> <cr></cr>	ASCII	
REPEAT WITIHN 100ms	COMMAND	Result:		
		#OK <cr></cr>		
	COMMAND	0xFFFF		
	тх	#DALI CMDR16:0xFFFF <cr></cr>		
	RX	#255,OK <cr></cr>		
Sends the DALI 16 bit command to the DALI b				
DALI COMMAND	ASCII	#DALI CMDA16: <command/> <cr></cr>	ASCII	YES
WITH 16 BITS	WRITE	#DCMDA16: <command/> <cr></cr>		
WAIT FOR ANSWER	COMMAND	Result:		
		#OK: <status>,<valuedec>,<valuehex><cr></cr></valuehex></valuedec></status>		
	COMMAND	0xFFFF		
	тх	#DALI CMDA16:0xFFFF <cr></cr>		
	RX	#255,OK:9,99,0x63 <cr></cr>		
Sends the DALI 16 bit command to the DALI	bus and waits for an a			1
STATUS:				
9: timeout no answer, VALUEDec is 99, VALU	JEHex is 0x63			
1: Answer is ok, one byte (8 bit) was received		lex is the received byte		
2: Answer is ok, one word (16 bit) was receive 3: Answer is ok, 24 bit frame was received VA		rnex is the received wind		
4: Answer is ok, 25 bit frame was received VA		Is the received 24 bit frame		
5: Answer is ok, 28 bit frame was received VA		is the received 25 bit frame		
6: Answer is ok, 32 bit frame was received VA	LUEDec.VALUEHex	is the received 25 bit frame		
			4.0.011	VEO
DALI COMMAND	ASCII	#DALI CMDRA16: <command/> <cr></cr>	ASCII	YES
WITH 16 BITS	WRITE	#DCMDRA16: <command/> <cr></cr>		
REPEAT WITHIN 100ms	COMMAND			
WAIT FOR ANSWER		#OK: <status>,<valuedec>,<valuehex><cr></cr></valuehex></valuedec></status>		
	COMMAND	0xFFFF		
	TX	#DALI CMDRA16:0xFFFF <cr></cr>		
	RX	#255,OK:9,99,0x63 <cr></cr>		
Sends the DALI 16 bit command to the DALI	bus twice within 100m	is and waits for an answer		
STATUS: 9: timeout no answer, VALUEDec is 99, VALU	IELION is OVE2			
1: Answer is ok, one byte (8 bit) was received		lex is the received byte		
2. Answer is ok, one word (16 bit) was received	ed VAI UFDec VAI UF	Hex is the received word		
2: Answer is ok, one word (16 bit) was receive 3: Answer is ok, 24 bit frame was received VA	LUEDec.VALUEHex	is the received 24 bit frame		
4: Answer is ok, 25 bit frame was received VA	LUEDec, VALUEHex	is the received 25 bit frame		
5: Answer is ok, 28 bit frame was received VA	LUEDec, VALUEHex	is the received 28 bit frame		
6: Answer is ok, 32 bit frame was received VA	ALUEDec,VALUEHex	is the received 32 bit frame		
DALI:24-BIT COMMANDS				
DALI COMMAND	ASCII	#DALI CMD24: <command/> <cr></cr>	ASCII	YES
WITH 24 BITS	WRITE	#DCMD24: <command/> <cr></cr>		
	COMMAND	Result:		
		#OK <cr></cr>		
	COMMAND	0xFFFFF		
	TX	#DALI CMD24:0xFFFFF< <cr></cr>		
	RX			
Condo the DALL 24 bit commond to the DALL		#255,OK <cr></cr>		
Sends the DALI 24 bit command to the DALI b	JUS			

				VEC
	ASCII WRITE	#DALI CMDR24: <command/> <cr></cr>	ASCII	YES
WITH 24 BITS REPEAT WITIHN 100ms	COMMAND	#DCMDR24: <command/> <cr> Result:</cr>		
REPEAT WITHIN LOUIS	COMIMAND			
	COMMAND			
	TX	#DALI CMDR24:0xFFFFFF <cr></cr>		
	RX	#255,OK <cr></cr>		
Sends the DALI 24 bit command to the I				
DALI COMMAND	ASCII	#DALI CMDA24: <command/> <cr></cr>	ASCII	YES
WITH 24 BITS	WRITE	#DCMDA24: <command/> <cr></cr>		
WAIT FOR ANSWER	COMMAND	Result: #OK: <status>,<valuedec>,<valuehex><cr></cr></valuehex></valuedec></status>		
	COMMAND	0xFFFFF		
	ТХ	#DALI CMDA24:0xFFFFFF <cr></cr>		
	RX	#255,OK:9,99,0x63 <cr></cr>		
Sends the DALI 24 bit command to the				1
9: timeout no answer, VALUEDec is 99, 1: Answer is ok, one byte (8 bit) was rec 2: Answer is ok, one word (16 bit) was r 3: Answer is ok, 24 bit frame was receiv 4: Answer is ok, 25 bit frame was receiv 5: Answer is ok, 28 bit frame was receiv 6: Answer is ok, 32 bit frame was receiv	ceived VALUEDec,VALUEF eceived VALUEDec,VALUE ed VALUEDec,VALUEHex ed VALUEDec,VALUEHex ed VALUEDec,VALUEHex	Ettex is the received word is the received 24 bit frame is the received 25 bit frame is the received 28 bit frame		
DALI COMMAND	ASCII	#DALI CMDRA24: <command/> <cr></cr>	ASCII	YES
WITH 24 BITS	WRITE	#DCMDRA24: <command/> <cr></cr>		
REPEAT WITHIN 100ms	COMMAND	Result:		
WAIT FOR ANSWER		#OK: <status>,<valuedec>,<valuehex><cr></cr></valuehex></valuedec></status>		
	COMMAND	0xFFFFF		
	ТХ	#DALI CMDRA24:0xFFFFF <cr></cr>		
	RX	#255.OK:9.99.0x63 <cr></cr>		
Sends the DALI 24 bit command to the STATUS: 9: timeout no answer, VALUEDec is 99, 1: Answer is ok, one byte (8 bit) was red 2: Answer is ok, one word (16 bit) was r 3: Answer is ok, 24 bit frame was receiv 4: Answer is ok, 25 bit frame was receiv 5: Answer is ok, 28 bit frame was receiv 6: Answer is ok, 32 bit frame was receiv	VALUEHex is 0x63 ceived VALUEDec,VALUEH eceived VALUEDec,VALUE red VALUEDec,VALUEHex red VALUEDec,VALUEHex red VALUEDec.VALUEHex	lex is the received byte EHex is the received word is the received 24 bit frame is the received 25 bit frame is the received 28 bit frame		
DALI:25-BIT COMMANDS:eDA	LI with automatic ger	neration of redundant bit		
DALI COMMAND	ASCII	#DALI CMD25: <command/> <cr></cr>	ASCII	YES
WITH 25 BITS	WRITE	#DCMD25: <command/> <cr></cr>		
	COMMAND	Result:		
		#OK <cr></cr>		
	COMMAND	0x00FF00		
	ТХ	#DALI CMD25:0x00FF00 <cr></cr>		
	RX	#255,OK <cr></cr>		
Sends the DALI 24 bit command to the I				

				VEC
	ASCII	#DALI CMDR25: <command/> <cr></cr>	ASCII	YES
WITH 25 BITS	WRITE	#DCMDR25: <command/> <cr></cr>		
REPEAT WITIHN 100ms	COMMAND	Result:		
		#OK <cr></cr>		
	COMMAND	0x00FF00		
	ТХ	#DALI CMDR25:0x00FF00 <cr></cr>		
	RX	#255,OK <cr></cr>		
Sends the DALI 24 bit command to the DAL	I bus twice within 100ms.	The redundant bit of the eDALI 25 bit frame is generated automatically.		
DALI COMMAND	ASCII	#DALI CMDA25: <command/> <cr></cr>	ASCII	YES
WITH 25 BITS	WRITE	#DCMDA25: <command/> <cr></cr>	ASCII	TLO
WAIT FOR ANSWER	COMMAND	Result:		
WAIT FOR ANSWER	CONINIAND	#OK: <status>,<valuedec>,<valuehex><cr></cr></valuehex></valuedec></status>		
	COMMAND	#OR. <status ,="" <br="" cr="" valuedec="" valuerex="">0x00FF00</status>		
		#DALI CMDA25:0x00FF00 <cr></cr>		
	RX			
Sends the DALL 24 hit command to the DAL		#255,OK:9,99,0x63 <cr> wer. The redundant bit of the eDALI 25 bit frame is generated automatically.</cr>		
STATUS:	ו היה מווח אמונה והו מון מווה	wei. The requiridant bit of the EDALI 25 bit flathe is generated automatically.		
9: timeout no answer, VALUEDec is 99, VAL	LUEHex is 0x63			
1: Answer is ok, one byte (8 bit) was receive 2: Answer is ok, one word (16 bit) was receive	ed VALUEDec, VALUEHex	is the received byte		
2: Answer is ok, one word (16 bit) was recei	ved VALUEDec, VALUEHe	x is the received word		
3: Answer is ok, 24 bit frame was received V 4: Answer is ok, 25 bit frame was received V	ALUEDEC, VALUEHEX IS T	ne received 25 bit frame		
5: Answer is ok, 28 bit frame was received V	/ALUEDEC, VALUEHEX IS I	he received 25 bit frame		
6: Answer is ok, 32 bit frame was received V	ALUEDec.VALUEHex is t	he received 20 bit frame		
			1001	
DALI COMMAND	ASCII	#DALI CMDRA25: <command/> <cr></cr>	ASCII	YES
WITH 25 BITS	WRITE	#DCMDRA25: <command/> <cr></cr>		
REPEAT WITHIN 100ms	COMMAND	Result:		
WAIT FOR ANSWER		#OK: <status>,<valuedec>,<valuehex><cr></cr></valuehex></valuedec></status>		
	COMMAND	0x00FF00		
	TX	#DALI CMDRA25:0x00FF00 <cr></cr>		
	RX	#255,OK:9,99,0x63 <cr></cr>		
Sends the DALI 24 bit command to the DAL	I bus twice within 100ms a	nd waits for an answer. The redundant bit of the eDALI 25 bit frame is generated automatically.		
STATUS: 9: timeout no answer, VALUEDec is 99, VAI				
1. Answer is ok one hyte (8 hit) was received	NALLIEDEC VALLIEHEN	is the received hyte		
1: Answer is ok, one byte (8 bit) was receive 2: Answer is ok, one word (16 bit) was receive	ved VALUEDec.VALUEHe	is the received word		
3: Answer is ok. 24 bit frame was received V	/ALUEDec.VALUEHex is t	he received 24 bit frame		
4. Answer is ok 25 hit frame was received \	ALLIEDec VALLIEHex is t	he received 25 hit frame		
5: Answer is ok, 28 bit frame was received V	ALUEDec, VALUEHex is t	he received 28 bit frame		
6: Answer is ok, 32 bit frame was received \	ALUEDEC, VALUEHEX IS t	ne received 32 dit frame		
DALI:25-BIT COMMANDS:eDALI w				
DALI COMMAND	ASCII	#DALI CMD25U: <command/> <cr></cr>	ASCII	YES
WITH 25 BITS	WRITE	#DCMD25U: <command/> <cr></cr>		
	COMMAND	Result:		
		#OK <cr></cr>		
	COMMAND	#OK <cr> 0x01FE00</cr>		

ASCII RESI-DALI-SIO, ETH Commands 2

	RX	#255,OK <cr></cr>		
Sends the DALL25 hit command to the DAL		of the eDALI 25 bit frame is part of the DALI frame.		
DALI COMMAND	ASCII	#DALI CMDR25U: <command/> <cr></cr>		YES
WITH 25 BITS	WRITE	#DALI CMDR250. <command/> <cr> #DCMDR25U:<command/><cr></cr></cr>	ASCII	TES
REPEAT WITHN 100ms	COMMAND	Result:		
REPEAT WITHIN 100HIS	COMINIAND	#OK <cr></cr>		
	COMMAND	0x01FE00		
	TX	#DALI CMDR25U:0x01FE00 <cr></cr>		
	RX	#255,OK <cr></cr>		
Sends the DALI 25 bit command to the DALI		s. The redundant bit of the eDALI 25 bit frame is part of the DALI frame.		
DALI COMMAND	ASCII	#DALI CMDA25U: <command/> <cr></cr>	ASCII	YES
WITH 25 BITS	WRITE	#DCMDA25U: <command/> <cr></cr>		
WAIT FOR ANSWER	COMMAND	Result:		
		#OK: <status>,<valuedec>,<valuehex><cr></cr></valuehex></valuedec></status>		
	COMMAND	0x01FE00		
	TX	#DALI CMDA25U:0x01FE00 <cr></cr>		
	RX	#255,OK:9,99,0x63 <cr> Inswer. The redundant bit of the eDALI 25 bit frame is part of the DALI frame.</cr>		
5: Answer is ok, 28 bit frame was received V 6: Answer is ok, 32 bit frame was received V DALI COMMAND WITH 25 BITS REPEAT WITHIN 100ms WAIT FOR ANSWER	ALUEDec, VALUEHex i ALUEDec, VALUEHex i ASCII WRITE COMMAND	is the received 28 bit frame is the received 32 bit frame #DALI CMDRA25U: <command/> <cr> #DCMDRA25U:<command/><cr> Result: #OK:<status>,<valuedec>,<valuehex><cr> 0x01FE00</cr></valuehex></valuedec></status></cr></cr>	ASCII	YES
	TX	#DALI CMDRA25U:0x01FE00 <cr></cr>		
	RX	#255,OK:9,99,0x63 <cr></cr>		
Sends the DALI 25 bit command to the DAL STATUS: 9: timeout no answer, VALUEDec is 99, VAI 1: Answer is ok, one byte (8 bit) was receive 2: Answer is ok, one word (16 bit) was receive 3: Answer is ok, 24 bit frame was received V 4: Answer is ok, 25 bit frame was received V 5: Answer is ok, 28 bit frame was received V 6: Answer is ok, 32 bit frame was received V DALI:28-BIT COMMANDS	LUEHex is 0x63 Id VALUEDec,VALUEHe ved VALUEDec,VALUE /ALUEDec,VALUEHex i /ALUEDec,VALUEHex i /ALUEDec,VALUEHex i	is the received 24 bit frame is the received 25 bit frame is the received 28 bit frame		
DALI COMMAND WITH 28 BITS	ASCII WRITE COMMAND	#DALI CMD28: <command/> <cr> #DCMD28:<command/><cr> Result:</cr></cr>	ASCII	YES
		#OK <cr></cr>		

	COMMAND	0xFFFFFF		
	TX	#DALI CMD28:0xFFFFFF <cr></cr>		
	RX	#255,OK <cr></cr>		
Sends the DALI 28 bit command to the				
DALI COMMAND	ASCII	#DALI CMDR28: <command/> <cr></cr>	ASCII	YES
WITH 28 BITS	WRITE	#DCMDR28: <command/> <cr></cr>		
REPEAT WITIHN 100ms	COMMAND	Result:		
		#OK <cr></cr>		
	COMMAND	0xFFFFFF		
	ТХ	#DALI CMDR28:0xFFFFFF <cr></cr>		
	RX	#255,OK <cr></cr>		
Sends the DALI 28 bit command to the	DALI bus twice within 100m	6		
DALI COMMAND	ASCII	#DALI CMDA28: <command/> <cr></cr>	ASCII	YES
WITH 28 BITS	WRITE	#DCMDA28: <command/> <cr></cr>		
WAIT FOR ANSWER	COMMAND	Result:		
		#OK: <status>,<valuedec>,<valuehex><cr></cr></valuehex></valuedec></status>		
	COMMAND	0xFFFFFF		
	ТХ	#DALI CMDA28:0xFFFFFF <cr></cr>		
Sends the DALI 28 bit command to the	RX	#255,OK:9,99,0x63 <cr></cr>		
6: Answer is ok, 32 bit frame was rece DALI COMMAND WITH 28 BITS REPEAT WITHIN 100ms WAIT FOR ANSWER	ASCII WRITE COMMAND	#DALI CMDRA28: <command/> <cr> #DCMDRA28:<command/><cr> Result: #OK:<status>,<valuedec>,<valuehex><cr></cr></valuehex></valuedec></status></cr></cr>	ASCII	YES
	COMMAND	0xFFFFFF		
	TX	#DALI CMDRA28:0xFFFFFF< <cr></cr>		
	RX	#255,OK:9,99,0x63 <cr></cr>		
Sends the DALI 28 bit command to the				1
STATUS: 9: timeout no answer, VALUEDec is 99 1: Answer is ok, one byte (8 bit) was re 2: Answer is ok, one word (16 bit) was	ceived VALUEDec.VALUEH	Hex is the received word		
3: Answer is ok, 24 bit frame was rece 4: Answer is ok, 25 bit frame was rece 5: Answer is ok, 28 bit frame was rece 6: Answer is ok, 32 bit frame was rece	ved VALUEDec,VALUEHex ved VALUEDec,VALUEHex ved VALUEDec,VALUEHex	is the received 25 bit frame is the received 28 bit frame		
4: Answer is ok, 25 bit frame was rece 5: Answer is ok, 28 bit frame was rece	ved VALUEDec,VALUEHex ved VALUEDec,VALUEHex ved VALUEDec,VALUEHex	is the received 25 bit frame is the received 28 bit frame		
4: Answer is ok, 25 bit frame was rece 5: Answer is ok, 28 bit frame was rece 6: Answer is ok, 32 bit frame was rece	ved VALUEDec,VALUEHex ved VALUEDec,VALUEHex ved VALUEDec,VALUEHex	is the received 25 bit frame is the received 28 bit frame	ASCII	YES

	COMMAND	0xFFFFFF		
	ТХ	#DALI CMD32:0xFFFFFFF <cr></cr>		
	RX	#255,OK <cr></cr>		
Sends the DALI 32 bit command to the DALI	bus			
DALI COMMAND	ASCII	#DALI CMDR32: <command/> <cr></cr>	ASCII	YES
WITH 32 BITS	WRITE	#DCMDR32: <command/> <cr></cr>		
REPEAT WITIHN 100ms	COMMAND	Result:		
		#OK <cr></cr>		
	COMMAND	0xFFFFFFF		
	ТХ	#DALI CMDR32:0xFFFFFFF <cr></cr>		
	RX	#255,OK <cr></cr>		
Sends the DALI 32 bit command to the DALI	bus twice within 100ms			
DALI COMMAND	ASCII	#DALI CMDA32: <command/> <cr></cr>	ASCII	YES
WITH 32 BITS	WRITE	#DCMDA32: <command/> <cr></cr>		
WAIT FOR ANSWER	COMMAND	Result:		
		#OK: <status>,<valuedec>,<valuehex><cr></cr></valuehex></valuedec></status>		
	COMMAND	0xFFFFFFF		
	ТХ	#DALI CMDA32:0xFFFFFFF <cr></cr>		
Sends the DALI 32 bit command to the DALI	RX	#255,OK:9,99,0x63 <cr></cr>		
1: Answer is ok, one byte (8 bit) was received 2: Answer is ok, one word (16 bit) was received 3: Answer is ok, 24 bit frame was received V/ 4: Answer is ok, 25 bit frame was received V/ 5: Answer is ok, 28 bit frame was received V/ 6: Answer is ok, 32 bit frame was received V/	ed VALUEDec,VALUEH ALUEDec,VALUEHex is ALUEDec,VALUEHex is ALUEDec.VALUEHex is	Hex is the received word s the received 24 bit frame s the received 25 bit frame s the received 28 bit frame		
DALI COMMAND	ASCII	#DALI CMDRA32: <command/> <cr></cr>	ASCII	YES
WITH 32 BITS	WRITE	#DCMDRA32: <command/> <cr></cr>	7.001	120
REPEAT WITHIN 100ms	COMMAND	Result:		
WAIT FOR ANSWER		#OK: <status>,<valuedec>,<valuehex><cr></cr></valuehex></valuedec></status>		
	COMMAND	0xFFFFFFF		
	ТХ	#DALI CMDRA32:0xFFFFFFF< <cr></cr>		
	RX	#255,OK:9,99,0x63 <cr></cr>		
Sends the DALI 32 bit command to the DALI STATUS: 9: timeout no answer, VALUEDec is 99, VALI 1: Answer is ok, one byte (8 bit) was received 2: Answer is ok, one word (16 bit) was received 3: Answer is ok, 24 bit frame was received V/ 4: Answer is ok, 25 bit frame was received V/ 5: Answer is ok, 28 bit frame was received V/ 6: Answer is ok, 32 bit frame was received V/	UEHex is 0x63 1 VALUEDec,VALUEHe ed VALUEDec,VALUEH ALUEDec,VALUEHex is ALUEDec,VALUEHex is ALUEDec,VALUEHex is	ex is the received byte Hex is the received word s the received 24 bit frame s the received 25 bit frame		

DALI BUS ERROR	ASCII	#DALI BUS ERROR <cr> ASCII</cr>	
	READ	#DBERR <cr></cr>	
	COMMAND	Result:	
		#DBERR: <buserrdec>,<buserrhex><cr></cr></buserrhex></buserrdec>	
	ТХ	#DALI BUS ERROR <cr></cr>	
	RX	#255,DBERR:0,0x0 <cr></cr>	
		0:BUS IS OK	
Shows the status of the DALI bus. BusErrDec,BusErrHex: =0:DALI bus is OK and fully functiona =1:DALI bus error, short circuit on DA	al ALLline, overload on DALLline	or no DALLnower supply	
SET PRIORITY SLOT	ASCII	#SET PRIORITY SLOT: <slot><cr> ASCII</cr></slot>	NO
	WRITE	#SPS: <slot><cr></cr></slot>	_
	COMMAND	Result:	
		#OK <cr></cr>	
	SLOT	3	
	ТХ	#SET PRIORITY SLOT:3 <cr></cr>	
	RX	N/A	
This command defines the DALI prio 0: 12ms: Highest priority, used for me	prity slot. There are 5 slots ava	ilable:	
1 · 13ms · User initiated arc nower cor	trol commands and command	Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time)	
1: 13ms: User initiated arc power con 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que	Itrol commands and command l commands ery messages ASCII	ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr> ASCII</cr>	
1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que	I commands and command ery messages ASCII READ	#GET PRIORITY SLOT <cr> #GPS<cr> ASCII</cr></cr>	
1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que	Itrol commands and command l commands ery messages ASCII	#GET PRIORITY SLOT <cr> #GPS<cr> Result: As which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) ASCII</cr></cr>	
 13ms: User initiated arc power cor 14ms: Configuration in general 15ms: Automatic arc power contro 16ms: Lowest priority, used for que 	I commands and commands ery messages ASCII READ COMMAND	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr></cr></priorityslothex></priorityslotdec></cr></cr>	
 13ms: User initiated arc power cor 14ms: Configuration in general 15ms: Automatic arc power contro 16ms: Lowest priority, used for que 	Itrol commands and commands ery messages ASCII READ COMMAND TX	ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr> ASCII #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>	
 13ms: User initiated arc power cor 14ms: Configuration in general 15ms: Automatic arc power contro 16ms: Lowest priority, used for que 	I commands and commands ery messages ASCII READ COMMAND	ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr> ASCII #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr></cr></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>	
1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT	Itrol commands and commands ery messages ASCII READ COMMAND TX RX	#GET PRIORITY SLOT <cr> ASCII #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #GET PRIORITY SLOT<cr> Current priority slot2</cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>	
1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT This command shows the current def 0: 12ms: Highest priority, used for me 1: 13ms: User initiated arc power con 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro	Itrol commands and commands ery messages ASCII READ COMMAND TX RX ined DALI priority slot. There is essages that must be repeated itrol commands and command I commands	ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available:</cr></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>	
1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT This command shows the current def 0: 12ms: Highest priority, used for me	Itrol commands and commands ery messages ASCII READ COMMAND TX RX ined DALI priority slot. There essages that must be repeated throl commands and commands ery messages	ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d</cr></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>	
13ms: User initiated arc power cor 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT This command shows the current def 0: 12ms: Highest priority, used for me 1: 13ms: Oser initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que DALI:LAST RECEIVED TELE	Itrol commands and commands ery messages ASCII READ COMMAND TX RX ined DALI priority slot. There essages that must be repeated throl commands and commands ery messages	ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d</cr></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>	YES
1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT This command shows the current def 0: 12ms: Highest priority, used for me 1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que	It commands and commands ery messages ASCII READ COMMAND TX RX ined DALI priority slot. There essages that must be repeated throl commands and command I commands ery messages	ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d s which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time)</cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>	YES
1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT This command shows the current def 0: 12ms: Highest priority, used for me 1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que DALI:LAST RECEIVED TELE DALI LAST RECEIVED	It commands and commands ery messages ASCII READ COMMAND TX RX ined DALI priority slot. There essages that must be repeated throl commands and command I commands ery messages GRAM ASCII	ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d swhich have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAST RECEIVED TELEGRAM<cr> ASCII</cr></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>	YES
1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT This command shows the current def 0: 12ms: Highest priority, used for me 1: 13ms: Oser initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que DALI:LAST RECEIVED TELE DALI LAST RECEIVED	It commands and commands ery messages ASCII READ COMMAND TX RX ined DALI priority slot. There essages that must be repeated throl commands and commands ery messages GRAM ASCII WRITE COMMAND	ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d swhich have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAST RECEIVED TELEGRAM<cr> #LAST RECEIVED TELEGRAM<cr> ASCII</cr></cr></cr></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>	YES
1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT This command shows the current def 0: 12ms: Highest priority, used for me 1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que DALI:LAST RECEIVED TELE DALI LAST RECEIVED	It commands and commands ery messages ASCII READ COMMAND TX RX ined DALI priority slot. There essages that must be repeated throl commands and commands ery messages GRAM ASCII WRITE COMMAND TX	ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d swhich have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAST RECEIVED TELEGRAM<cr> #LAST RECEIVED TELEGRAM<cr> Result: #LAST RECEIVED TELEGRAM</cr></cr></cr></cr></cr></cr></cr></cr></cr></cr></cr></cr></cr></cr></cr></cr></cr></cr></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>	YES
1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT This command shows the current def 0: 12ms: Highest priority, used for me 1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que DALI:LAST RECEIVED TELE DALI LAST RECEIVED	It commands and commands ery messages ASCII READ COMMAND TX RX ined DALI priority slot. There essages that must be repeated throl commands and commands ery messages GRAM ASCII WRITE COMMAND	ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #GET PRIORITY SLOT<cr> #Z55,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d swhich have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAST RECEIVED TELEGRAM<cr> #LAST RECEIVED TELEGRAM<cr> #LAST RECEIVED TELEGRAM<cr> Result: #LRT<cr> Result: #LRT<counter>,<bits>,<dali-frame><cr></cr></dali-frame></bits></counter></cr></cr></cr></cr></cr></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>	YES
1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT This command shows the current def 0: 12ms: Highest priority, used for me 1: 13ms: Oser initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que DALI:LAST RECEIVED TELE DALI LAST RECEIVED	It commands and commands ery messages ASCII READ COMMAND TX RX ined DALI priority slot. There essages that must be repeated throl commands and commands ery messages GRAM ASCII WRITE COMMAND TX	#GET PRIORITY SLOT <cr> ASCII #GPS<cr> Result: #GET PRIORITY SLOT<cr> #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #Z55,GPS:2,0x2<cr> Current priority slot2 Current priority slot2 2 Current priority slot2 #LAST RECEIVED TELEGRAM<cr> ASCII #LAST RECEIVED TELEGRAM<cr> ASCII #LAST RECEIVED TELEGRAM<cr> #LAST RECEIVED TELEGRAM<cr> #LAST RECEIVED TELEGRAM<cr> #LAST RECEIVED TELEGRAM<cr> #LAST RECEIVED TELEGRAM<cr> #LAST RECEIVED TELEGRAM<<cr></cr></cr></cr></cr></cr></cr></cr></cr></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr></cr>	YES
1: 13ms: User initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT This command shows the current def 0: 12ms: Highest priority, used for me 1: 13ms: Oser initiated arc power cor 2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for que DALI:LAST RECEIVED TELE DALI LAST RECEIVED	It commands and commands ery messages ASCII READ COMMAND TX RX ined DALI priority slot. There essages that must be repeated throl commands and commands ery messages GRAM ASCII WRITE COMMAND TX	#GET PRIORITY SLOT <cr> ASCII #GPS<cr< td=""> Result: #GFT PRIORITY SLOT<cr> #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #GET PRIORITY SLOT<cr> #GET PRIORITY SLOT<cr> </cr></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr<></cr>	YES

Returns the last received DALI frame. A 16 bit counter is used for indicating a change in the received data: Every time the gateway receives a new DALI frame this counter will be incremented by one.

DALI CHECK TELEGRAM	ASCII	#CHECK TELEGRAM RING <cr></cr>	ASCII	YES
RING PUFFER	WRITE	#CTR <cr></cr>		
	COMMAND	Result:		
		#CTR: <counter>,<bits>,<dali-frame><cr></cr></dali-frame></bits></counter>		
	ТХ	#CHECK TELEGRAM RING <cr></cr>		
	RX	#255,CTR:2,2,24,0x0088840e,29539 <cr></cr>		
		Current counter for DALI frames:2		
		Flags of DALI frame:2 -> 2:RX		
		Bit length of DALI frame:24		
		DALI frame:0x0088840e		
		Gap to previous DALI frame:29539ms -> 29,539s		

ALI BUS ERROR	ASCII	#DALI BUS ERROR <cr></cr>	ASCII	
	READ	#DBERR <cr></cr>		
	COMMAND	Result:		
		#DBERR: <buserrdec>,<buserrhex><cr></cr></buserrhex></buserrdec>		
	TX	#DALI BUS ERROR <cr></cr>		
	RX	#255,DBERR:0,0x0 <cr></cr>		
		0:BUS IS OK		
nows the status of the DALI bus. JSErrDec,BusErrHex: DCALI bus is OK and fully function CDALI bus error, short circuit on D	al ALI line, overload on DALI line	e or no DALI power supply		
ET PRIORITY SLOT	ASCII	#SET PRIORITY SLOT: <slot><cr></cr></slot>	ASCII	NO
	WRITE	#SPS: <slot><cr></cr></slot>		
	COMMAND	Result:		
		#OK <cr></cr>		
	SLOT	3		
	ТХ	#SET PRIORITY SLOT:3 <cr></cr>		
	RX	N/A		
14ms: Configuration in general 15ms: Automatic arc power contro		ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time)		
16ms: Lowest priority, used for qu	ery messages			
	ery messages	#GET PRIORITY SLOT <cr></cr>	ASCII	
	ery messages ASCII	#GET PRIORITY SLOT <cr> #GPS<cr></cr></cr>	ASCII	
	ery messages ASCII READ	#GPS <cr></cr>	ASCII	
	ery messages ASCII	#GPS <cr> Result:</cr>	ASCII	
	ery messages ASCII READ COMMAND	#GPS <cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr></cr></priorityslothex></priorityslotdec></cr>	ASCII	
	ery messages ASCII READ COMMAND TX	#GPS <cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr></cr></cr></priorityslothex></priorityslotdec></cr>	ASCII	
	ery messages ASCII READ COMMAND	#GPS <cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr></cr></cr></cr></priorityslothex></priorityslotdec></cr>	ASCII	
ET PRIORITY SLOT	ery messages ASCII READ COMMAND TX RX	#GPS <cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms</cr></cr></cr></priorityslothex></priorityslotdec></cr>	ASCII	
ET PRIORITY SLOT his command shows the current de 12ms: Highest priority, used for m 13ms: User initiated arc power con 14ms: Configuration in general 15ms: Automatic arc power contro	ery messages ASCII READ COMMAND TX RX fined DALI priority slot. There a essages that must be repeated ntrol commands and commands ol commands	#GPS <cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms</cr></cr></cr></priorityslothex></priorityslotdec></cr>	ASCII	
ET PRIORITY SLOT is command shows the current de 12ms: Highest priority, used for m 13ms: User initiated arc power con 14ms: Configuration in general 15ms: Automatic arc power contro 16ms: Lowest priority, used for qu ALI 2.0 DT8 XY-COORDINA	ery messages ASCII READ COMMAND TX RX fined DALI priority slot. There essages that must be repeated introl commands and command ol commands ery messages TES ASCII	#GPS <cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP XY:<lamp>,<power>,<x>,<y><cr></cr></y></x></power></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cr>	ASCII	NO
ET PRIORITY SLOT is command shows the current de 12ms: Highest priority, used for m 13ms: User initiated arc power con 14ms: Configuration in general 15ms: Automatic arc power contro 16ms: Lowest priority, used for qu ALI 2.0 DT8 XY-COORDINA	ery messages ASCII READ COMMAND TX RX fined DALI priority slot. There essages that must be repeated introl commands and command of commands ery messages TES ASCII WRITE	#GPS <cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP XY:<lamp>,<power>,<x>,<y><cr> #LAMP XY:<lamp>,<power>,<x>,<y><cr></cr></y></x></power></lamp></cr></y></x></power></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cr>		NO
ET PRIORITY SLOT is command shows the current de 12ms: Highest priority, used for m 13ms: User initiated arc power co 14ms: Configuration in general 15ms: Automatic arc power contro 16ms: Lowest priority, used for qu ALI 2.0 DT8 XY-COORDINA	ery messages ASCII READ COMMAND TX RX fined DALI priority slot. There essages that must be repeated introl commands and command ol commands ery messages TES ASCII	#GPS <cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP XY:<lamp>,<power>,<x>,<y><cr></cr></y></x></power></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cr>		NO
ET PRIORITY SLOT is command shows the current de 12ms: Highest priority, used for m 13ms: User initiated arc power co 14ms: Configuration in general 15ms: Automatic arc power contro 16ms: Lowest priority, used for qu ALI 2.0 DT8 XY-COORDINA	ery messages ASCII READ COMMAND TX RX fined DALI priority slot. There essages that must be repeated introl commands and command of commands ery messages TES ASCII WRITE	#GPS <cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP XY:<lamp>,<power>,<x>,<y><cr> #LAMP XY:<lamp>,<power>,<x>,<y><cr></cr></y></x></power></lamp></cr></y></x></power></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cr>		NO
ET PRIORITY SLOT is command shows the current de 12ms: Highest priority, used for m 13ms: User initiated arc power co 14ms: Configuration in general 15ms: Automatic arc power contro 16ms: Lowest priority, used for qu ALI 2.0 DT8 XY-COORDINA	ery messages ASCII READ COMMAND TX RX fined DALI priority slot. There essages that must be repeated introl commands and command of commands ery messages TES ASCII WRITE	#GPS <cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP XY:<lamp>,<power>,<x>,<y><cr> #LXY:<lamp>,<power>,<x>,<y><cr> Result:</cr></y></x></power></lamp></cr></y></x></power></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cr>		NO
ET PRIORITY SLOT is command shows the current de 12ms: Highest priority, used for m 13ms: User initiated arc power co 14ms: Configuration in general 15ms: Automatic arc power contro 16ms: Lowest priority, used for qu ALI 2.0 DT8 XY-COORDINA	ery messages ASCII READ COMMAND TX RX fined DALI priority slot. There is a sages that must be repeated introl commands and commands ery messages ATES ASCII WRITE COMMAND	#GPS <cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP XY:<lamp>,<power>,<x>,<y><cr> #LXY:<lamp>,<power>,<x>,<y><cr> Result: #OK<cr></cr></cr></y></x></power></lamp></cr></y></x></power></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cr>		NO
ET PRIORITY SLOT is command shows the current de 12ms: Highest priority, used for m 13ms: User initiated arc power co 14ms: Configuration in general 15ms: Automatic arc power contro 16ms: Lowest priority, used for qu ALI 2.0 DT8 XY-COORDINA	ery messages ASCII READ COMMAND TX RX fined DALI priority slot. There issages that must be repeated ntrol commands and commands ery messages TES ASCII WRITE COMMAND POWER	#GPS <cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT #Z55,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP XY:<lamp>,<power>,<x>,<y><cr> #LXY:<lamp>,<power>,<x>,<y><cr> Result: #OK<cr> 127 50,0 0,99997 0,99997</cr></cr></y></x></power></lamp></cr></y></x></power></lamp></cr></cr></priorityslothex></priorityslotdec></cr>		NO
ET PRIORITY SLOT is command shows the current de 12ms: Highest priority, used for m 13ms: User initiated arc power co 14ms: Configuration in general 15ms: Automatic arc power contro 16ms: Lowest priority, used for qu ALI 2.0 DT8 XY-COORDINA	ery messages ASCII READ COMMAND TX RX fined DALI priority slot. There is essages that must be repeated ntrol commands and commands ery messages NTES ASCII WRITE COMMAND POWER X Y	#GPS <cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP XY:<lamp>,<power>,<x>,<y><cr> #LXY:<lamp>,<power>,<x>,<y><cr> Result: #OK<cr> 127 50,0 0,99997 0,99997 0,99997 0,99997</cr></cr></y></x></power></lamp></cr></y></x></power></lamp></cr></cr></priorityslothex></priorityslotdec></cr>		NO
ET PRIORITY SLOT is command shows the current de 12ms: Highest priority, used for m 13ms: User initiated arc power co 14ms: Configuration in general 15ms: Automatic arc power contro 16ms: Lowest priority, used for qu ALI 2.0 DT8 XY-COORDINA	ery messages ASCII READ COMMAND TX RX fined DALI priority slot. There essages that must be repeated ntrol commands and commands ery messages TES ASCII WRITE COMMAND POWER X Y LAMP	#GPS <cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP XY:<lamp>,<power>,<x>,<y><cr> #LXY:<lamp>,<power>,<x>,<y><cr> Result: #OK<cr> 127 50,0 0,99997 0,99997 0,99997 0,99997 127 50,0 127 50,0 127 50,0 0,99997 0,99997 0,99997 0,99997 0,99997 0,99997</cr></cr></y></x></power></lamp></cr></y></x></power></lamp></cr></cr></priorityslothex></priorityslotdec></cr>		NO
16ms: Lowest priority, used for que EET PRIORITY SLOT his command shows the current de 12ms: Highest priority, used for m 13ms: User initiated arc power co 14ms: Configuration in general 15ms: Automatic arc power contro 16ms: Lowest priority, used for que ALI 2.0 DT8 XY-COORDINA OR LAMP	ery messages ASCII READ COMMAND TX RX fined DALI priority slot. There is essages that must be repeated ntrol commands and commands ery messages NTES ASCII WRITE COMMAND POWER X Y	#GPS <cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP XY:<lamp>,<power>,<x>,<y><cr> #LXY:<lamp>,<power>,<x>,<y><cr> Result: #OK<cr> 127 50,0 0,99997 0,99997 0,99997 0,99997</cr></cr></y></x></power></lamp></cr></y></x></power></lamp></cr></cr></priorityslothex></priorityslotdec></cr>		NO

Sets for a lamp short address a new brightness level between 0 and 254. 255 means MASK. This command sets in addition the new XY colour coordinate for a DALI device type 8 lamp with XY coordinate colour dimming feature. The XY coordinate is in the range 0.0 to 0.99997

DALI 2.0 DT8 XY-COORDINATES FOR LAMP	ASCII WRITE COMMAND	#LAMP XY DIGITS: <lamp>,<power>, #LXYD:<lamp>,<power><xdigit>,<y Result: #OK<cr></cr></y </xdigit></power></lamp></power></lamp>	•	ASCII	NO
	POWER	127	50,0		
	XDIGIT	65534	0,99997		
	YDIGIT	65534	0,99997		
	LAMP	10:LAMP SHORT ADDRESS 11			
	ТХ	#LAMP XY DIGITS:10,127,65534,65534<	LAMP XY DIGITS:10,127,65534,65534 <cr></cr>		
	RX	N/A			
Sets for a lamp short address a new brightne	ess level between 0 and	254. 255 means MASK. This command sets in additio	n the new XY colour coordinate for a DALI device type 8 lamp with XY coordir	nate colour dimming	feature. The XY

coordinate is in the range of 0 to 65534. 65535 means mask. The real XY coordinate is calulated with digits/65536.

DALI 2.0 DT8 QUERY	ASCII	#LAMP QUERY XY: <lamp><cr></cr></lamp>	ASCII	YES
Y-COORDINATES	WRITE	#LQXY: <lamp><cr></cr></lamp>		
OR LAMP	COMMAND	Result:		
		#LQXY: <lamp>,<power>,<x>,<kelvin><cr></cr></kelvin></x></power></lamp>		
	LAMP	10:LAMP SHORT ADDRESS 11		
	ТХ	#LAMP QUERY XY:10 <cr></cr>		
	RX	#255,LQXYC:10,0,65534,33023,0xFFFE,0x80FF,0.00002,0.00003 <cr></cr>		
		Current lamp short address:10->LAMP SHORT ADDRESS 11		
		Current arc power level:0->0,00%		
		Current X coordinate in digits:65534		
		Current Y coordinate in digits:33023		
		Current X coordinate:0.00002		
		Current Y coordinate:0.00003		

OALI BUS ERROR	ASCII	#DALI BUS ERROR <cr></cr>	ASCII	
	READ	#DBERR <cr></cr>		
	COMMAND	Result:		
		#DBERR: <buserrdec>,<buserrhex><cr></cr></buserrhex></buserrdec>		
	ТХ	#DALI BUS ERROR <cr></cr>		
	RX	#255,DBERR:0,0x0 <cr></cr>		
		0:BUS IS OK		
hows the status of the DALI bus. usErrDec,BusErrHex: 0:DALI bus is OK and fully functiona 1:DALI bus error, short circuit on DA		or no DALL power supply		
ET PRIORITY SLOT	ASCII	#SET PRIORITY SLOT: <slot><cr></cr></slot>	ASCII	NO
ET FRIORITT SEOT	WRITE	#SPS: <slot><cr></cr></slot>	ASCII	NO
	COMMAND	Result:		
	COMMAND	#OK <cr></cr>		
	CL OT			
	SLOT	3		
	ТХ	#SET PRIORITY SLOT:3 <cr></cr>		
	RX	N/A		
12ms: Highest priority, used for me 13ms: User initiated arc power con 14ms: Configuration in general 15ms: Automatic arc power control	commands	d Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time)		
12ms: Highest priority, used for me 13ms: User initiated arc power con 14ms: Configuration in general 15ms: Automatic arc power control 16ms: Lowest priority, used for que	essages that must be repeated trol commands and commands commands ery messages	d Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time)		
: 12ms: Highest priority, used for me : 13ms: User initiated arc power con : 14ms: Configuration in general : 15ms: Automatic arc power control : 16ms: Lowest priority, used for que	essages that must be repeated trol commands and command commands ery messages ASCII	d Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr></cr>	ASCII	
12ms: Highest priority, used for me 13ms: User initiated arc power con 14ms: Configuration in general 15ms: Automatic arc power control 16ms: Lowest priority, used for que	essages that must be repeated trol commands and command ery messages ASCII READ	d Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr> #GPS<cr></cr></cr>	ASCII	
12ms: Highest priority, used for me 13ms: User initiated arc power con 14ms: Configuration in general 15ms: Automatic arc power control 16ms: Lowest priority, used for que	essages that must be repeated trol commands and command commands ery messages ASCII	d Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr> #GPS<cr> Result:</cr></cr>	ASCII	
12ms: Highest priority, used for me 13ms: User initiated arc power con 14ms: Configuration in general 15ms: Automatic arc power control 16ms: Lowest priority, used for que	essages that must be repeated trol commands and command ery messages ASCII READ COMMAND	d Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr></cr></priorityslothex></priorityslotdec></cr></cr>	ASCII	
12ms: Highest priority, used for me 13ms: User initiated arc power con 14ms: Configuration in general 15ms: Automatic arc power control 16ms: Lowest priority, used for que	essages that must be repeated trol commands and command ery messages ASCII READ COMMAND TX	d Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>	ASCII	
12ms: Highest priority, used for me 13ms: User initiated arc power con 14ms: Configuration in general 15ms: Automatic arc power control 16ms: Lowest priority, used for que	essages that must be repeated trol commands and command ery messages ASCII READ COMMAND	d Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr></cr></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>	ASCII	
: 12ms: Highest priority, used for me : 13ms: User initiated arc power con : 14ms: Configuration in general : 15ms: Automatic arc power control : 16ms: Lowest priority, used for que GET PRIORITY SLOT	essages that must be repeated trol commands and command ery messages ASCII READ COMMAND TX RX	d Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #GET PRIORITY SLOT<cr> Current priority slot:2->14ms</cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>	ASCII	
: 12ms: Highest priority, used for me : 13ms: User initiated arc power con : 14ms: Configuration in general : 15ms: Automatic arc power control : 16ms: Lowest priority, used for que SET PRIORITY SLOT his command shows the current def : 12ms: Highest priority, used for me : 13ms: User initiated arc power con : 14ms: Configuration in general	ASCII READ COMMAND TX RX ined DALI priority slot. There a essages that must be repeated trol commands and command	d Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #GET PRIORITY SLOT<cr> #GET PRIORITY SLOT<cr> are 5 slots available:</cr></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>	ASCII	
: 12ms: Highest priority, used for me : 13ms: User initiated arc power con : 14ms: Configuration in general : 15ms: Automatic arc power control : 16ms: Lowest priority, used for que GET PRIORITY SLOT his command shows the current def : 12ms: Highest priority, used for me : 13ms: User initiated arc power con : 14ms: Configuration in general : 15ms: Automatic arc power control : 16ms: Lowest priority, used for que	ASCII READ COMMAND TX RX ined DALI priority slot. There issages that must be repeated trol commands and command commands and command ASCII	d Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP TC MIREK:<lamp>,<power>,<mirek><cr></cr></mirek></power></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>	ASCII	
12ms: Highest priority, used for me 13ms: User initiated arc power con 14ms: Configuration in general 15ms: Automatic arc power control 16ms: Lowest priority, used for que ET PRIORITY SLOT is command shows the current def 12ms: Highest priority, used for me 13ms: User initiated arc power con 14ms: Configuration in general 15ms: Automatic arc power control 16ms: Lowest priority, used for que ALI 2.0 DT8 CW-WW	ASCII READ COMMAND TX RX ined DALI priority slot. There is ssages that must be repeated trol commands and command commands ery messages	d Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time)</cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>		NO
12ms: Highest priority, used for me 13ms: User initiated arc power con 14ms: Configuration in general 15ms: Automatic arc power control 16ms: Lowest priority, used for que ET PRIORITY SLOT is command shows the current def 12ms: Highest priority, used for me 13ms: User initiated arc power con 14ms: Configuration in general 15ms: Automatic arc power control 16ms: Lowest priority, used for que ALI 2.0 DT8 CW-WW	ASCII READ COMMAND TX RX ined DALI priority slot. There issages that must be repeated trol commands and command commands and command ASCII	d Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP TC MIREK:<lamp>,<power>,<mirek><cr></cr></mirek></power></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>		NO
12ms: Highest priority, used for me 13ms: User initiated arc power con 14ms: Configuration in general 15ms: Automatic arc power control 16ms: Lowest priority, used for que ET PRIORITY SLOT is command shows the current def 12ms: Highest priority, used for me 13ms: User initiated arc power con 14ms: Configuration in general 15ms: Automatic arc power control 16ms: Lowest priority, used for que ALI 2.0 DT8 CW-WW	ASCII RX Interference of the server of the s	d ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d swhich have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP TC MIREK:<lamp>,<power>,<mirek><cr> #LTCM:<lamp>,<power><mirek><cr></cr></mirek></power></lamp></cr></mirek></power></lamp></cr></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>		NO
12ms: Highest priority, used for me 13ms: User initiated arc power con 14ms: Configuration in general 15ms: Automatic arc power control 16ms: Lowest priority, used for que ET PRIORITY SLOT is command shows the current def 12ms: Highest priority, used for me 13ms: User initiated arc power con 14ms: Configuration in general 15ms: Automatic arc power control 16ms: Lowest priority, used for que ALI 2.0 DT8 CW-WW	ASCII RX Interference of the server of the s	d ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #GET PRIORITY SLOT<cr> #GET PRIORITY SLOT<cr> #GET PRIORITY SLOT<cr> #GET PRIORITY SLOT<cr> #Current priority slot:2->14ms are 5 slots available: d s which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP TC MIREK:<lamp>,<power>,<mirek><cr> #LTCM:<lamp>,<power><mirek><cr> Result: #LAMP TC MIREK:<lamp>,<power>,<mirek><cr> Result:</cr></mirek></power></lamp></cr></mirek></power></lamp></cr></mirek></power></lamp></cr></cr></cr></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>		NO
12ms: Highest priority, used for me 13ms: User initiated arc power con 14ms: Configuration in general 15ms: Automatic arc power control 16ms: Lowest priority, used for que EET PRIORITY SLOT his command shows the current def 12ms: Highest priority, used for me 13ms: User initiated arc power con 14ms: Configuration in general 15ms: Automatic arc power control 16ms: Lowest priority, used for que ALI 2.0 DT8 CW-WW	ASCII READ COMMAND TX RX ined DALI priority slot. There is essages that must be repeated trol commands and command commands and command ery messages ASCII WRITE COMMAND	d ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #GET PRIORITY SLOT<cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d swhich have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP TC MIREK:<lamp>,<power>,<mirek><cr> #LAMP TC MIREK:</cr></mirek></power></lamp></cr></mirek></power></lamp></cr></mirek></power></lamp></cr></mirek></power></lamp></cr></mirek></power></lamp></cr></mirek></power></lamp></cr></mirek></power></lamp></cr></mirek></power></lamp></cr></mirek></power></lamp></cr></mirek></power></lamp></cr></cr></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>		NO
12ms: Highest priority, used for me 13ms: User initiated arc power con 14ms: Configuration in general 15ms: Automatic arc power control 16ms: Lowest priority, used for que EET PRIORITY SLOT his command shows the current def 12ms: Highest priority, used for me 13ms: User initiated arc power con 14ms: Configuration in general 15ms: Automatic arc power control 16ms: Lowest priority, used for que ALI 2.0 DT8 CW-WW	ASCII READ COMMAND TX RX ined DALI priority slot. There is essages that must be repeated trol commands and command commands and command commands ery messages ASCII WRITE COMMAND POWER MIREK	d ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr> #GPS<cp> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP TC MIREK:<lamp>,<power>,<mirek><cr> #LAMP TC MIREK:<lamp>,<power>,<mirek><cr> #LTCM:<lamp>,<power>,<mirek><cr> #LTCM:<lamp>,<power>,<mirek><cr> #CTCM:<lamp>,<power>,<mirek><cr> #LTCM:<lamp>,<power>,<mirek><cr> #LTCM:<lamp>,<power>,<mirek><cr> #DK<</cr></mirek></power></lamp></cr></mirek></power></lamp></cr></mirek></power></lamp></cr></mirek></power></lamp></cr></mirek></power></lamp></cr></mirek></power></lamp></cr></mirek></power></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cp></cr>		NO
12ms: Highest priority, used for me 13ms: User initiated arc power con 14ms: Configuration in general 15ms: Automatic arc power control 16ms: Lowest priority, used for que EET PRIORITY SLOT his command shows the current def 12ms: Highest priority, used for me 13ms: User initiated arc power con 14ms: Configuration in general 15ms: Automatic arc power control 16ms: Lowest priority, used for que ALI 2.0 DT8 CW-WW	ASCII READ COMMAND TX RX ined DALI priority slot. There issages that must be repeated trol commands and command commands ery messages ASCII WRITE COMMAND POWER MIREK LAMP	d ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr> #GPS<cp> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #Z55,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d s which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP TC MIREK:<lamp>,<power>,<mirek><cr> #LAMP TC MIREK:<lamp>,<power>,<mirek><cr> #LTCM:<lamp>,<power><mirek><cr> Result: #OK<cr> 127 50,0 9090 110 4:LAMP SHORT ADDRESS 5</cr></cr></mirek></power></lamp></cr></mirek></power></lamp></cr></mirek></power></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cp></cr>		NO
: 12ms: Highest priority, used for me : 13ms: User initiated arc power con : 14ms: Configuration in general : 15ms: Automatic arc power control : 16ms: Lowest priority, used for que GET PRIORITY SLOT his command shows the current def : 12ms: Highest priority, used for me	ASCII READ COMMAND TX RX ined DALI priority slot. There is essages that must be repeated trol commands and command commands and command commands ery messages ASCII WRITE COMMAND POWER MIREK	d ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #GET PRIORITY SLOT <cr> #GPS<cp> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP TC MIREK:<lamp>,<power>,<mirek><cr> #LAMP TC MIREK:<lamp>,<power>,<mirek><cr> #LTCM:<lamp>,<power>,<mirek><cr> #LTCM:<lamp>,<power>,<mirek><cr> #Current priority slot:2->1 127 50,0 9090 110</cr></mirek></power></lamp></cr></mirek></power></lamp></cr></mirek></power></lamp></cr></mirek></power></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cp></cr>		NO

ASCII RESI-DALI-SIO, ETH DT8 Tc Commands

DALI 2.0 DT8 CW-WW	ASCII		ASCII	NO
FOR LAMP	WRITE	#LAMP TC KELVIN: <lamp>,<power>,<kelvin><cr> #LTCK:<lamp>,<power><kelvin><cr></cr></kelvin></power></lamp></cr></kelvin></power></lamp>	ASCII	NO
FOR LAWF	COMMAND	Result:		
	COMINIAND	#OK <cr></cr>		
	POWER	127 50,0		
	KELVIN	16,000 16		
	LAMP	4:LAMP SHORT ADDRESS 5		
	TX	#LAMP TC KELVIN:4,127,16 <cr></cr>		
	RX	N/A		
Sets for a lamp short address a new l		1 254. 255 means MASK. This command sets in addition the new KELVIN level between 16 and 1000000 for a DALI device type 8	lamp with CW-WW di	mming feature
	singinarioso lovor bottivoon o ant			ining locatoro.
DALI 2.0 DT8 CW-WW	ASCII	#GROUP TC MIREK: <group>,<power>,<mirek><cr></cr></mirek></power></group>	ASCII	NO
FOR LAMP GROUP	WRITE	#GTCM: <group>,<power><mirek><cr></cr></mirek></power></group>		
	COMMAND	Result:		
		#OK <cr></cr>		
	POWER	<mark>127</mark> 50,0		
	MIREK	9090 110		
	GROUP	8:LAMP GROUP 9		
	TX	#GROUP TC MIREK:8,127,9090 <cr></cr>		
	PY	N/A		
Sets for a lamp group a new brightne	ss level between 0 and 254. 2	55 means MASK. This command sets in addition the new MIREK level between 1 and 65534 for a DALI device type 8 lamp group	with CW-WW dimmin	g feature. 65535
DALI 2.0 DT8 CW-WW	ASCII	#GROUP TC KELVIN: <group>,<power>,<kelvin><cr></cr></kelvin></power></group>	ASCII	NO
FOR LAMP GROUP	WRITE	#GTCK: <group>,<power><kelvin><cr></cr></kelvin></power></group>		
	COMMAND	Result:		
		#OK <cr></cr>		
	POWER	<mark>127</mark> 50,0		
	KELVIN	<mark>16,000 16 16 16 16 16 16 16 16 16 16 16 16 16 </mark>		
	GROUP	8:LAMP GROUP 9		
	ТХ	#GROUP TC KELVIN:8,127,16 <cr></cr>		
	RX	N/A		
Sets for a lamp group a new brightne	ss level between 0 and 254. 2	55 means MASK. This command sets in addition the new KELVIN level between 16 and 1000000 for a DALI device type 8 lamp gr	oup with CW-WW dim	ming feature.
DALI 2.0 DT8 CW-WW	ASCII			NO
FOR ALL LAMPS	WRITE	#ALL TC MIREK: <power>,<mirek><cr> #ATCM:<power><mirek><cr></cr></mirek></power></cr></mirek></power>	ASCII	NO
	COMMAND	Result:		
	COMINAND	#OK <cr></cr>		
	POWER	127 50,0		
	MIREK	9090 110		
	TX	#ALL TC MIREK:127,9090 <cr></cr>		
	RX	#ALL TC MIRER.127,9090 <cr> N/A</cr>		
Sets for all lamps a new brightness le		neans MASK. This command sets in addition the new MIREK level between 1 and 65534 for all DALI device type 8 lamps with CW	-WW dimming feature	65535 means
MASK for MIREK level.	wer between o and 204. 200 h	וימוס אוי וסוג. דווס נסווווומות סכנס וו מתמווסר מוכ רוכש אוווגבוג וכעבו שבושכנו ב מות ססססיי וסו מו שאבו תבעוב נעוב נעוב אוווים שוווים אווור כא	www.unningicaluit	
DALI 2.0 DT8 CW-WW	ASCII	#ALL TC KELVIN: <power>,<kelvin><cr></cr></kelvin></power>	ASCII	NO
FOR ALL LAMPS	WRITE	#ATCK: <power><kelvin><cr></cr></kelvin></power>		
_	COMMAND	Result:		
		#OK <cr></cr>	1	

ELVIN	16,000 16		
X	#ALL TC KELVIN:127,16 <cr></cr>		
X	N/A		
n 0 and 254. 255 me	ans MASK. This command sets in addition the new KELVIN level between 16 and 1000000 for all DALI device type 8 lamps with CW	-WW dimming featu	ire.
SCII	#LAMP OUERY TC: <lamp><cr></cr></lamp>	ASCII	YES
VRITE			
OMMAND	Result:		
	#LQTC: <lamp>,<power>,<mirek>,<kelvin><cr></cr></kelvin></mirek></power></lamp>		
AMP	0:LAMP SHORT ADDRESS 1		
X	#LAMP QUERY TC:0 <cr></cr>		
X	#255,LQTC:0,127,150,0x0096,6666.667 <cr></cr>		
	Current lamp short address:0->LAMP SHORT ADDRESS 1		
	Current arc power level:127->50,00%		
	Current colour temperature Tc in Mirek:150		
	Current colour temperature Tc in Kelvin:6666.667		
	X 1 0 and 254. 255 me SCII /RITE OMMAND AMP X	X N/A 0 and 254. 255 means MASK. This command sets in addition the new KELVIN level between 16 and 1000000 for all DALI device type 8 lamps with CW SCII #LAMP QUERY TC: <lamp><cr> /RITE #LQTC:<lamp><cr> OMMAND Result: #LQTC:<lamp>,<power>,<mirek>,<kelvin><cr> AMP 0:LAMP SHORT ADDRESS 1 X #LAMP QUERY TC:0<cr> X #255,LQTC:0,127,150,0x0096,6666.667<cr> Current lamp short address:0->LAMP SHORT ADDRESS 1 Current arc power level:127->50,00% Current colour temperature Tc in Mirek:150</cr></cr></cr></kelvin></mirek></power></lamp></cr></lamp></cr></lamp>	X N/A 10 and 254. 255 means MASK. This command sets in addition the new KELVIN level between 16 and 1000000 for all DALI device type 8 lamps with CW-WW dimming featu SCII #LAMP QUERY TC: <lamp><cr> ASCII //RITE #LQTC:<lamp><cr> ASCII OMMAND Result: #LQTC:<lamp>,<power>,<mirek>,<kelvin><cr> AMP 0:LAMP SHORT ADDRESS 1 Image: Comparison of the comparison of the</cr></kelvin></mirek></power></lamp></cr></lamp></cr></lamp>

DALI BUS ERROR	ASCII READ	#DALI BUS ERROR <cr> #DBERR<cr></cr></cr>	ASCII	
	COMMAND	Result:		
		#DBERR: <buserrdec>,<buserrhex><cr></cr></buserrhex></buserrdec>		
	ТХ	#DALI BUS ERROR <cr></cr>		
	RX	#255,DBERR:0,0x0 <cr></cr>		
		0:BUS IS OK		
Shows the status of the DALI bus. BusErrDec,BusErrHex: =0:DALI bus is OK and fully functional		0.20010 01		
=1:DALI bus error, short circuit on DAI	LI line, overload on DALI line	or no DALI power supply		
SET PRIORITY SLOT	ASCII	#SET PRIORITY SLOT: <slot><cr></cr></slot>	ASCII	NO
	WRITE	#SPS: <slot><cr></cr></slot>		
	COMMAND	Result:		
		#OK <cr></cr>		
	SLOT	3		
	ТХ	#SET PRIORITY SLOT:3 <cr></cr>		
This command defines the DALI prior	RX	N/A		
0: 12ms: Highest priority, used for me	ssages that must be repeated	d Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time)		
2: 14ms: Configuration in general 3: 15ms: Automatic arc power control	commands	is which have an impact on manual aujustment of arc power (e.g., unn up with a special fate time)		
 2: 14ms: Configuration in general 3: 15ms: Automatic arc power control 4: 16ms: Lowest priority, used for que 	commands ry messages		ASCII	
 2: 14ms: Configuration in general 3: 15ms: Automatic arc power control 4: 16ms: Lowest priority, used for que 	commands ry messages ASCII	#GET PRIORITY SLOT <cr></cr>	ASCII	
 2: 14ms: Configuration in general 3: 15ms: Automatic arc power control 4: 16ms: Lowest priority, used for que 	commands ry messages ASCII READ	#GET PRIORITY SLOT <cr> #GPS<cr></cr></cr>	ASCII	
 2: 14ms: Configuration in general 3: 15ms: Automatic arc power control 4: 16ms: Lowest priority, used for que 	commands ry messages ASCII	#GET PRIORITY SLOT <cr> #GPS<cr> Result:</cr></cr>	ASCII	
 2: 14ms: Configuration in general 3: 15ms: Automatic arc power control 4: 16ms: Lowest priority, used for que 	commands ry messages ASCII READ COMMAND	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr></cr></priorityslothex></priorityslotdec></cr></cr>	ASCII	
 2: 14ms: Configuration in general 3: 15ms: Automatic arc power control 4: 16ms: Lowest priority, used for que 	commands ry messages ASCII READ COMMAND TX	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>	ASCII	
1: 13ms: User initiated arc power cont 2: 14ms: Configuration in general 3: 15ms: Automatic arc power control 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT	commands ry messages ASCII READ COMMAND	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>	ASCII	
2: 14ms: Configuration in general 3: 15ms: Automatic arc power control 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT Fhis command shows the current defin 0: 12ms: Highest priority, used for mes	commands ry messages ASCII READ COMMAND TX RX ned DALI priority slot. There ssages that must be repeated	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available:</cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>	ASCII	
2: 14ms: Configuration in general 3: 15ms: Automatic arc power control 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT Fhis command shows the current defin 0: 12ms: Highest priority, used for mes 1: 13ms: User initiated arc power cont 2: 14ms: Configuration in general	commands ry messages ASCII READ COMMAND TX RX ned DALI priority slot. There ssages that must be repeated rol commands and command	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available:</cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>	ASCII	
2: 14ms: Configuration in general 3: 15ms: Automatic arc power control 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT This command shows the current defin 1: 12ms: Highest priority, used for mes 1: 13ms: User initiated arc power cont 2: 14ms: Configuration in general 3: 15ms: Automatic arc power control 4: 16ms: Lowest priority, used for que DALI 2.0 DT8 PRIMARY N	commands ry messages ASCII READ COMMAND TX RX ned DALI priority slot. There ssages that must be repeated rol commands and command ry messages ASCII	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available:] Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP PRIMARY N:<lamp>,<power>,<channel1>,<channel2>,<channel3>,</channel3></channel2></channel1></power></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>	ASCII	YES
2: 14ms: Configuration in general 3: 15ms: Automatic arc power control 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT This command shows the current defin 1: 12ms: Highest priority, used for mest 1: 13ms: User initiated arc power cont 2: 14ms: Configuration in general 3: 15ms: Automatic arc power control 4: 16ms: Lowest priority, used for que DALI 2.0 DT8 PRIMARY N CHANNELS	commands ry messages ASCII READ COMMAND TX RX Ned DALI priority slot. There ssages that must be repeated rol commands and command ry messages	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available:] Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP PRIMARY N:<lamp>,<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr></cr></channel6></channel5></channel4></channel3></channel2></channel1></power></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>		YES
2: 14ms: Configuration in general 3: 15ms: Automatic arc power control 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT This command shows the current defin 1: 12ms: Highest priority, used for mest 1: 13ms: User initiated arc power cont 2: 14ms: Configuration in general 3: 15ms: Automatic arc power control 4: 16ms: Lowest priority, used for que DALI 2.0 DT8 PRIMARY N CHANNELS	commands ry messages ASCII READ COMMAND TX RX ned DALI priority slot. There ssages that must be repeated rol commands and command ry messages ASCII	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: 1 s which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP PRIMARY N:<lamp>,<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel1>,<channel3>, <channel4>,<channel5>,<channel1>,<channel3>,</channel3></channel1></channel5></channel4></channel3></channel1></channel5></channel4></channel3></channel2></channel1></channel5></channel4></channel3></channel2></channel1></power></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>		YES
2: 14ms: Configuration in general 3: 15ms: Automatic arc power control 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT This command shows the current defin 1: 12ms: Highest priority, used for mest 1: 13ms: User initiated arc power cont 2: 14ms: Configuration in general 3: 15ms: Automatic arc power control 4: 16ms: Lowest priority, used for que DALI 2.0 DT8 PRIMARY N CHANNELS	commands ry messages	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d s which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP PRIMARY N:<lamp>,<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> #LPN:<lamp>,<power>,<channel5>,<channel2>,<channel3>, <channel4>,<channel5>,<channel5>,<channel6><cr> #LPN:<lamp>,<power>,<channel5>,<channel6><cr> Result: #OK<cr></cr></cr></channel6></channel5></power></lamp></cr></channel6></channel5></channel5></channel4></channel3></channel2></channel5></power></lamp></cr></channel6></channel5></channel4></channel3></channel2></channel1></power></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>		YES
2: 14ms: Configuration in general 3: 15ms: Automatic arc power control 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT This command shows the current defin 1: 12ms: Highest priority, used for mest 1: 13ms: User initiated arc power cont 2: 14ms: Configuration in general 3: 15ms: Automatic arc power control 4: 16ms: Lowest priority, used for que DALI 2.0 DT8 PRIMARY N CHANNELS	commands ry messages ASCII READ COMMAND TX RX ned DALI priority slot. There ssages that must be repeater rol commands and command commands and command ry messages ASCII WRITE	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: J Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP PRIMARY N:<lamp>,<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> #LPN:<lamp>,<power>,<channel1>,<channel3>, <channel4>,<channel5>,<channel6><cr> #LPN:<lamp>,<power>,<channel5>,<channel6><cr> Result:</cr></channel6></channel5></power></lamp></cr></channel6></channel5></channel4></channel3></channel1></power></lamp></cr></channel6></channel5></channel4></channel3></channel2></channel1></power></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>		YES
2: 14ms: Configuration in general 3: 15ms: Automatic arc power control 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT This command shows the current defin 1: 12ms: Highest priority, used for mest 1: 13ms: User initiated arc power cont 2: 14ms: Configuration in general 3: 15ms: Automatic arc power control 4: 16ms: Lowest priority, used for que DALI 2.0 DT8 PRIMARY N CHANNELS	commands ry messages	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d s which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP PRIMARY N:<lamp>,<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> #LPN:<lamp>,<power>,<channel5>,<channel2>,<channel3>, <channel4>,<channel5>,<channel5>,<channel6><cr> #LPN:<lamp>,<power>,<channel5>,<channel6><cr> Result: #OK<cr></cr></cr></channel6></channel5></power></lamp></cr></channel6></channel5></channel5></channel4></channel3></channel2></channel5></power></lamp></cr></channel6></channel5></channel4></channel3></channel2></channel1></power></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>		YES
2: 14ms: Configuration in general 3: 15ms: Automatic arc power control 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT This command shows the current defin 1: 12ms: Highest priority, used for mest 1: 13ms: User initiated arc power cont 2: 14ms: Configuration in general 3: 15ms: Automatic arc power control 4: 16ms: Lowest priority, used for que DALI 2.0 DT8 PRIMARY N CHANNELS	commands ry messages ASCII READ COMMAND TX RX ned DALI priority slot. There ssages that must be repeater rol commands and command commands ry messages ASCII WRITE COMMAND POWER	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GET PRIORITY SLOT<cr> #GET PRIORITY SLOT<cr> #Z55,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: j js which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP PRIMARY N:<lamp>,<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> #LPN:<lamp>,<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> #LOMP >,<power>,<channel6><cr> #LOMP >,<power>,<channel6><cr> #CHANNEL4>,<channel5>,<channel6><cr> Result: #OK<<cr> 127 50,0</cr></cr></channel6></channel5></cr></channel6></power></cr></channel6></power></cr></channel6></channel5></channel4></channel3></channel2></channel1></power></lamp></cr></channel6></channel5></channel4></channel3></channel2></channel1></power></lamp></cr></cr></cr></cr></cr>		YES
2: 14ms: Configuration in general 3: 15ms: Automatic arc power control 4: 16ms: Lowest priority, used for que GET PRIORITY SLOT This command shows the current defin 0: 12ms: Highest priority, used for mes	commands ry messages ASCII READ COMMAND TX RX Note: TX RX RX Note: TX RX	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GET PRIORITY SLOT<cr> #GET PRIORITY SLOT<cr> #Z55,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: j s which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP PRIMARY N:<lamp>,<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> #LPN:<lamp>,<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> #cHNNEL4>,<channel5>,<channel6><cr> Result: #OK<cr> 127 50,0 6:CHANNEL 1-6</cr></cr></channel6></channel5></cr></channel6></channel5></channel4></channel3></channel2></channel1></power></lamp></cr></channel6></channel5></channel4></channel3></channel2></channel1></power></lamp></cr></cr></cr></cr></cr>		YES

1	CHANNEL4	<mark>26214</mark> 40,0		
	CHANNEL5	32767 50,0		
	CHANNEL6	39321 60,0		
	LAMP	1:LAMP SHORT ADDRESS 2		
	TX	#LAMP PRIMARY N:1,127,6554,13107,19661,26214,32767,39321 <cr></cr>		
	RX	N/A		
Sets for a lamp short address a new bri feature. 65535 means MASK for chann HINT: The amount of command argume	ightness level between 0 an el level. ents depends on how many	d 254. 255 means MASK. This command sets in addition for up to 6 channels the new level between 0 and 65534 for a DALI dev channels are written!	vice type 8 lamp with pri	mary N colour
DALI 2.0 DT8 PRIMARY N CHANNELS FOR LAMP GROUP	ASCII WRITE COMMAND	#GROUP PRIMARY N: <group>,<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> #GPN:<group>,<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> Result: #OK<cr></cr></cr></channel6></channel5></channel4></channel3></channel2></channel1></power></group></cr></channel6></channel5></channel4></channel3></channel2></channel1></power></group>	ASCII	NO
	POWER	<mark>127</mark> 50,0		
	CHANNELS	6:CHANNEL 1-6		
	CHANNEL1	<mark>6554 10,0 10,0 10,0 10,0 10,0 10,0 10,0 10,</mark>		
	CHANNEL2	<mark>13107</mark> 20,0		
	CHANNEL3	<mark>19661</mark> 30,0		
	CHANNEL4	<mark>26214</mark> 40,0		
	CHANNEL5	<mark>32767 50,0 50,0 50,0 50,0 50,0 50,0 50,0 50,</mark>		
	CHANNEL6	<mark>39321 60,0</mark>		
	GROUP	8:LAMP GROUP 9		
	ТХ	8:LAMP GROUP 9 #GROUP PRIMARY N:8,127,6554,13107,19661,26214,32767,39321 <cr></cr>		
	TX BX	8:LAMP GROUP 9 #GROUP PRIMARY N:8,127,6554,13107,19661,26214,32767,39321 <cr></cr>		
HINT: The amount of command argume	TX RX s level between 0 and 254. 2 el level. ents depends on how many	8:LAMP GROUP 9 #GROUP PRIMARY N:8,127,6554,13107,19661,26214,32767,39321 <cr> N/A 55 means MASK. This command sets in addition for up to 6 channels the new level between 0 and 65534 for a DALI device type channels are written!</cr>		
Sets for a lamp group a new brightness feature. 65535 means MASK for chann HINT: The amount of command argum DALI 2.0 DT8 PRIMARY N CHANNELS FOR ALL LAMPS	TX RX s level between 0 and 254. 2 el level.	8:LAMP GROUP 9 #GROUP PRIMARY N:8,127,6554,13107,19661,26214,32767,39321 <cr> N/A 55 means MASK. This command sets in addition for up to 6 channels the new level between 0 and 65534 for a DALI device type channels are written! #ALL PRIMARY N:<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> #APN:<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> #APN:<power>,<channel5>,<channel6><cr> Result: #OK<cr></cr></cr></channel6></channel5></power></cr></channel6></channel5></channel4></channel3></channel2></channel1></channel4></channel3></channel2></channel1></power></cr></channel6></channel5></channel4></channel3></channel2></channel1></power></cr>	e 8 lamp group with prin	nary N colour
HINT: The amount of command argume DALI 2.0 DT8 PRIMARY N CHANNELS	TX RX el level between 0 and 254. 2 el level. ents depends on how many ASCII WRITE COMMAND	8:LAMP GROUP 9 #GROUP PRIMARY N:8,127,6554,13107,19661,26214,32767,39321 <cr> N/A 55 means MASK. This command sets in addition for up to 6 channels the new level between 0 and 65534 for a DALI device type channels are written! #ALL PRIMARY N:<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> #APN:<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> #APN:<power>,<channel5>,<channel6><cr> Result: #OK<cr> 127</cr></cr></channel6></channel5></power></cr></channel6></channel5></channel4></channel3></channel2></channel1></power></cr></channel6></channel5></channel4></channel3></channel2></channel1></power></cr>		
HINT: The amount of command argume DALI 2.0 DT8 PRIMARY N CHANNELS	TX RX el level between 0 and 254. 2 el level. ents depends on how many ASCII WRITE COMMAND POWER CHANNELS	8:LAMP GROUP 9 #GROUP PRIMARY N:8,127,6554,13107,19661,26214,32767,39321 <cr> N/A 55 means MASK. This command sets in addition for up to 6 channels the new level between 0 and 65534 for a DALI device type channels are written! #ALL PRIMARY N:<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> #APN:<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> Result: #OK<cr> 127 50,0 6:CHANNEL 1-6</cr></cr></channel6></channel5></channel4></channel3></channel2></channel1></power></cr></channel6></channel5></channel4></channel3></channel2></channel1></power></cr>		
HINT: The amount of command argume DALI 2.0 DT8 PRIMARY N CHANNELS	TX RX el level between 0 and 254. 2 el level. ents depends on how many ASCII WRITE COMMAND POWER CHANNELS CHANNEL1	8:LAMP GROUP 9 #GROUP PRIMARY N:8,127,6554,13107,19661,26214,32767,39321 <cr> N/A 55 means MASK. This command sets in addition for up to 6 channels the new level between 0 and 65534 for a DALI device type channels are written! #ALL PRIMARY N:<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> #APN:<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> #APN:<power>,<channel5>,<channel6><cr> Result: #OK<cr> 127 50,0 6:CHANNEL 1-6 6554 10,0</cr></cr></channel6></channel5></power></cr></channel6></channel5></channel4></channel3></channel2></channel1></power></cr></channel6></channel5></channel4></channel3></channel2></channel1></power></cr>		
HINT: The amount of command argume DALI 2.0 DT8 PRIMARY N CHANNELS	TX RX el level between 0 and 254. 2 el level. ents depends on how many ASCII WRITE COMMAND POWER CHANNELS CHANNEL1 CHANNEL2	8:LAMP GROUP 9 #GROUP PRIMARY N:8,127,6554,13107,19661,26214,32767,39321 <cr> N/A 55 means MASK. This command sets in addition for up to 6 channels the new level between 0 and 65534 for a DALI device type channels are written! #ALL PRIMARY N:<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> #APN:<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> #APN:<power>,<channel6><cr> Result: #OK<cr> 127 50,0 6:CHANNEL 1-6 6554 10,0 13107 20,0</cr></cr></channel6></power></cr></channel6></channel5></channel4></channel3></channel2></channel1></power></cr></channel6></channel5></channel4></channel3></channel2></channel1></power></cr>		
HINT: The amount of command argume DALI 2.0 DT8 PRIMARY N CHANNELS	TX RX el level between 0 and 254. 2 el level. ents depends on how many ASCII WRITE COMMAND POWER CHANNELS CHANNEL1	8:LAMP GROUP 9 #GROUP PRIMARY N:8,127,6554,13107,19661,26214,32767,39321 <cr> N/A 55 means MASK. This command sets in addition for up to 6 channels the new level between 0 and 65534 for a DALI device type channels are written! #ALL PRIMARY N:<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> #APN:<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> #OK<cr> 127 50,0 6:CHANNEL 1-6 6554 10,0 13107 20,0 19661 30,0</cr></cr></channel6></channel5></channel4></channel3></channel2></channel1></power></cr></channel6></channel5></channel4></channel3></channel2></channel1></power></cr>		
HINT: The amount of command argume DALI 2.0 DT8 PRIMARY N CHANNELS	TX RX el level between 0 and 254. 2 el level. ents depends on how many ASCII WRITE COMMAND POWER CHANNELS CHANNEL1 CHANNEL2	8:LAMP GROUP 9 #GROUP PRIMARY N:8,127,6554,13107,19661,26214,32767,39321 <cr> N/A 55 means MASK. This command sets in addition for up to 6 channels the new level between 0 and 65534 for a DALI device type channels are written! #ALL PRIMARY N:<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> #APN:<power>,<channel5>,<channel2>,<channel3>, <channel4>,<channel5>,<channe15>,<channe15>,<channe15>, #APN:<power>,<channel5>,<channe15>,<channe12>,<channe13>, <channe14>,<channe15>,<channe15>,<channe15>, #APN:<power>,<channe15>,<channe15>,<channe15>, #APN: 50,0 6:CHANNE1 1-6 50,0 6554 10,0 13107 20,0 19661 30,0 26214 40,0</channe15></channe15></channe15></power></channe15></channe15></channe15></channe14></channe13></channe12></channe15></channel5></power></channe15></channe15></channe15></channel5></channel4></channel3></channel2></channel5></power></cr></channel6></channel5></channel4></channel3></channel2></channel1></power></cr>		
HINT: The amount of command argume DALI 2.0 DT8 PRIMARY N CHANNELS	TX RX elevel between 0 and 254. 2 el level. ents depends on how many ASCII WRITE COMMAND POWER CHANNELS CHANNEL1 CHANNEL2 CHANNEL3	8:LAMP GROUP 9 #GROUP PRIMARY N:8,127,6554,13107,19661,26214,32767,39321 <cr> N/A 55 means MASK. This command sets in addition for up to 6 channels the new level between 0 and 65534 for a DALI device type channels are written! #ALL PRIMARY N:<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> #APN:<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> #OK<cr> 127 50,0 6:CHANNEL 1-6 6554 10,0 13107 20,0 19661 30,0</cr></cr></channel6></channel5></channel4></channel3></channel2></channel1></power></cr></channel6></channel5></channel4></channel3></channel2></channel1></power></cr>		
HINT: The amount of command argume DALI 2.0 DT8 PRIMARY N CHANNELS	TX RX elevel between 0 and 254. 2 el level. ents depends on how many ASCII WRITE COMMAND POWER CHANNELS CHANNEL1 CHANNEL2 CHANNEL3 CHANNEL4	8:LAMP GROUP 9 #GROUP PRIMARY N:8,127,6554,13107,19661,26214,32767,39321 <cr> N/A 55 means MASK. This command sets in addition for up to 6 channels the new level between 0 and 65534 for a DALI device type channels are written! #ALL PRIMARY N:<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> #APN:<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> #APN:<power>,<channel5>,<channel6><cr> Result: #OK<cr> 127 50,0 6:CHANNEL 1-6 6554 10,0 13107 20,0 19661 30,0 26214 40,0</cr></cr></channel6></channel5></power></cr></channel6></channel5></channel4></channel3></channel2></channel1></power></cr></channel6></channel5></channel4></channel3></channel2></channel1></power></cr>		
HINT: The amount of command argume DALI 2.0 DT8 PRIMARY N CHANNELS	TX RX el level between 0 and 254. 2 el level. ents depends on how many ASCII WRITE COMMAND POWER CHANNELS CHANNEL1 CHANNEL2 CHANNEL3 CHANNEL4 CHANNEL5	8:LAMP GROUP 9 #GROUP PRIMARY N:8,127,6554,13107,19661,26214,32767,39321 <cr> N/A 55 means MASK. This command sets in addition for up to 6 channels the new level between 0 and 65534 for a DALI device type channels are written! #ALL PRIMARY N:<power>,<channel1>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> #APN:<power>,<channel6><cr> #APN:<power>,<channel5>,<channel2>,<channel3>, <channel4>,<channel5>,<channel6><cr> #APN:<power>,<channel5>,<channel6><cr> Result: #OK<cr> 127 50,0 6:CHANNEL 1-6 6554 10,0 13107 20,0 19661 30,0 26214 40,0 32767 50,0</cr></cr></channel6></channel5></power></cr></channel6></channel5></channel4></channel3></channel2></channel5></power></cr></channel6></power></cr></channel6></channel5></channel4></channel3></channel2></channel1></power></cr>		

INT: The amount of command arguments de ALI 2.0 DT8 QUERY PRIMARY N	ASCII	#LAMP QUERY PRIMARY N: <lamp>,<channels><cr></cr></channels></lamp>	ASCII	NO
HANNELS	WRITE	#LQPN: <lamp>,<channels><cr></cr></channels></lamp>		
OR LAMP	COMMAND	Result:		
		#LQPN: <lamp>,<power>,<channel1>,,<channelx><cr></cr></channelx></channel1></power></lamp>		
	LAMP	4:LAMP SHORT ADDRESS 5		
	CHANNELS	2:CHANNEL 1+2		
	ТХ	#LAMP QUERY PRIMARY N:4,2 <cr></cr>		
	RX	'LQPN:4,127,65534,192,45,32768,65534,1234		
		Current lamp short address: 4->LAMP SHORT ADDRESS 5		
		Current arc power level:127->50,00%		
		Current level for PRIMARY N channel 1:65534->100,00%		
		Current level for PRIMARY N channel 2:192->0,29%		
		Current level for PRIMARY N channel 3:45->0,07%		
		Current level for PRIMARY N channel 4:32768->12900,79%		
		Current level for PRIMARY N channel 5:65534->100,00%		
		Current level for PRIMARY N channel 6:1234->1,88%		

DALI BUS ERROR	ASCII	#DALI BUS ERROR <cr></cr>	ASCII	
	READ	#DBERR <cr></cr>		
	COMMAND	Result:		
		#DBERR: <buserrdec>,<buserrhex><cr></cr></buserrhex></buserrdec>		
	ТХ	#DALI BUS ERROR <cr></cr>		
	RX	#255,DBERR:0,0x0 <cr></cr>		
		0:BUS IS OK		
Shows the status of the DALI bus. BusErrDec,BusErrHex: D:DALI bus is OK and fully functiona 1:DALI bus error, short circuit on D/	al Al-Lline, everlead on DAL-Lline	er ne DALL newer supply		
ET PRIORITY SLOT	ALT IITIE, OVERIDAU OIT DALT IITIE ASCII	#SET PRIORITY SLOT: <slot><cr></cr></slot>	ASCII	NO
ET FRIORITT SEOT	WRITE	#SPS: <slot><cr></cr></slot>	ASCII	NO
	COMMAND	Result:		
	COMMAND	#OK <cr></cr>		
	SLOT	3		
	TX	#SET PRIORITY SLOT:3 <cr></cr>		
	RX	N/A		
his command defines the DALI pric				
: 14ms: Configuration in general : 15ms: Automatic arc power contro	l commands	Is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time)		
2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 4: 16ms: Lowest priority, used for qu GET PRIORITY SLOT	l commands ery messages ASCII	#GET PRIORITY SLOT <cr></cr>	ASCII	
2: 14ms: Configuration in general 3: 15ms: Automatic arc power contro 1: 16ms: Lowest priority, used for qu	l commands ery messages ASCII READ	#GET PRIORITY SLOT <cr> #GPS<cr></cr></cr>	ASCII	
: 14ms: Configuration in general : 15ms: Automatic arc power contro : 16ms: Lowest priority, used for qu	l commands ery messages ASCII	#GET PRIORITY SLOT <cr> #GPS<cr> Result:</cr></cr>	ASCII	
: 14ms: Configuration in general : 15ms: Automatic arc power contro : 16ms: Lowest priority, used for qu	l commands ery messages ASCII READ COMMAND	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr></cr></priorityslothex></priorityslotdec></cr></cr>	ASCII	
: 14ms: Configuration in general : 15ms: Automatic arc power contro : 16ms: Lowest priority, used for qu	ASCII READ COMMAND	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>	ASCII	
: 14ms: Configuration in general : 15ms: Automatic arc power contro : 16ms: Lowest priority, used for qu	l commands ery messages ASCII READ COMMAND	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>	ASCII	
: 14ms: Configuration in general : 15ms: Automatic arc power contro : 16ms: Lowest priority, used for qu GET PRIORITY SLOT	ASCII READ COMMAND TX RX	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms</cr></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>	ASCII	
14ms: Configuration in general 15ms: Automatic arc power contro 16ms: Lowest priority, used for qu GET PRIORITY SLOT This command shows the current de 12ms: Highest priority, used for me	ASCII READ COMMAND TX RX fined DALI priority slot. There essages that must be repeated throl commands and command	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available:</cr></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>	ASCII	
: 14ms: Configuration in general : 15ms: Automatic arc power contro : 16ms: Lowest priority, used for qu GET PRIORITY SLOT his command shows the current de : 12ms: Highest priority, used for m : 13ms: User initiated arc power cor : 14ms: Configuration in general : 15ms: Automatic arc power control	ASCII READ COMMAND TX RX fined DALI priority slot. There essages that must be repeated throl commands and command I commands ery messages	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d swhich have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP RGBWAF:<lamp>,<power>,<red>,<green>,<blue>,</blue></green></red></power></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>	ASCII	YES
: 14ms: Configuration in general : 15ms: Automatic arc power contro : 16ms: Lowest priority, used for qu GET PRIORITY SLOT his command shows the current de : 12ms: Highest priority, used for me : 13ms: User initiated arc power cor : 14ms: Configuration in general : 15ms: Automatic arc power contro : 16ms: Lowest priority, used for qu DALI 2.0 DT8 RGBWAF CHANNELS	ASCII READ COMMAND TX RX fined DALI priority slot. There essages that must be repeated throl commands and command I commands	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d s which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP RGBWAF:<lamp>,<power>,<red>,<green>,<blue>, <white>,<amber>,<freecolour><cr></cr></freecolour></amber></white></blue></green></red></power></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>		YES
: 14ms: Configuration in general : 15ms: Automatic arc power contro : 16ms: Lowest priority, used for qu GET PRIORITY SLOT his command shows the current de : 12ms: Highest priority, used for me : 13ms: User initiated arc power cor : 14ms: Configuration in general : 15ms: Automatic arc power contro : 16ms: Lowest priority, used for qu DALI 2.0 DT8 RGBWAF	ASCII READ COMMAND TX RX fined DALI priority slot. There essages that must be repeated throl commands and command I commands ery messages	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d s which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP RGBWAF:<lamp>,<power>,<red>,<green>,<blue>, ,<amber>,<freecolour><cr> #LRGBWAF:<lamp>,<power>,<red>,<green>,<blue>, </blue></green></red></power></lamp></cr></freecolour></amber></blue></green></red></power></lamp></cr></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>		YES
Attack the second secon	ASCII READ COMMAND TX RX fined DALI priority slot. There essages that must be repeated trol commands and command I commands ery messages ASCII WRITE	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS: #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP RGBWAF:<lamp>,<power>,<red>,<green>,<blue>, #LAMP RGBWAF:<lamp>,<power>,<red>,<green>,<blue>, #LAMP RGBWAF:<lamp>,<power>,<red>,<green>,<blue>, #LAMP RGBWAF:<lamp>,<power>,<red>,<green>,<blue>, #LAMP RGBWAF:<lamp>,<power>,<red>,<green>,<blue>, #LRGBWAF:<lamp>,<power>,<red>,<blue>, are 5, <amber 5,="" <freecolour=""><</amber></blue></red></power></lamp></blue></green></red></power></lamp></blue></green></red></power></lamp></blue></green></red></power></lamp></blue></green></red></power></lamp></blue></green></red></power></lamp></cr></cr></cr></cr>		YES
: 14ms: Configuration in general : 15ms: Automatic arc power contro : 16ms: Lowest priority, used for qu GET PRIORITY SLOT his command shows the current de : 12ms: Highest priority, used for me : 13ms: User initiated arc power cor : 14ms: Configuration in general : 15ms: Automatic arc power contro : 16ms: Lowest priority, used for qu DALI 2.0 DT8 RGBWAF CHANNELS	ASCII READ COMMAND TX RX fined DALI priority slot. There essages that must be repeated trol commands and command I commands ery messages ASCII WRITE	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #255,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d swhich have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP RGBWAF:<lamp>,<power>,<red>,<green>,<blue>, <white>,<amber>,<freecolour><cr> #LRGBWAF:<lamp>,<power>,<red>,<green>,<blue>, <white>,<amber>,<freecolour><cr> Result:</cr></freecolour></amber></white></blue></green></red></power></lamp></cr></freecolour></amber></white></blue></green></red></power></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>		YES
: 14ms: Configuration in general : 15ms: Automatic arc power contro : 16ms: Lowest priority, used for qu GET PRIORITY SLOT his command shows the current de : 12ms: Highest priority, used for me : 13ms: User initiated arc power cor : 14ms: Configuration in general : 15ms: Automatic arc power contro : 16ms: Lowest priority, used for qu DALI 2.0 DT8 RGBWAF CHANNELS	ASCII READ COMMAND TX RX fined DALI priority slot. There essages that must be repeated throl commands and command I commands ery messages ASCII WRITE COMMAND	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #Z55,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: d s which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP RGBWAF:<lamp>,<power>,<red>,<green>,<blue>, <white>,<amber>,<freecolour><cr> #LRGBWAF:<lamp>,<power>,<red>,<green>,<blue>, <white>,<amber>,<freecolour><cr> #LRGBWAF:<lamp>,<power>,<red>,<green>,<blue>, <white>,<amber>,<freecolour><cr> Result: #OK<cr></cr></cr></freecolour></amber></white></blue></green></red></power></lamp></cr></freecolour></amber></white></blue></green></red></power></lamp></cr></freecolour></amber></white></blue></green></red></power></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>		YES
14ms: Configuration in general 15ms: Automatic arc power contro 16ms: Lowest priority, used for qu ET PRIORITY SLOT nis command shows the current de 12ms: Highest priority, used for me 13ms: User initiated arc power cor 14ms: Configuration in general 15ms: Automatic arc power contro 16ms: Lowest priority, used for qu ALI 2.0 DT8 RGBWAF HANNELS	ASCII READ COMMAND TX RX fined DALI priority slot. There essages that must be repeated throl commands and command I commands ery messages ASCII WRITE COMMAND	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GPS:<priorityslotdec>,<priorityslothex><cr> #GET PRIORITY SLOT<cr> #Z55,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: J ds which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP RGBWAF:<lamp>,<power>,<red>,<green>,<blue>, <white>,<amber>,<freecolour><cr> #LRGBWAF:<lamp>,<power>,<red>,<green>,<blue>, <white>,<amber>,<freecolour><cr> #LRGBWAF:<lamp>,<power>,<red>,<green>,<blue>, <white>,<amber>,<freecolour< td=""> Result: #OK<cr> 127 50,0</cr></freecolour<></amber></white></blue></green></red></power></lamp></cr></freecolour></amber></white></blue></green></red></power></lamp></cr></freecolour></amber></white></blue></green></red></power></lamp></cr></cr></cr></priorityslothex></priorityslotdec></cr></cr>		YES
Att 2.0 DT8 RGBWAF Att 2.0 DT8 RGBWAF Att 2.0 DT8 RGBWAF Att 2.0 DT8 RGBWAF Configuration in general Att 2.0 DT8 RGBWAF Configuration in general Att 2.0 DT8 RGBWAF Configuration in general Configuration in general Co	ASCII READ COMMAND TX RX fined DALI priority slot. There essages that must be repeated throl commands and command I commands ery messages ASCII WRITE COMMAND	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GET PRIORITY SLOT<cr> #GET PRIORITY SLOT<cr> #Z55,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: J // Swhich have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP RGBWAF:<lamp>,<power>,<red>,<green>,<blue>, // WHITE>,<amber>,<freecolour<cr> #LRGBWAF:<lamp>,<power>,<red>,<green>,<blue>, // WHITE>,<amber>,<freecolour< td=""> // Result: #OK<cr> 127 50,0 3:RGB</cr></freecolour<></amber></blue></green></red></power></lamp></freecolour<cr></amber></blue></green></red></power></lamp></cr></cr></cr></cr></cr>		YES
Att 2.0 DT8 RGBWAF Att 2.0 DT8 RGBWAF Att 2.0 DT8 RGBWAF Att 2.0 DT8 RGBWAF Configuration in general Att 2.0 DT8 RGBWAF Configuration in general Att 2.0 DT8 RGBWAF Configuration in general Configuration in general Co	ASCII READ COMMAND TX RX fined DALI priority slot. There essages that must be repeated throl commands and command I commands ery messages ASCII WRITE COMMAND	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GET PRIORITY SLOT #GET PRIORITY SLOT #GET PRIORITY SLOT #Z55,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: J is which have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP RGBWAF:<lamp>,<power>,<red>,<green>,<blue>, #LAMP RGBWAF:<lamp>,<power>,<red>,<green>,<blue>, #LRGBWAF:<lamp>,<power>,<red>,<green>,<blue>, #LRGBWAF:<lamp>,<power>,<red>,<green>,<blue>, #LRGBWAF:<lamp>,<power>,<red>,<green>,<blue>, #URGBWAF:<lamp>,<power>,<red>,<green>,<blue>, #URGBWAF:<lamp>,<power>,<red>,<green>,<blue>, #URGBWAF:<lamp>,<power>,<red>,<green>,<blue>, #URGBWAF:<lamp>,<power>,<red>,<green>,<blue>, #URGBWAF:<lamp>,<freecolour< td=""> SO,0 3:RGB 0 0,0</freecolour<></lamp></blue></green></red></power></lamp></blue></green></red></power></lamp></blue></green></red></power></lamp></blue></green></red></power></lamp></blue></green></red></power></lamp></blue></green></red></power></lamp></blue></green></red></power></lamp></blue></green></red></power></lamp></blue></green></red></power></lamp></cr></cr></cr>		YES
: 14ms: Configuration in general : 15ms: Automatic arc power contro : 16ms: Lowest priority, used for qu GET PRIORITY SLOT his command shows the current de : 12ms: Highest priority, used for me : 13ms: User initiated arc power cor : 14ms: Configuration in general : 15ms: Automatic arc power contro : 16ms: Lowest priority, used for qu DALI 2.0 DT8 RGBWAF CHANNELS	ASCII READ COMMAND TX RX fined DALI priority slot. There essages that must be repeated introl commands and command I commands and command I commands ery messages ASCII WRITE COMMAND	#GET PRIORITY SLOT <cr> #GPS<cr> Result: #GET PRIORITY SLOT<cr> #GET PRIORITY SLOT<cr> #Z55,GPS:2,0x2<cr> Current priority slot:2->14ms are 5 slots available: J // Swhich have an impact on manual adjustment of arc power (e.g., dim up with a special fade time) #LAMP RGBWAF:<lamp>,<power>,<red>,<green>,<blue>, // WHITE>,<amber>,<freecolour<cr> #LRGBWAF:<lamp>,<power>,<red>,<green>,<blue>, // WHITE>,<amber>,<freecolour< td=""> // Result: #OK<cr> 127 50,0 3:RGB</cr></freecolour<></amber></blue></green></red></power></lamp></freecolour<cr></amber></blue></green></red></power></lamp></cr></cr></cr></cr></cr>		YES

	WHITE	40,0		
	AMBER	127 50,0		
	FREECOLOUR	153 60,0		
	LAMP	1:LAMP SHORT ADDRESS 2		
	TX	#LAMP RGBWAF:1,127,0,0,254 <cr></cr>		
	RX	#255.OK <cr></cr>		
Sets for a lamp short address a new br feature. 65535 means MASK for chanr HINT: The amount of command argum	rightness level between 0 and 2 nel level. aents depends on how many ch	254. 255 means MASK. This command sets in addition for up to 6 channels the new level between 0 and 65534 for a DAL annels are written!	I device type 8 lamp with RC	BWAF colour
DALI 2.0 DT8 RGBWAF CHANNELS FOR LAMP GROUP	ASCII WRITE COMMAND	#GROUP RGBWAF: <group>,<power>,<red>,<green>,<blue>, <white>,<amber>,<freecolour><cr> #GRGBWAF:<group>,<power>,<red>,<green>,<blue>, <white>,<amber>,<freecolour><cr> Result: #OK<cr></cr></cr></freecolour></amber></white></blue></green></red></power></group></cr></freecolour></amber></white></blue></green></red></power></group>	ASCII	NO
	POWER	<mark>127 5</mark> 0,0		
	CHANNELS	4:RGB+WHITE		
	RED	<mark>26 1</mark> 0,0		
	GREEN	<mark>51 2</mark> 0,0		
	BLUE	<mark>77</mark> 30,0		
	WHITE	40,0		
	AMBER	<mark>127</mark> 50,0		
	AMBER FREECOLOUR	127 50,0 153 60,0		
		153 60,0 8:LAMP GROUP 9		
	FREECOLOUR	153 60,0		
	FREECOLOUR GROUP TX RX	153 60,0 8:LAMP GROUP 9 #GROUP RGBWAF:8,127,26,51,77,102 <cr> N/A</cr>		
Sets for a lamp group a new brightness feature. 65535 means MASK for chanr HINT: The amount of command argum	FREECOLOUR GROUP TX RX s level between 0 and 254. 255 nel level.	153 60,0 8:LAMP GROUP 9 #GROUP RGBWAF:8,127,26,51,77,102 <cr> N/A means MASK. This command sets in addition for up to 6 channels the new level between 0 and 65534 for a DALI device</cr>	e type 8 lamp group with RGE	3WAF colour
HINT: The amount of command argum DALI 2.0 DT8 RGBWAF	FREECOLOUR GROUP TX RX s level between 0 and 254. 255 nel level.	153 60,0 8:LAMP GROUP 9 #GROUP RGBWAF:8,127,26,51,77,102 <cr> N/A means MASK. This command sets in addition for up to 6 channels the new level between 0 and 65534 for a DALI device</cr>	e type 8 lamp group with RGE	3WAF colour
HINT: The amount of command argum DALI 2.0 DT8 RGBWAF CHANNELS	FREECOLOUR GROUP TX RX s level between 0 and 254. 255 nel level. nents depends on how many ch ASCII WRITE	153 60,0 8:LAMP GROUP 9 #GROUP RGBWAF:8,127,26,51,77,102 <cr> N/A means MASK. This command sets in addition for up to 6 channels the new level between 0 and 65534 for a DALI device annels are written! #ALL RGBWAF:<power>,<red>,<green>,<blue>, *WHITE>,<amber>,<freecolour><cr> #ARGBWAF:<power>,<red>,<green>,<blue>, <white>,<amber>,<freecolour><cr> #ARGBWAF:<power>,<freecolour><cr> Result:</cr></freecolour></power></cr></freecolour></amber></white></blue></green></red></power></cr></freecolour></amber></blue></green></red></power></cr>		
HINT: The amount of command argum DALI 2.0 DT8 RGBWAF CHANNELS	FREECOLOUR GROUP TX RX is level between 0 and 254. 255 nel level. hents depends on how many ch ASCII WRITE COMMAND	153 60,0 8:LAMP GROUP 9 #GROUP RGBWAF:8,127,26,51,77,102 <cr> N/A means MASK. This command sets in addition for up to 6 channels the new level between 0 and 65534 for a DALI device anels are written! #ALL RGBWAF:<power>,<red>,<green>,<blue>, <white>,<amber>,<freecolour><cr> #ARGBWAF:<power>,<red>,<green>,<blue>, <white>,<amber>,<freecolour><cr> #ARGBWAF:<power>,<red>,<green>,<blue>, <white>,<amber>,<freecolour><cr> Result: #OK<cr></cr></cr></freecolour></amber></white></blue></green></red></power></cr></freecolour></amber></white></blue></green></red></power></cr></freecolour></amber></white></blue></green></red></power></cr>		
HINT: The amount of command argum DALI 2.0 DT8 RGBWAF CHANNELS	FREECOLOUR GROUP TX RX is level between 0 and 254. 255 nel level. hents depends on how many ch ASCII WRITE COMMAND	153 60,0 8:LAMP GROUP 9 #GROUP RGBWAF:8,127,26,51,77,102 <cr> N/A means MASK. This command sets in addition for up to 6 channels the new level between 0 and 65534 for a DALI device annels are written! #ALL RGBWAF: #ALL RGBWAF: #ALL RGBWAF: POWER>, #ALL RGBWAF: WHITE>, AMBER>, #ARGBWAF: #ARGBWAF: #ARGBWAF: #ARGBWAF: #ARGBWAF: #ARGBWAF: #ARGBWAF: #OK<</cr>		
HINT: The amount of command argum DALI 2.0 DT8 RGBWAF CHANNELS	FREECOLOUR GROUP TX RX s level between 0 and 254. 255 nel level. nents depends on how many ch ASCII WRITE COMMAND POWER CHANNELS	153 60,0 8:LAMP GROUP 9 #GROUP RGBWAF:8,127,26,51,77,102 <cr> N/A means MASK. This command sets in addition for up to 6 channels the new level between 0 and 65534 for a DALI device annels are written! #ALL RGBWAF:<power>,<red>,<green>,<blue>, <white>,<amber>,<freecolour><cr> #ARGBWAF:<power>,<red>,<green>,<blue>, <white>,<amber>,<freecolour><cr> #ARGBWAF:<power>,<red>,<green>,<blue>, <white>,<amber>,<freecolour><cr> Result: #OK<cr> 127 50,0 3:RGB 10,0 51 20,0</cr></cr></freecolour></amber></white></blue></green></red></power></cr></freecolour></amber></white></blue></green></red></power></cr></freecolour></amber></white></blue></green></red></power></cr>		
HINT: The amount of command argum DALI 2.0 DT8 RGBWAF CHANNELS	FREECOLOUR GROUP TX RX s level between 0 and 254. 255 nel level. nents depends on how many ch ASCII WRITE COMMAND POWER CHANNELS RED	153 60,0 8:LAMP GROUP 9 #GROUP RGBWAF:8,127,26,51,77,102 <cr> N/A means MASK. This command sets in addition for up to 6 channels the new level between 0 and 65534 for a DALI device annels are written! #ALL RGBWAF:<power>,<red>,<green>,<blue>, <white>,<amber>,<freecolour><cr> #ARGBWAF:<power>,<red>,<green>,<blue>, <white>,<amber>,<freecolour><cr> #ARGBWAF:<power>,<freecolour><cr> Result: #OK<cr> 127 50,0 3:RGB 10,0</cr></cr></freecolour></power></cr></freecolour></amber></white></blue></green></red></power></cr></freecolour></amber></white></blue></green></red></power></cr>		
HINT: The amount of command argum DALI 2.0 DT8 RGBWAF CHANNELS	FREECOLOUR GROUP TX RX s level between 0 and 254. 255 nel level. nents depends on how many ch ASCII WRITE COMMAND POWER CHANNELS RED GREEN	153 60,0 8:LAMP GROUP 9 #GROUP RGBWAF:8,127,26,51,77,102 <cr> N/A means MASK. This command sets in addition for up to 6 channels the new level between 0 and 65534 for a DALI device annels are written! #ALL RGBWAF:<power>,<red>,<green>,<blue>, <kwhite>,<amber>,<freecolour< td=""> <result:< td=""> #OK<cr> 127 50,0 3:RGB 26 10,0 51 20,0</cr></result:<></freecolour<></amber></kwhite></blue></green></red></power></cr>		
HINT: The amount of command argum DALI 2.0 DT8 RGBWAF CHANNELS	FREECOLOUR GROUP TX RX is level between 0 and 254. 255 nel level. is level between 0 and 254. 255 nel level. is level between 0 and 254. 255 nel level. is level between 0 and 254. 255 nel level. is level between 0 and 254. 255 nel level. is level between 0 and 254. 255 nel level. is level between 0 and 254. 255 nel level. is level between 0 and 254. 255 nel level. is level between 0 and 254. 255 Not set the se	153 60,0 8:LAMP GROUP 9 #GROUP RGBWAF:8,127,26,51,77,102 <cr> N/A means MASK. This command sets in addition for up to 6 channels the new level between 0 and 65534 for a DALI device annels are written! #ALL RGBWAF:<power>,<red>,<green>,<blue>, #ALL RGBWAF:<power>,<red>,<green>,<blue>, #ALL RGBWAF:<power>,<red>,<green>,<blue>, #ARGBWAF:<power>,<red>,<green>,<blue>, #ARGBWAF:<power>,<red>,<green>,<blue>, #ARGBWAF:<power>,<freecolour< td=""> #ARGBWAF: #ARGBWAF: #ARGBWAF: #ARGBWAF: #ARGBWAF: #ARGBWAF: #ARGBWAF: #ARGBWAF:</freecolour<></power></blue></green></red></power></blue></green></red></power></blue></green></red></power></blue></green></red></power></blue></green></red></power></cr>		
HINT: The amount of command argum DALI 2.0 DT8 RGBWAF CHANNELS	FREECOLOUR GROUP TX RX is level between 0 and 254. 255 nel level. hents depends on how many ch ASCII WRITE COMMAND POWER CHANNELS RED GREEN BLUE WHITE	153 60,0 8:LAMP GROUP 9 #GROUP RGBWAF:8,127,26,51,77,102 <cr> N/A means MASK. This command sets in addition for up to 6 channels the new level between 0 and 65534 for a DALI device annels are written! #ALL RGBWAF:<power>,<red>,<green>,<blue>, #ALL RGBWAF:<power>,<freecolour< td=""> VHITE>,<amber>,<freecolour< td=""> #ARGBWAF:<power>,<red>,<green>,<blue>, *WHITE>,<amber>,<freecolour< td=""> #ARGBWAF:<power>,<red>,<green>,<blue>, *WHITE>,<amber>,<freecolour< td=""> \$0,0 3:RGB 26 10,0 51 20,0 77 30,0 102 40,0</freecolour<></amber></blue></green></red></power></freecolour<></amber></blue></green></red></power></freecolour<></amber></freecolour<></power></blue></green></red></power></cr>		
HINT: The amount of command argum DALI 2.0 DT8 RGBWAF CHANNELS	FREECOLOUR GROUP TX RX is level between 0 and 254. 255 nel level. hents depends on how many ch ASCII WRITE COMMAND POWER CHANNELS RED GREEN BLUE WHITE AMBER	153 60,0 8:LAMP GROUP 9 #GROUP RGBWAF:8,127,26,51,77,102 <cr> N/A means MASK. This command sets in addition for up to 6 channels the new level between 0 and 65534 for a DALI device annels are written! #ALL RGBWAF:<power>,<red>,<green>,<blue>, *WHITE>,<amber>,<freecolour><cr> #ARGBWAF:<power>,<red>,<green>,<blue>, *WHITE>,<amber>,<freecolour><cr> #ARGBWAF:<power>,<freecolour><cr> Result: #OK<cr> 127 50,0 3:RGB 26 10,0 51 20,0 77 30,0 102 40,0 127 50,0</cr></cr></freecolour></power></cr></freecolour></amber></blue></green></red></power></cr></freecolour></amber></blue></green></red></power></cr>		

ALI 2.0 DT8 QUERY RGBWAF	ASCII	#LAMP QUERY RGBWAF: <lamp>,<channels><cr></cr></channels></lamp>	ASCII	YES
HANNELS	WRITE	#LQRGBWAF: <lamp>,<channels><cr></cr></channels></lamp>		
OR LAMP	COMMAND	Result:		
		#LQRGBWAF: <lamp>,<power>,<red>,<green>,<blue>,</blue></green></red></power></lamp>		
		<white>,<amber>,<freecolour><cr></cr></freecolour></amber></white>		
	LAMP	1:LAMP SHORT ADDRESS 2		
	CHANNELS	3:RGB		
	ТХ	#LAMP QUERY RGBWAF:1,3 <cr></cr>		
	RX	#255,LQRGBWAF:1,127,0,226,84 <cr></cr>		
		Current lamp short address:1->LAMP SHORT ADDRESS 2		
		Current arc power level:127->50,00%		
		Current level forRED channel 1:0->0,00%		
		Current level for GREEN channel 2:226->88,98%		
		Current level for BLUE channel 3:84->33,07%		
		Current level for WHITE channel 4:N/A->N/A		
		Current level for AMBER channel 5:N/A->N/A		
		Current level for FREECOLOUR channel 6:N/A->N/A		